

Date		Task	Comment
Beginning	End		
Dec. 15	Dec. 19	Reception and test at LAL of 2 PCB (PCB A and B) with 4 FLCPhy2 chip each	
Dec. 22	Jan. 4	Holidays	
Jan. 6	Jan. 9	2 PCB at LLR : test on PCB A and PCB B with LLR DAQ	<b>If test is Ok we send PCB A in UK</b>
<b>Jan. 12</b>	<b>Feb. 20</b>	<b>Test of PCB A with UK DAQ in UK</b>	
Jan. 12	Jan. 16	Gluing one Wafer on PCB B	
Jan. 19	Jan. 23	Test of PCB B + Wafer with LLR DAQ and cosmic bench	<b>If test is Ok : start to equip the PCB C with all chips</b>
~ Jan. 30		Reception of the full equiped PCB C at LLR	
Feb. 2	Feb. 6	Gluing six Wafer on PCB C	
Feb. 9	~ Feb. 20	<b>UK DAQ at LLR for test with PCB C and cosmic bench</b> in parallel with the LLR DAQ	First test after gluing !