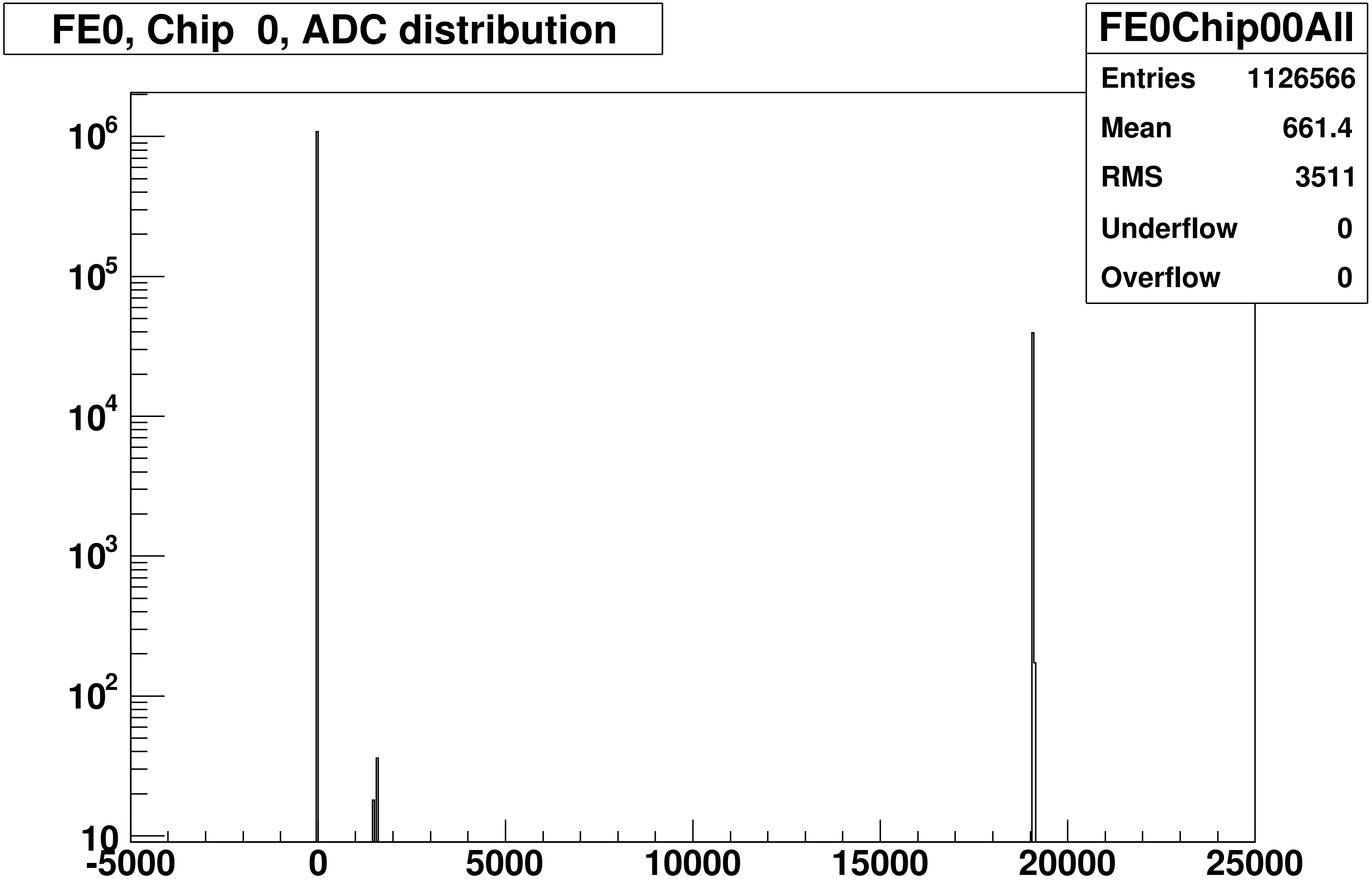
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 1126566
Mean 661.4
RMS 3511
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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10000
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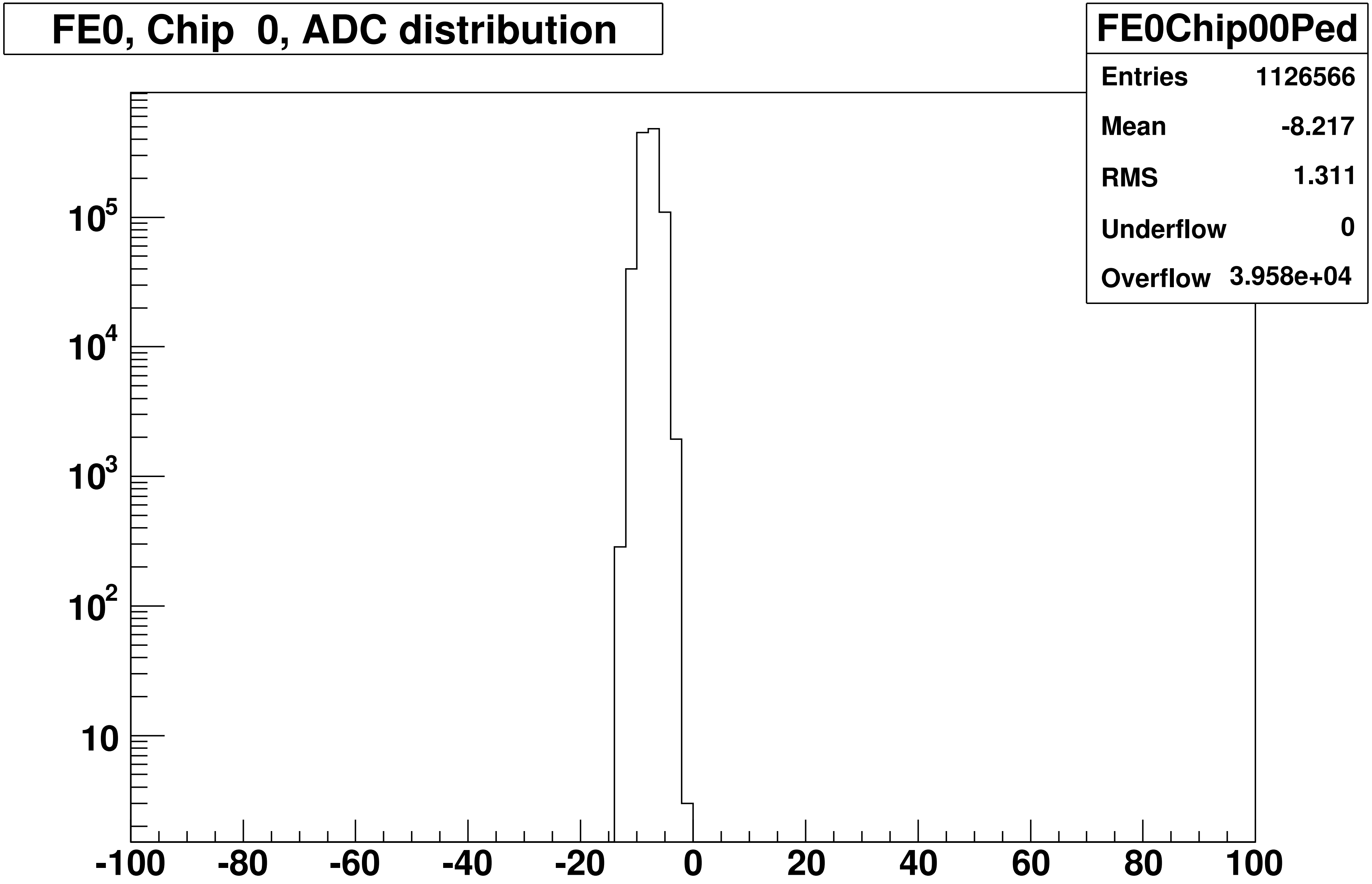
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 1126566
Mean -8.217
RMS 1.311
Underflow 0
Overflow 3.958e+04
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10^4
10^3
10^2
10
-100
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-60
-40
-20
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40
60
80
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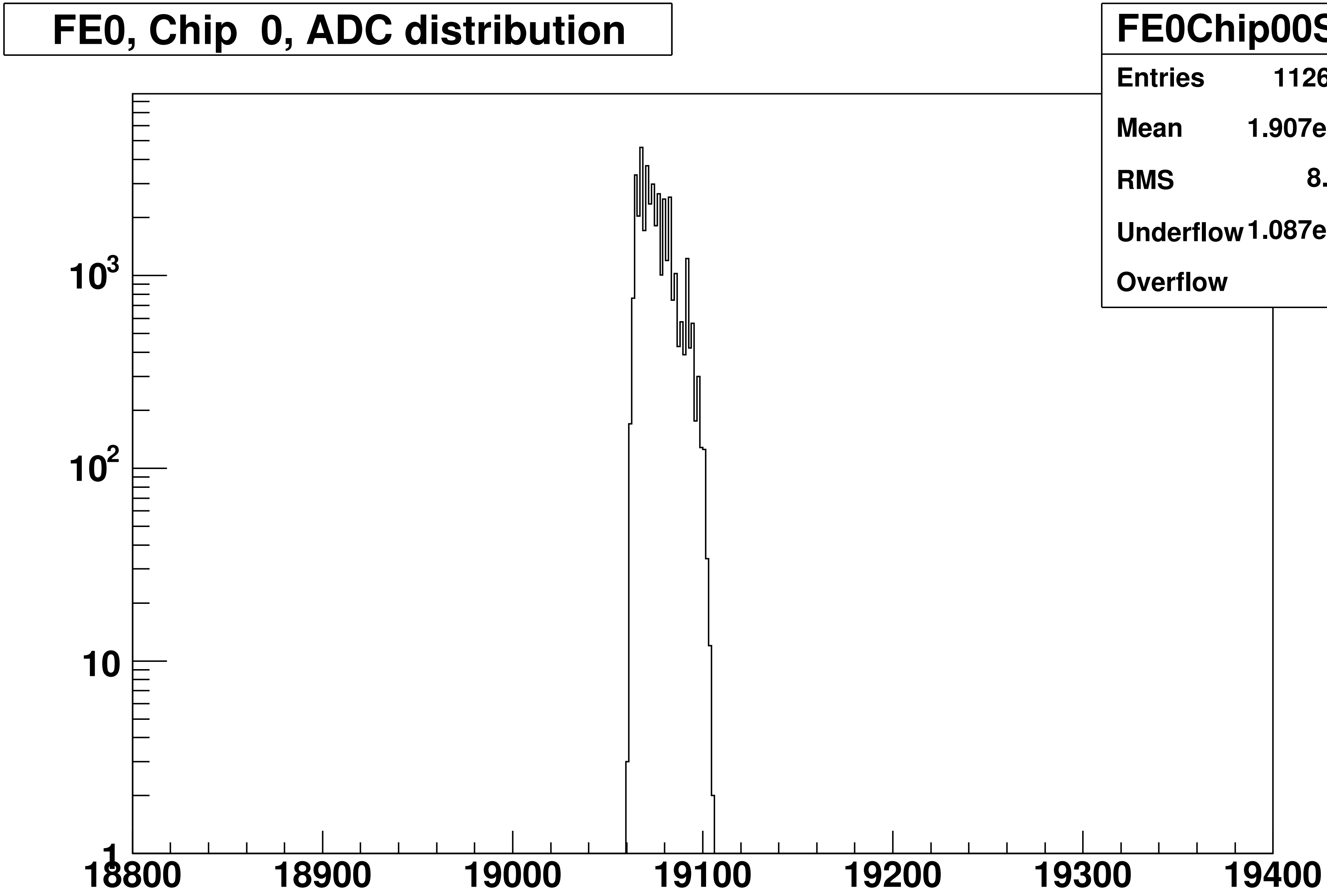
FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 1126566
Mean 1.907e+04
RMS 8.533
Underflow 1.087e+06
Overflow 0
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10^2
10
1
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18900
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19100
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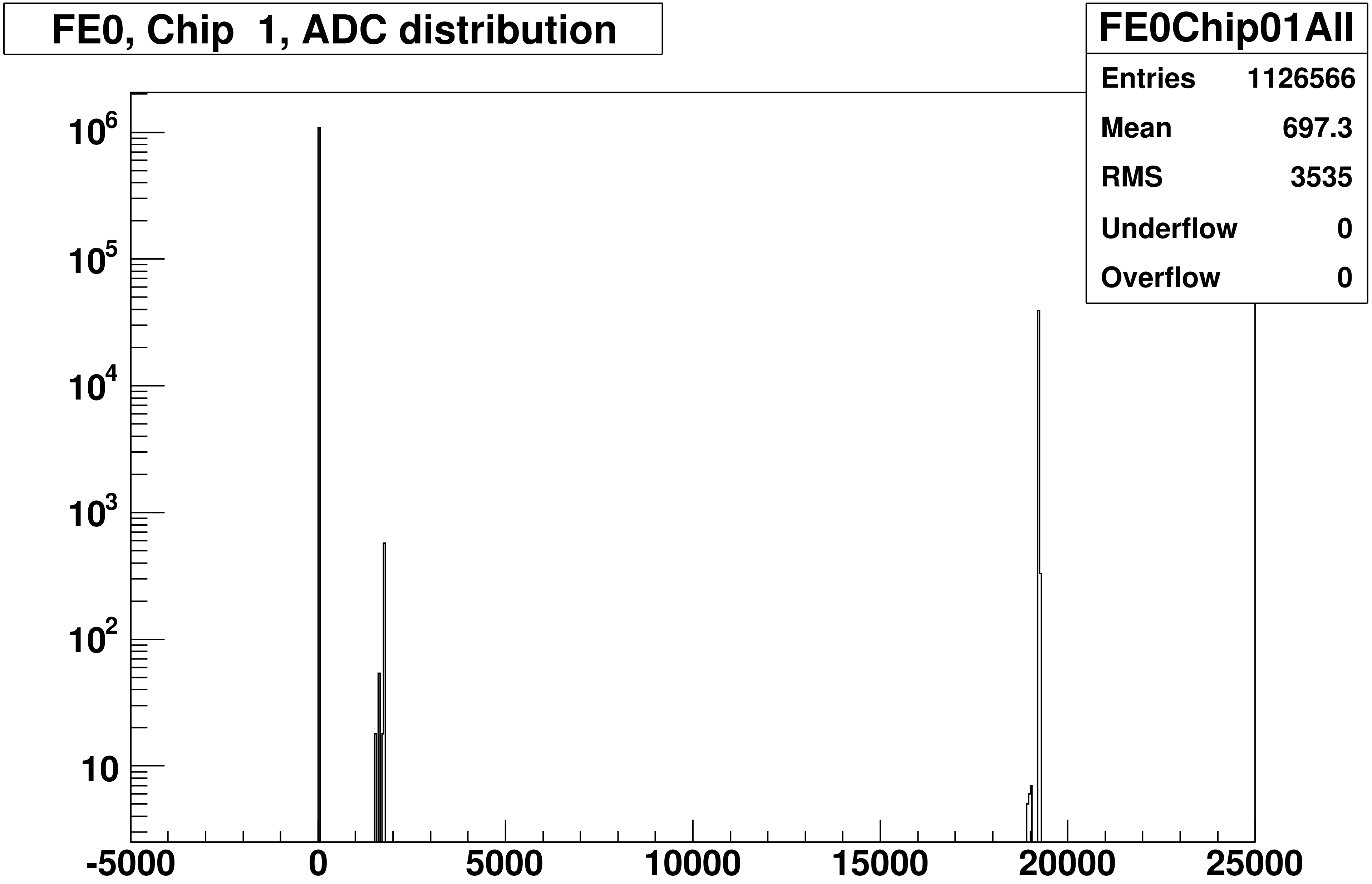
19300
19400


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 1126566
Mean 697.3
RMS 3535
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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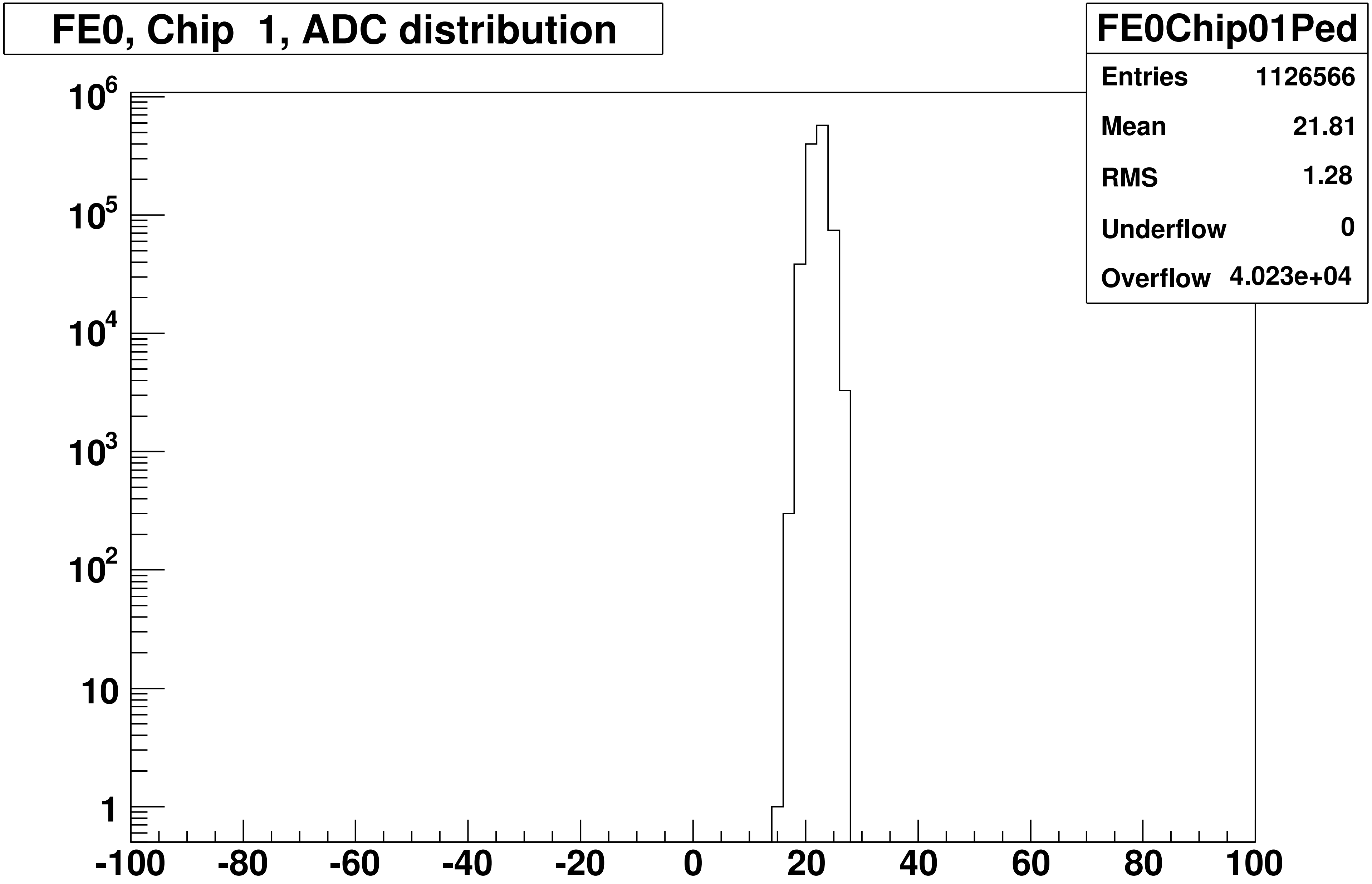
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 1126566
Mean 21.81
RMS 1.28
Underflow 0
Overflow 4.023e+04
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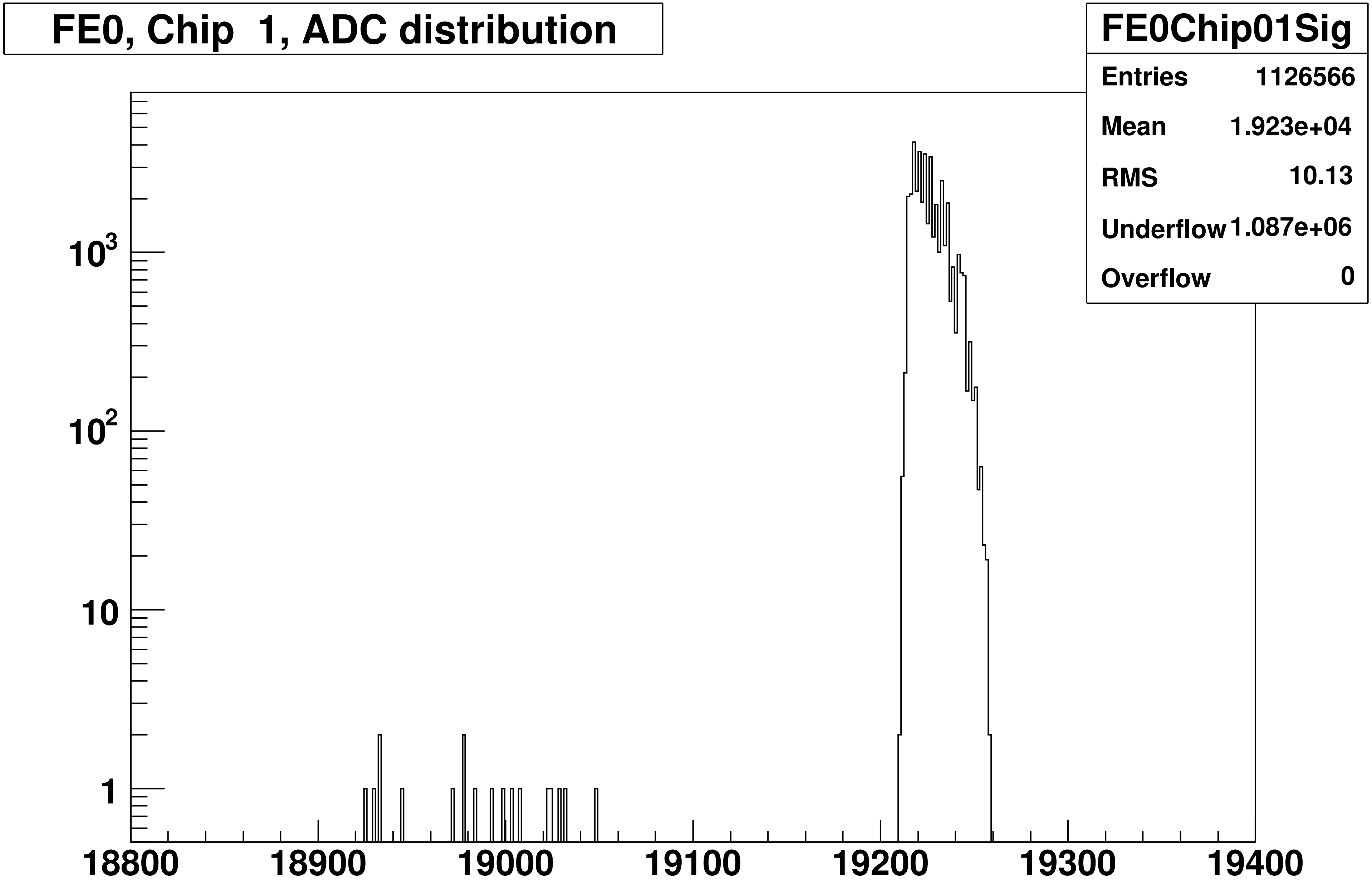
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 1126566
Mean 1.923e+04
RMS 10.13
Underflow 1.087e+06
Overflow 0
10^3
10^2
10
1
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18900
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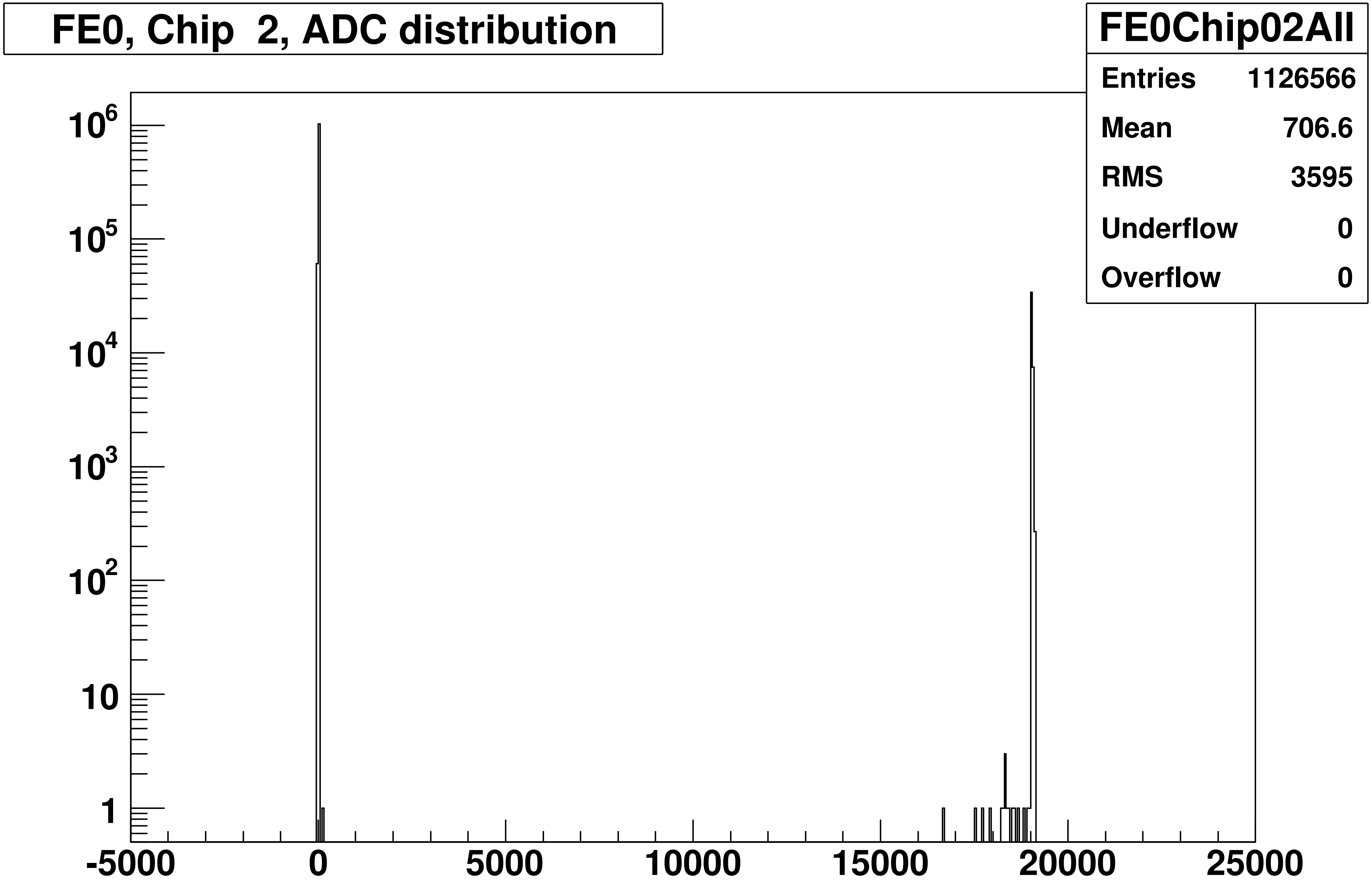
19100
19200
19300
19400


FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 1126566
Mean 706.6
RMS 3595
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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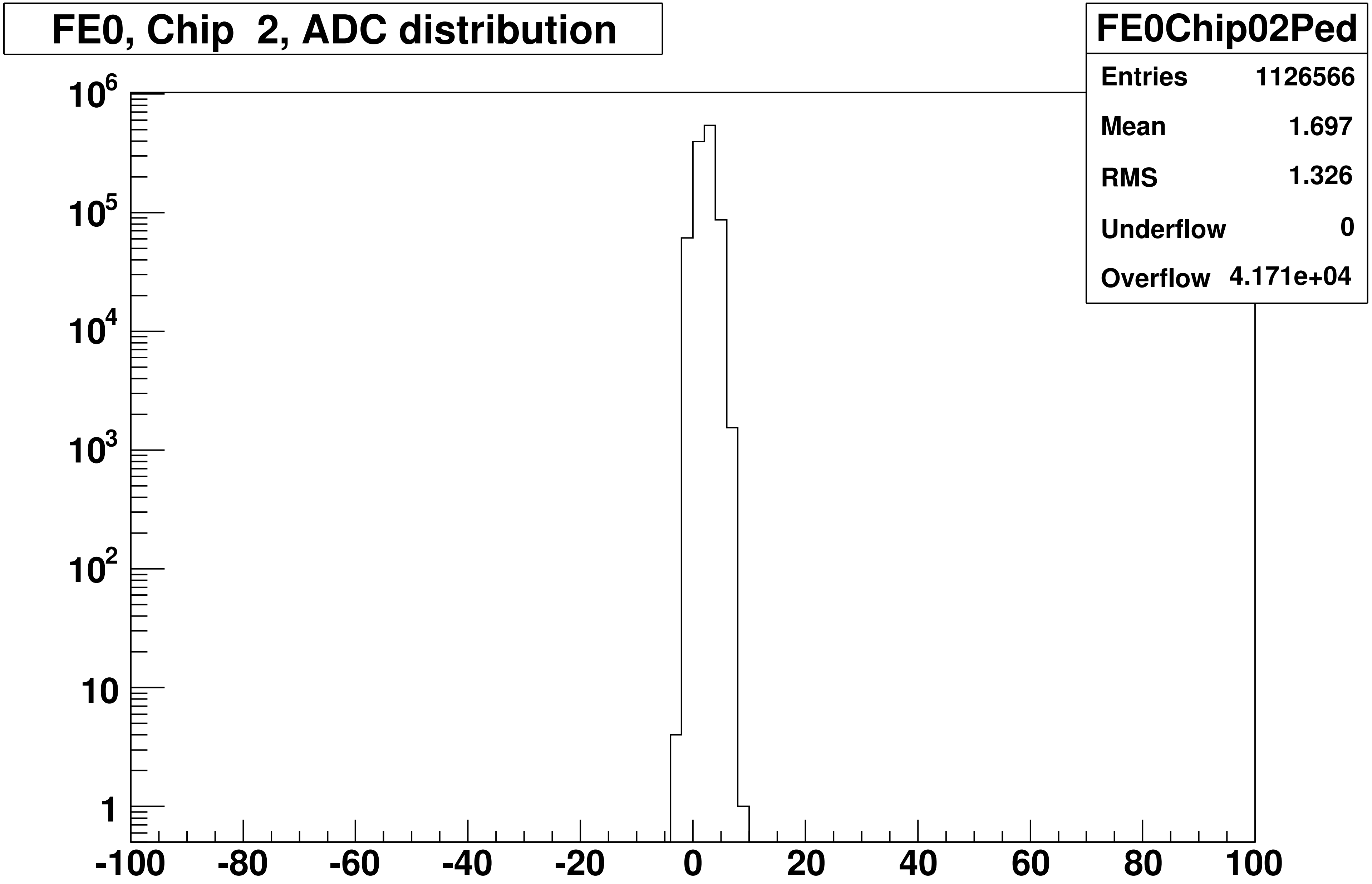
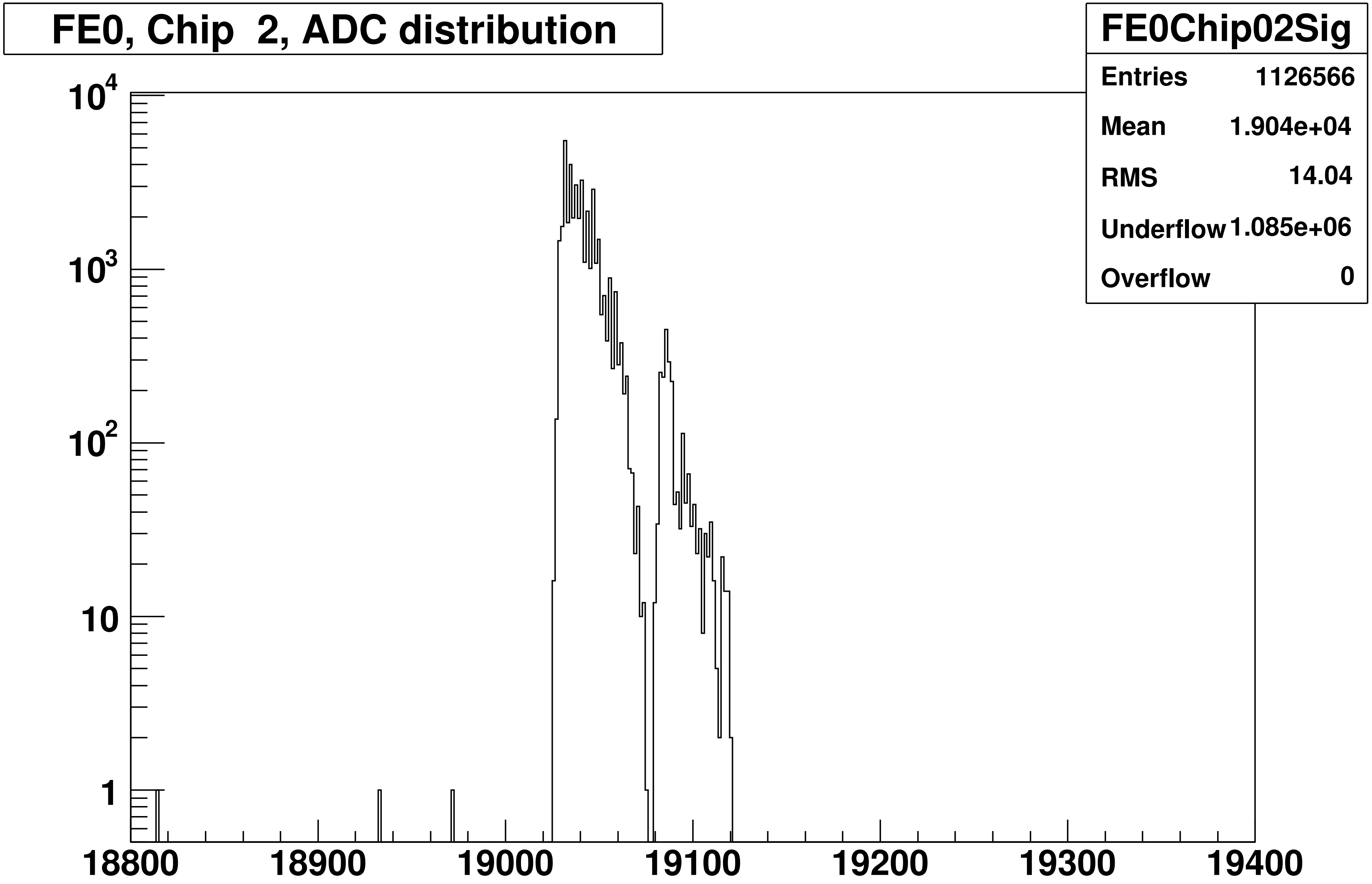
10000
15000
20000
25000



FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 1126566
Mean 1.697
RMS 1.326
Underflow 0
Overflow 4.171e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 1126566
Mean 1.904e+04
RMS 14.04
Underflow 1.085e+06
Overflow 0
10^4
10^3
10^2
10
1
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18900
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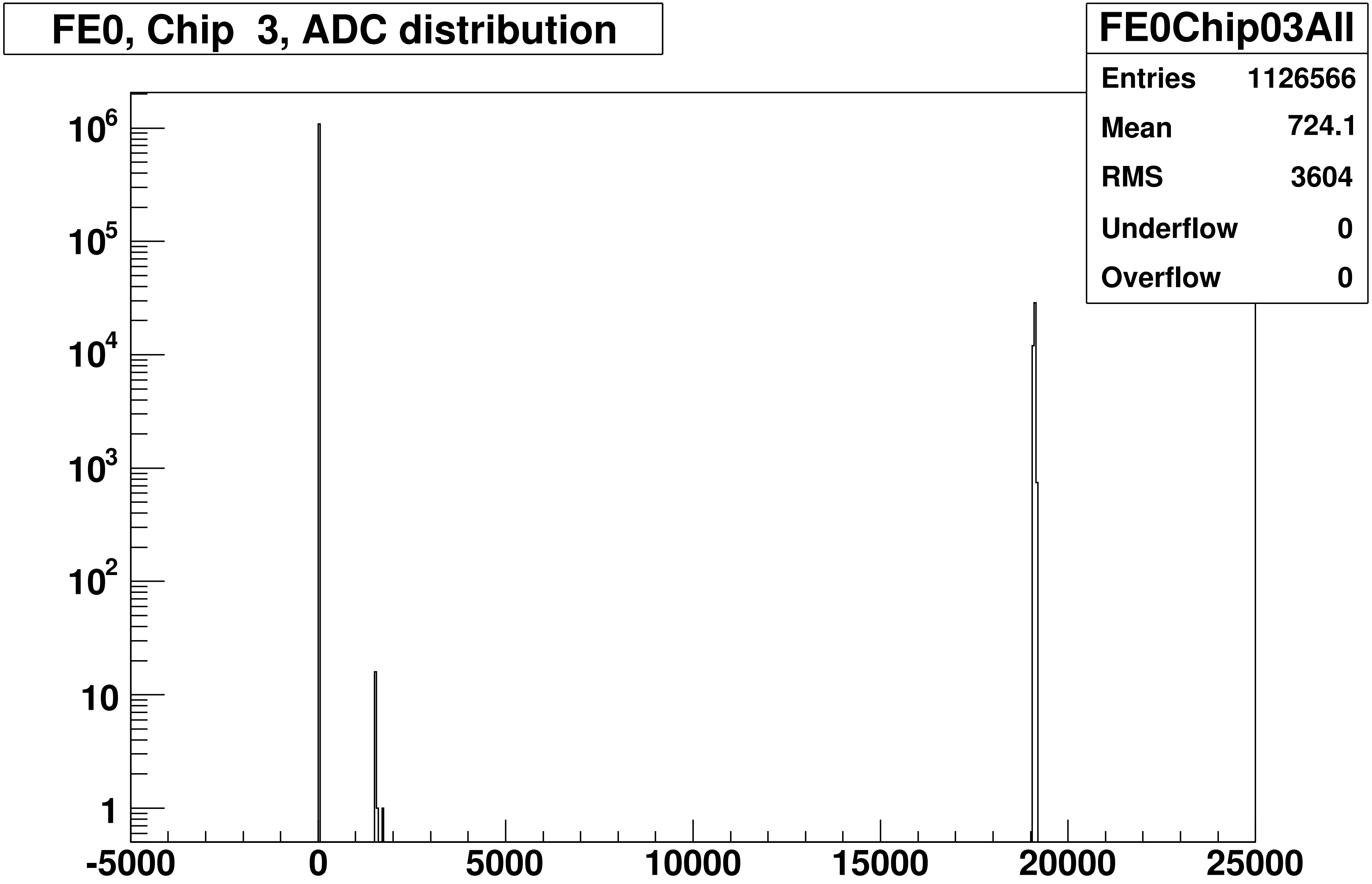
19300
19400


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 1126566
Mean 724.1
RMS 3604
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
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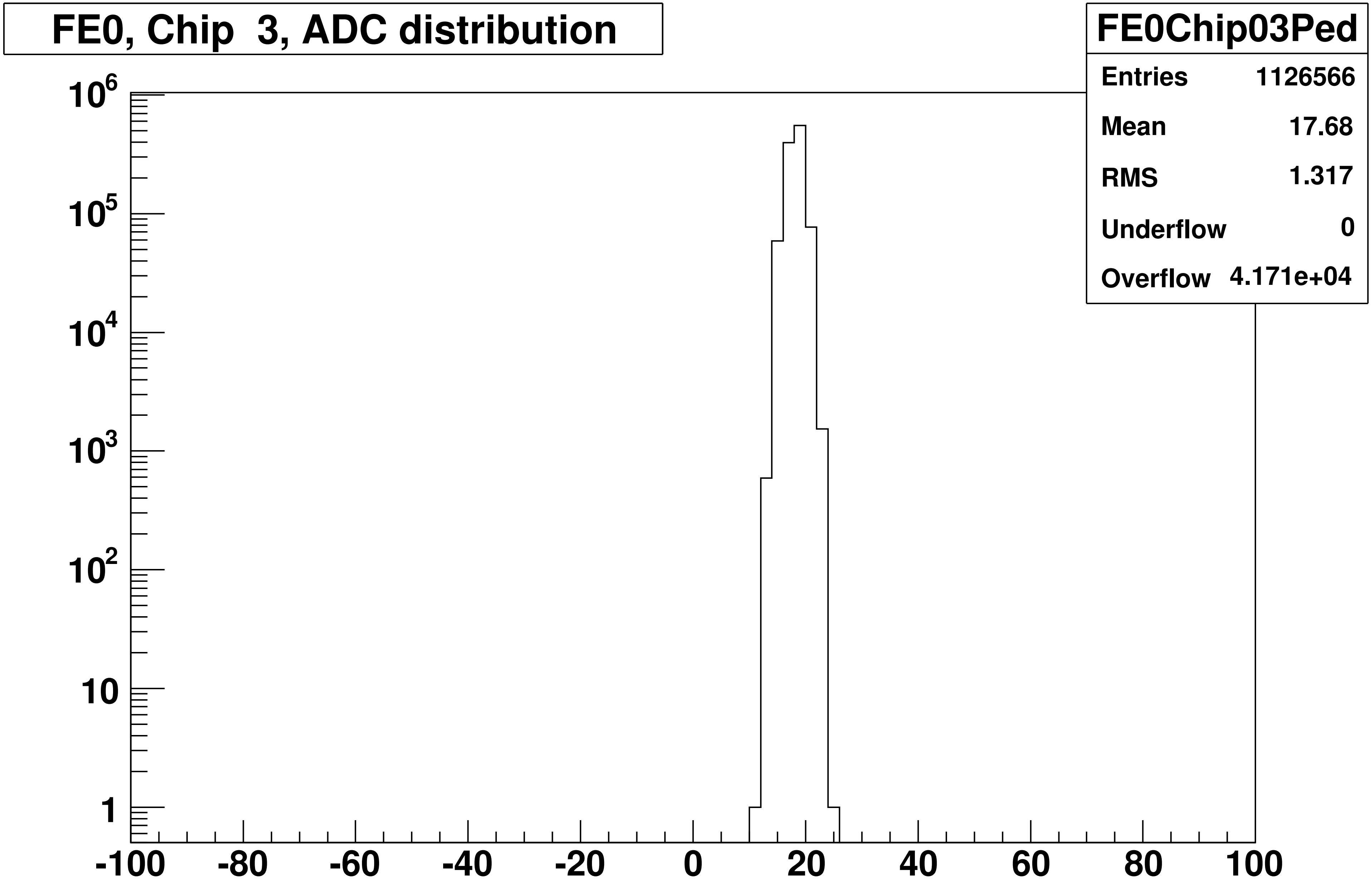
20000
25000


FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 1126566
Mean 17.68
RMS 1.317
Underflow 0
Overflow 4.171e+04
10^6
10^5
10^4
10^3
10^2
10
1
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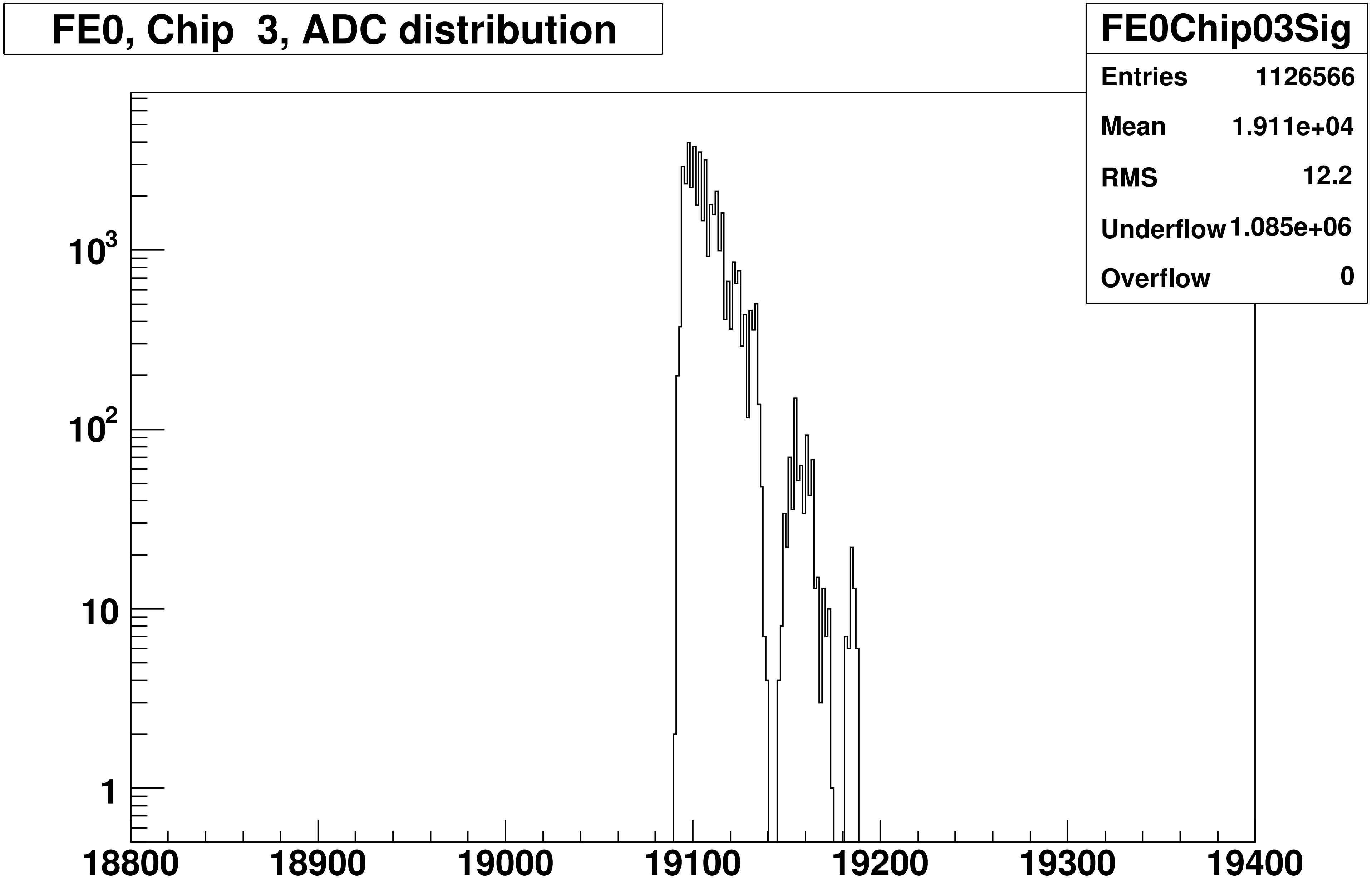
-20
0
20
40
60
80
100


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 1126566
Mean 1.911e+04
RMS 12.2
Underflow 1.085e+06
Overflow 0
10^3
10^2
10
1
18800
18900
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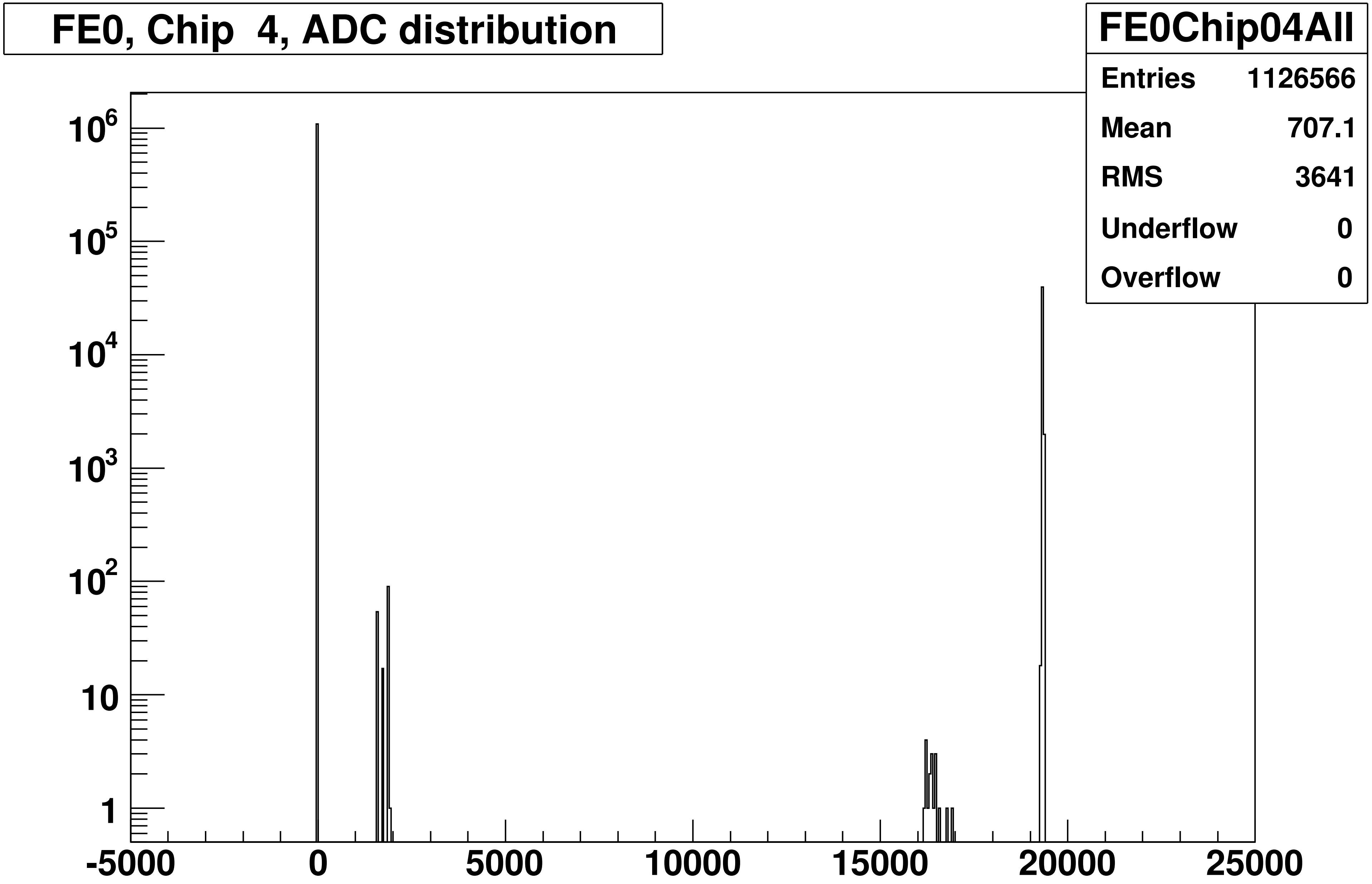
19200
19300
19400



FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 1126566
Mean 707.1
RMS 3641
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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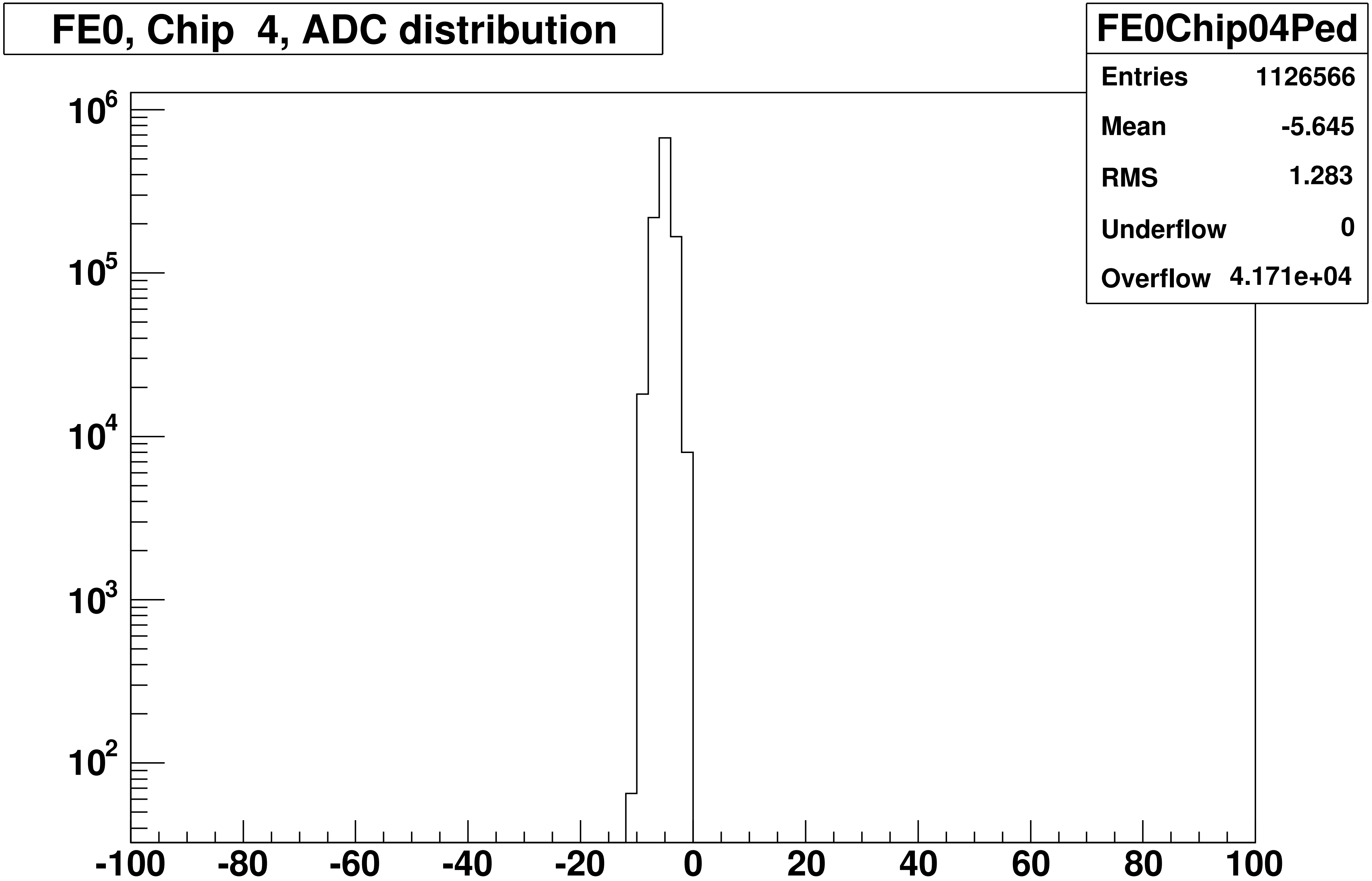
10000
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 1126566
Mean -5.645
RMS 1.283
Underflow 0
Overflow 4.171e+04
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10^2
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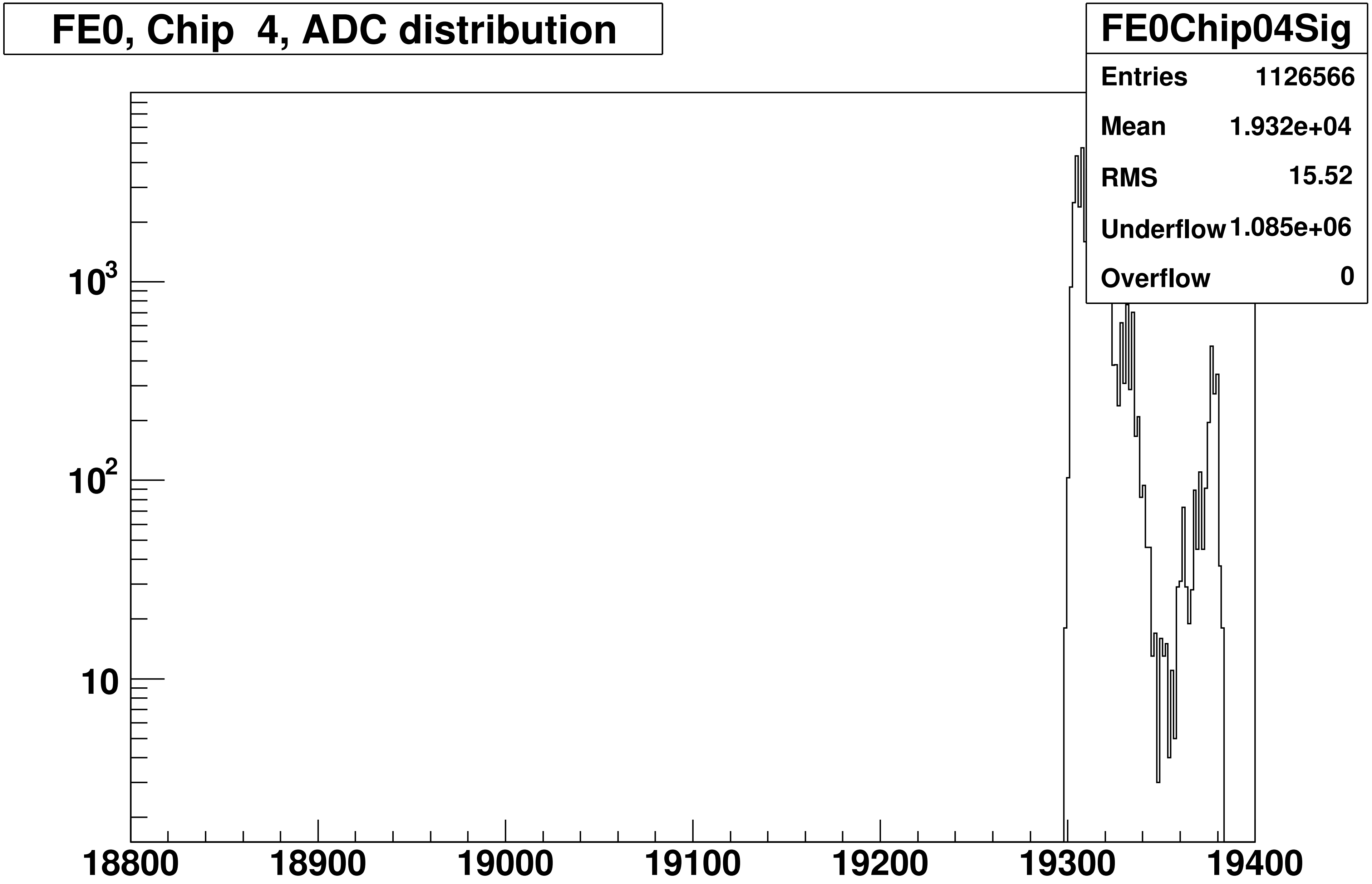
FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 1126566
Mean 1.932e+04
RMS 15.52
Underflow 1.085e+06
Overflow 0
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10^2
10^3
18800
18900
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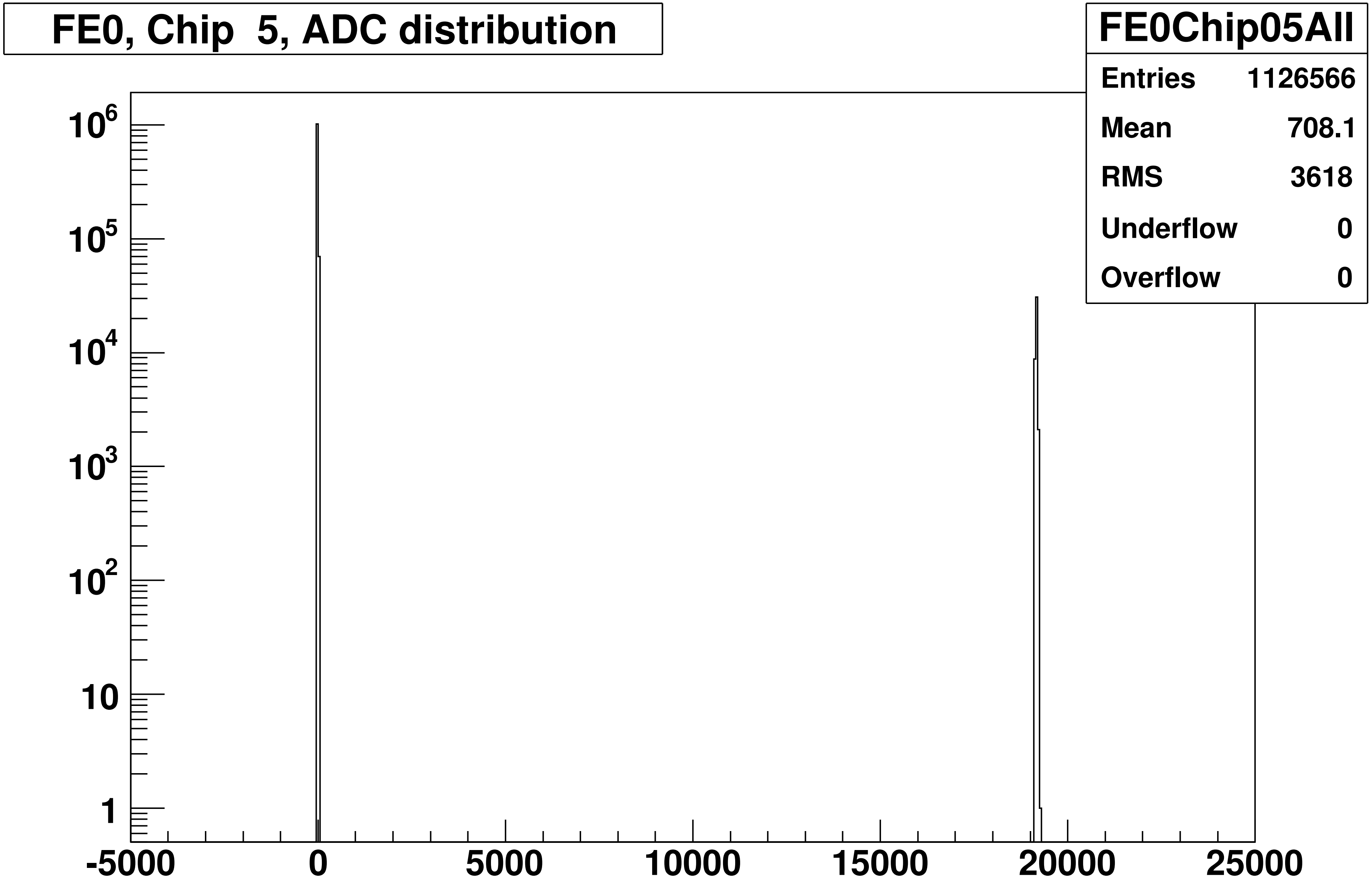
19100
19200
19300
19400


FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 1126566
Mean 708.1
RMS 3618
Underflow 0
Overflow 0
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10^4
10^3
10^2
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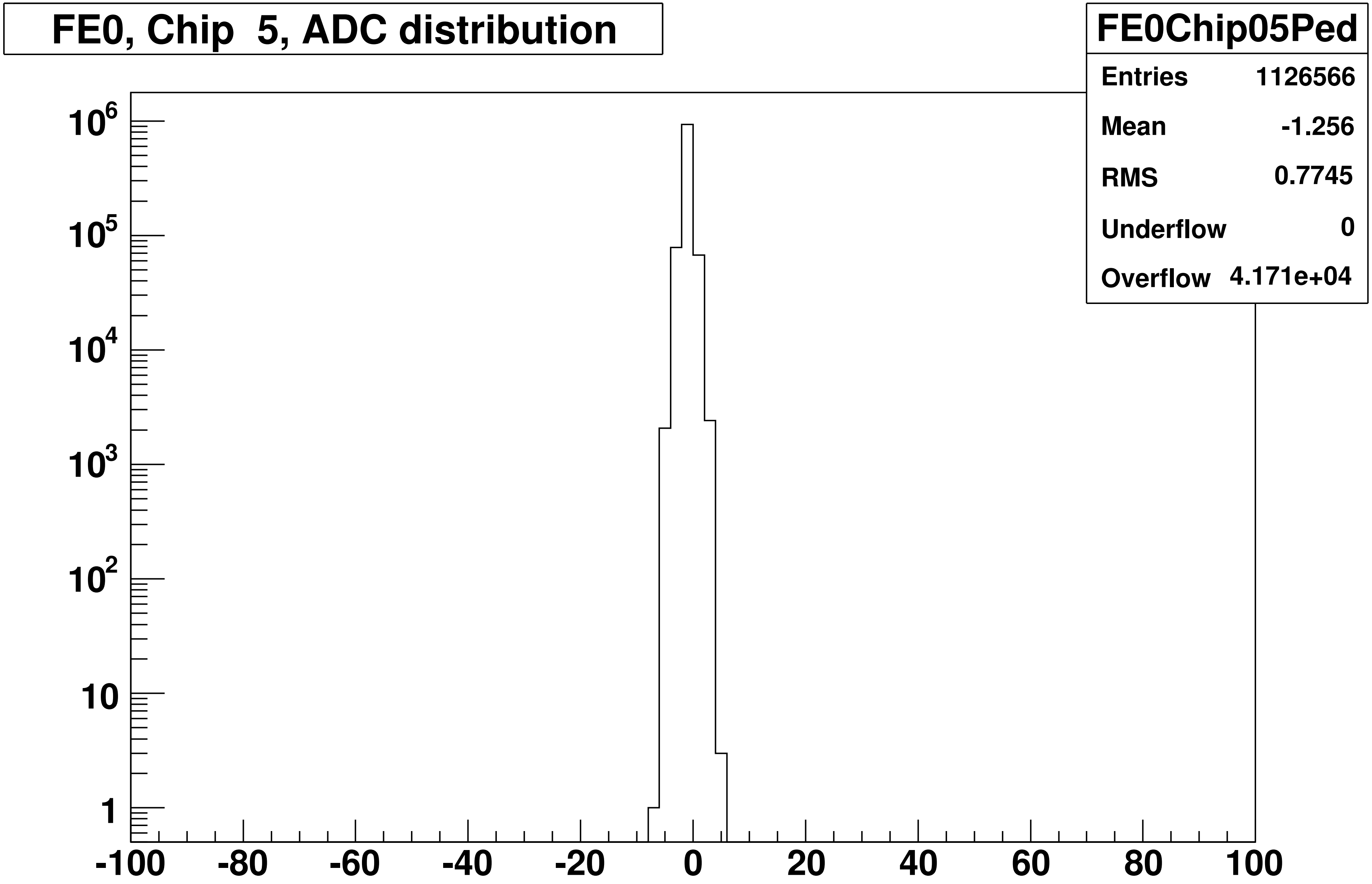
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 1126566
Mean -1.256
RMS 0.7745
Underflow 0
Overflow 4.171e+04
10^6
10^5
10^4
10^3
10^2
10
1
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-40
-20
0
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40
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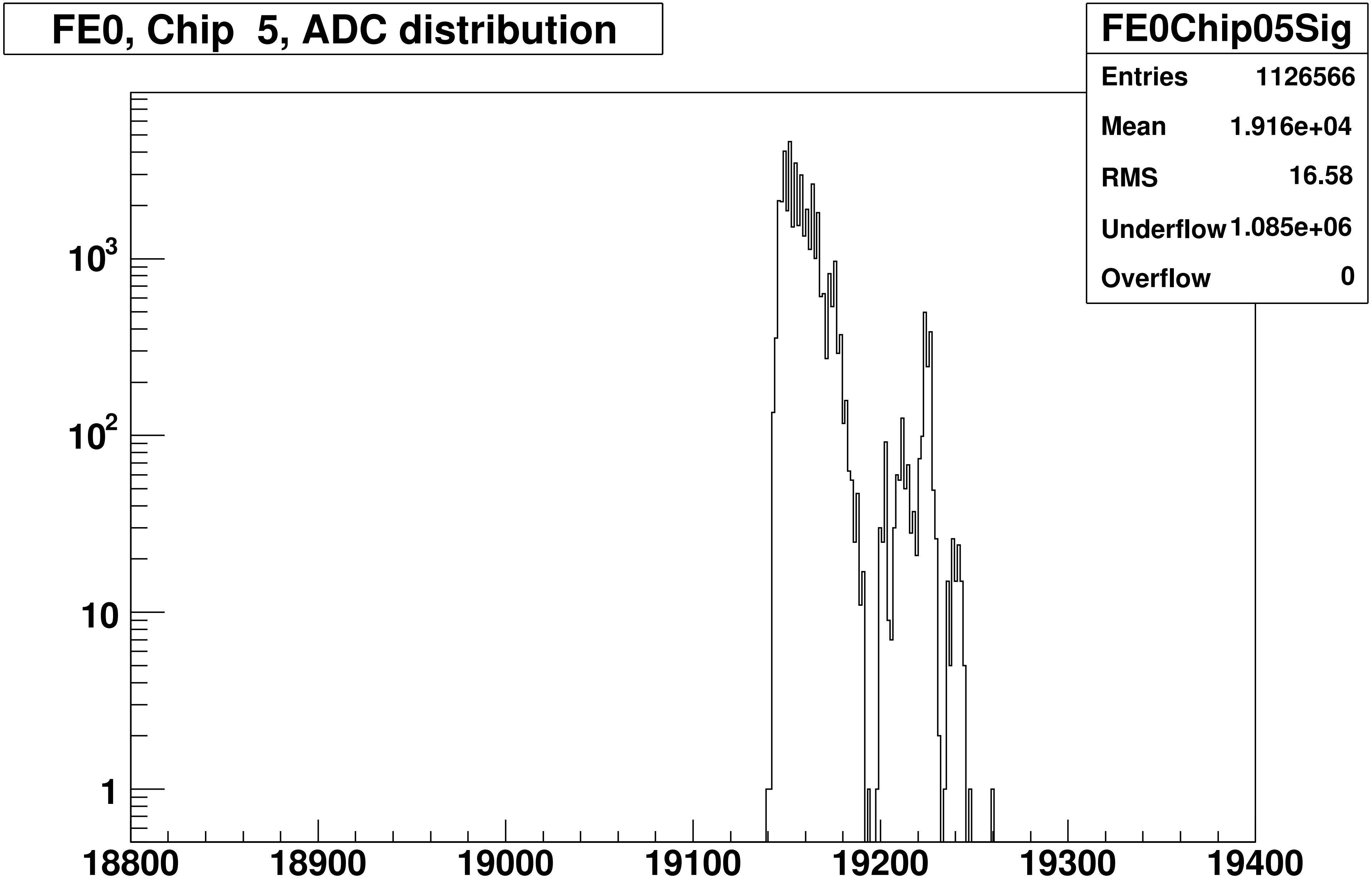
80
100



FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 1126566
Mean 1.916e+04
RMS 16.58
Underflow 1.085e+06
Overflow 0
10^3
10^2
10
1
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18900
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19100
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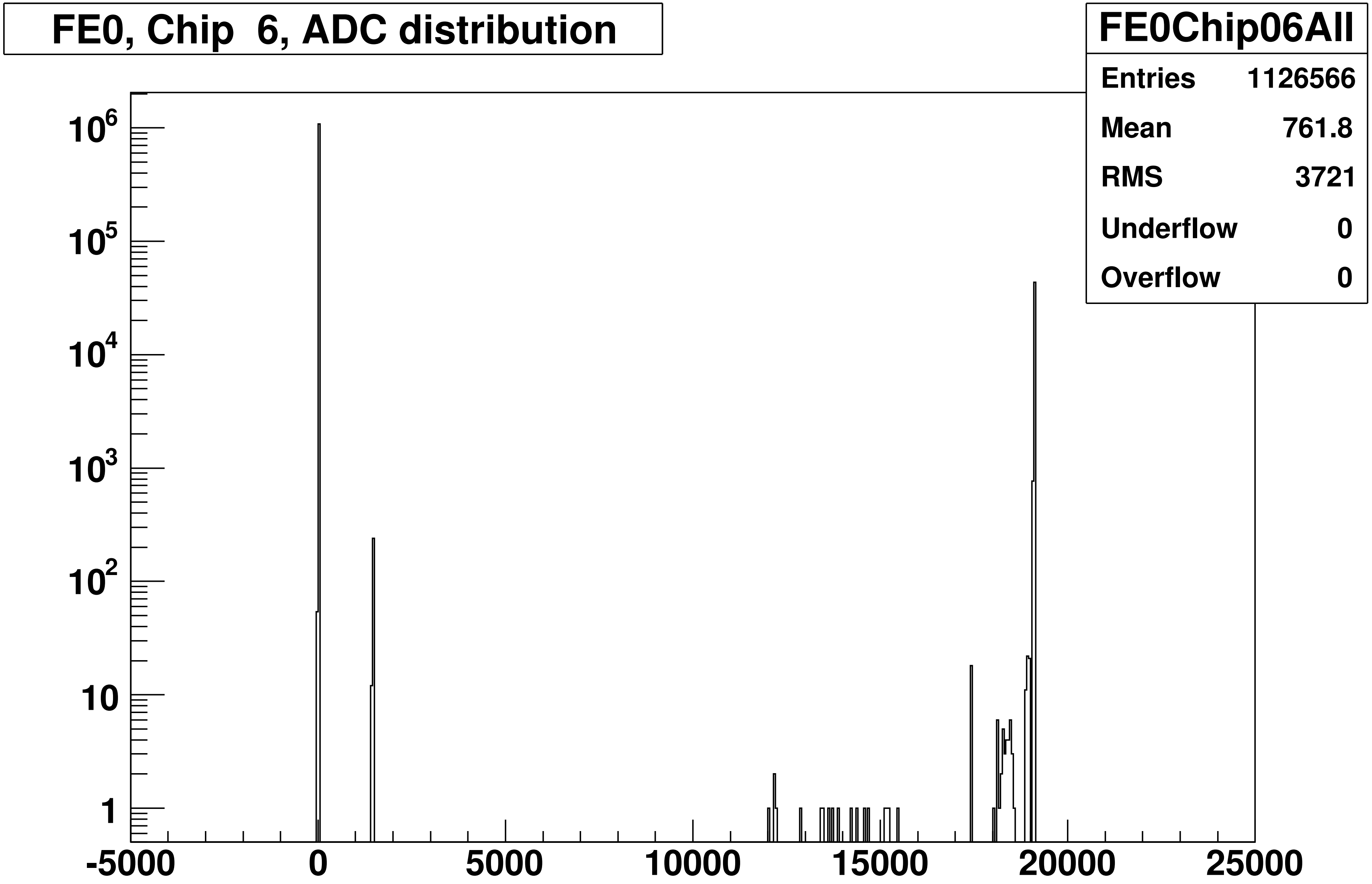
19300
19400



FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 1126566
Mean 761.8
RMS 3721
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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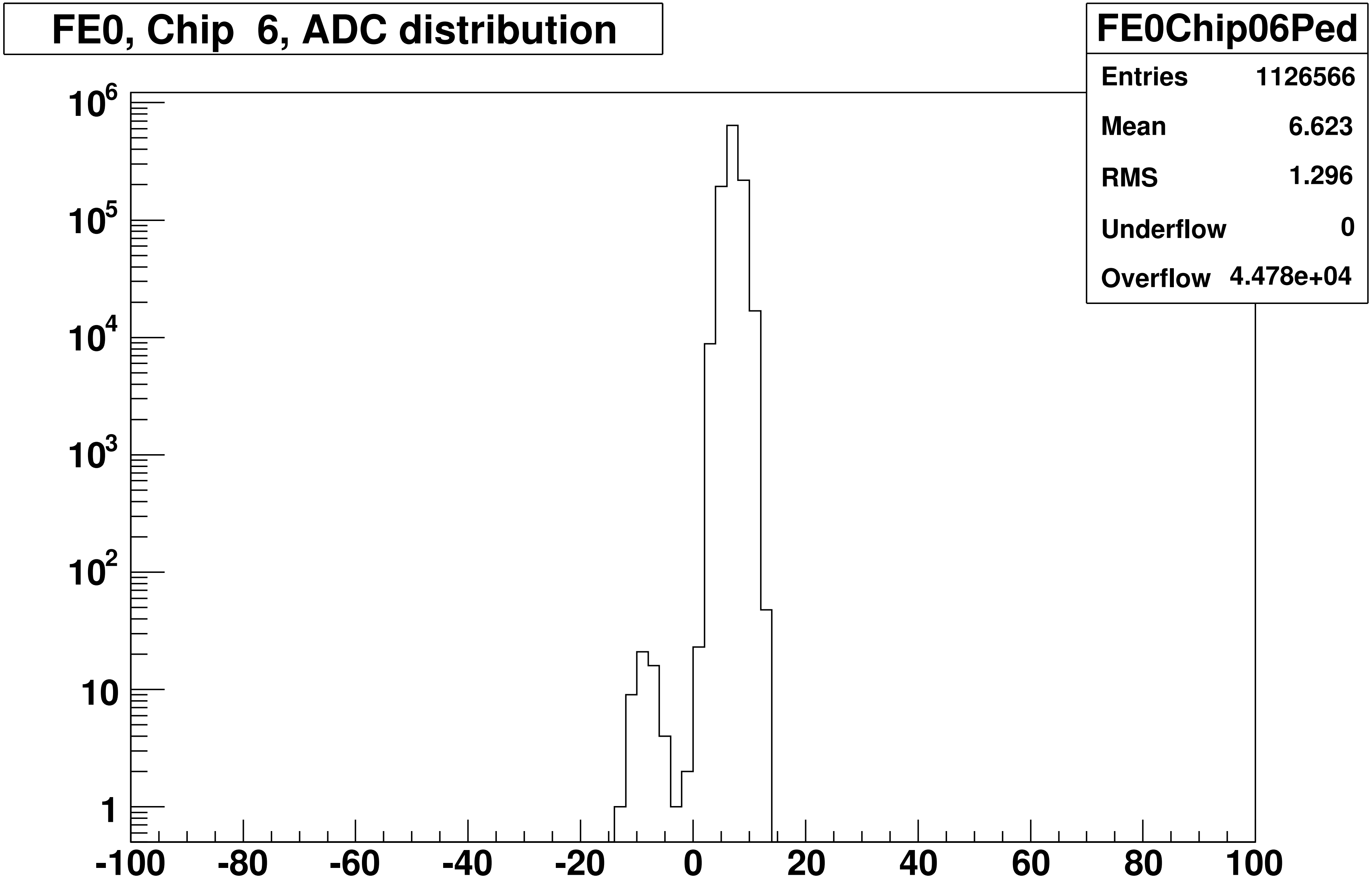
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 1126566
Mean 6.623
RMS 1.296
Underflow 0
Overflow 4.478e+04
10^6
10^5
10^4
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10^2
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1
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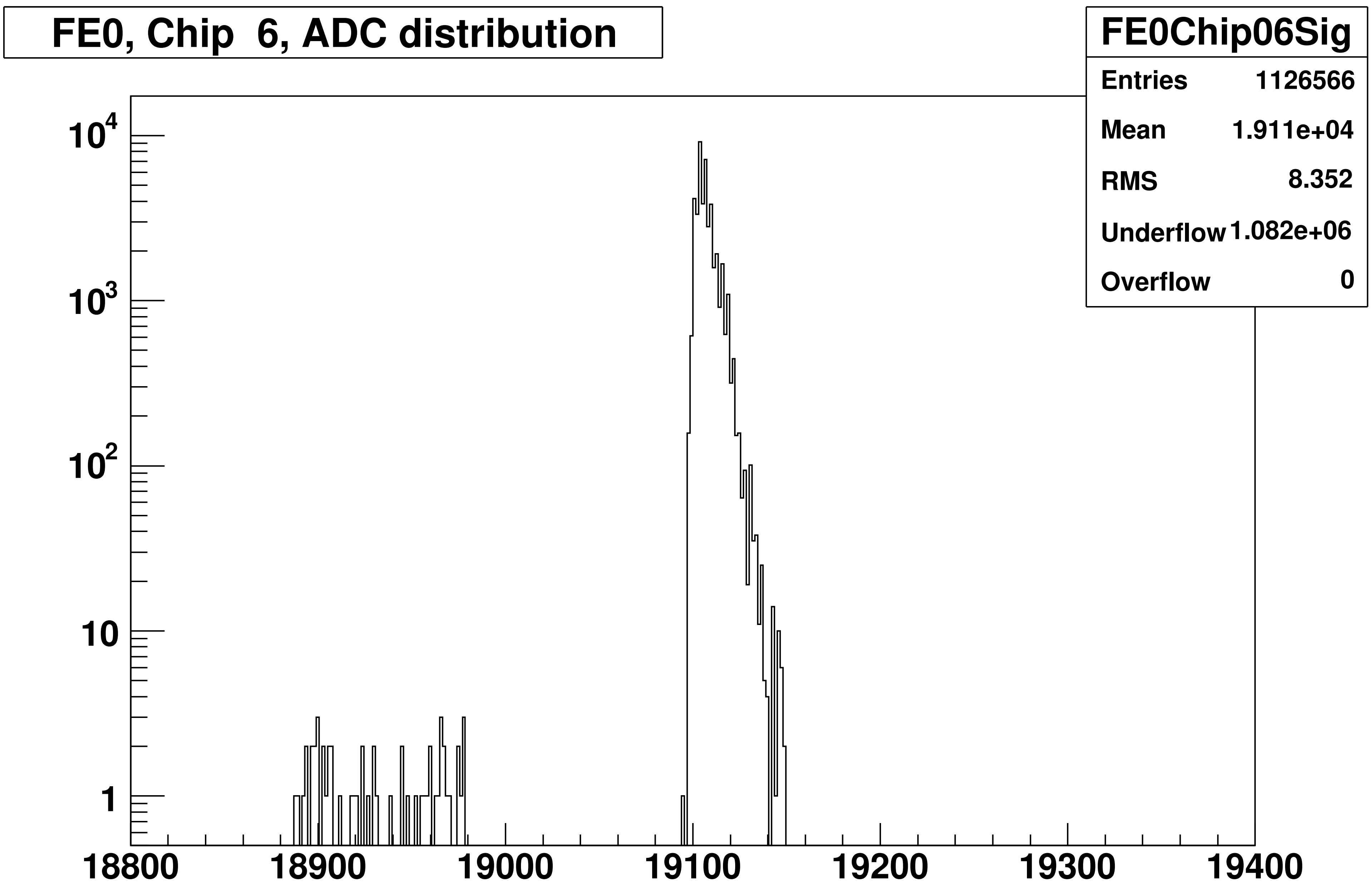

FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 1126566
Mean 1.911e+04
RMS 8.352
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
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18900
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19100
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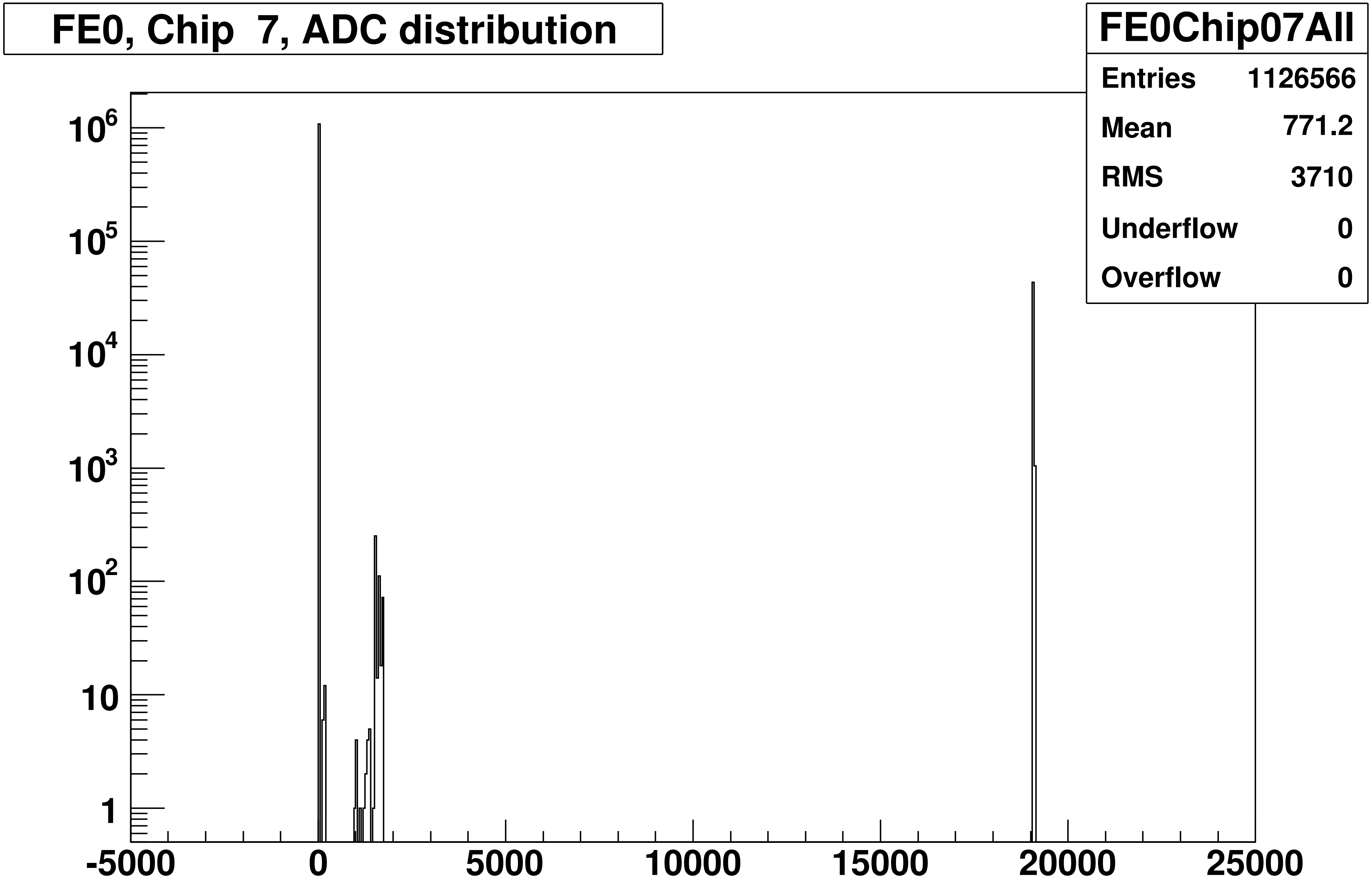
19300
19400


FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 1126566
Mean 771.2
RMS 3710
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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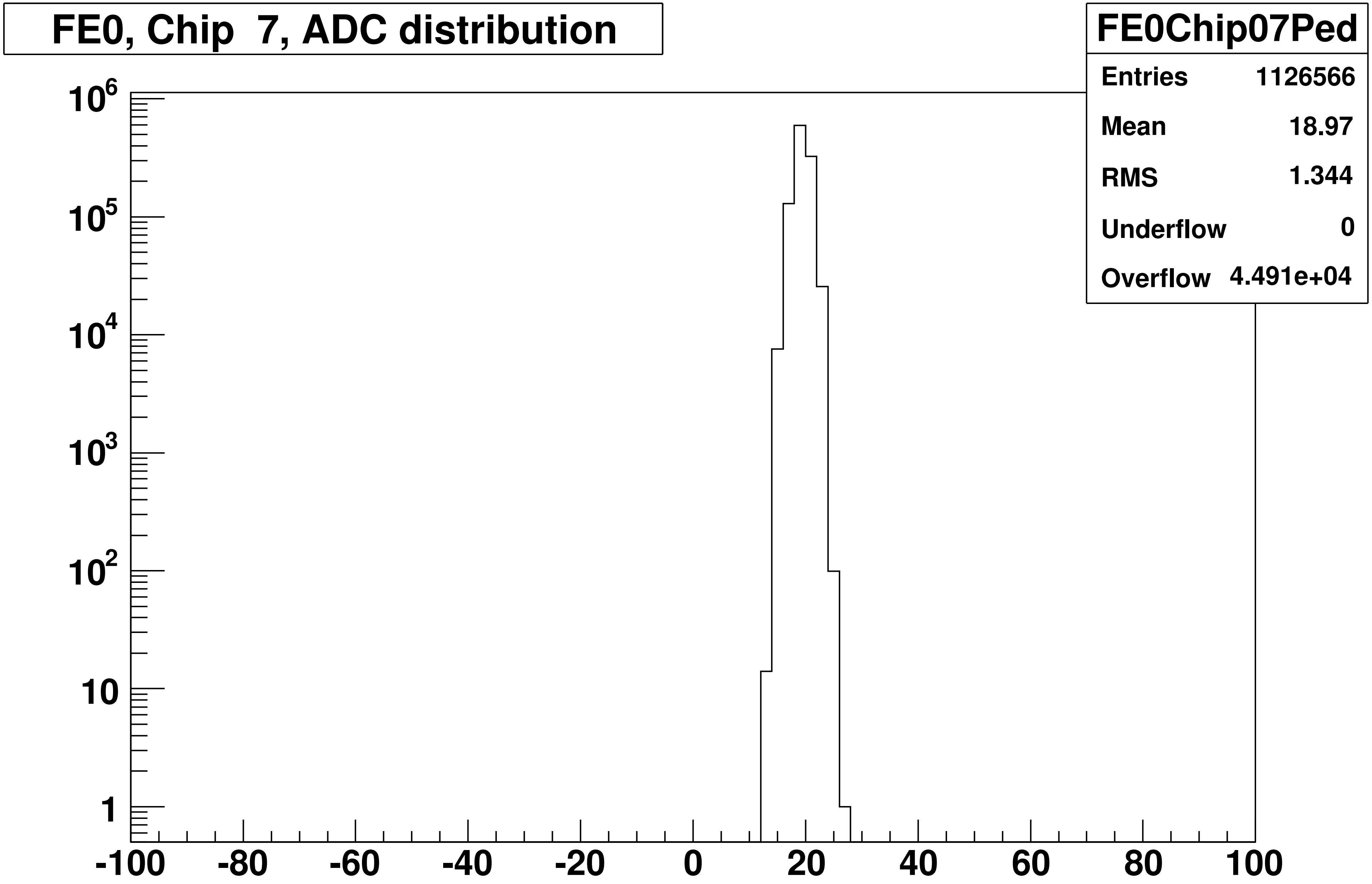
10000
15000
20000
25000



FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 1126566
Mean 18.97
RMS 1.344
Underflow 0
Overflow 4.491e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
0
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80
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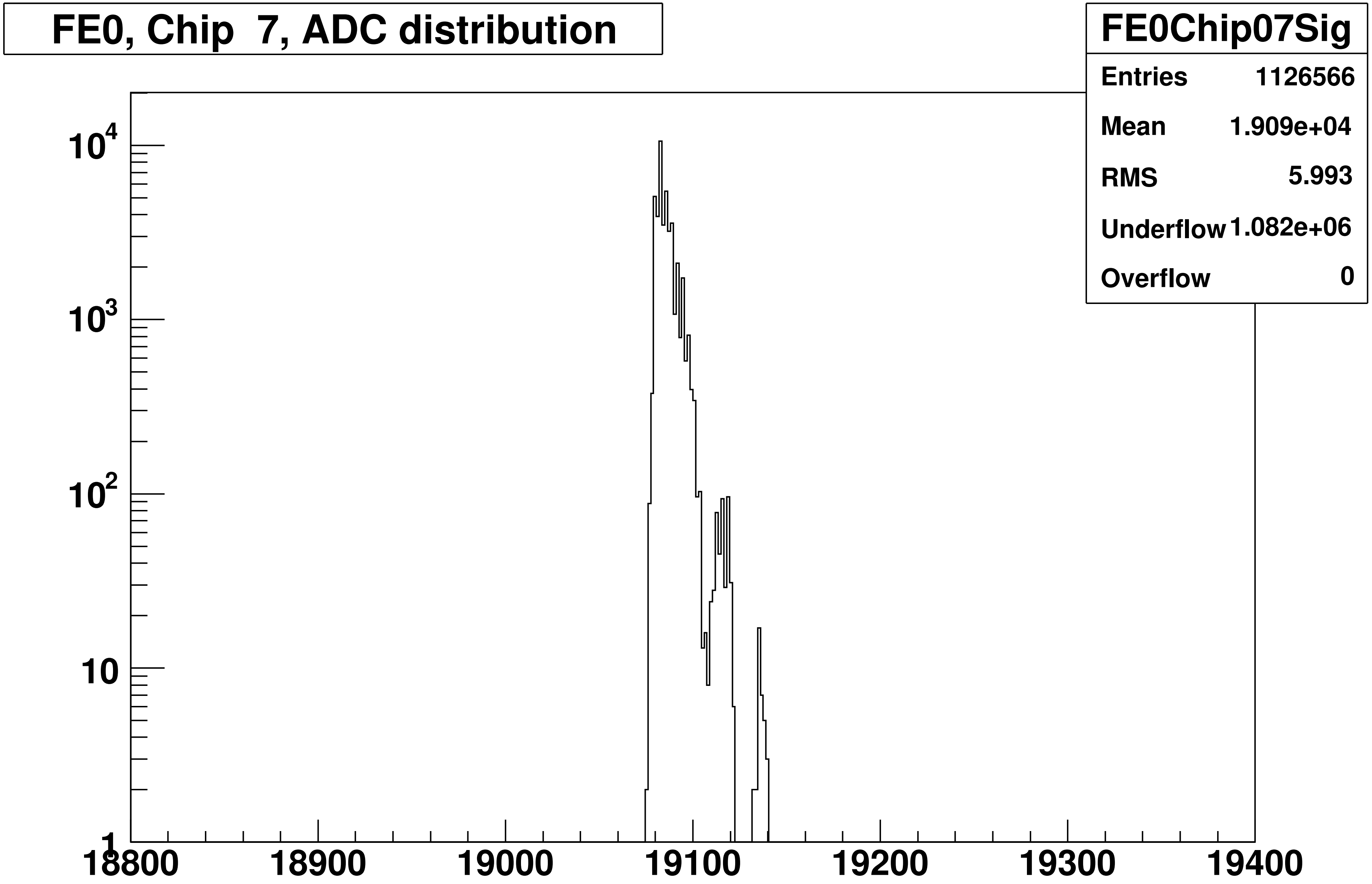

FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 1126566
Mean 1.909e+04
RMS 5.993
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
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18900
19000
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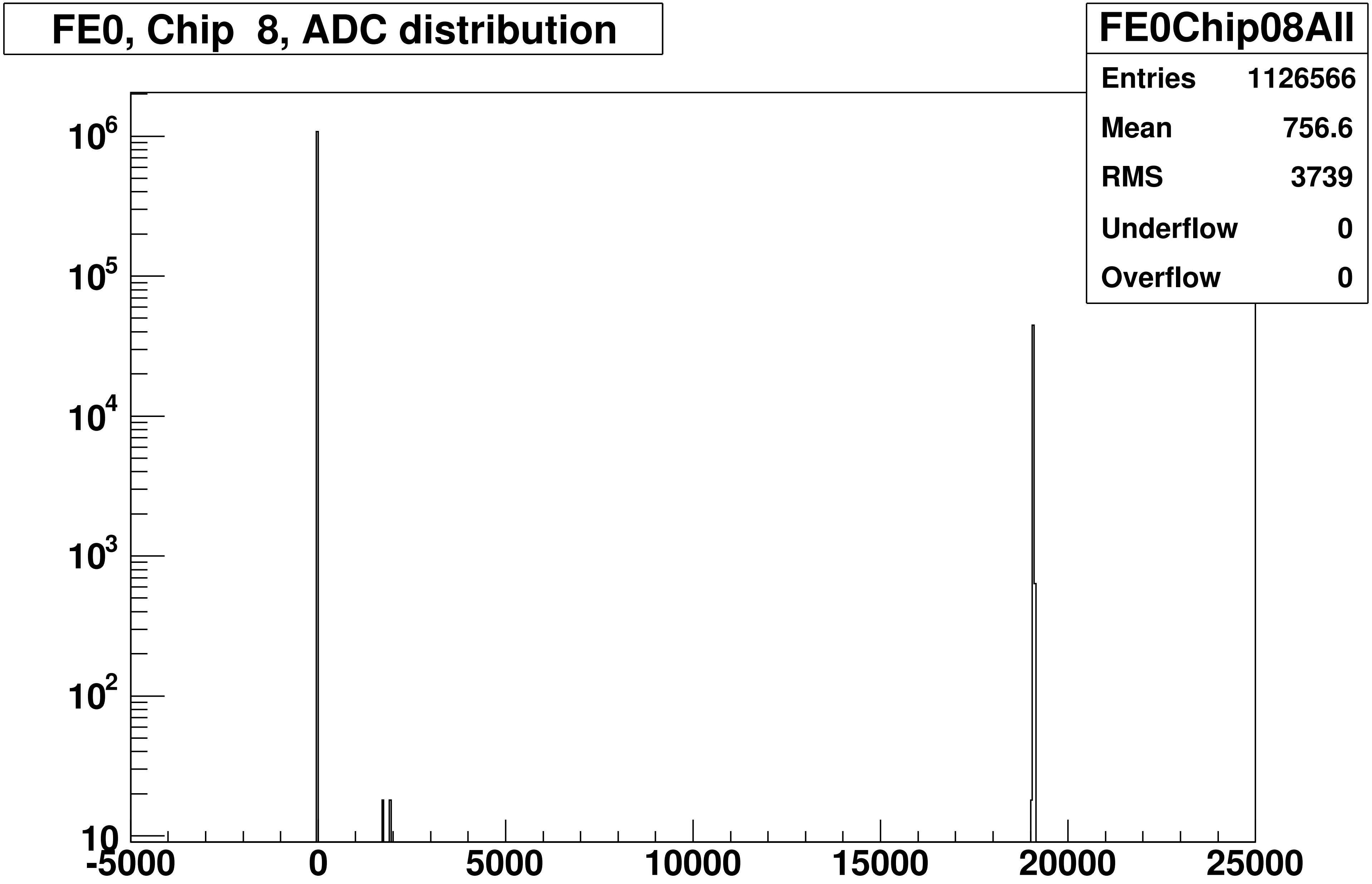

FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 1126566
Mean 756.6
RMS 3739
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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10000
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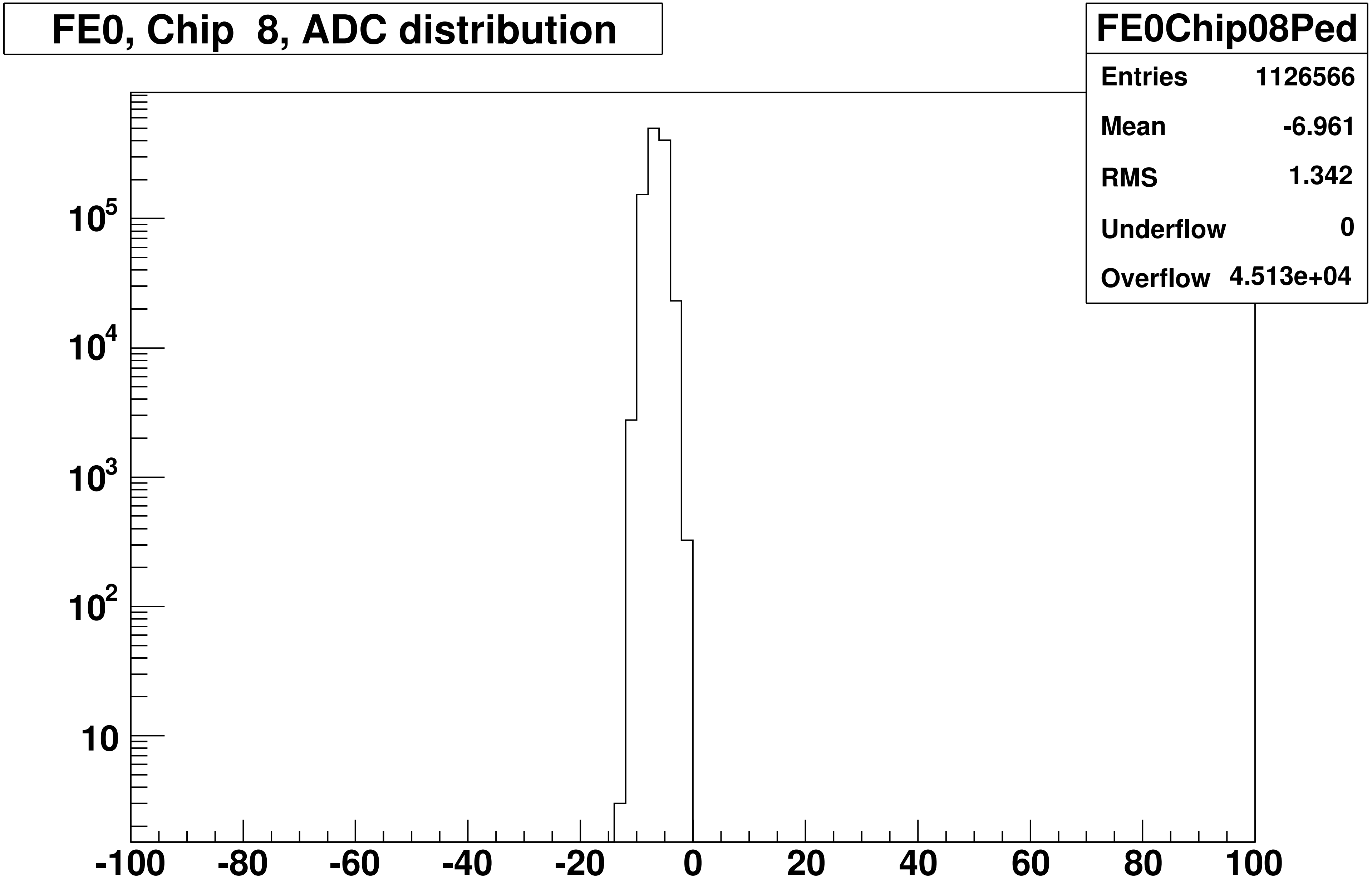
25000



FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 1126566
Mean -6.961
RMS 1.342
Underflow 0
Overflow 4.513e+04
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10^4
10^3
10^2
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-100
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-40
-20
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80
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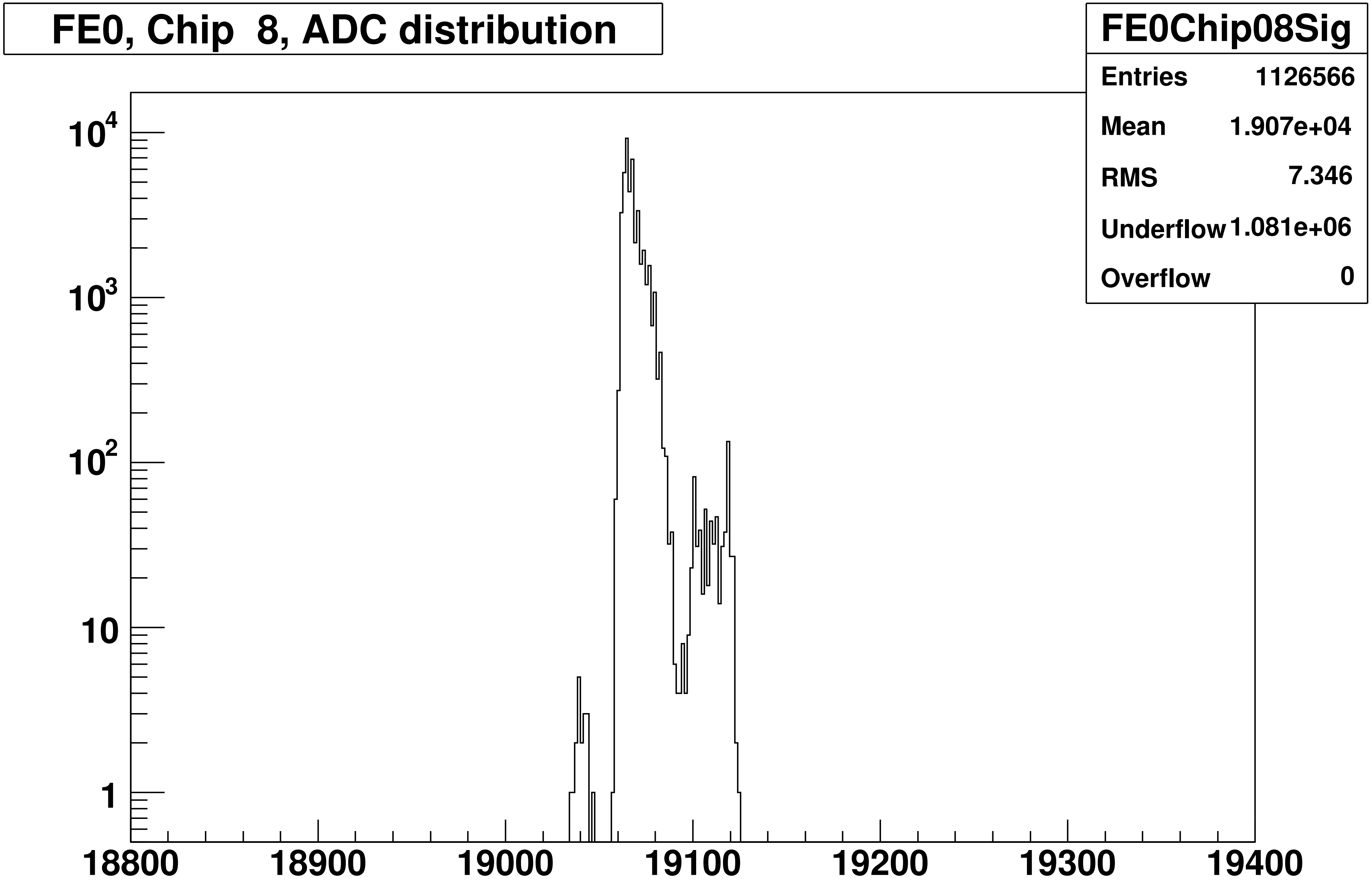

FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 1126566
Mean 1.907e+04
RMS 7.346
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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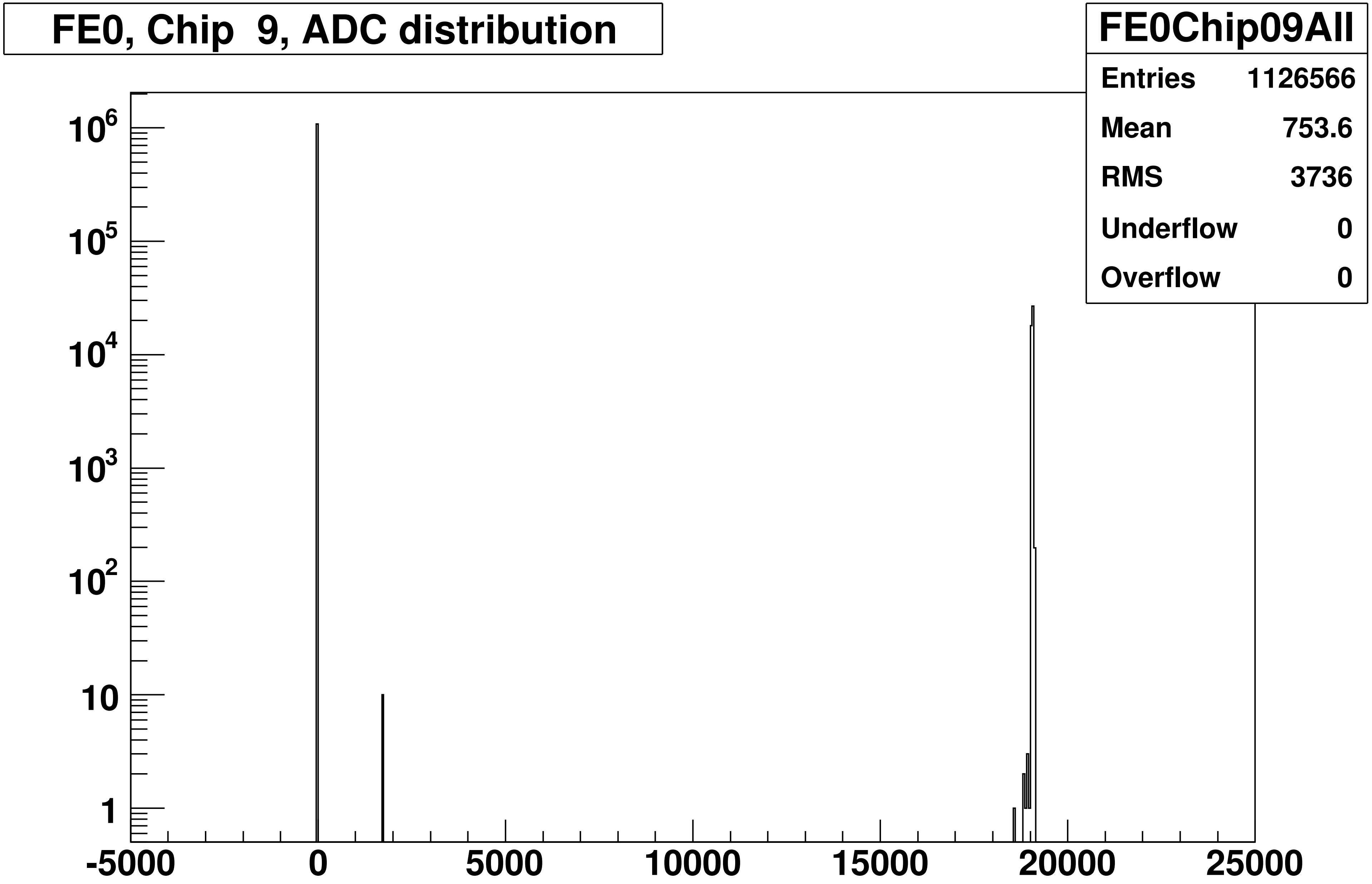
19200
19300
19400



FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 1126566
Mean 753.6
RMS 3736
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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10000
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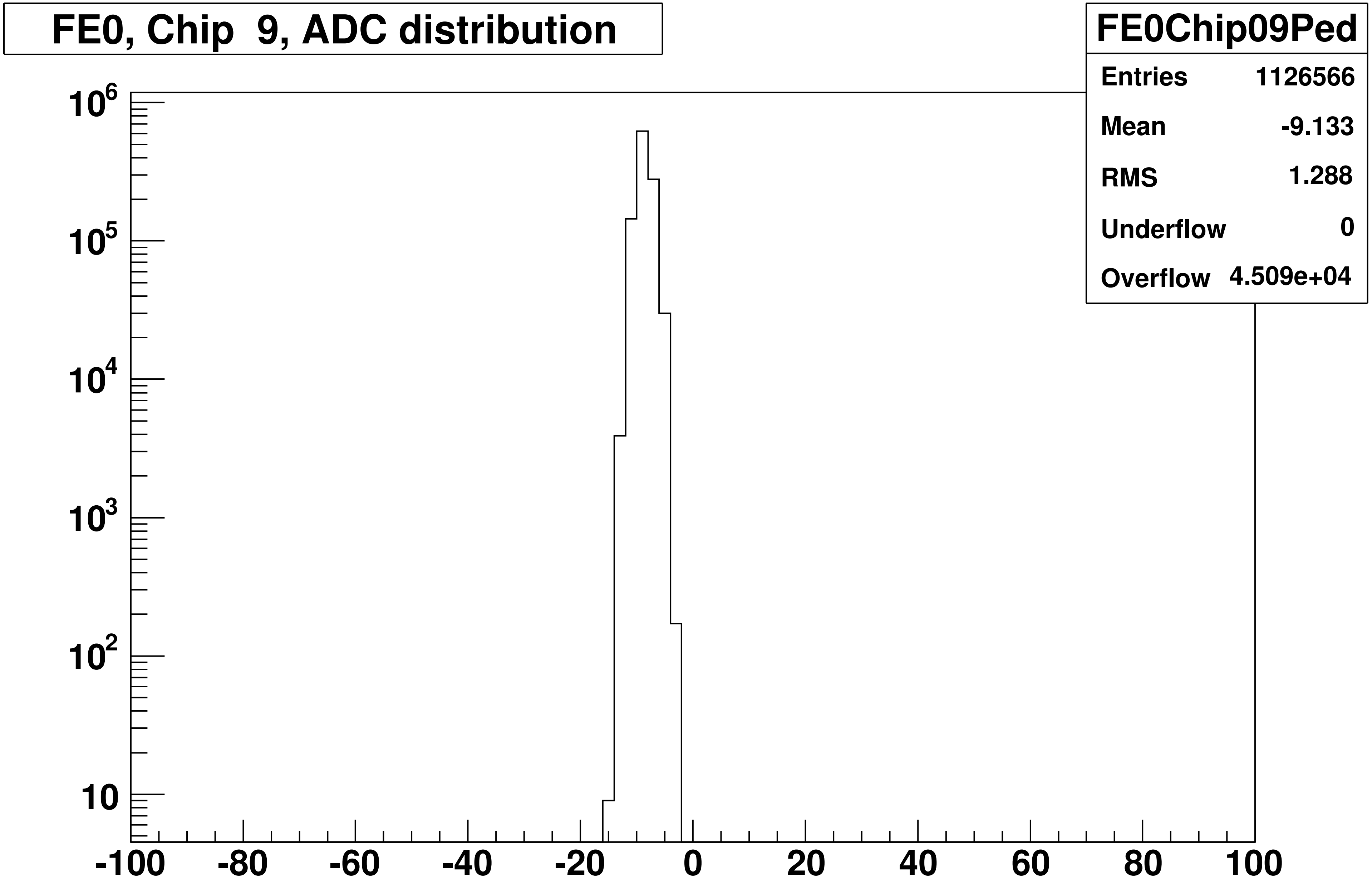
20000
25000



FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 1126566
Mean -9.133
RMS 1.288
Underflow 0
Overflow 4.509e+04
10^6
10^5
10^4
10^3
10^2
10
-100
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-20
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80
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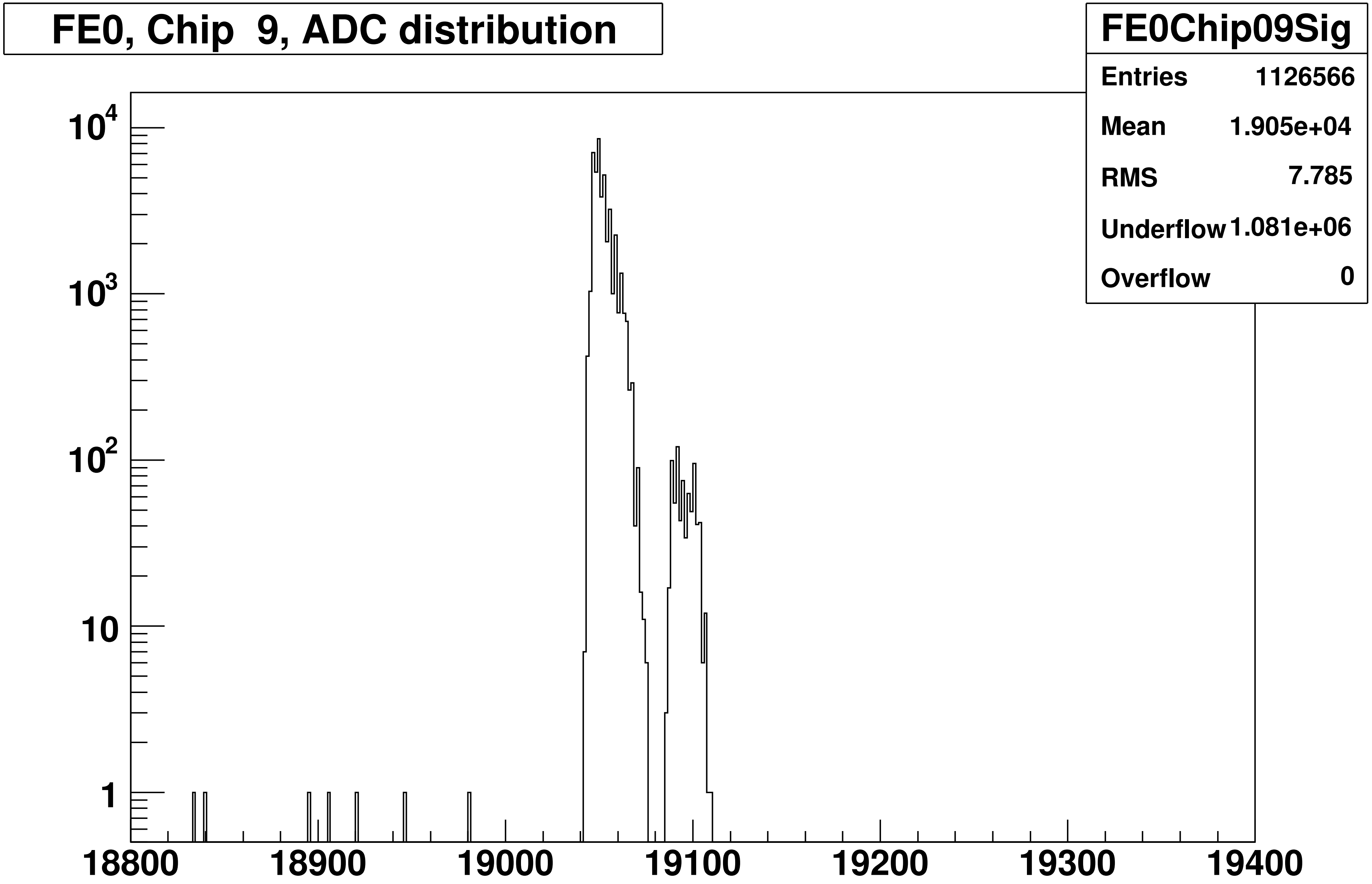

FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 1126566
Mean 1.905e+04
RMS 7.785
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
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18900
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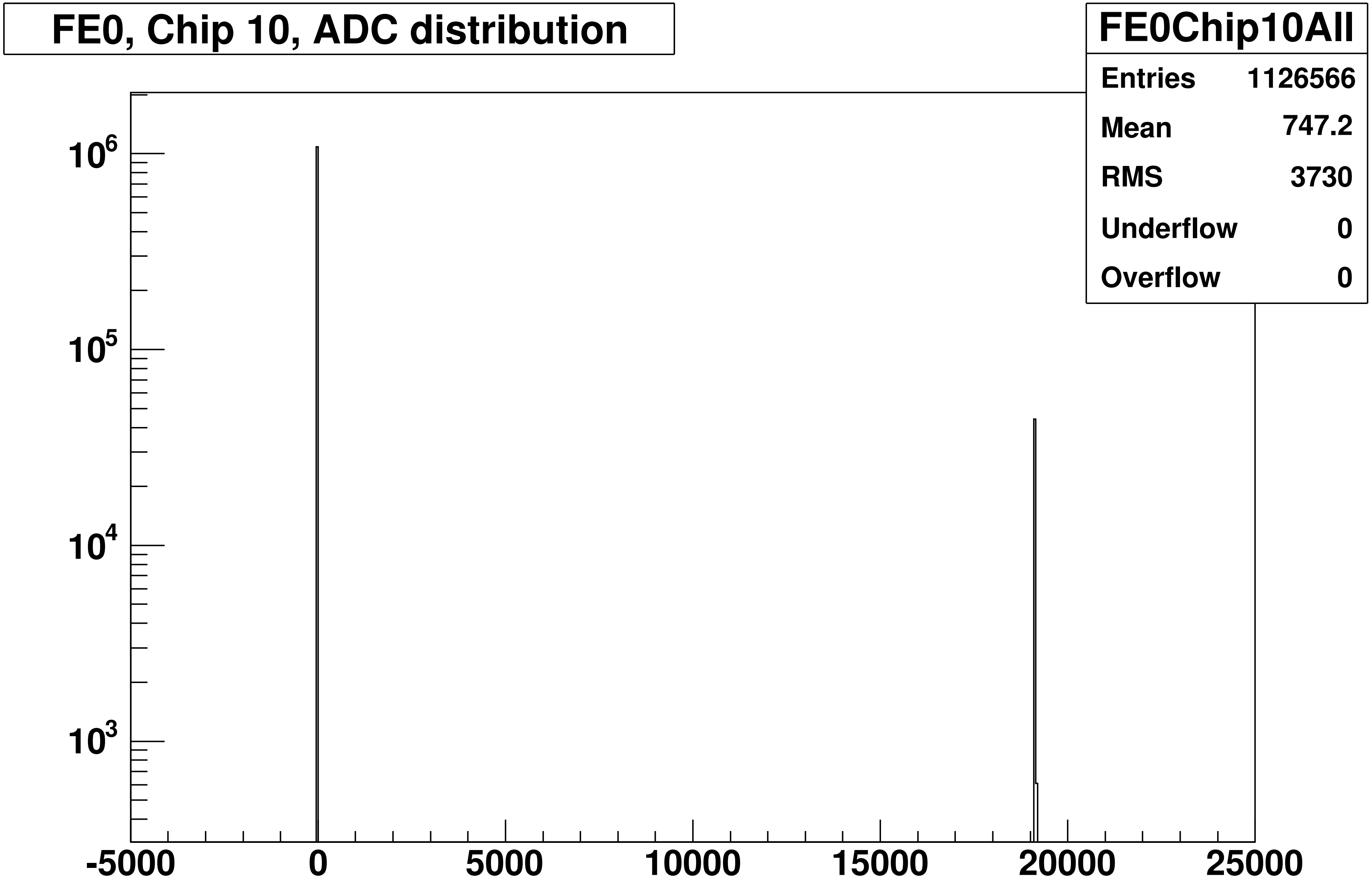

FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 1126566
Mean 747.2
RMS 3730
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
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10000
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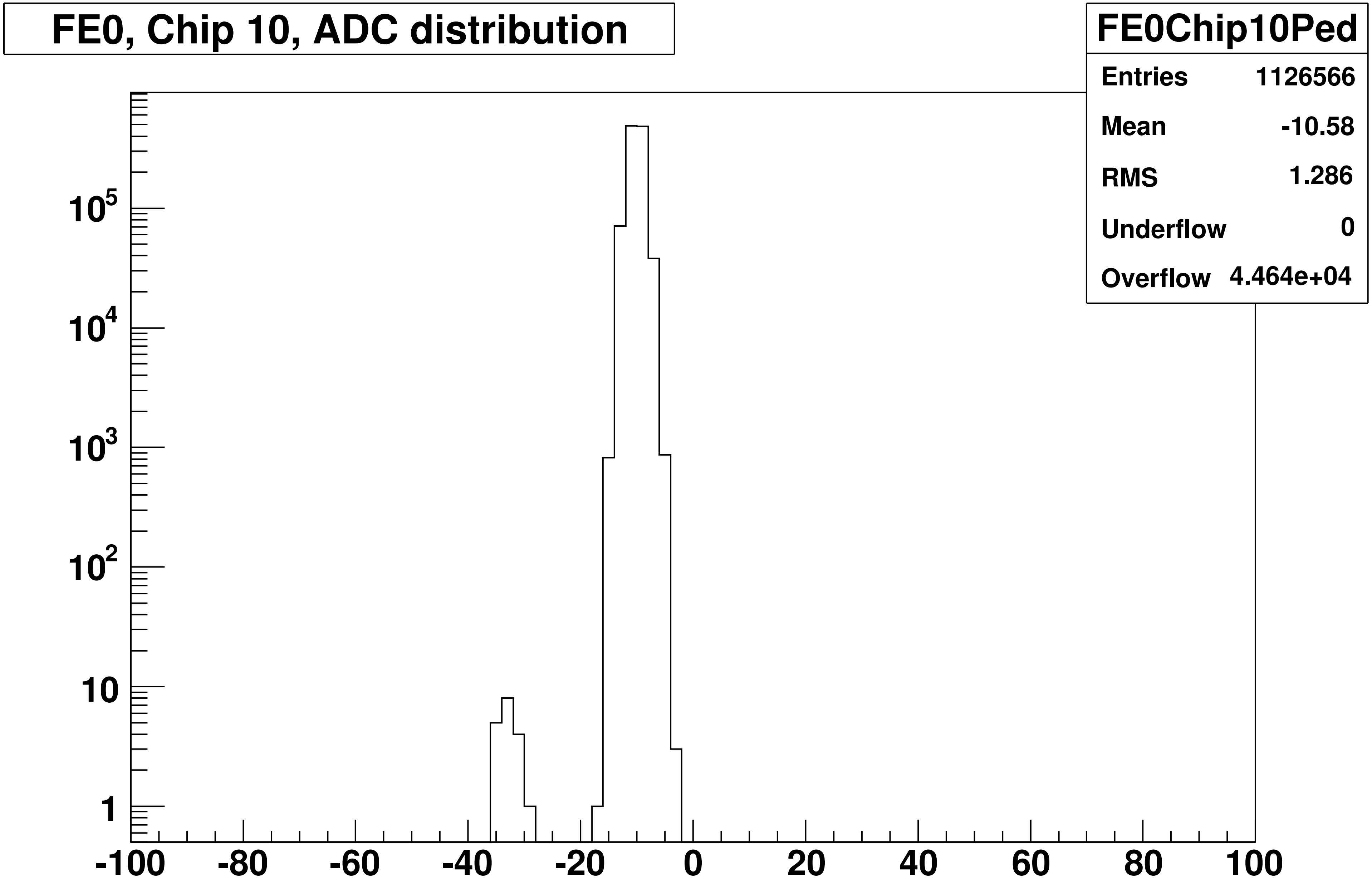
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 1126566
Mean -10.58
RMS 1.286
Underflow 0
Overflow 4.464e+04
10^5
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10^3
10^2
10
1
-100
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-20
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80
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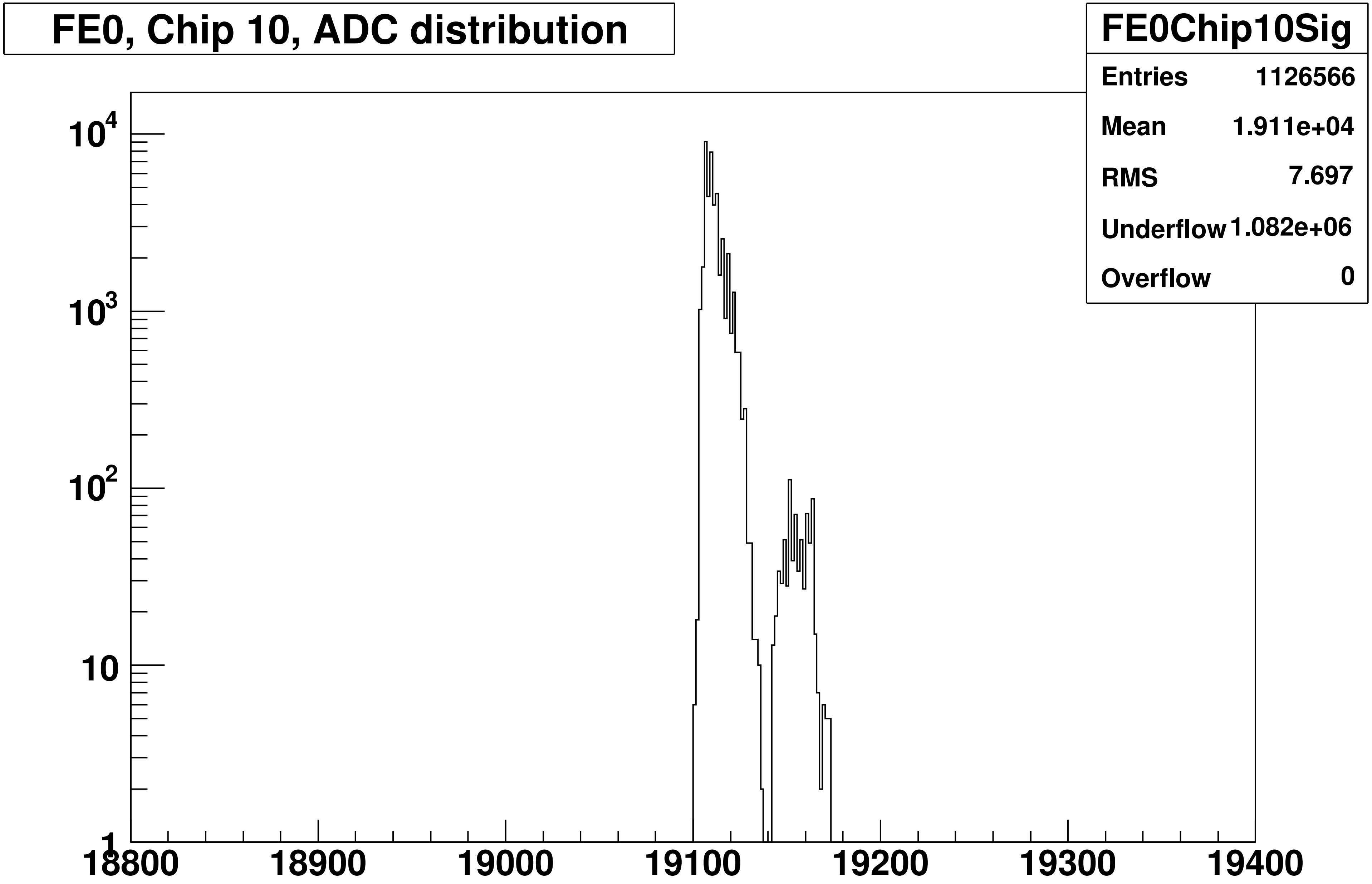

FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 1126566
Mean 1.911e+04
RMS 7.697
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
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18900
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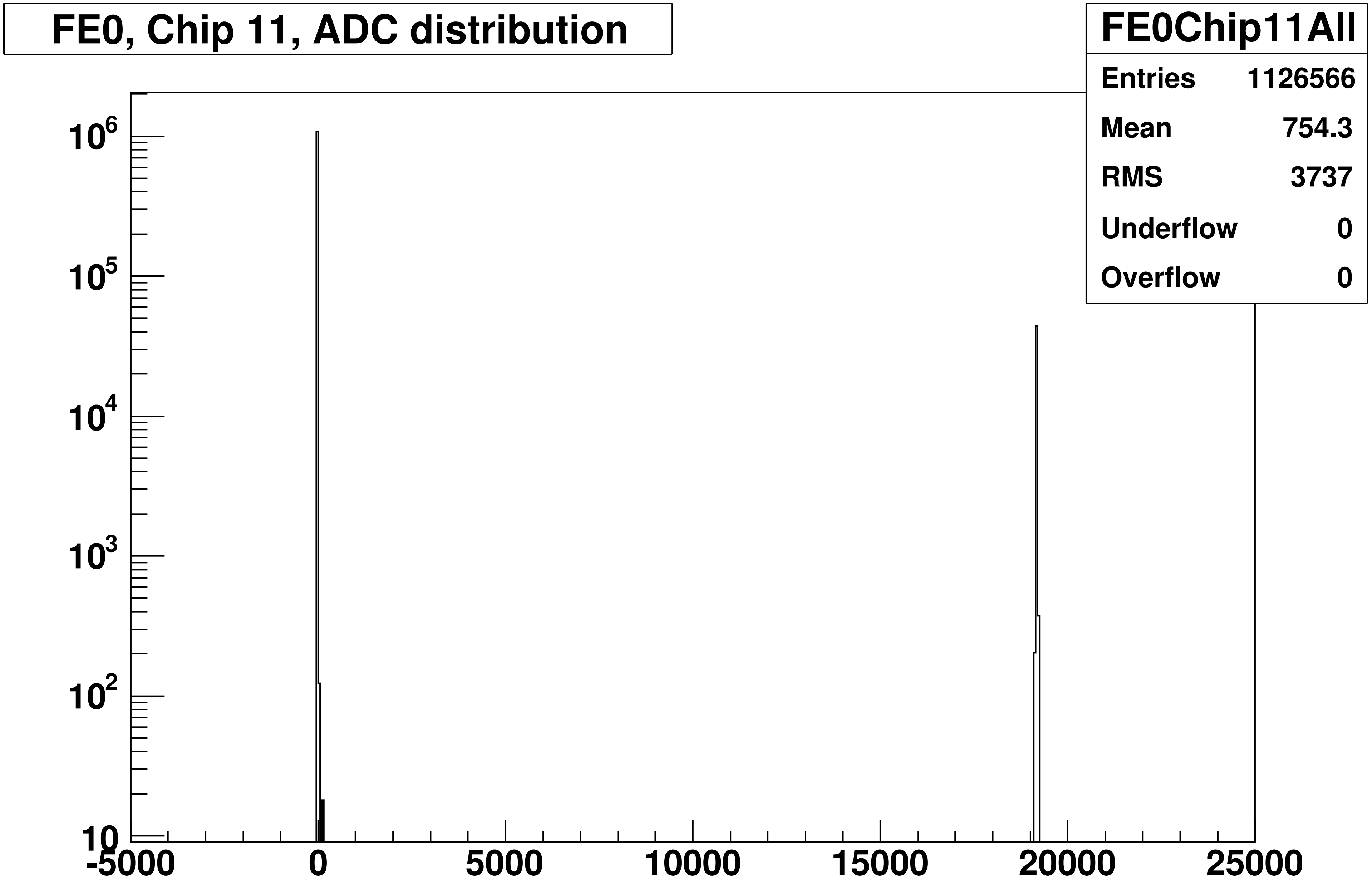
19300
19400



FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 1126566
Mean 754.3
RMS 3737
Underflow 0
Overflow 0
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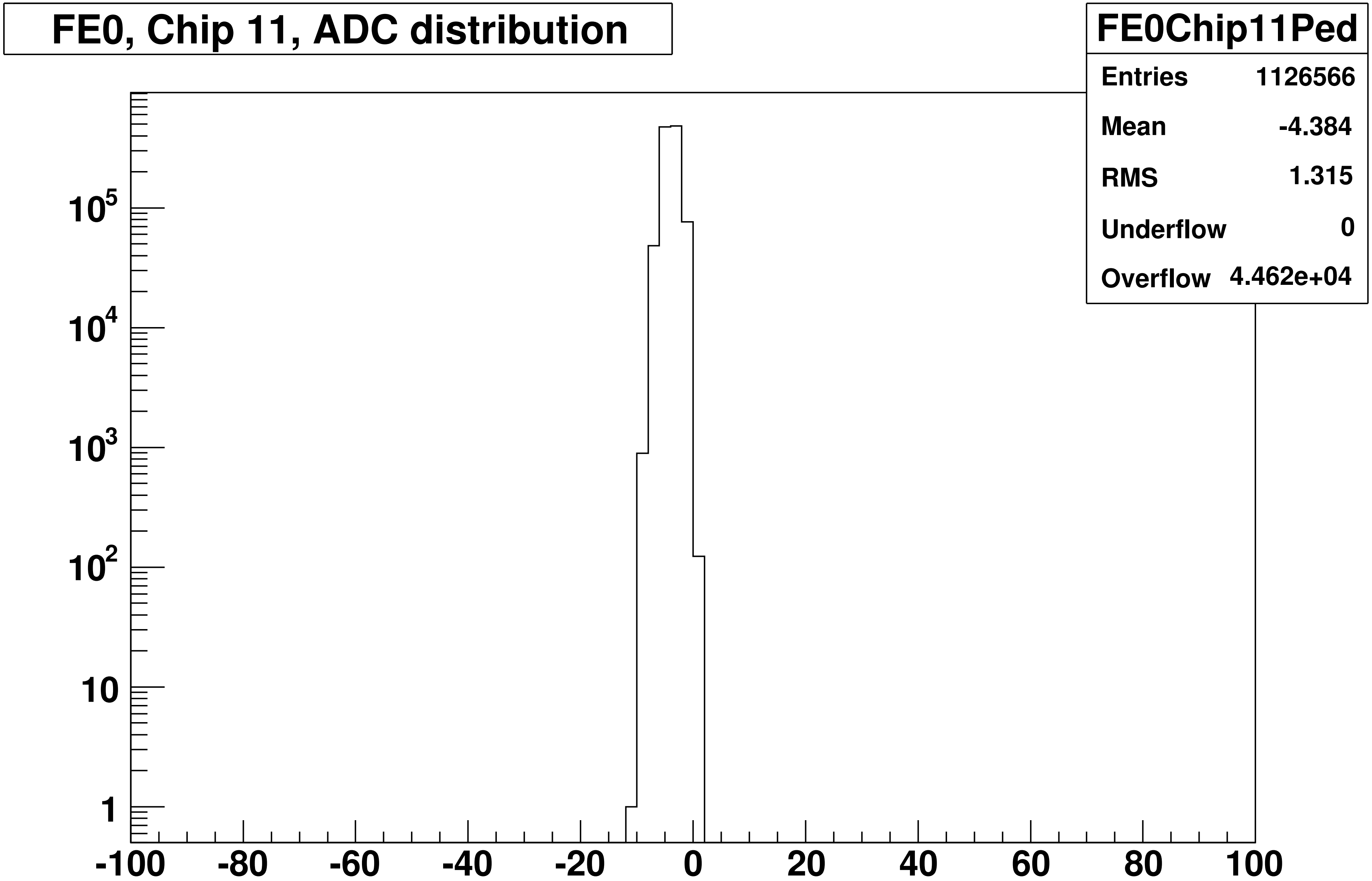
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 1126566
Mean -4.384
RMS 1.315
Underflow 0
Overflow 4.462e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
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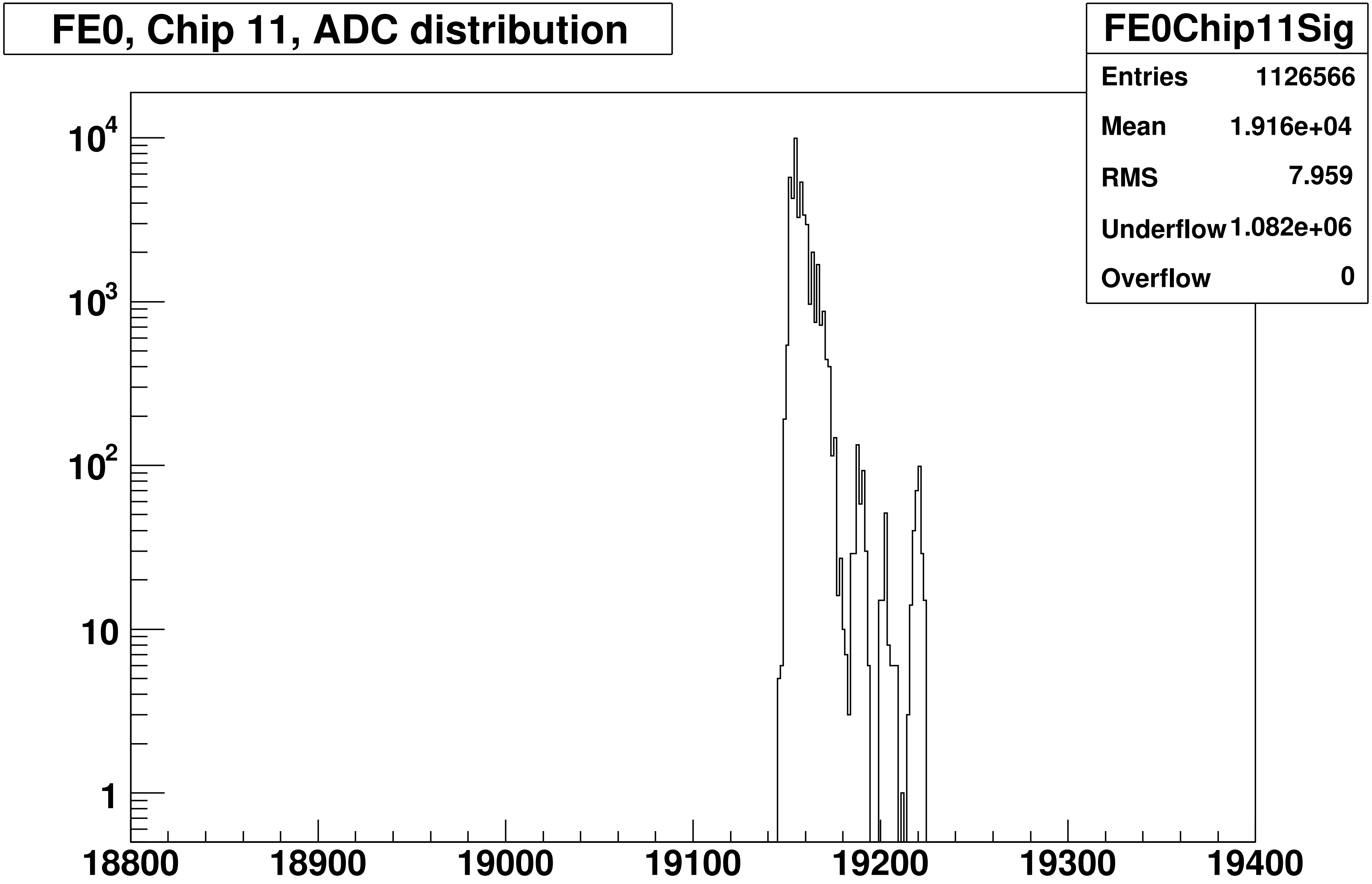
20
40
60
80
100


FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 1126566
Mean 1.916e+04
RMS 7.959
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
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18900
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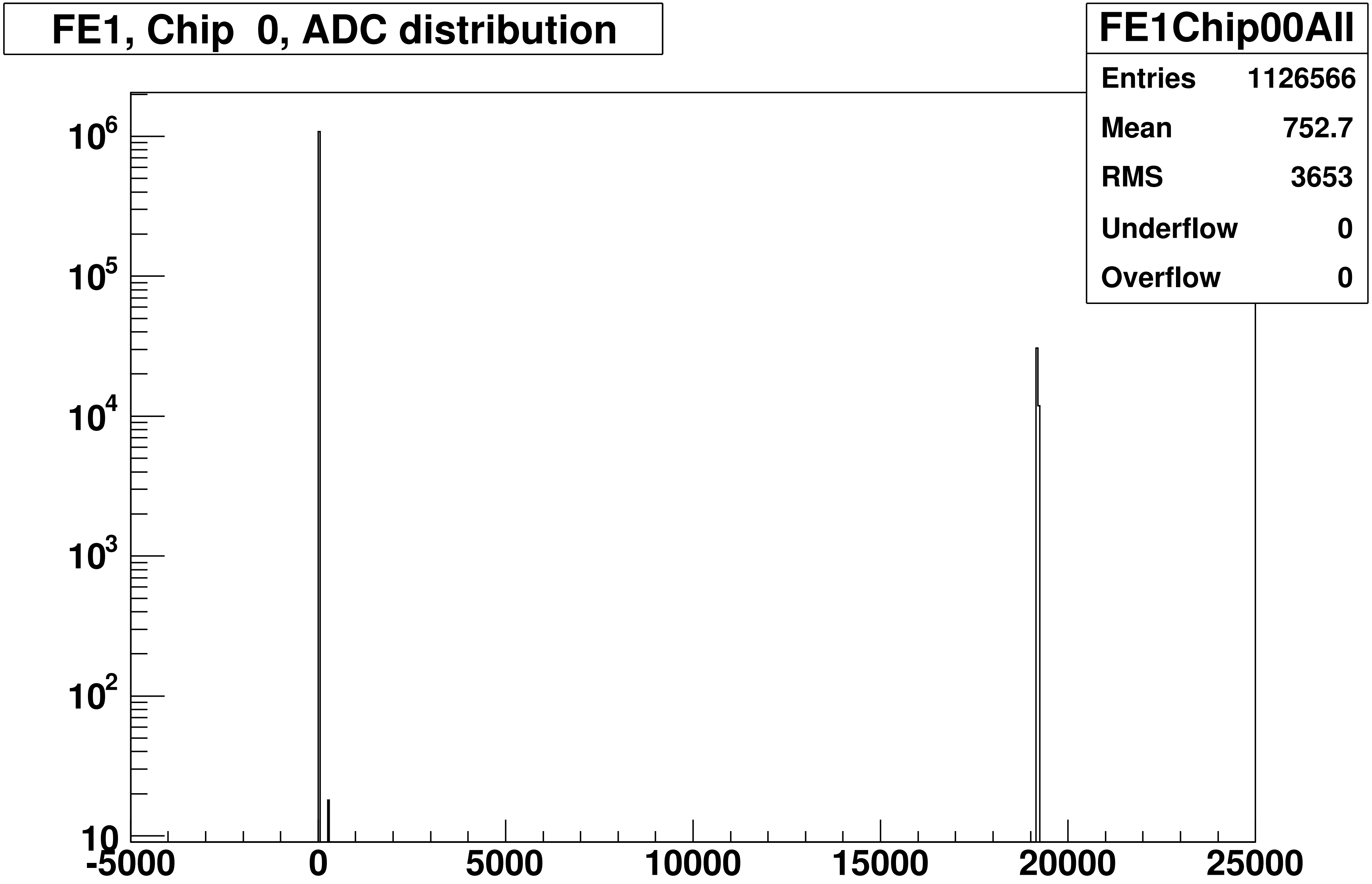
FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 1126566
Mean 752.7
RMS 3653
Underflow 0
Overflow 0
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10000
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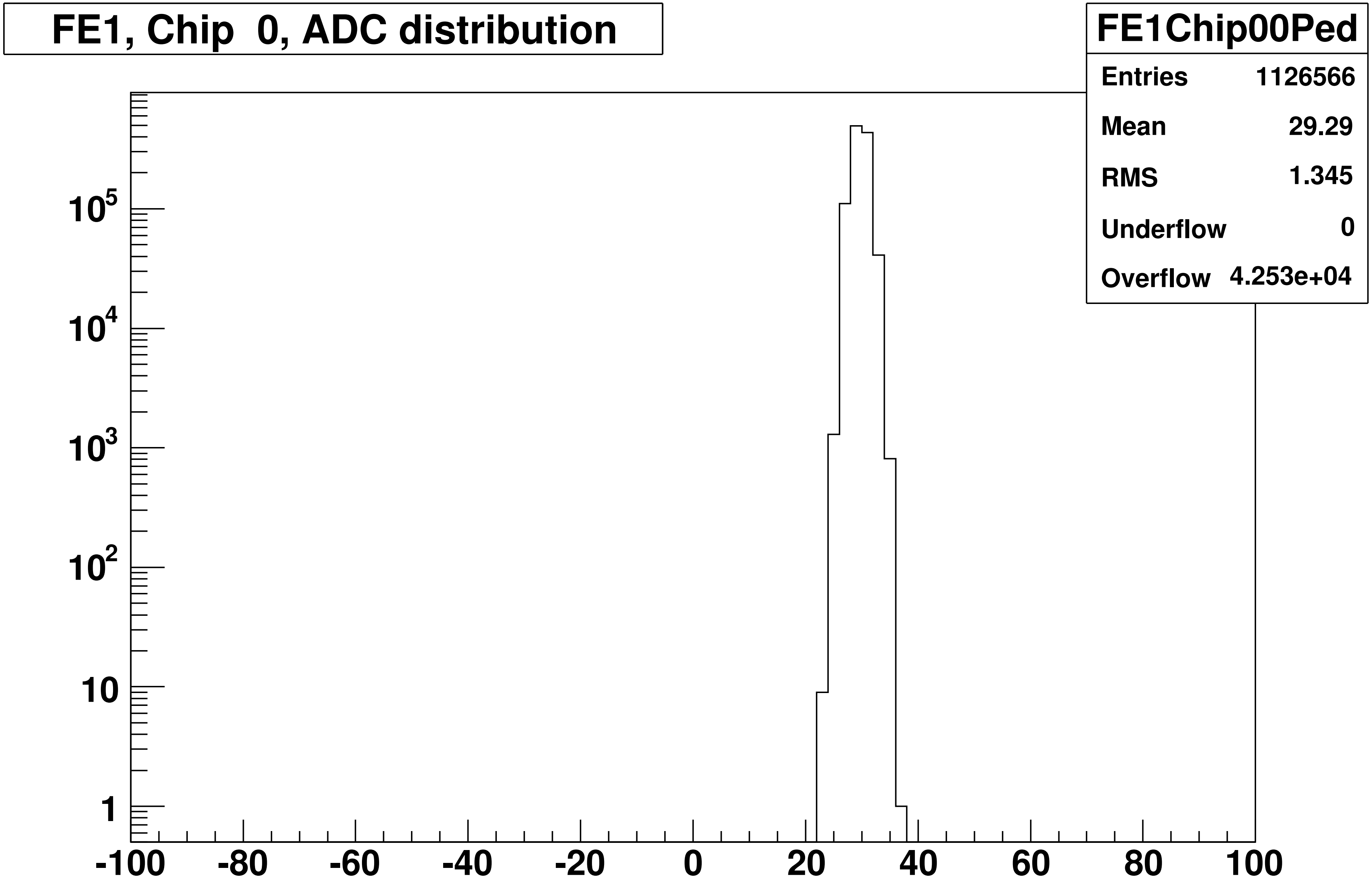
10^4
10^5
10^6


FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 1126566
Mean 29.29
RMS 1.345
Underflow 0
Overflow 4.253e+04
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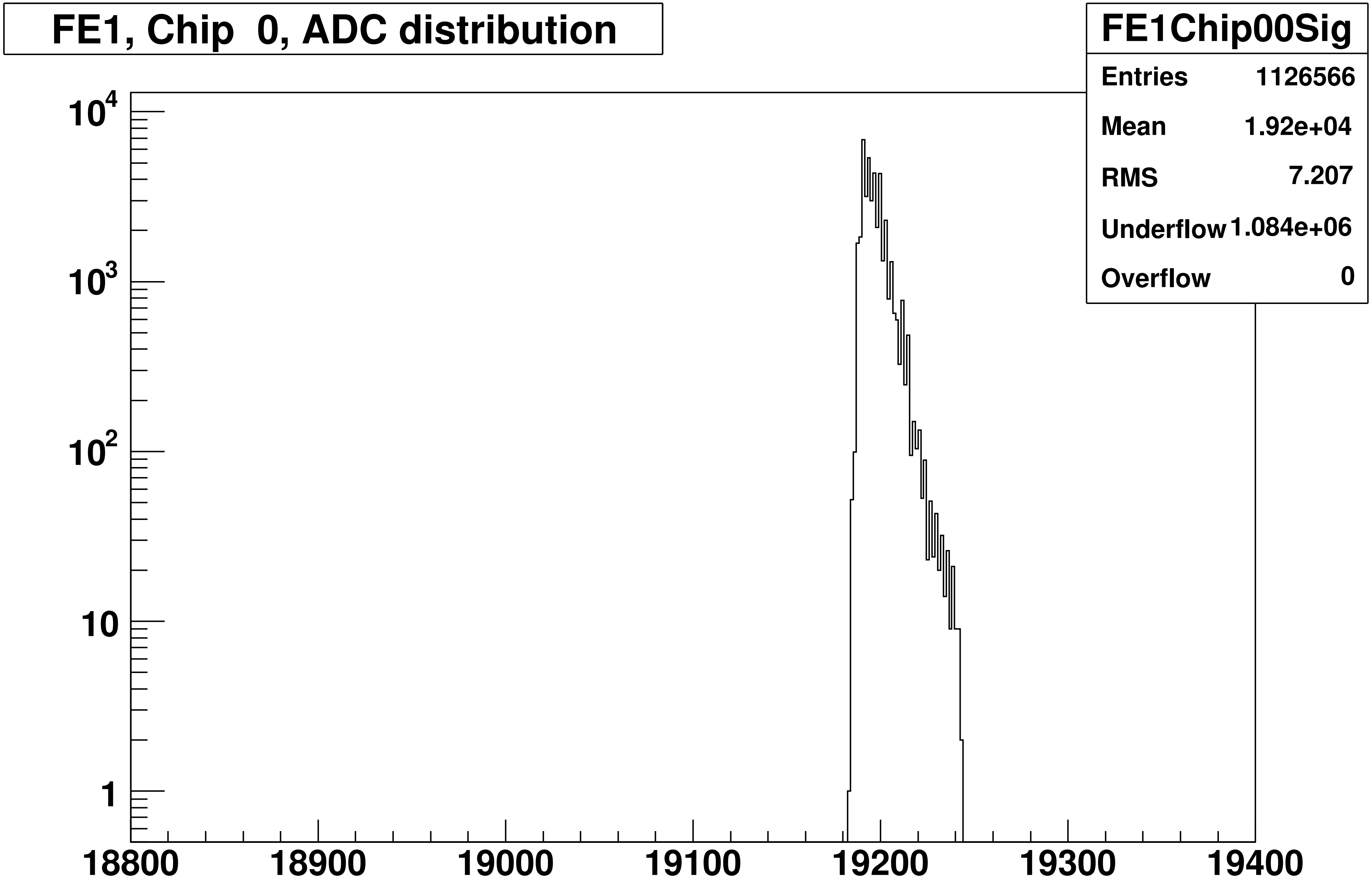
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 1126566
Mean 1.92e+04
RMS 7.207
Underflow 1.084e+06
Overflow 0
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10^3
10^2
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18900
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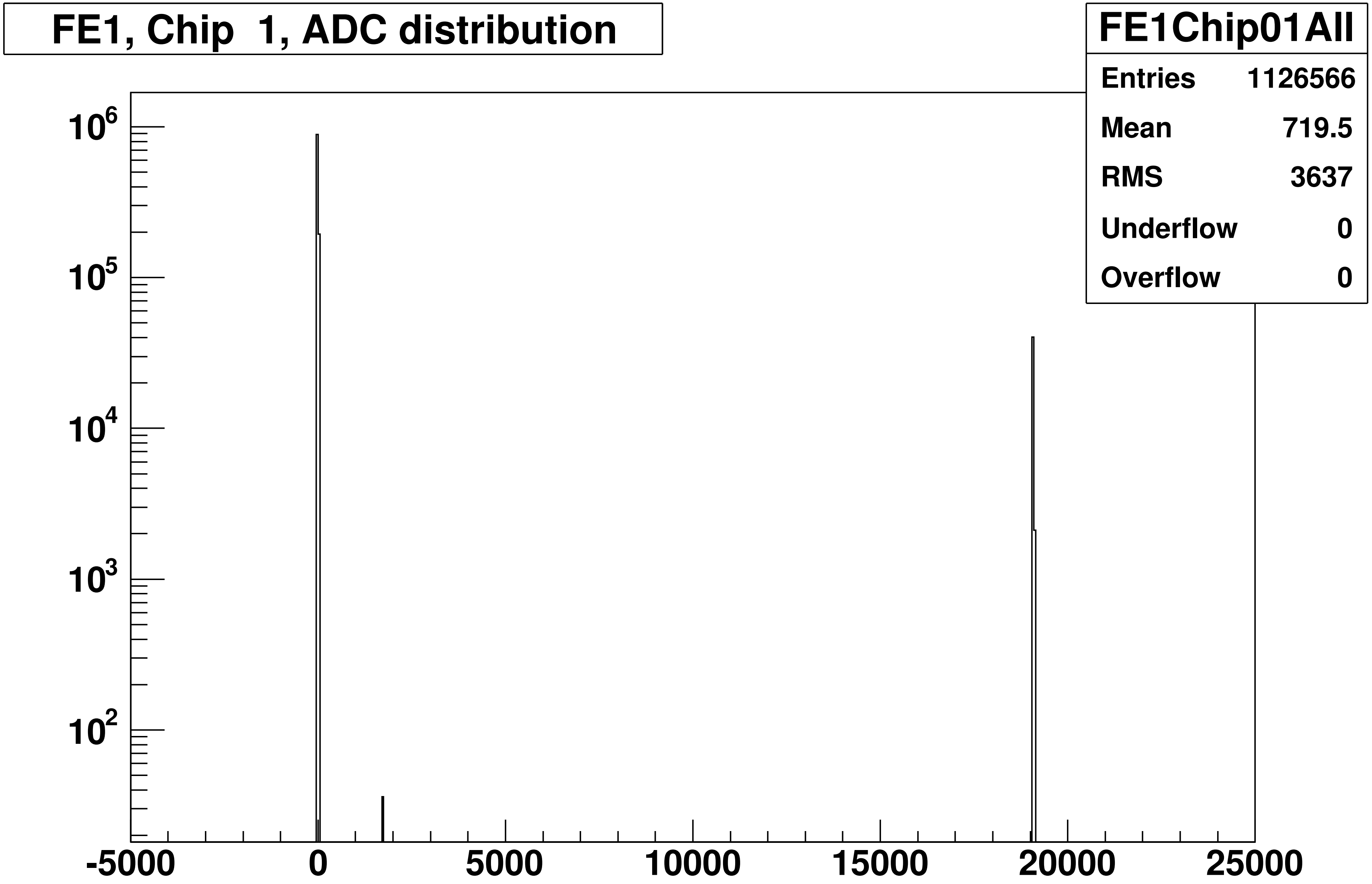
19400


FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 1126566
Mean 719.5
RMS 3637
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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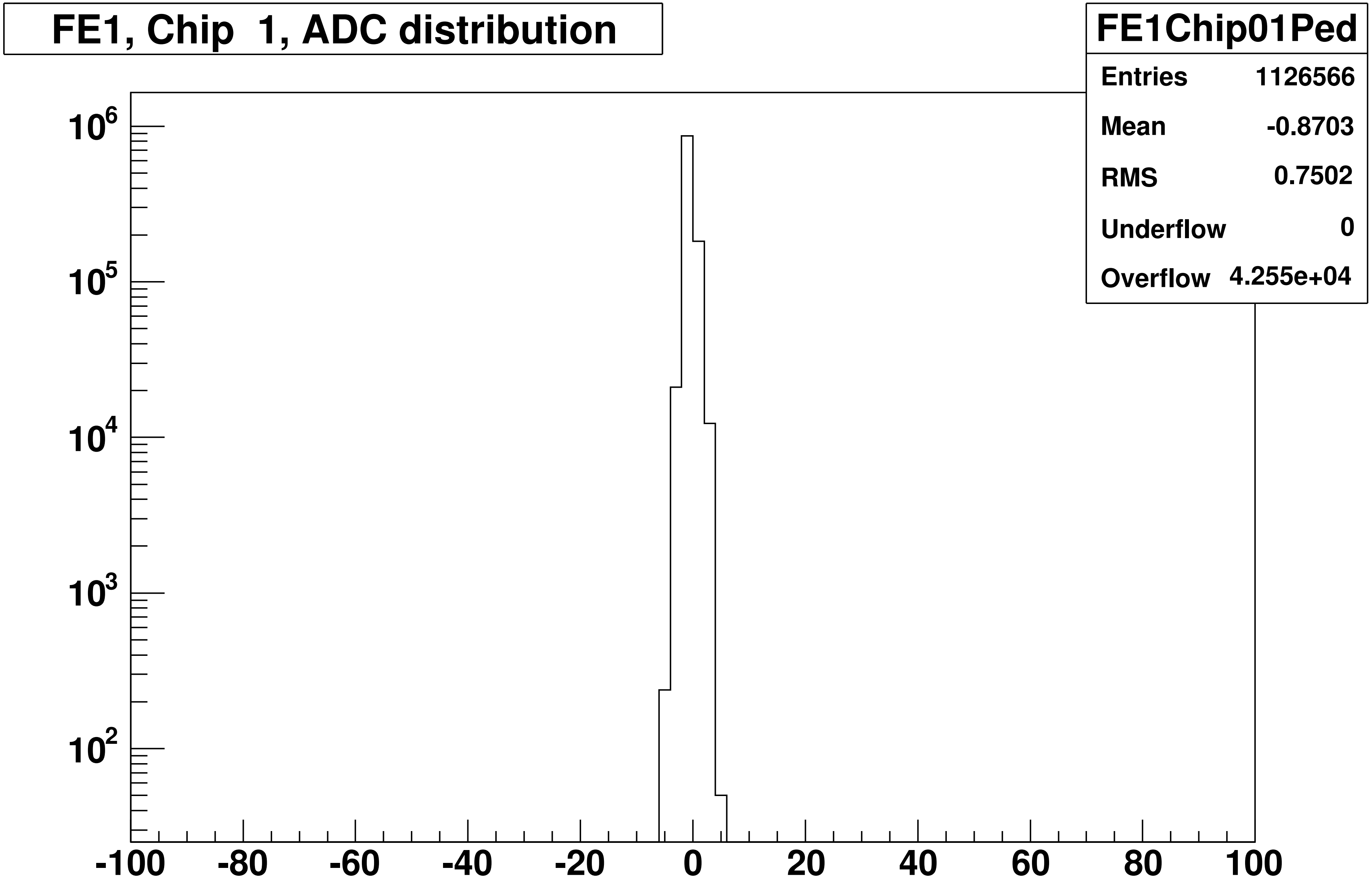
FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 1126566
Mean -0.8703
RMS 0.7502
Underflow 0
Overflow 4.255e+04
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10^5
10^4
10^3
10^2
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-40
-20
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80
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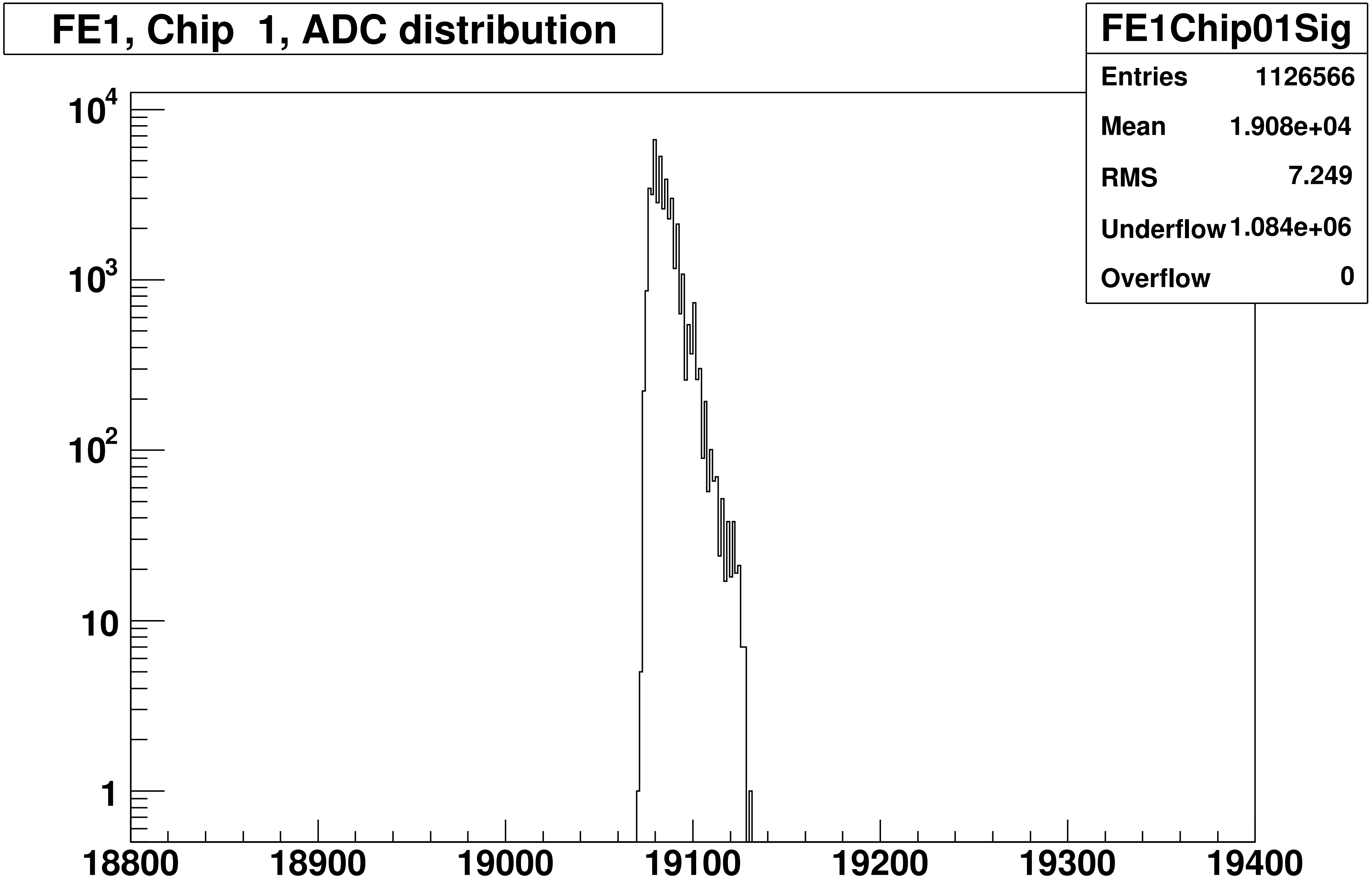
FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 1126566
Mean 1.908e+04
RMS 7.249
Underflow 1.084e+06
Overflow 0
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10^3
10^2
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18900
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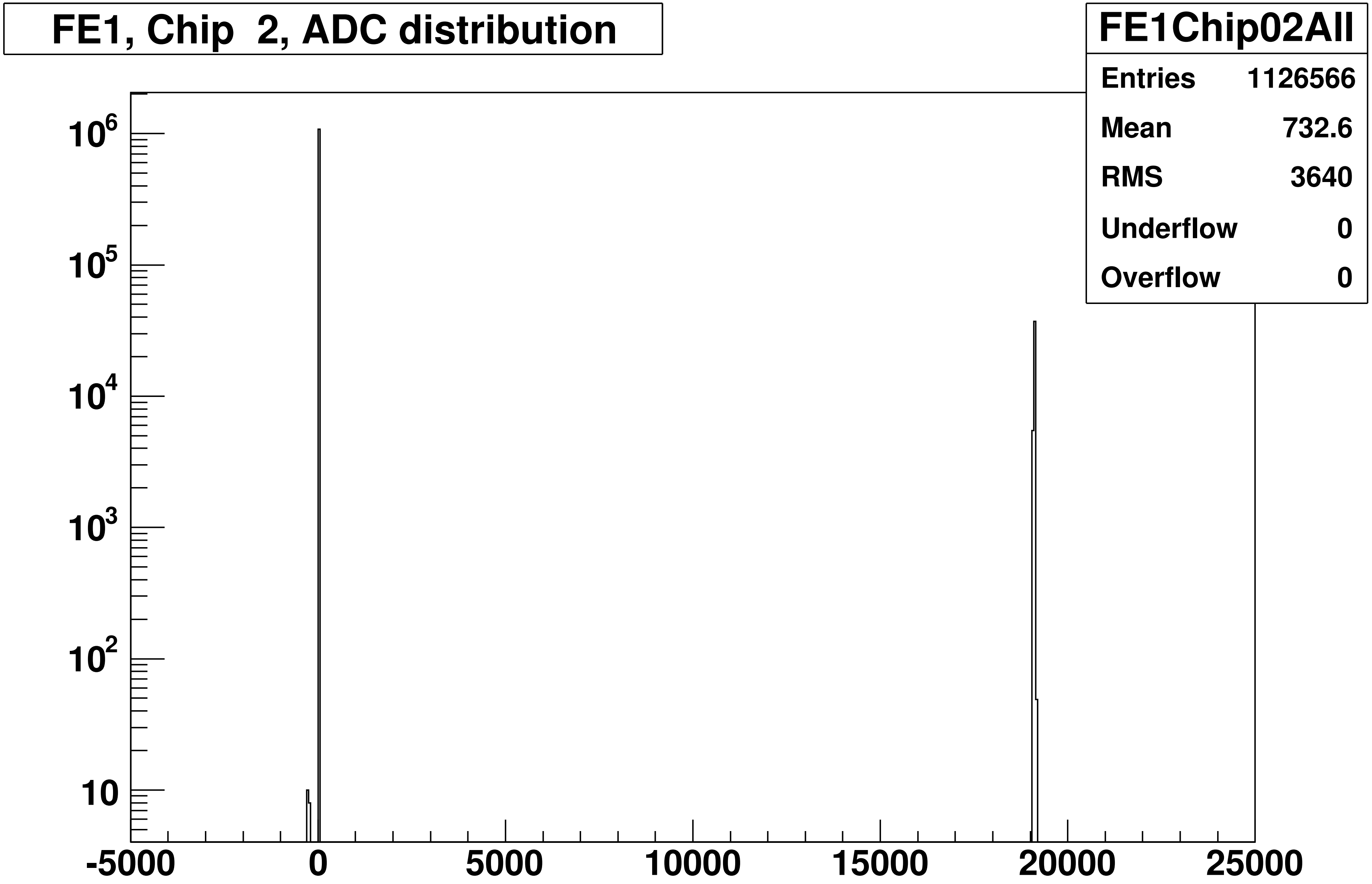
FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 1126566
Mean 732.6
RMS 3640
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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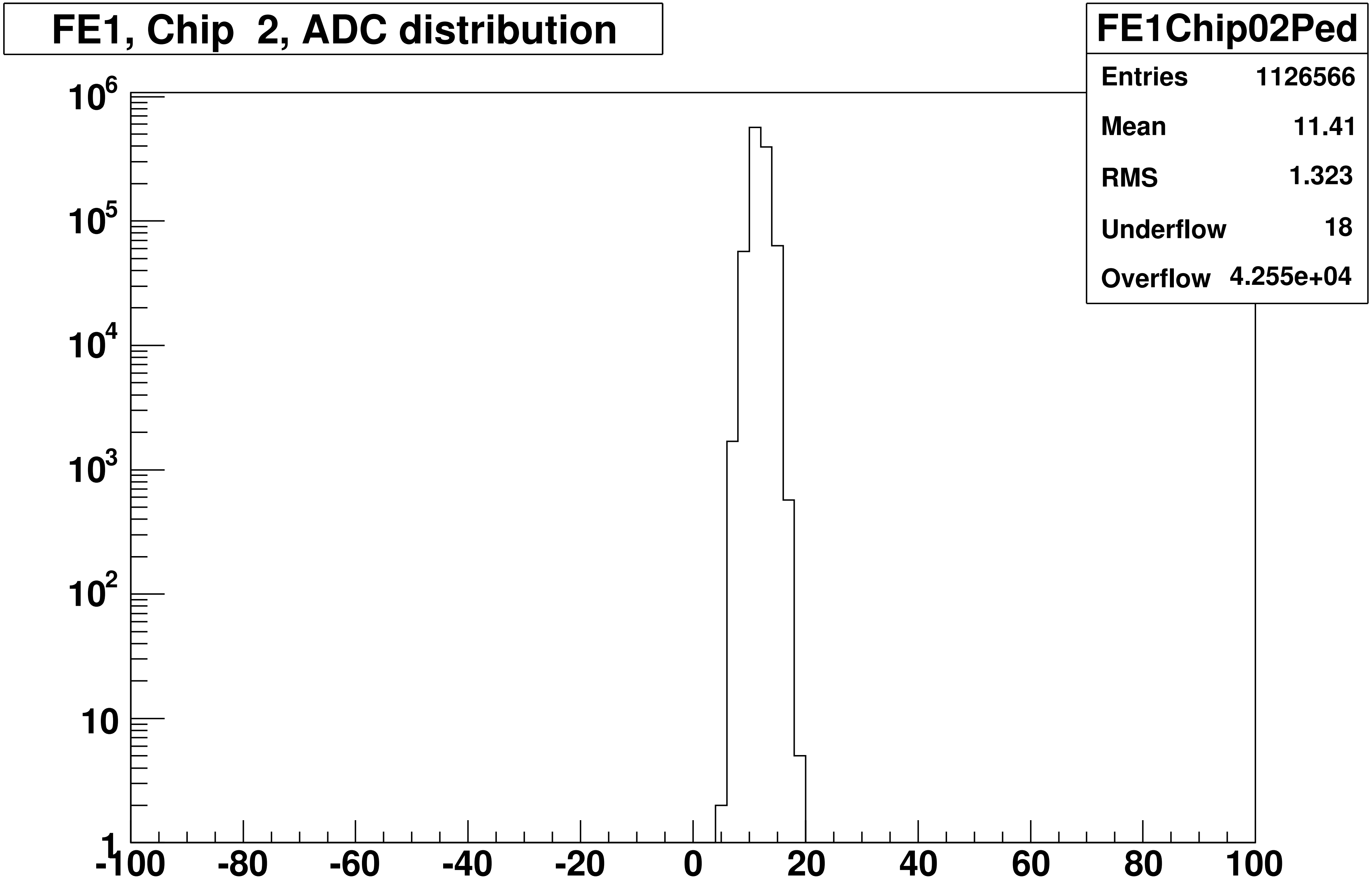

FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 1126566
Mean 11.41
RMS 1.323
Underflow 18
Overflow 4.255e+04
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10^5
10^4
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10^2
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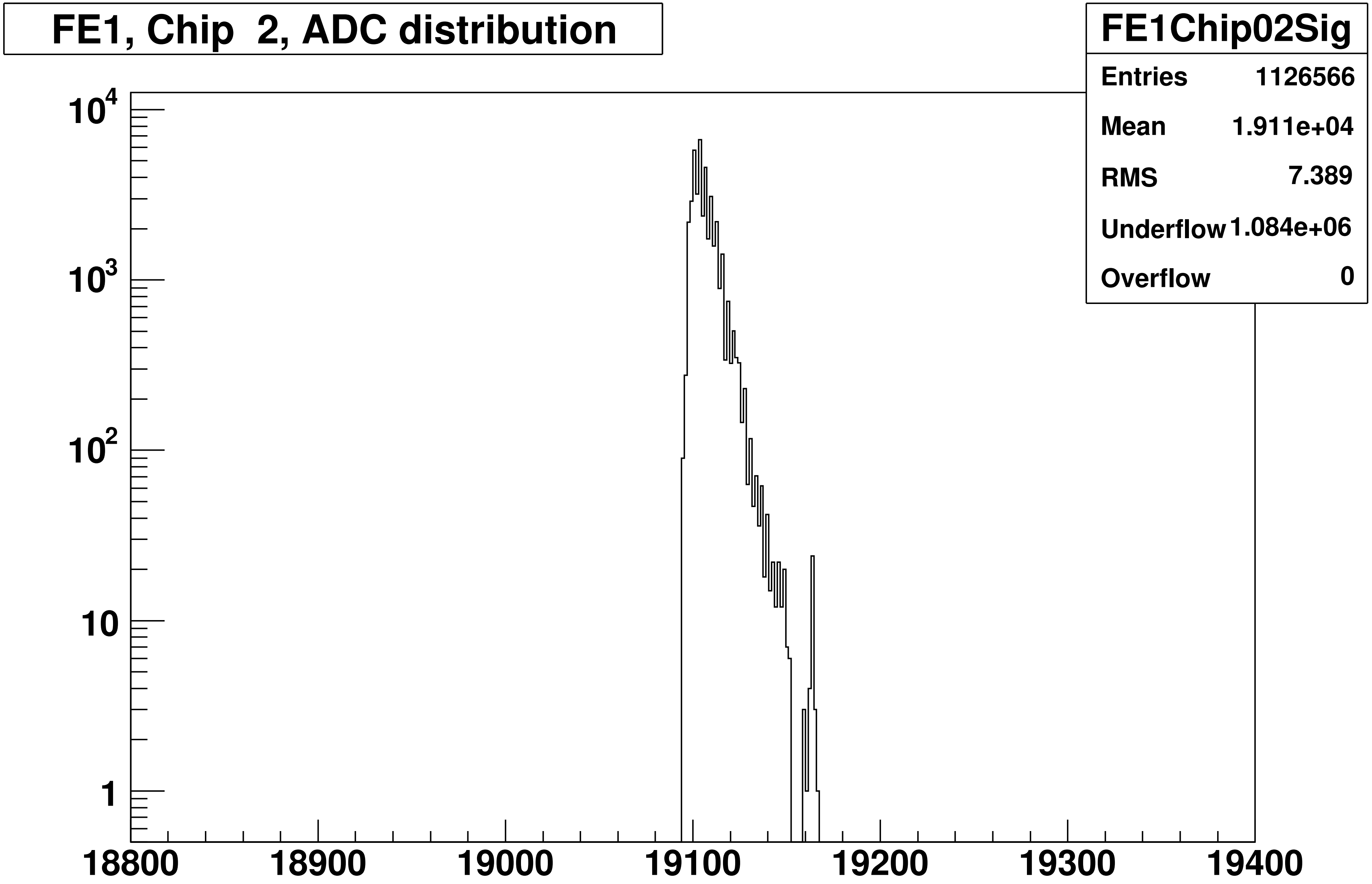

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 1126566
Mean 1.911e+04
RMS 7.389
Underflow 1.084e+06
Overflow 0
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10^2
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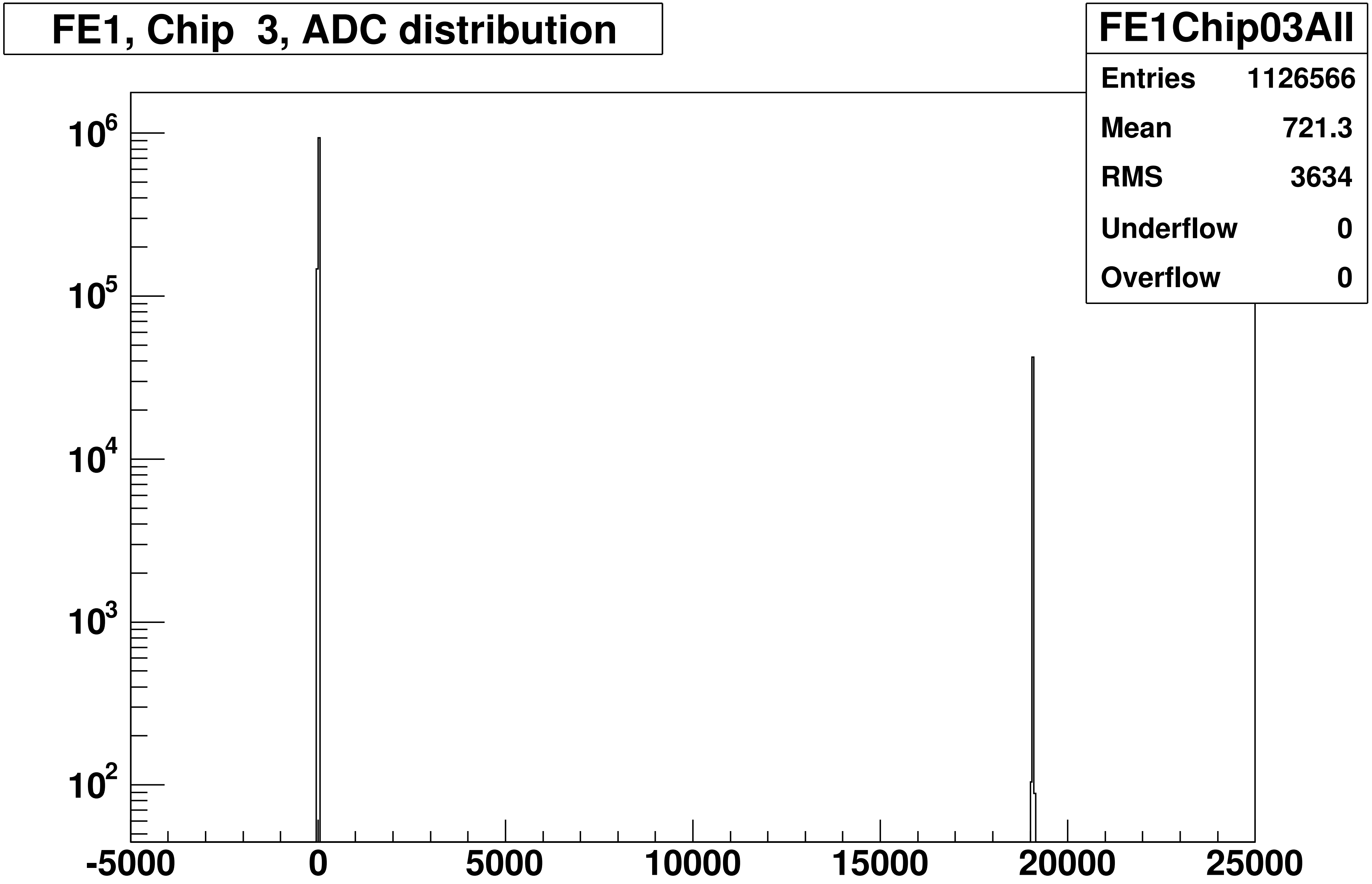

FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 1126566
Mean 721.3
RMS 3634
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
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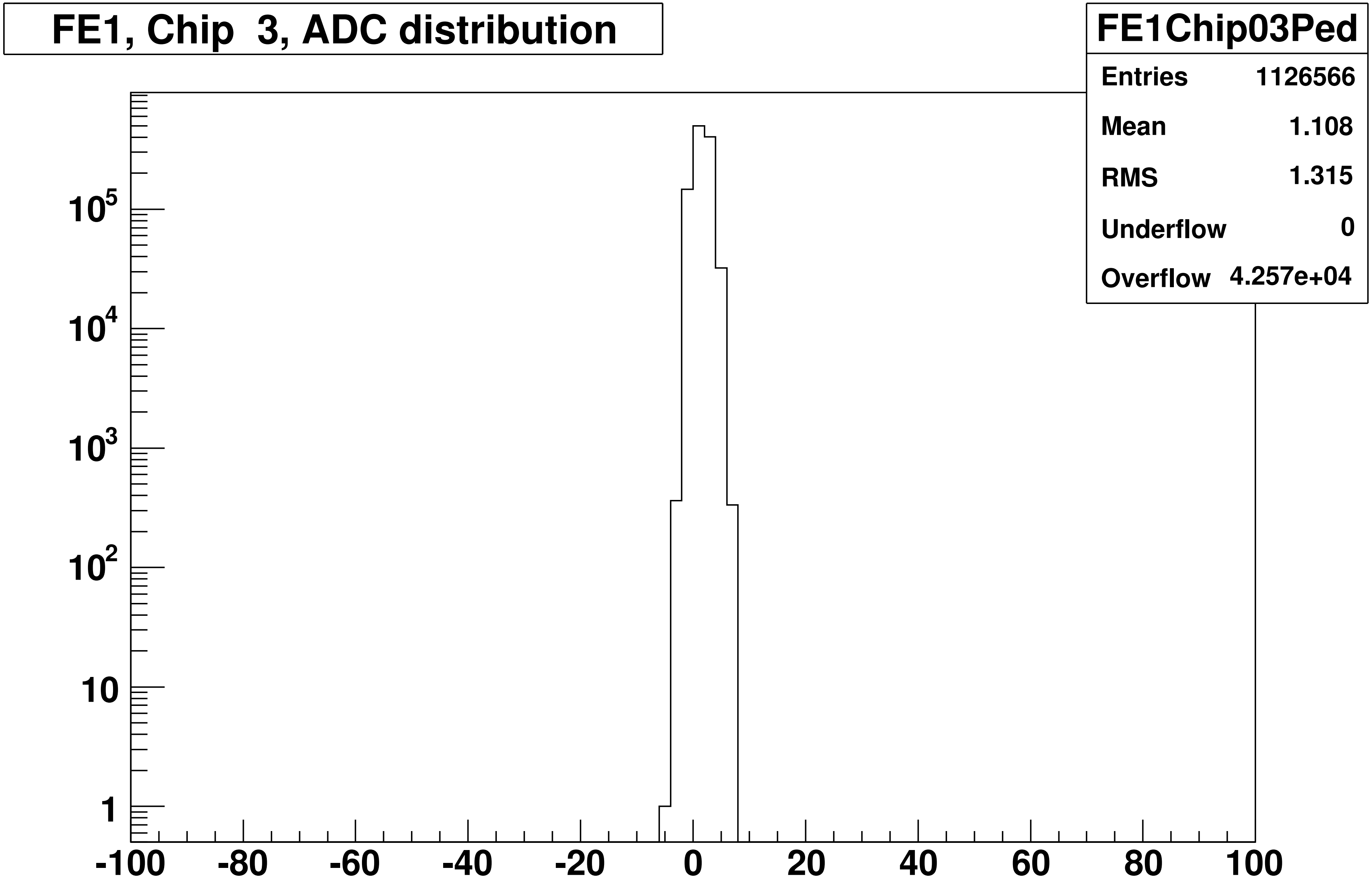

FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 1126566
Mean 1.108
RMS 1.315
Underflow 0
Overflow 4.257e+04
10^5
10^4
10^3
10^2
10
1
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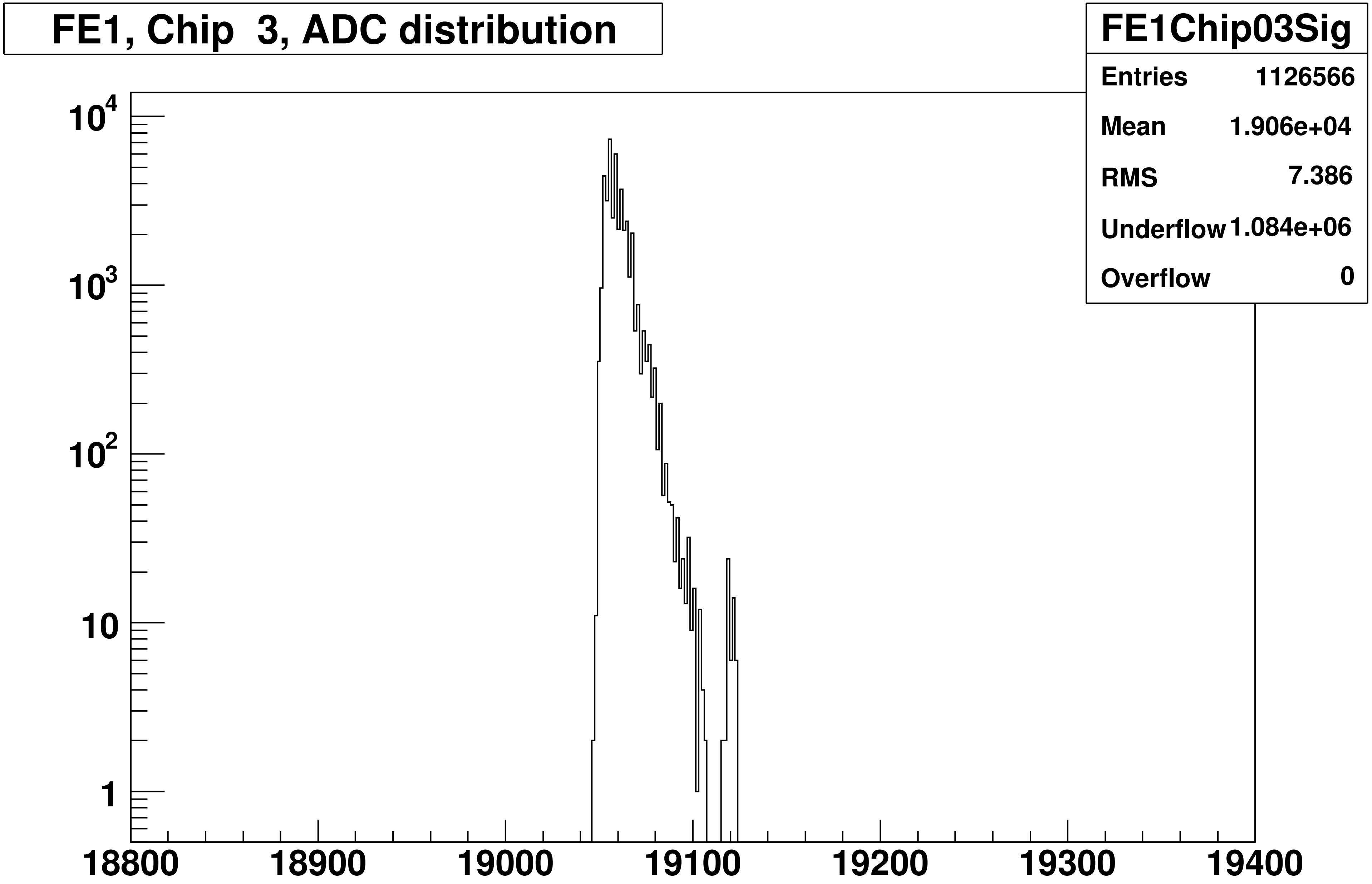
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 1126566
Mean 1.906e+04
RMS 7.386
Underflow 1.084e+06
Overflow 0
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10^3
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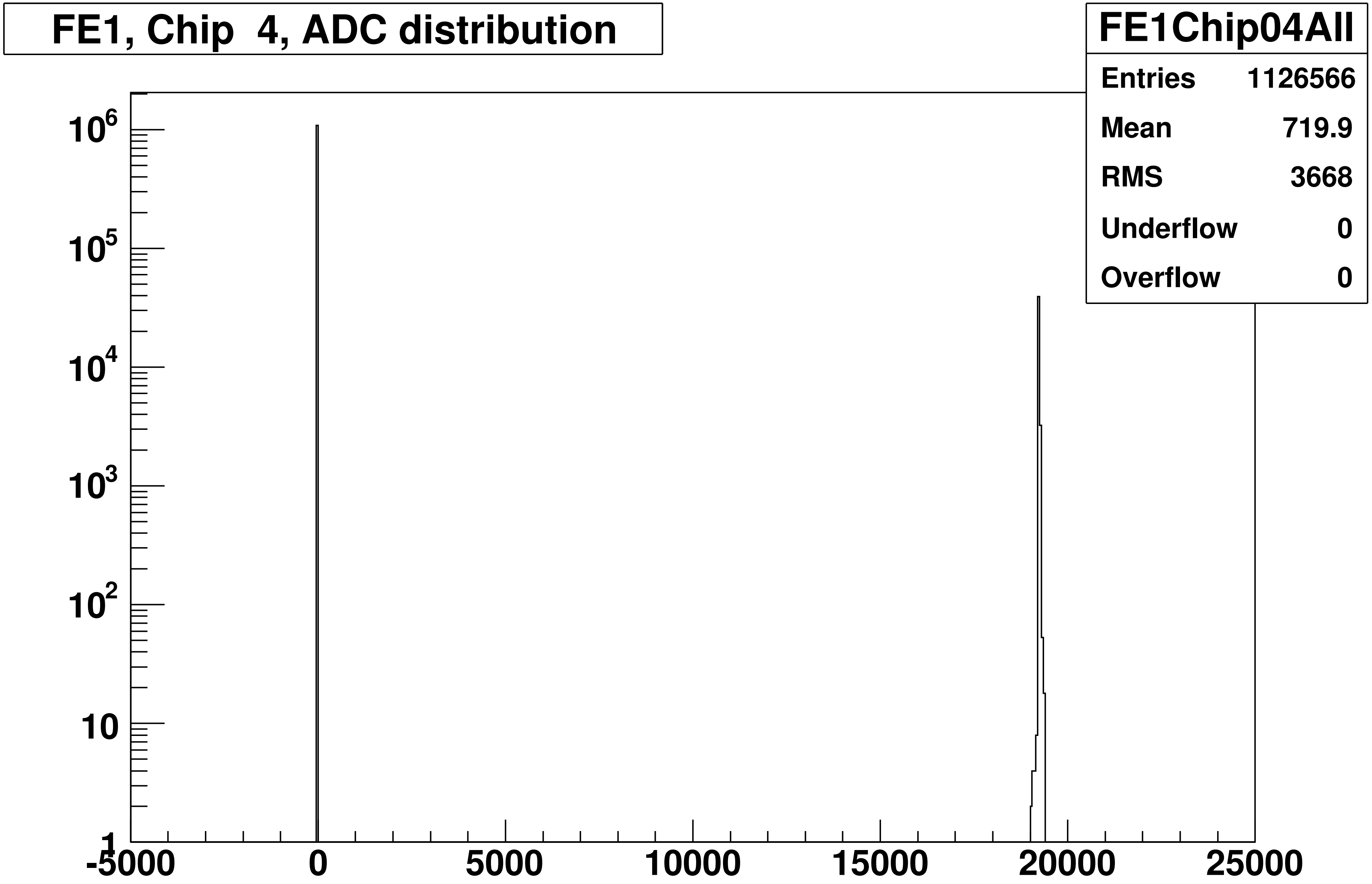
FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 1126566
Mean 719.9
RMS 3668
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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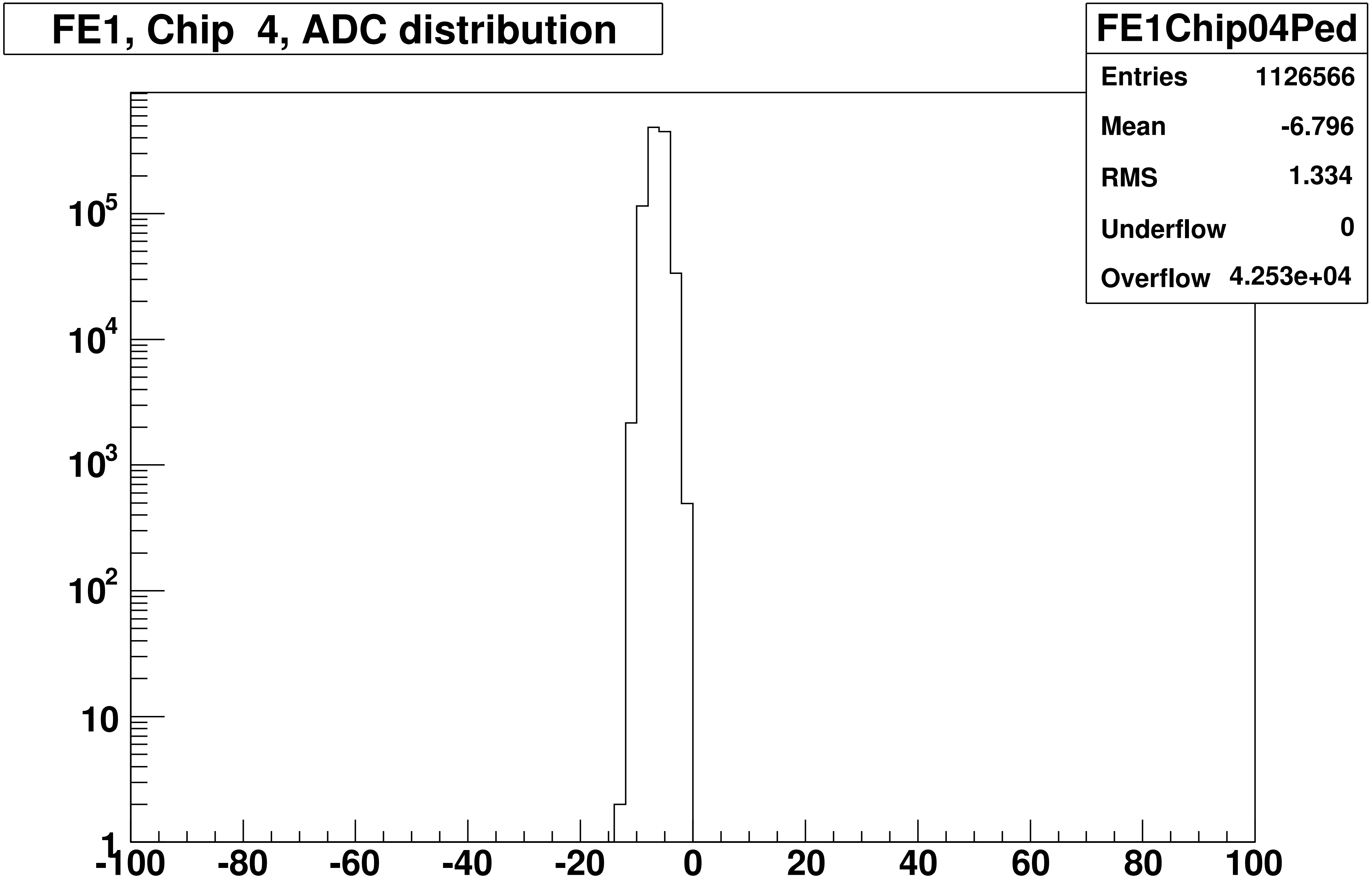
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 1126566
Mean -6.796
RMS 1.334
Underflow 0
Overflow 4.253e+04
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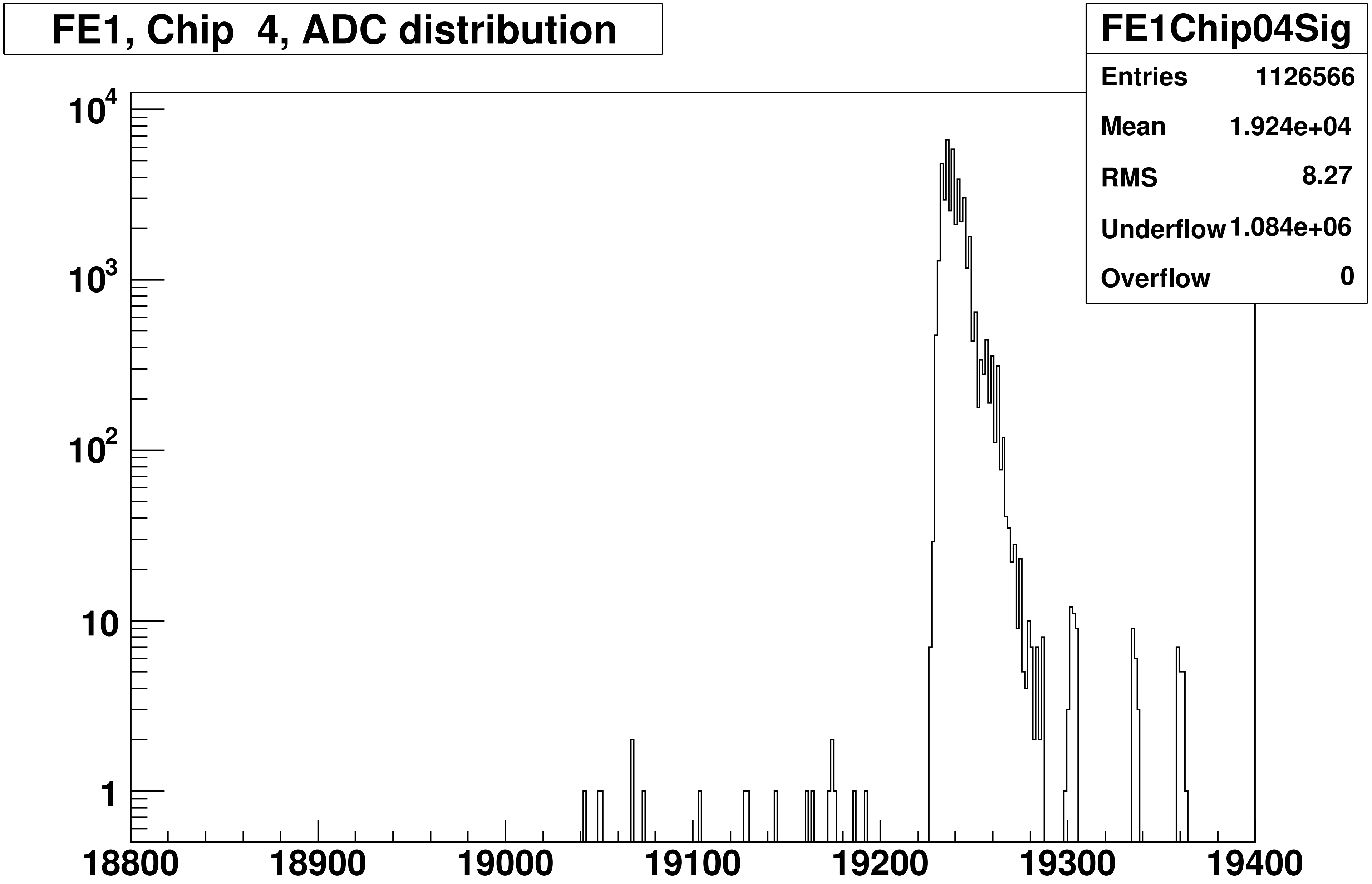
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 1126566
Mean 1.924e+04
RMS 8.27
Underflow 1.084e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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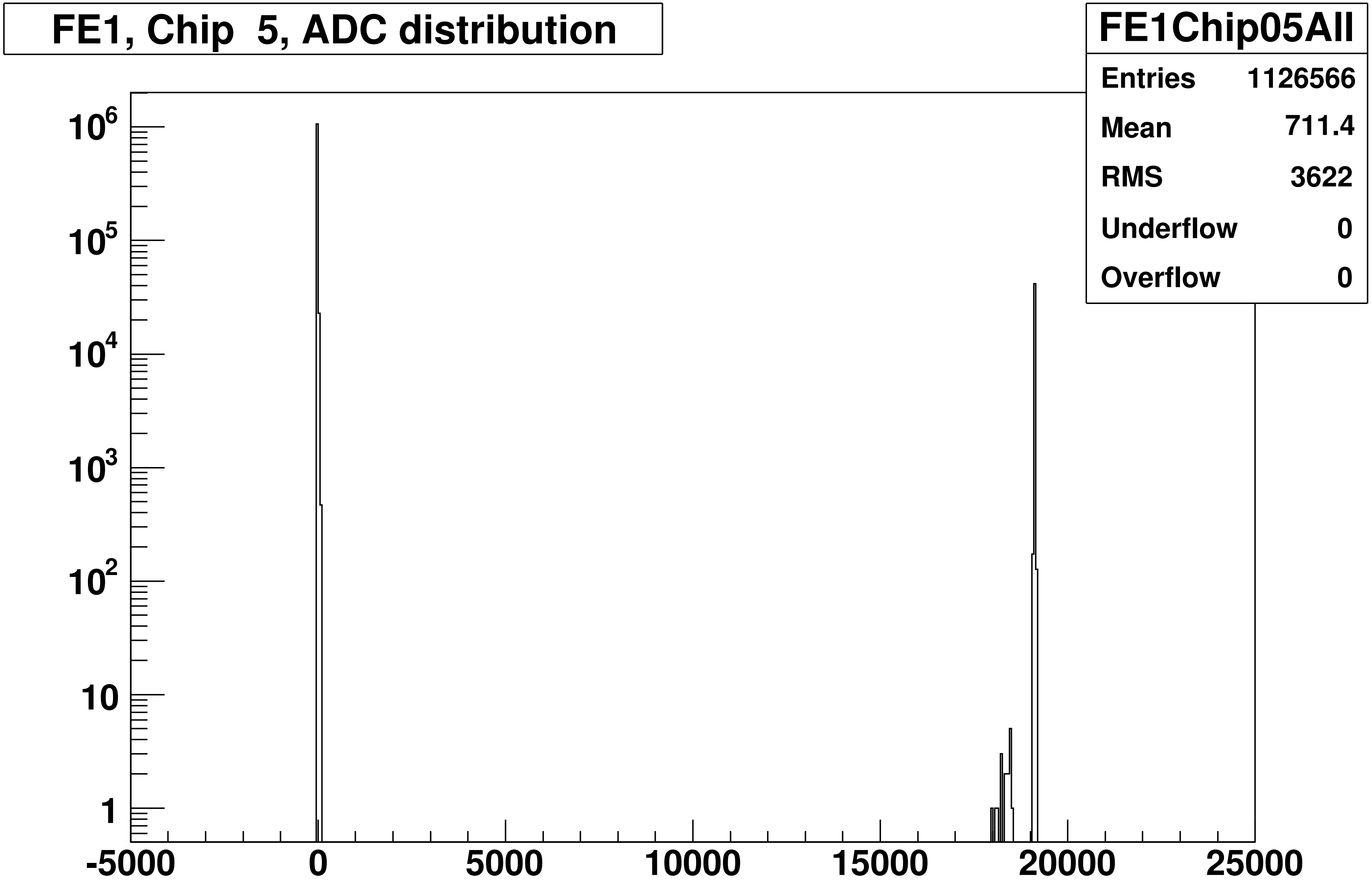
19100
19200
19300
19400



FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 1126566
Mean 711.4
RMS 3622
Underflow 0
Overflow 0
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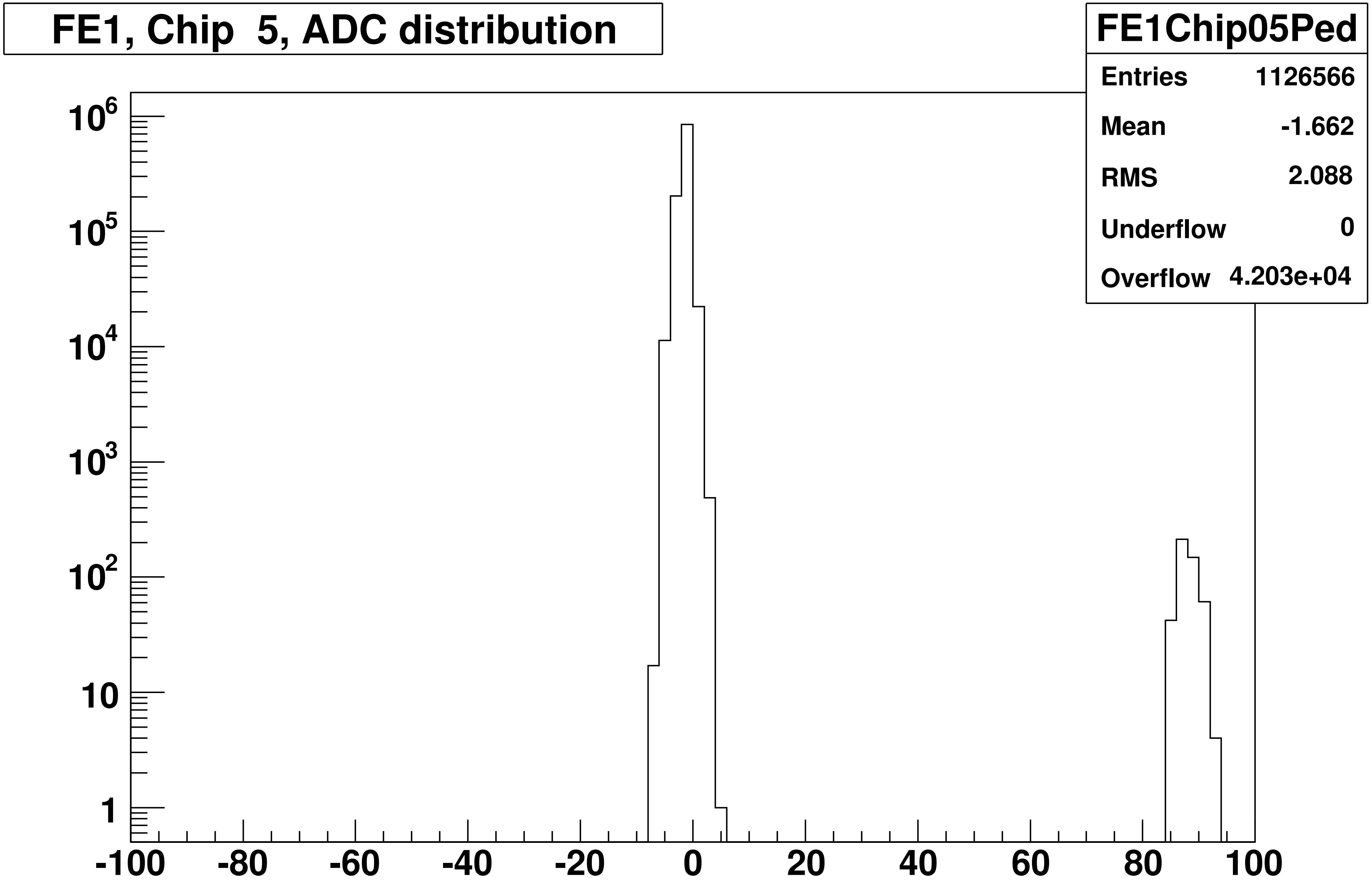
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 1126566
Mean -1.662
RMS 2.088
Underflow 0
Overflow 4.203e+04
10^6
10^5
10^4
10^3
10^2
10
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-40
-20
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60
80
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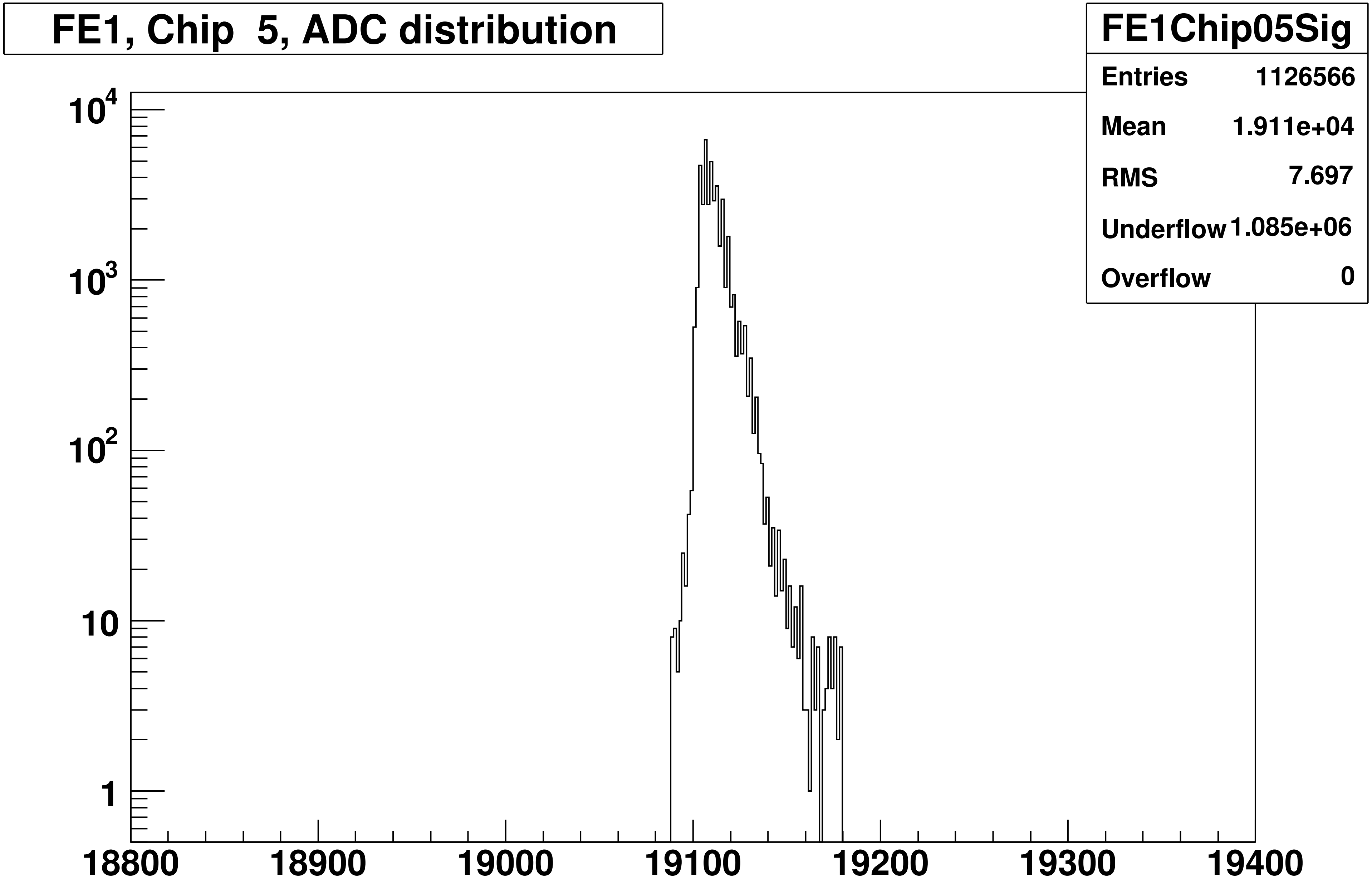
FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 1126566
Mean 1.911e+04
RMS 7.697
Underflow 1.085e+06
Overflow 0
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10^3
10^2
10
1
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18900
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19200
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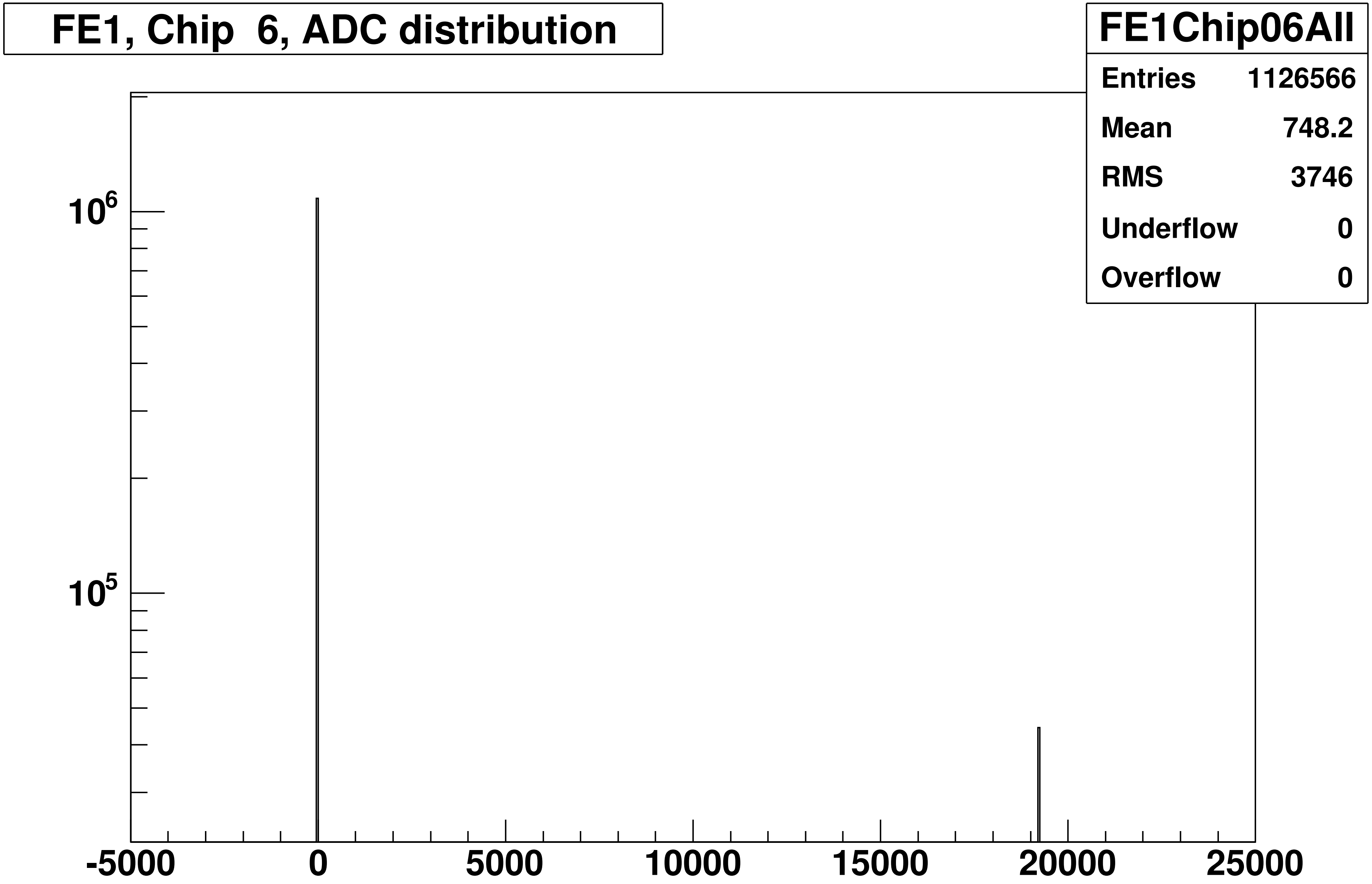
19400


FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 1126566
Mean 748.2
RMS 3746
Underflow 0
Overflow 0
10^6
10^5
-5000
0
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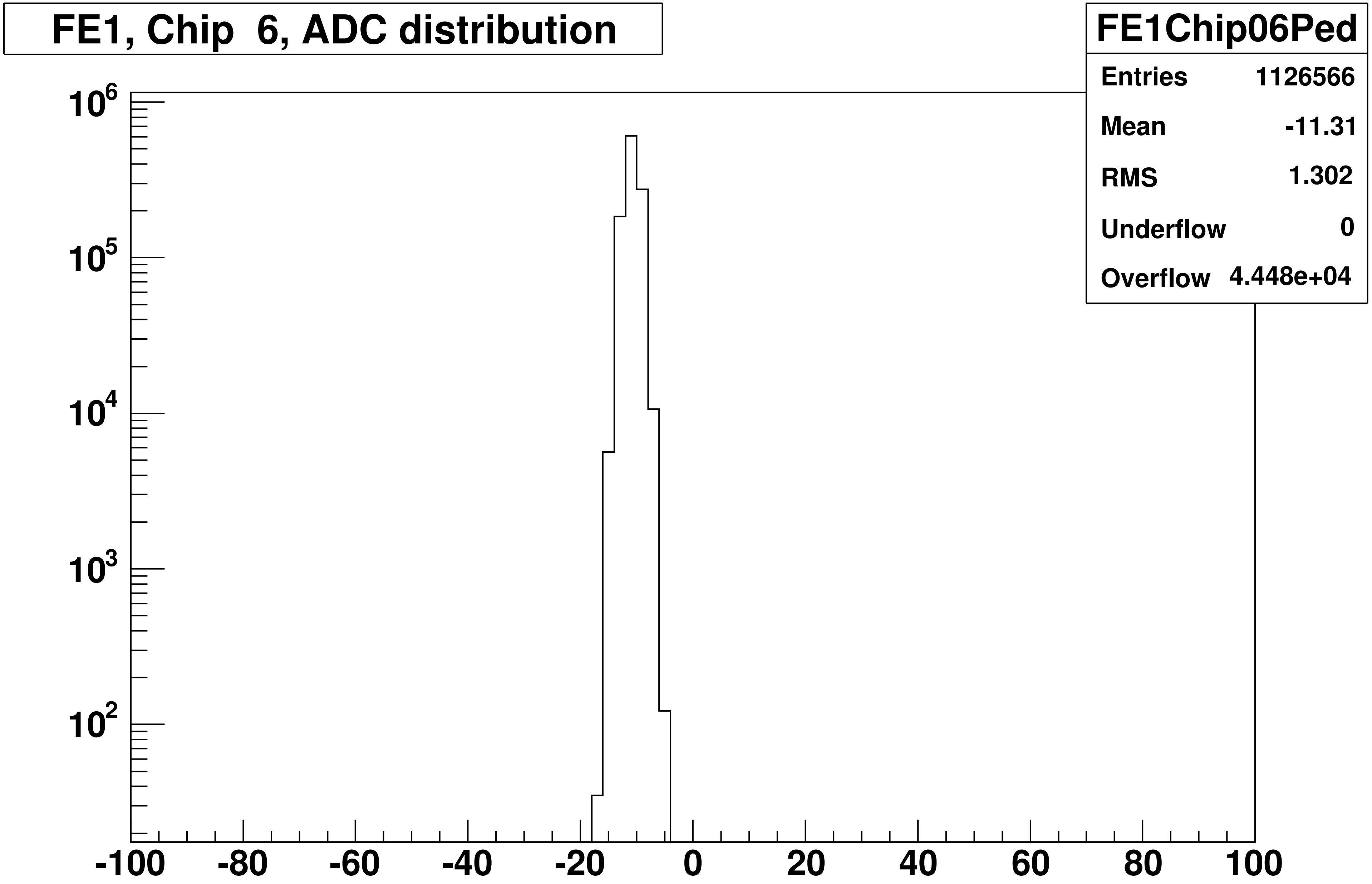
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 1126566
Mean -11.31
RMS 1.302
Underflow 0
Overflow 4.448e+04
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10^5
10^4
10^3
10^2
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-40
-20
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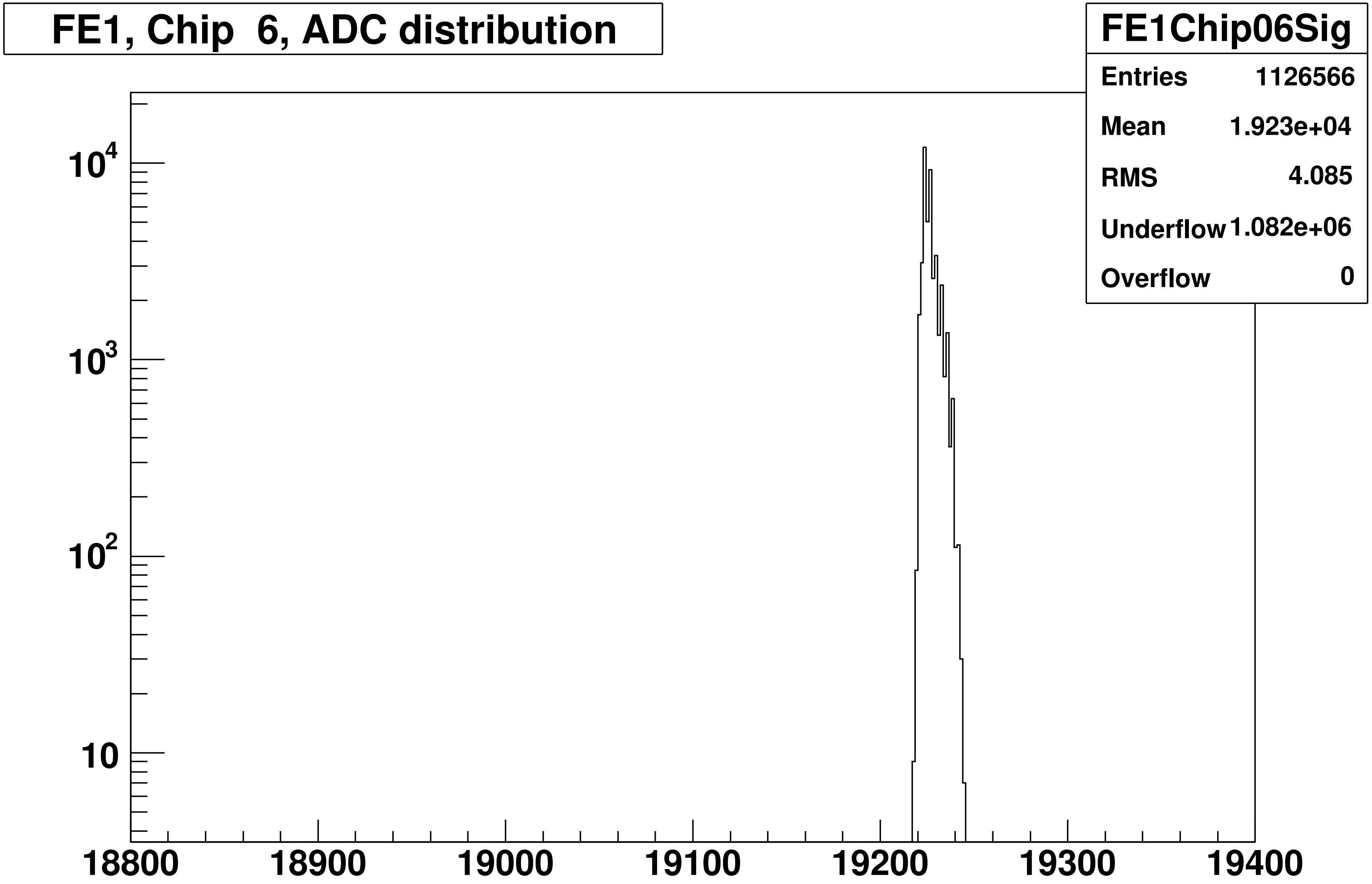
40
60
80
100



FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 1126566
Mean 1.923e+04
RMS 4.085
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
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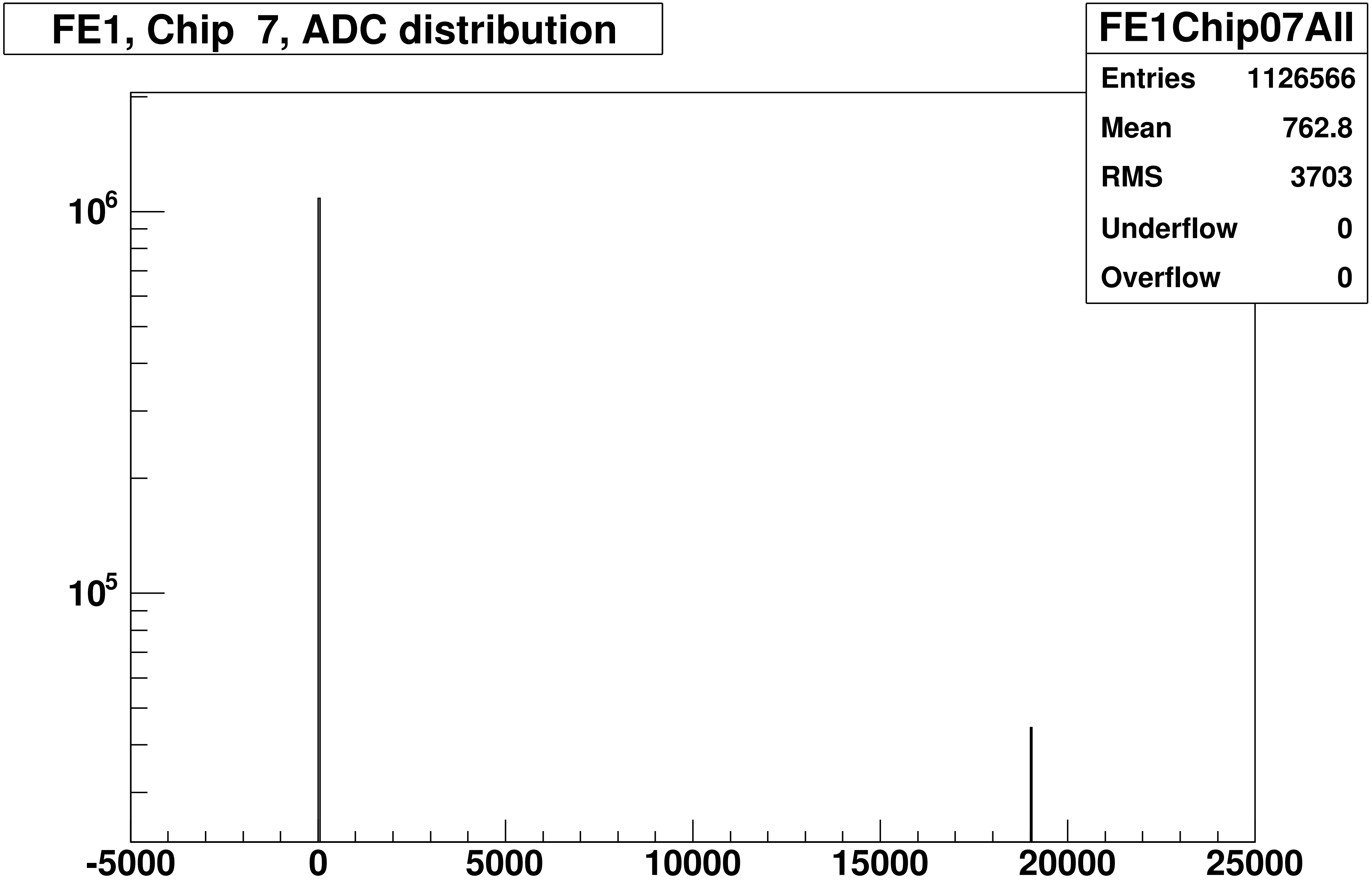
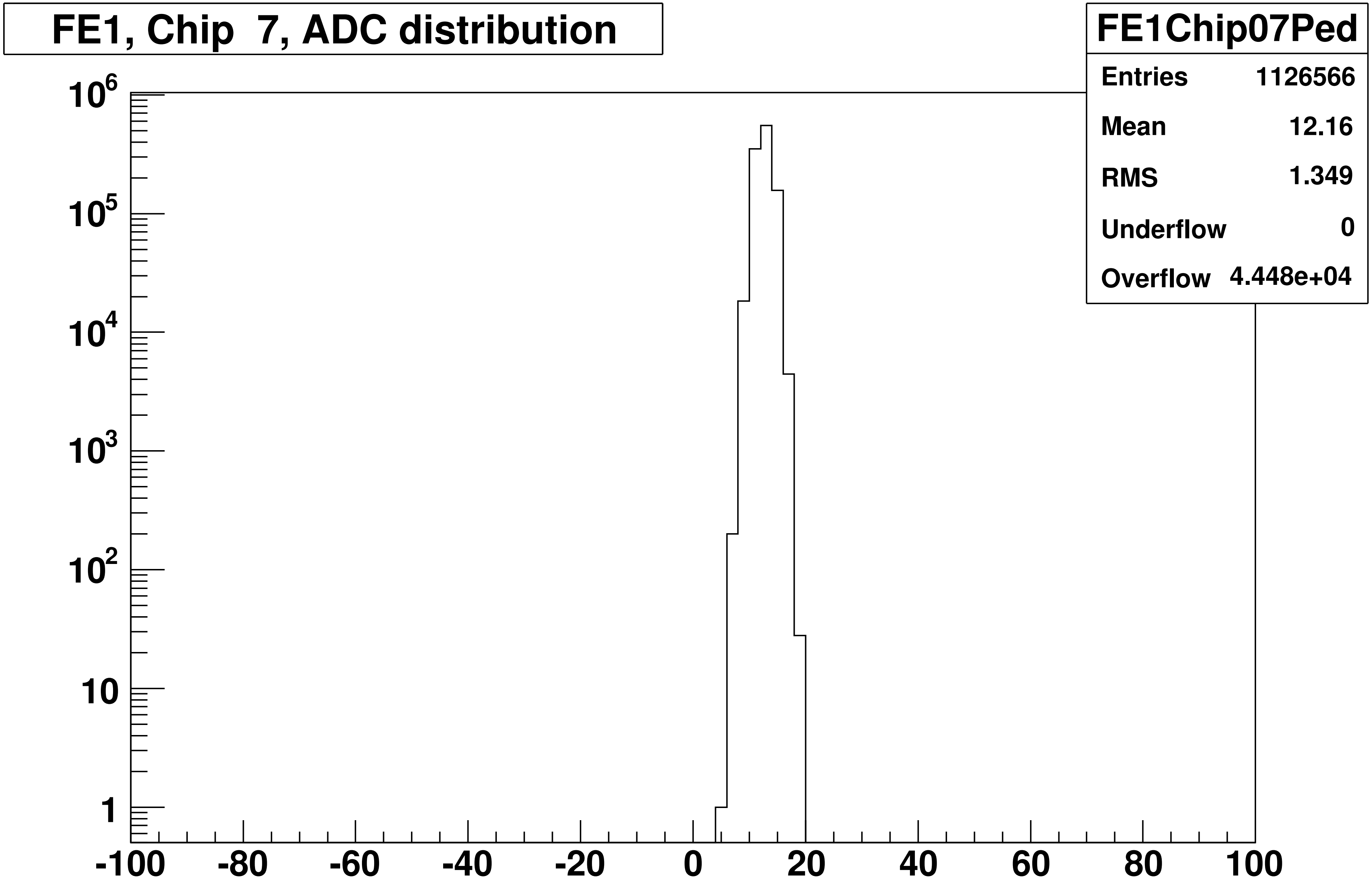
19400


FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 1126566
Mean 762.8
RMS 3703
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 1126566
Mean 12.16
RMS 1.349
Underflow 0
Overflow 4.448e+04
10^6
10^5
10^4
10^3
10^2
10
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-20
0
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60
80
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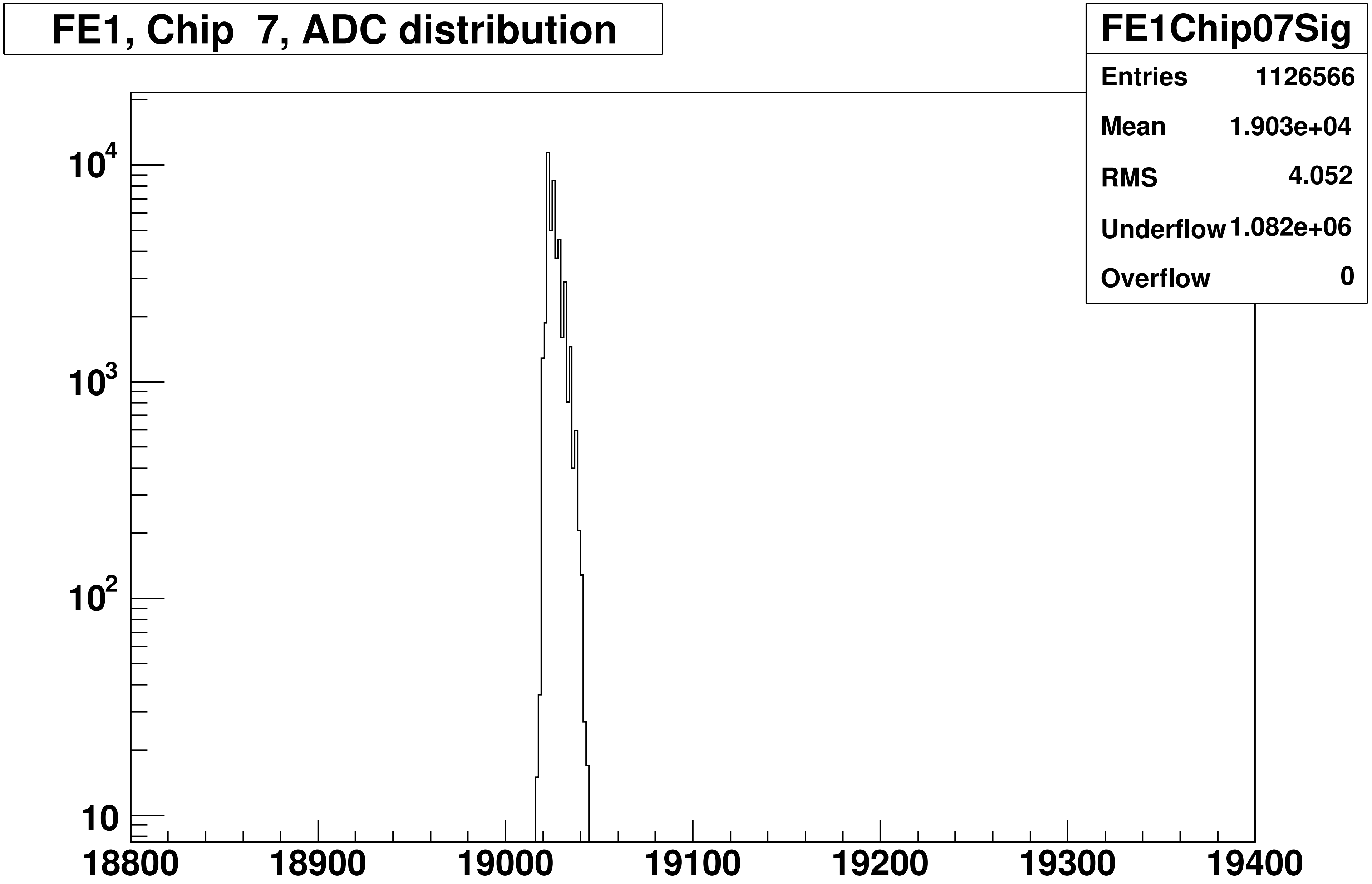

FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 1126566
Mean 1.903e+04
RMS 4.052
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
18800
18900
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19200
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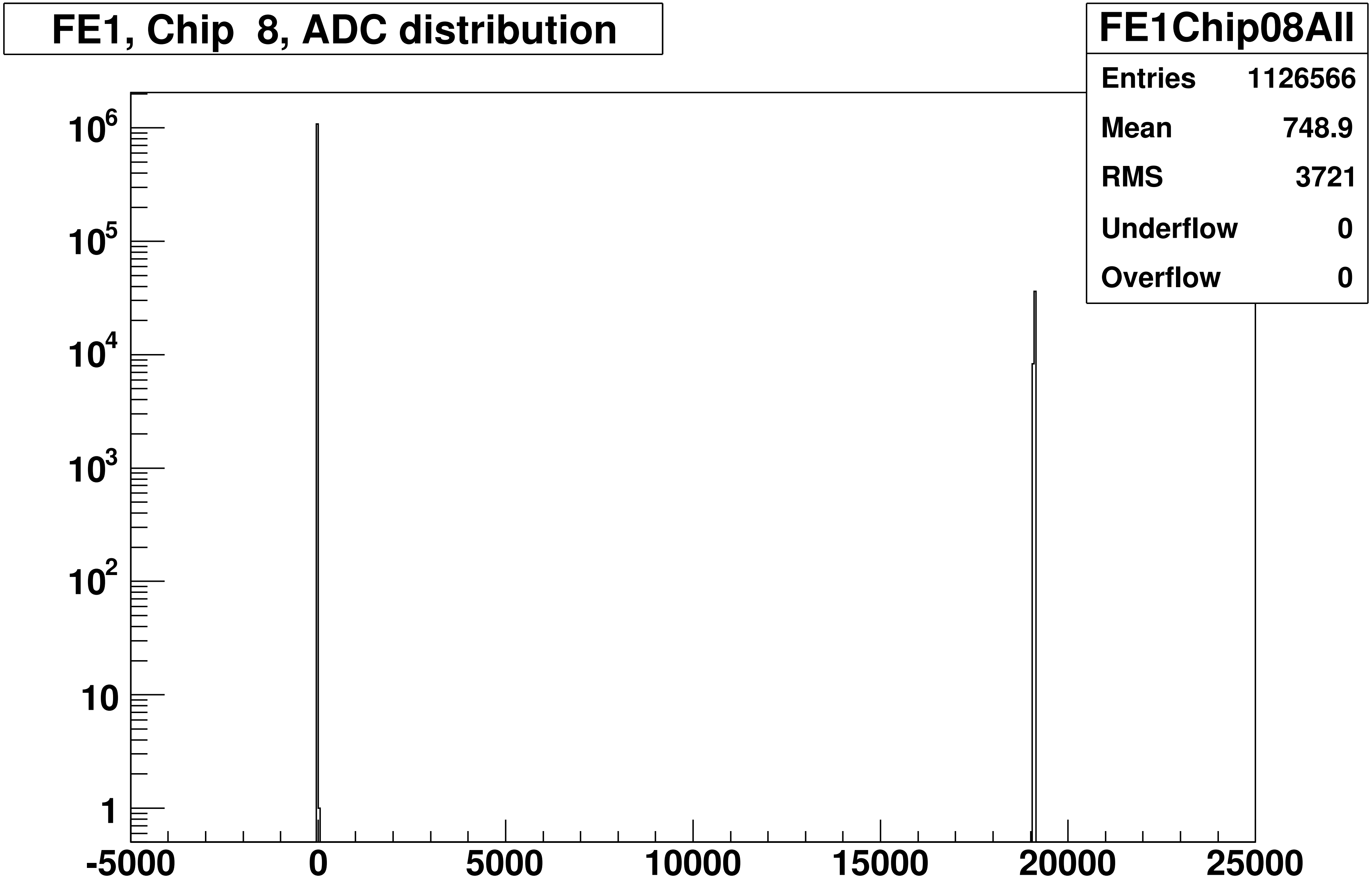
FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 1126566
Mean 748.9
RMS 3721
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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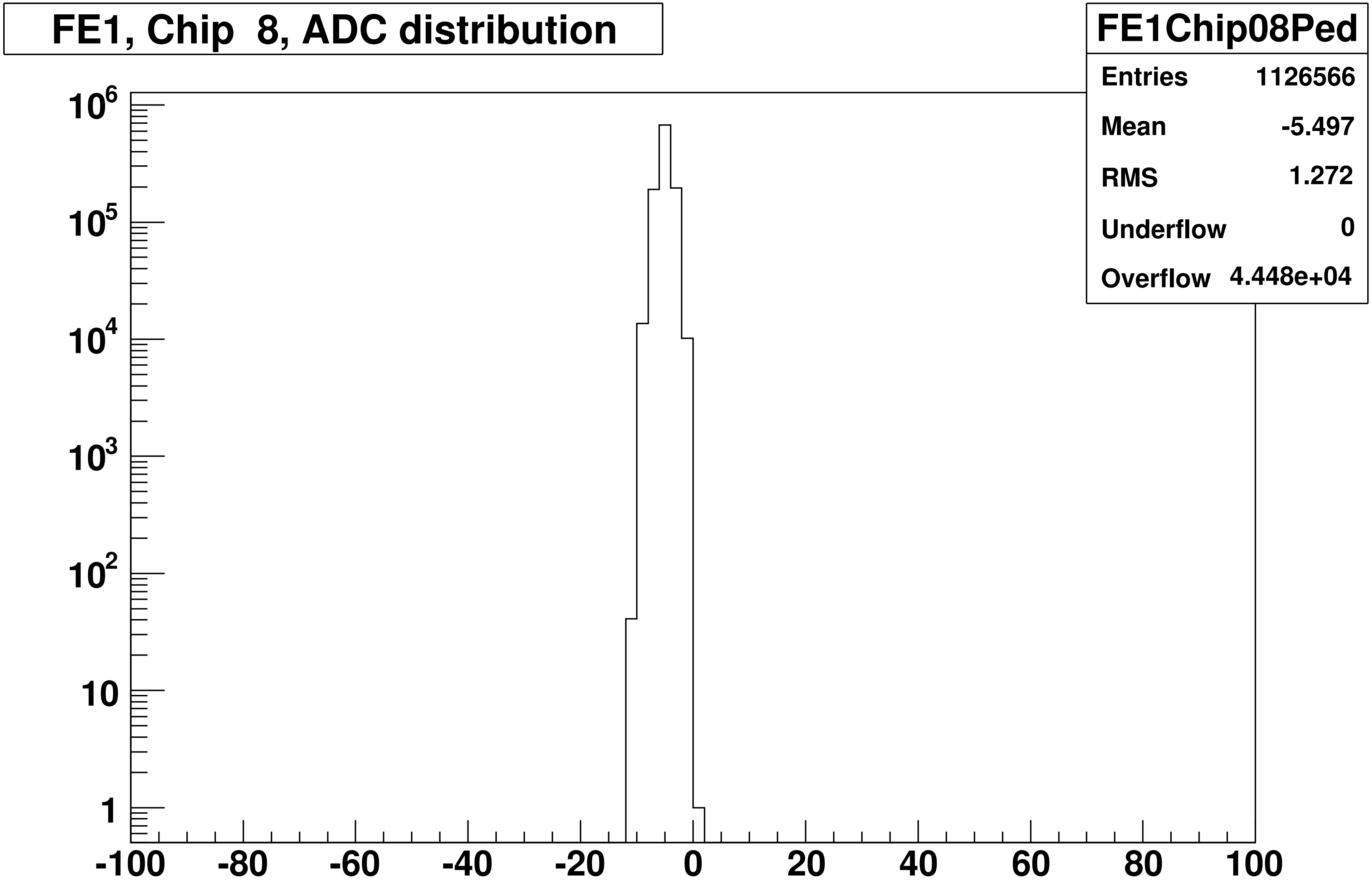
FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 1126566
Mean -5.497
RMS 1.272
Underflow 0
Overflow 4.448e+04
10^6
10^5
10^4
10^3
10^2
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1
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-20
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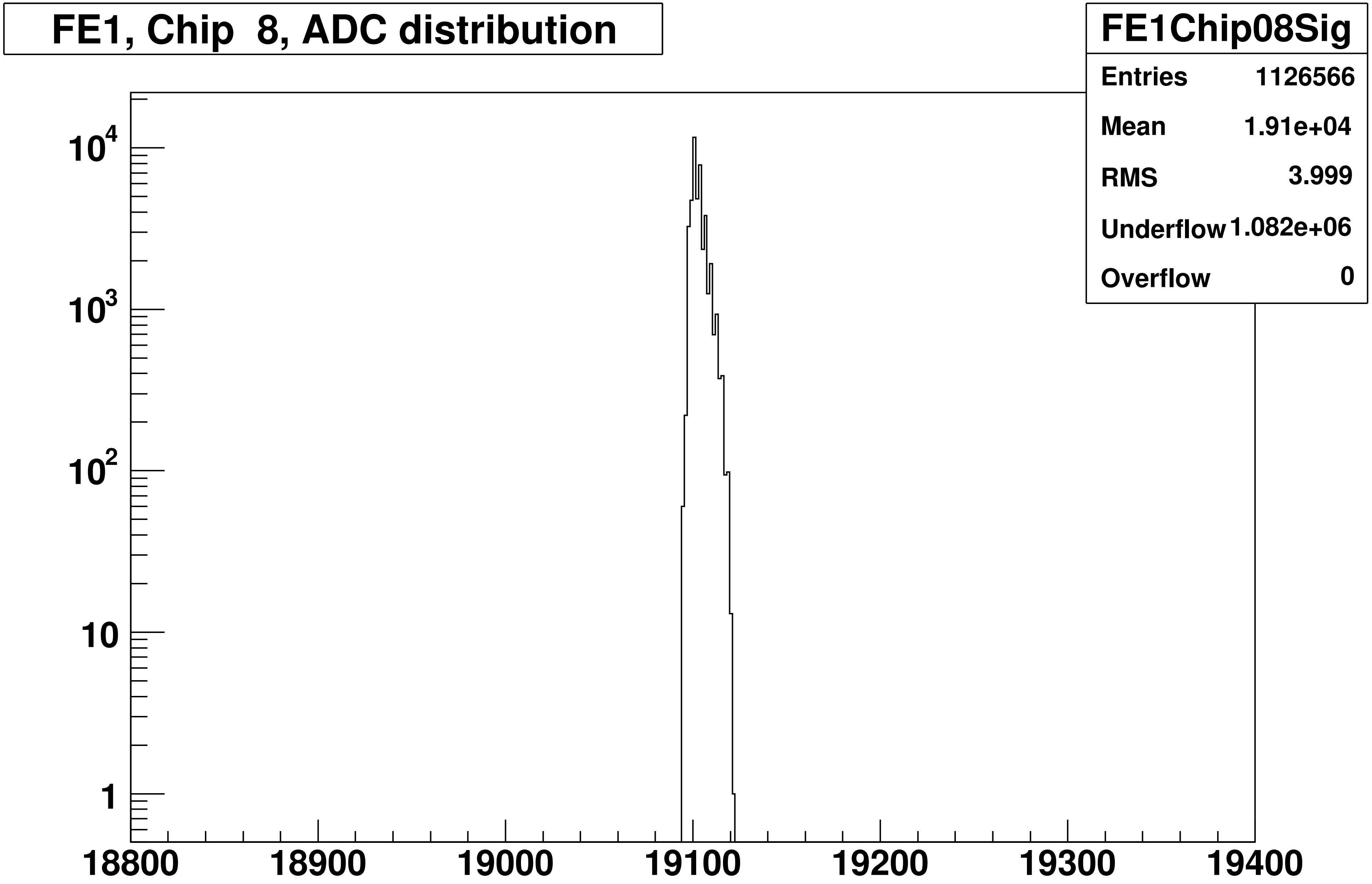

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 1126566
Mean 1.91e+04
RMS 3.999
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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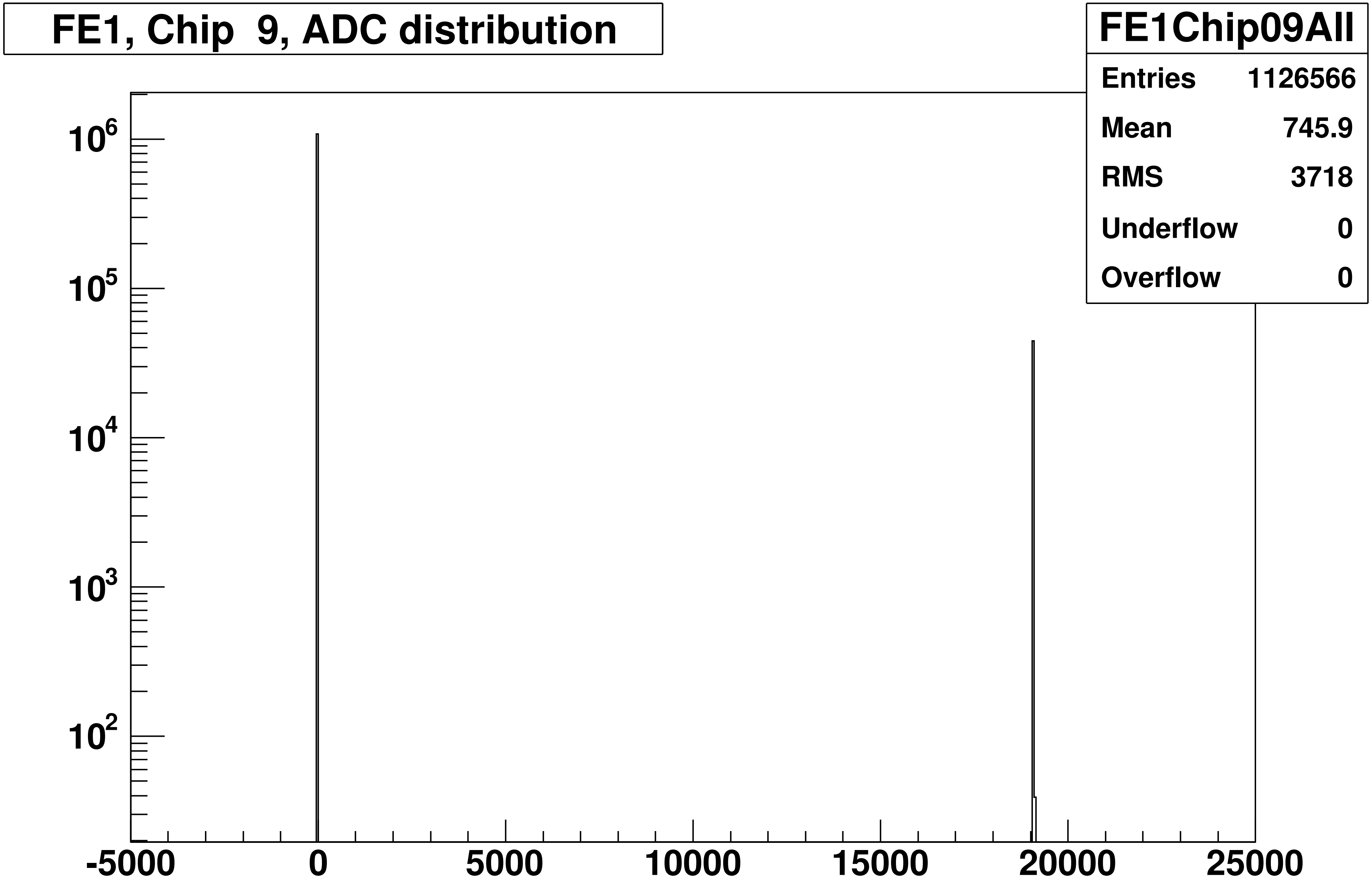

FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 1126566
Mean 745.9
RMS 3718
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
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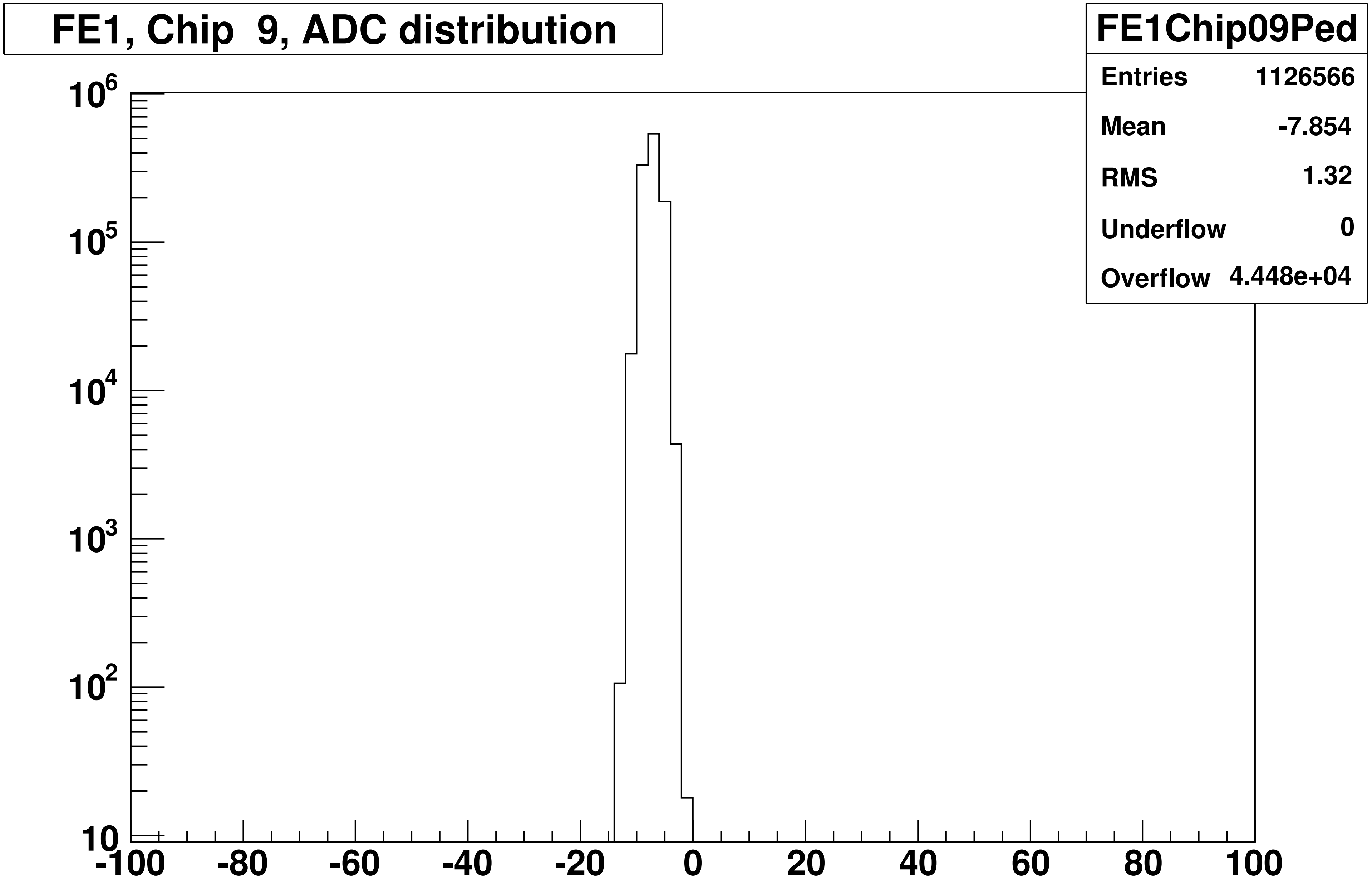

FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 1126566
Mean -7.854
RMS 1.32
Underflow 0
Overflow 4.448e+04
10
10^2
10^3
10^4
10^5
10^6
-100
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-20
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80
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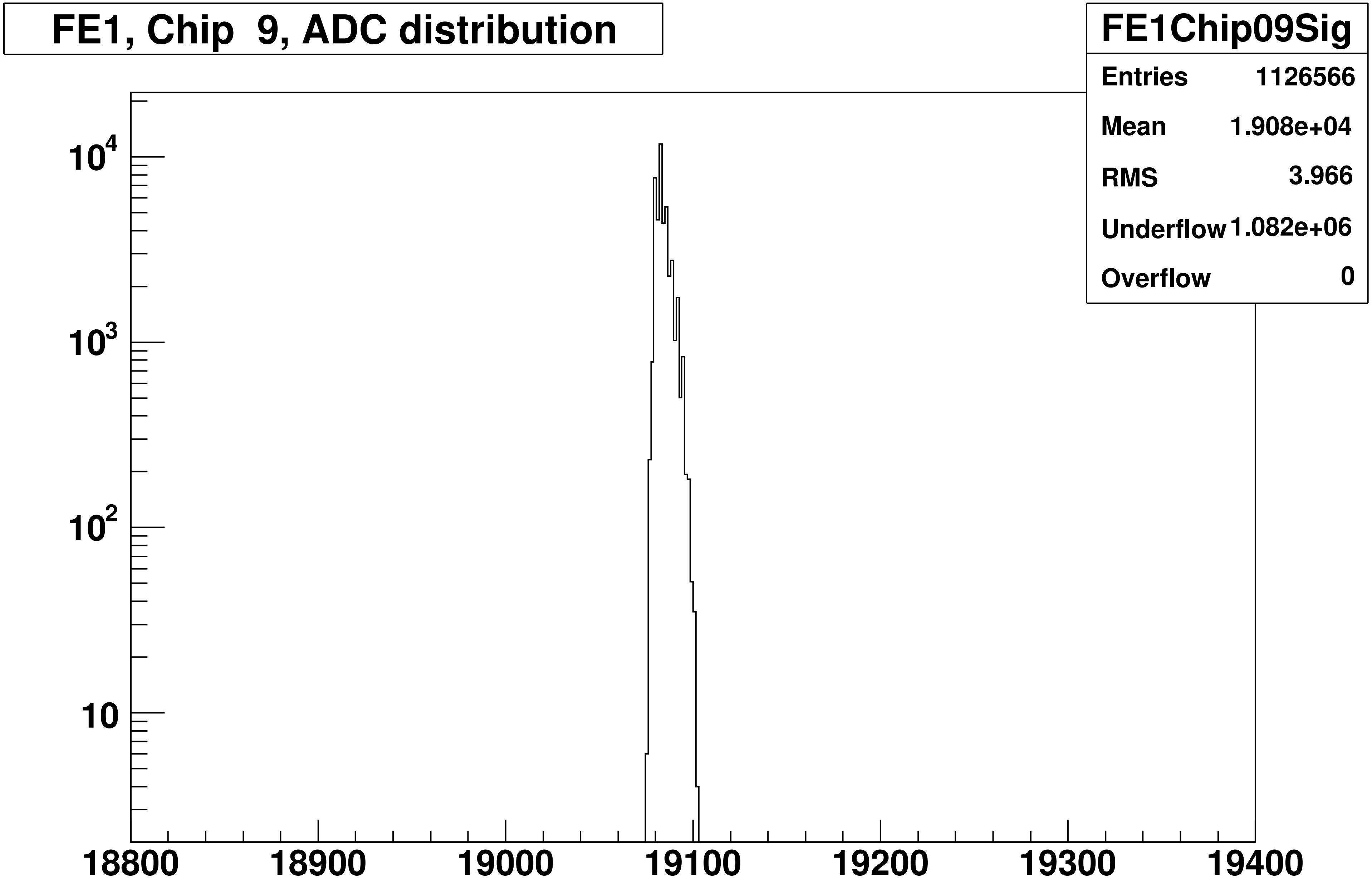
FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 1126566
Mean 1.908e+04
RMS 3.966
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
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18900
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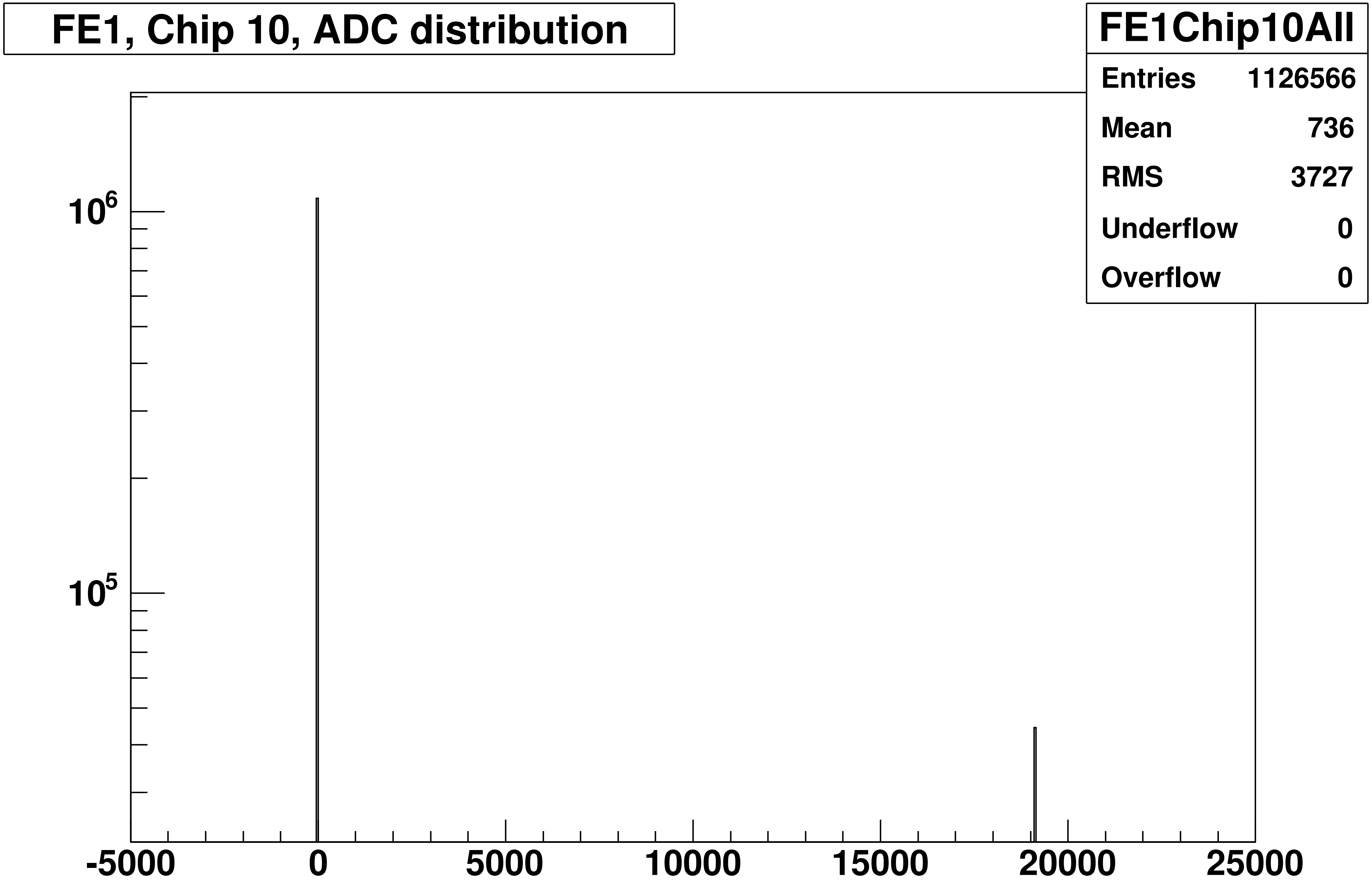
19400



FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 1126566
Mean 736
RMS 3727
Underflow 0
Overflow 0
10^6
10^5
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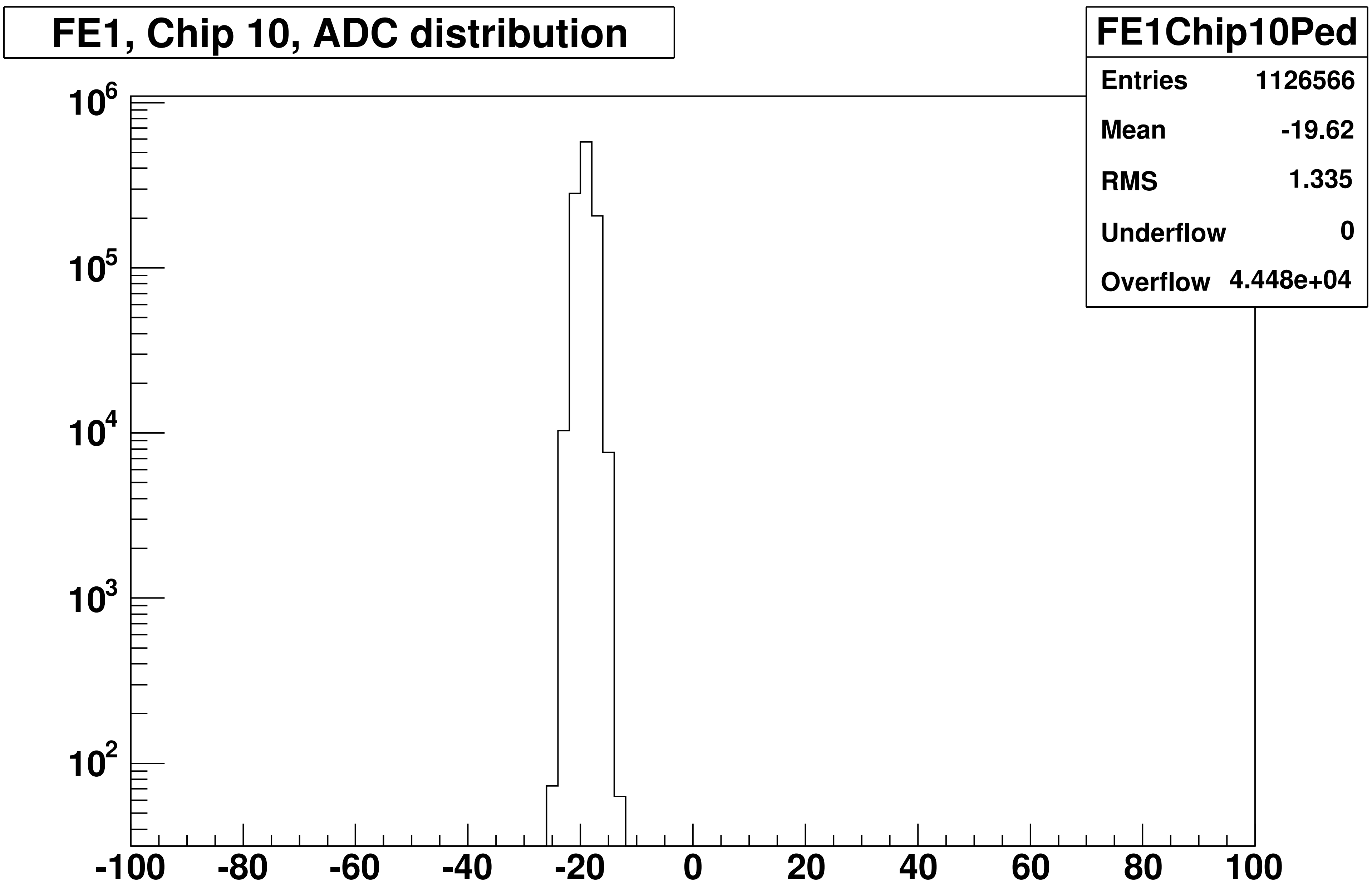
0
5000
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 1126566
Mean -19.62
RMS 1.335
Underflow 0
Overflow 4.448e+04
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10^5
10^4
10^3
10^2
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80
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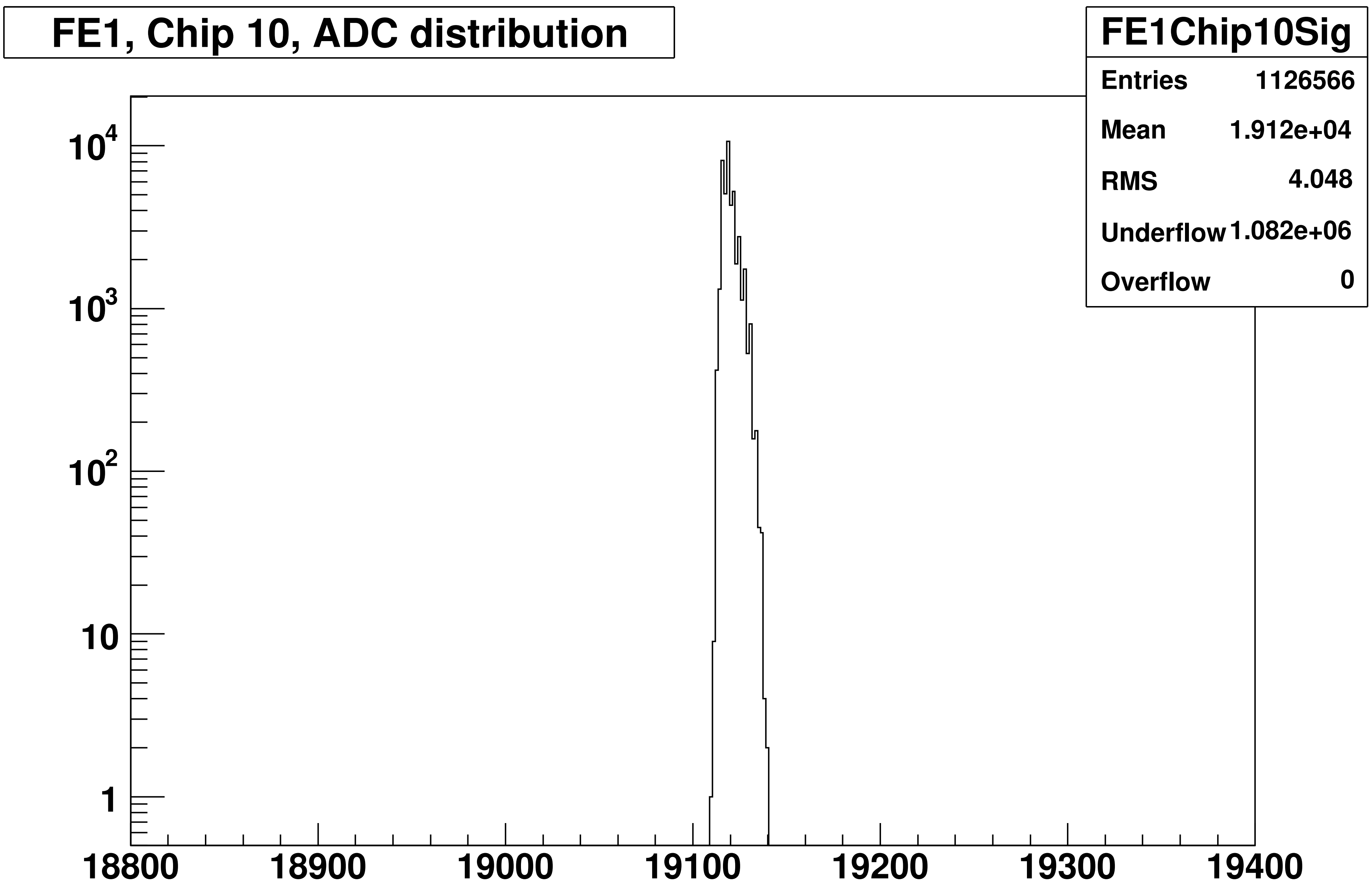
FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 1126566
Mean 1.912e+04
RMS 4.048
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
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18900
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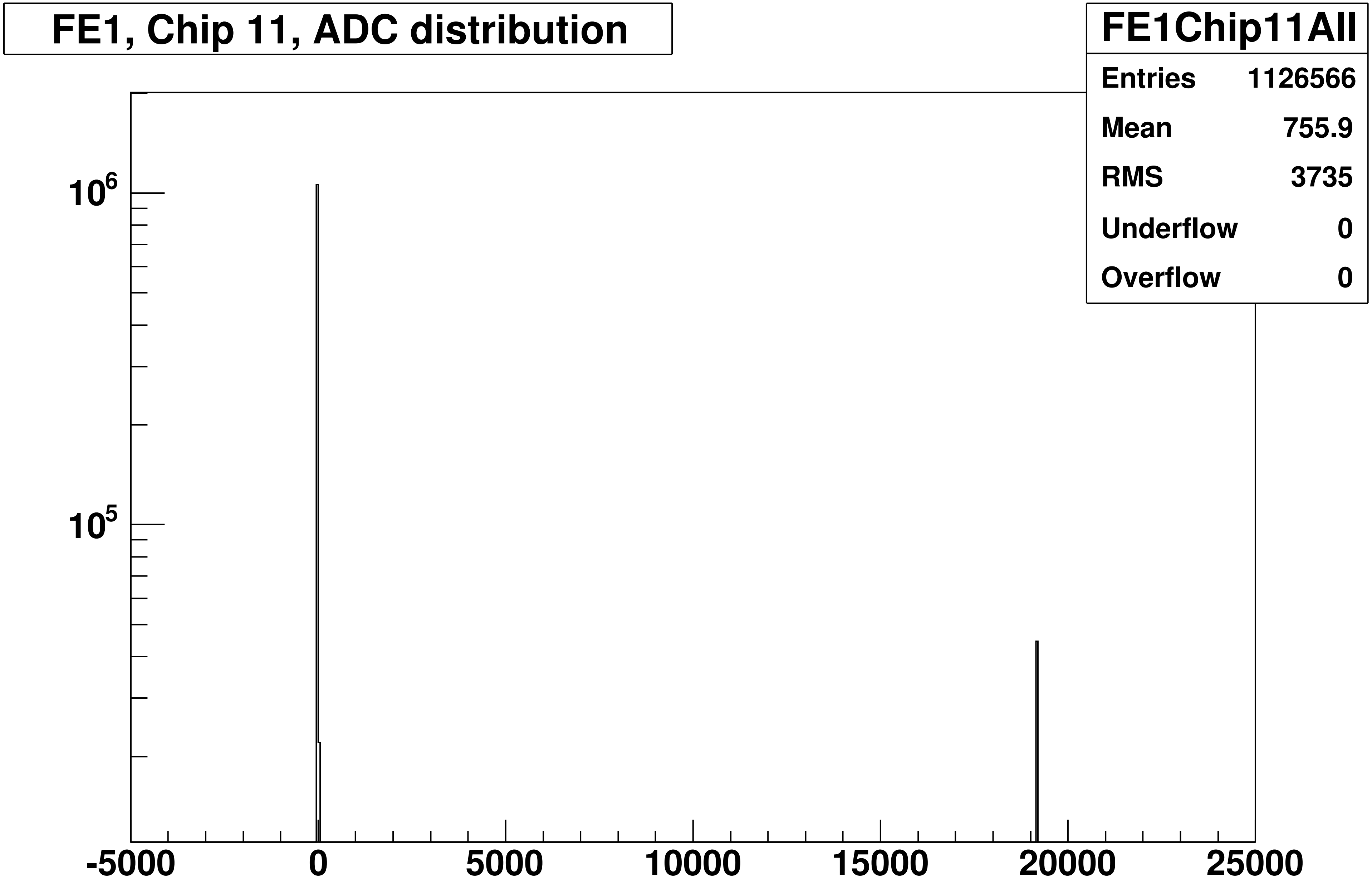
FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 1126566
Mean 755.9
RMS 3735
Underflow 0
Overflow 0
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10^5
-5000
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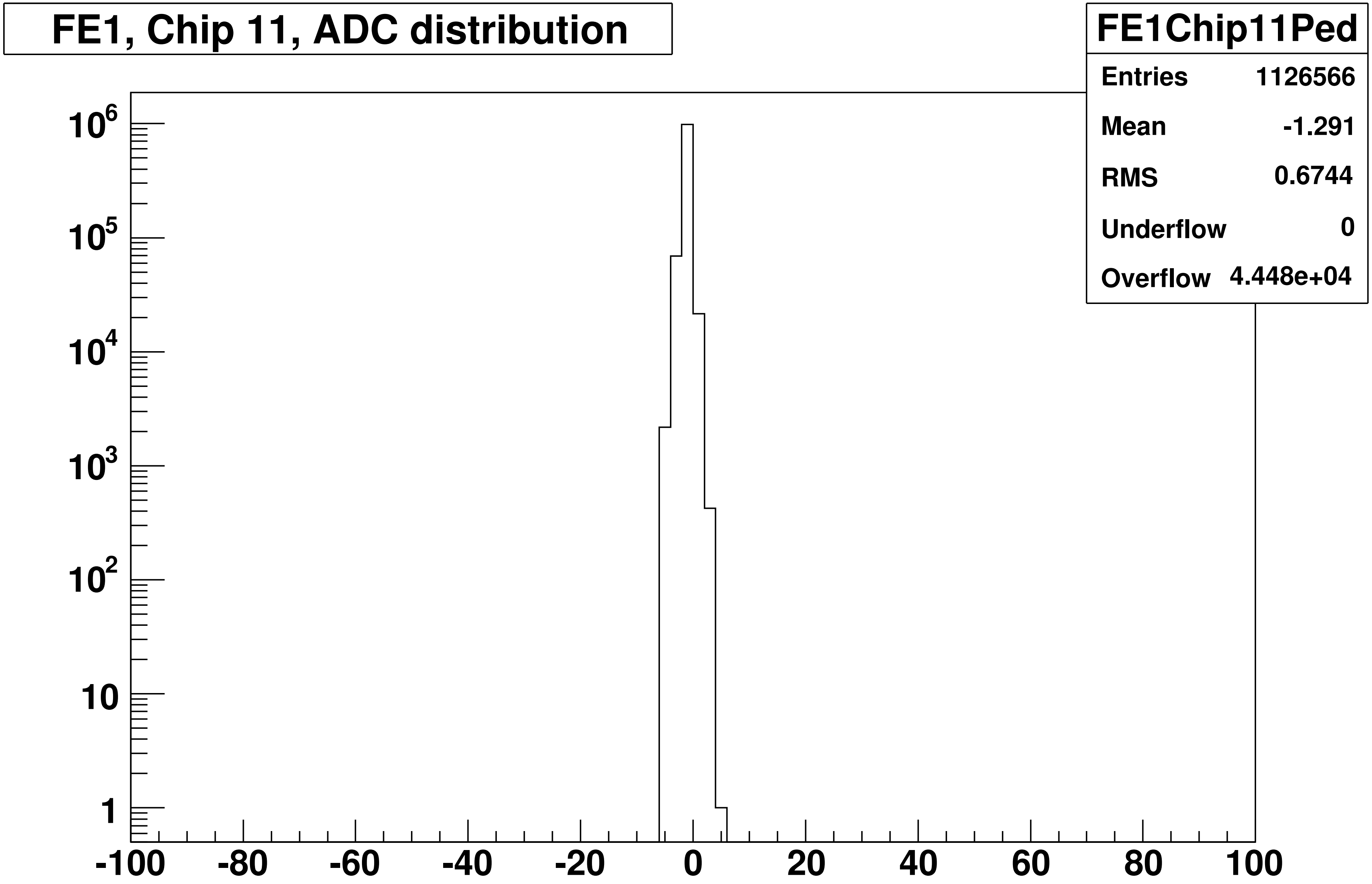
25000



FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 1126566
Mean -1.291
RMS 0.6744
Underflow 0
Overflow 4.448e+04
10^6
10^5
10^4
10^3
10^2
10
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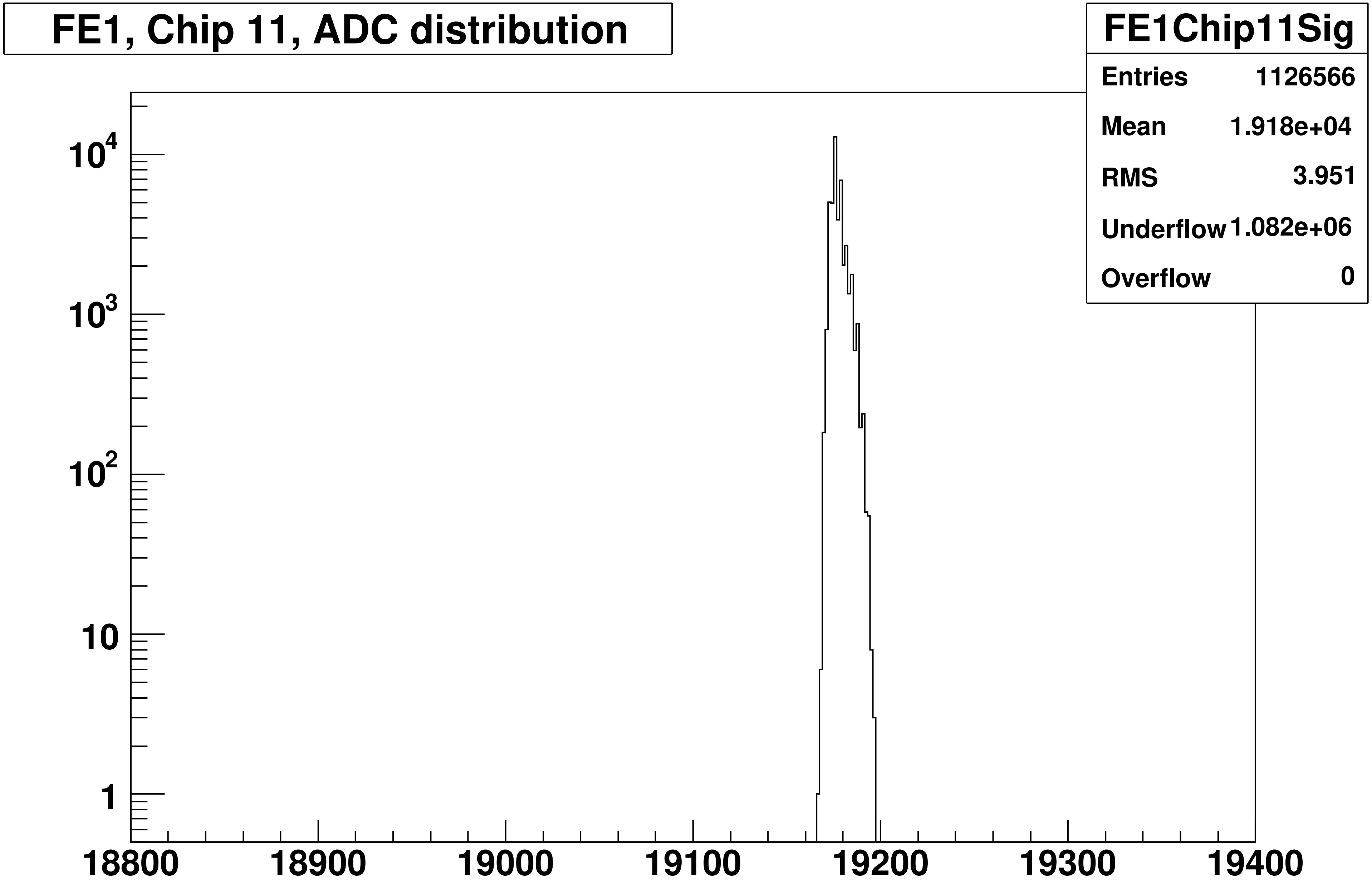
FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 1126566
Mean 1.918e+04
RMS 3.951
Underflow 1.082e+06
Overflow 0
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10^3
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18900
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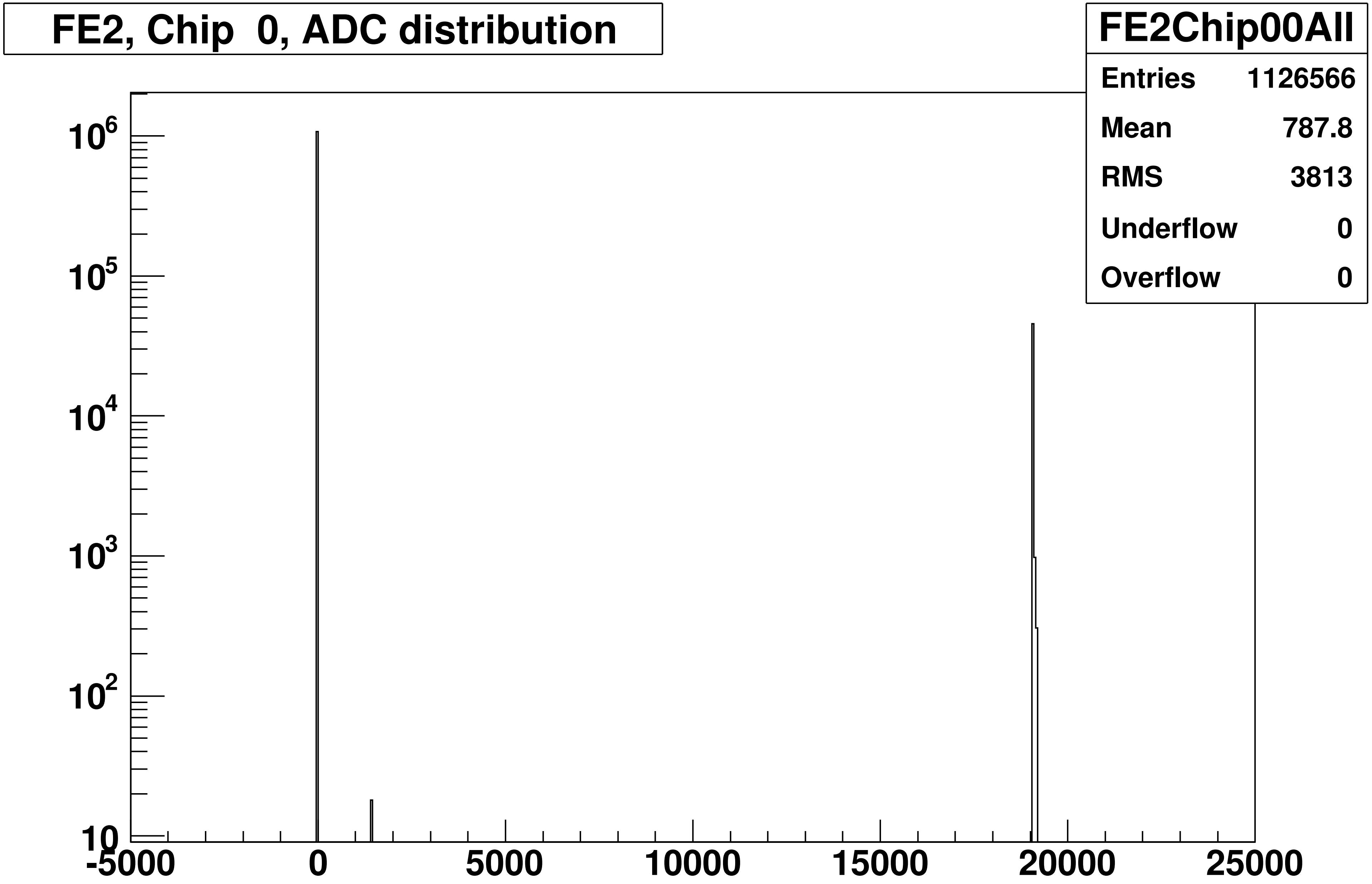

FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 1126566
Mean 787.8
RMS 3813
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
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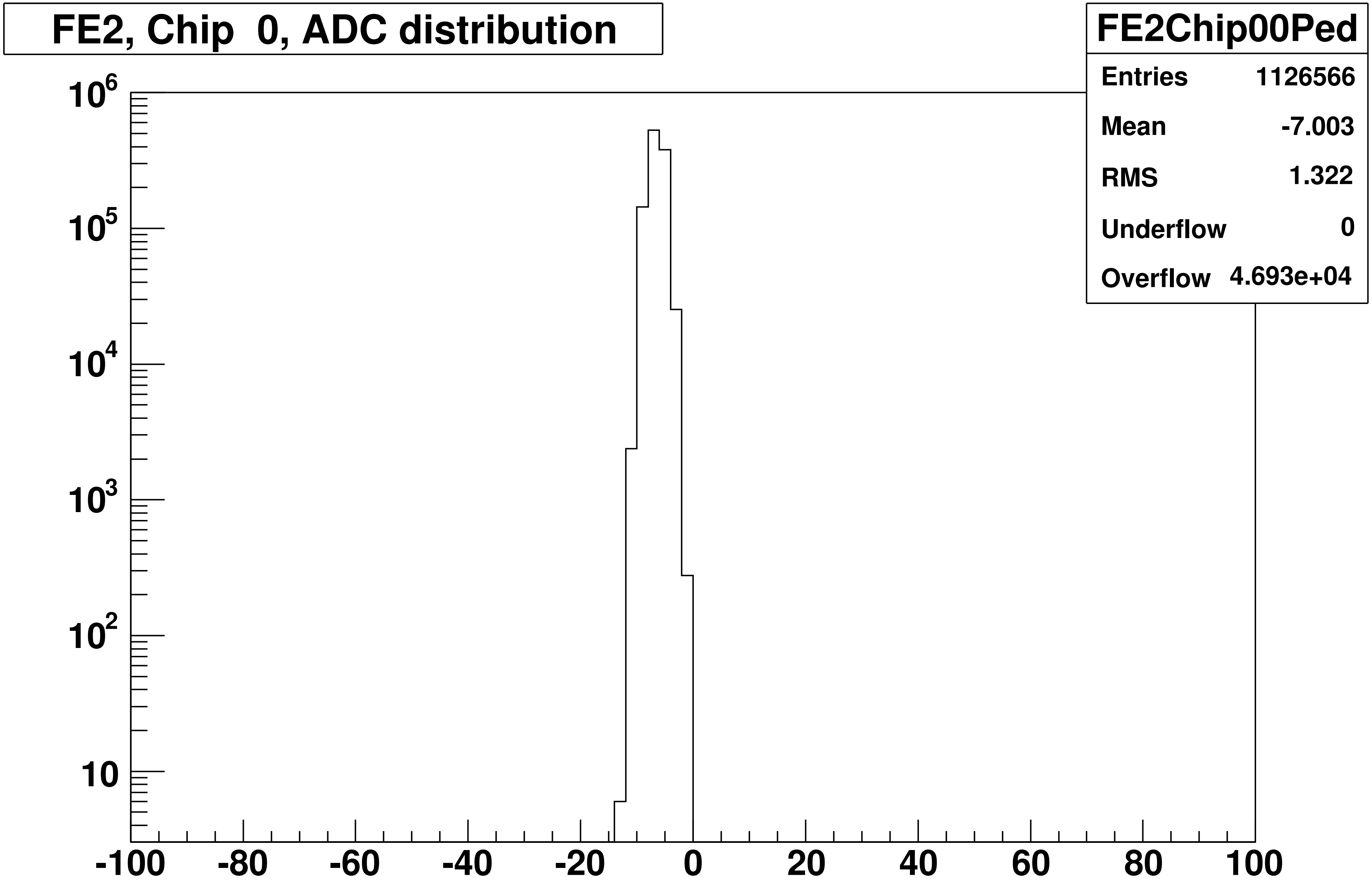
FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 1126566
Mean -7.003
RMS 1.322
Underflow 0
Overflow 4.693e+04
10^6
10^5
10^4
10^3
10^2
10
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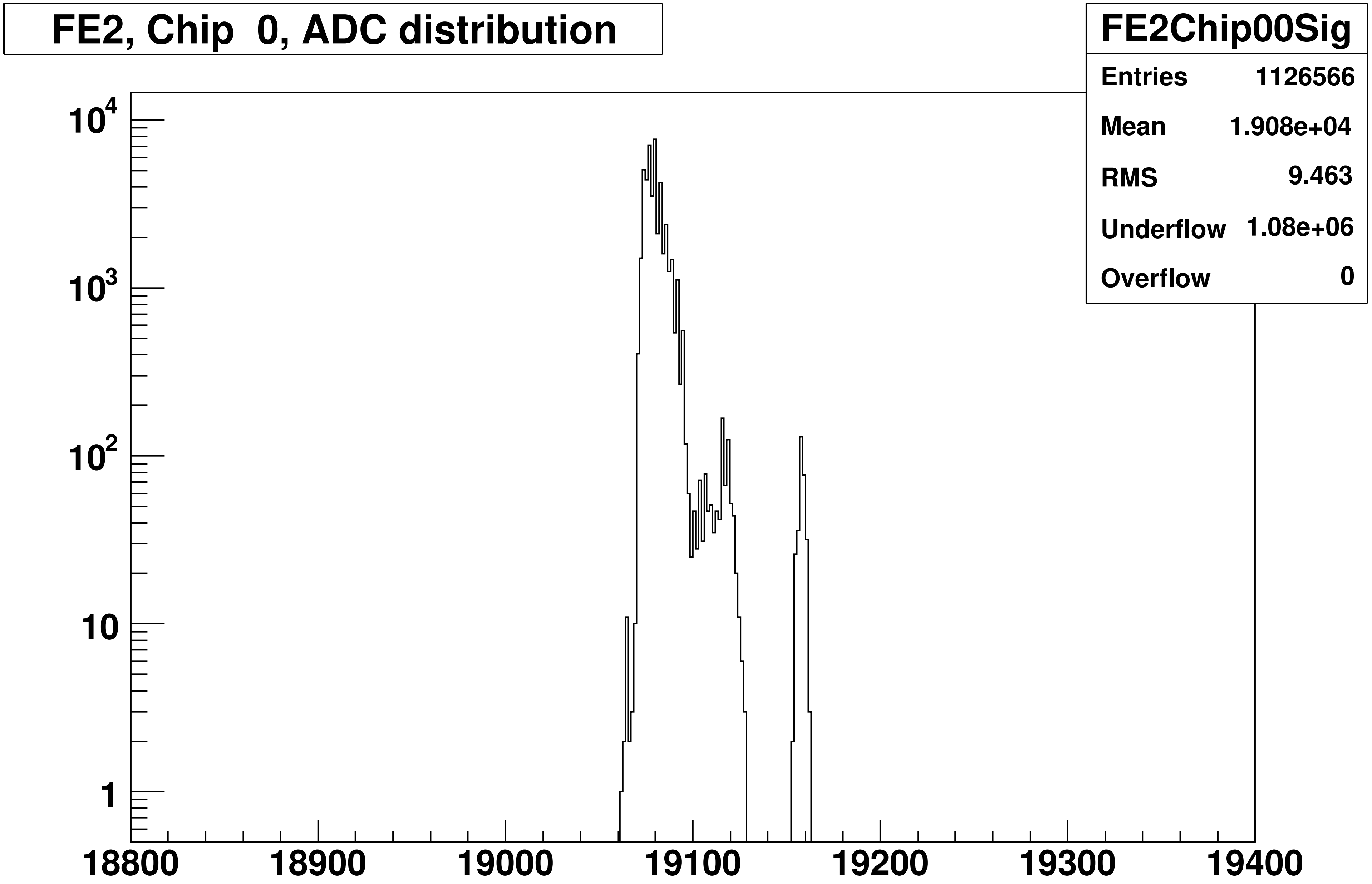
80
100


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 1126566
Mean 1.908e+04
RMS 9.463
Underflow 1.08e+06
Overflow 0
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10^3
10^2
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18900
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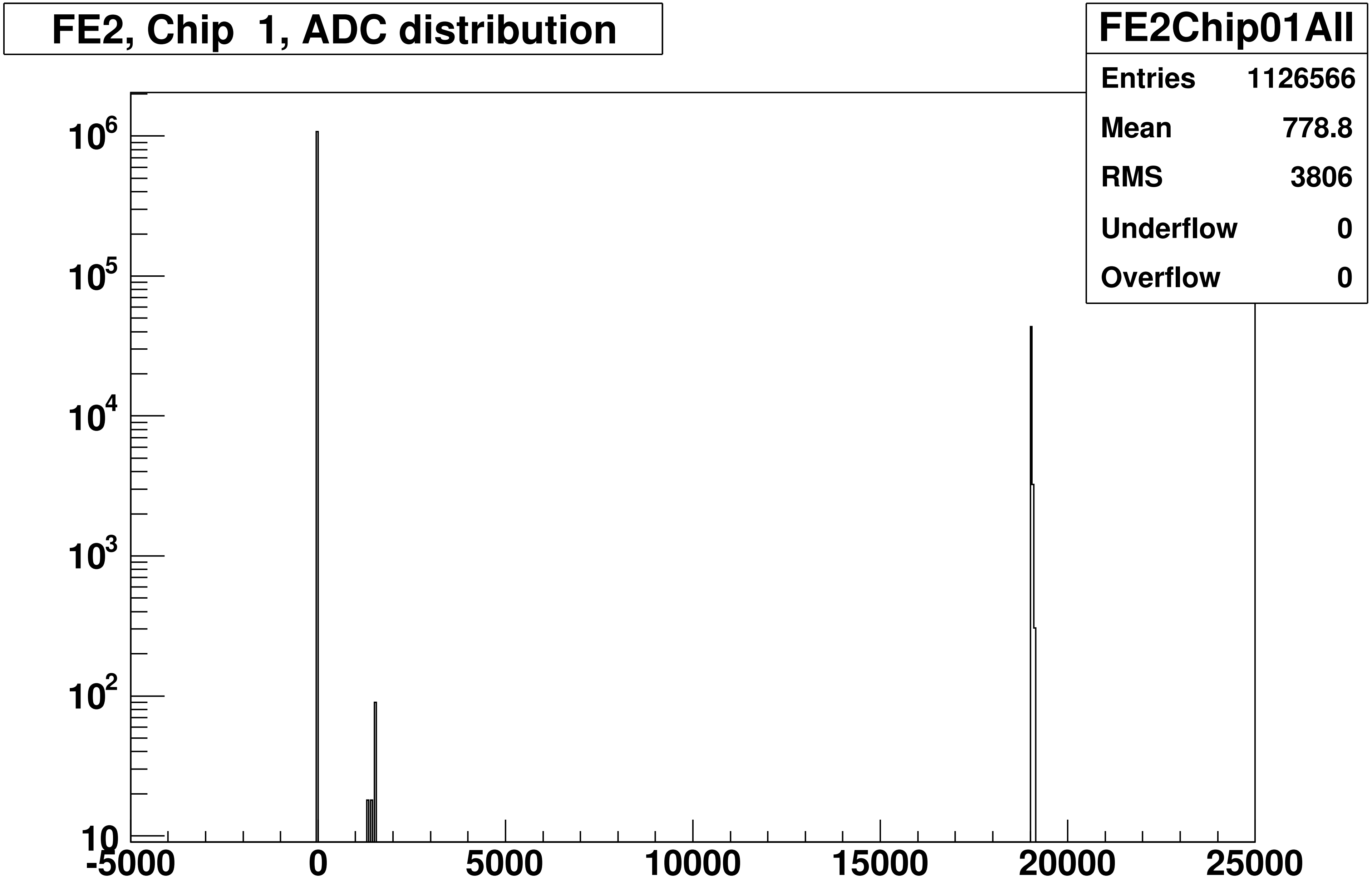
FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 1126566
Mean 778.8
RMS 3806
Underflow 0
Overflow 0
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10^3
10^4
10^5
10^6
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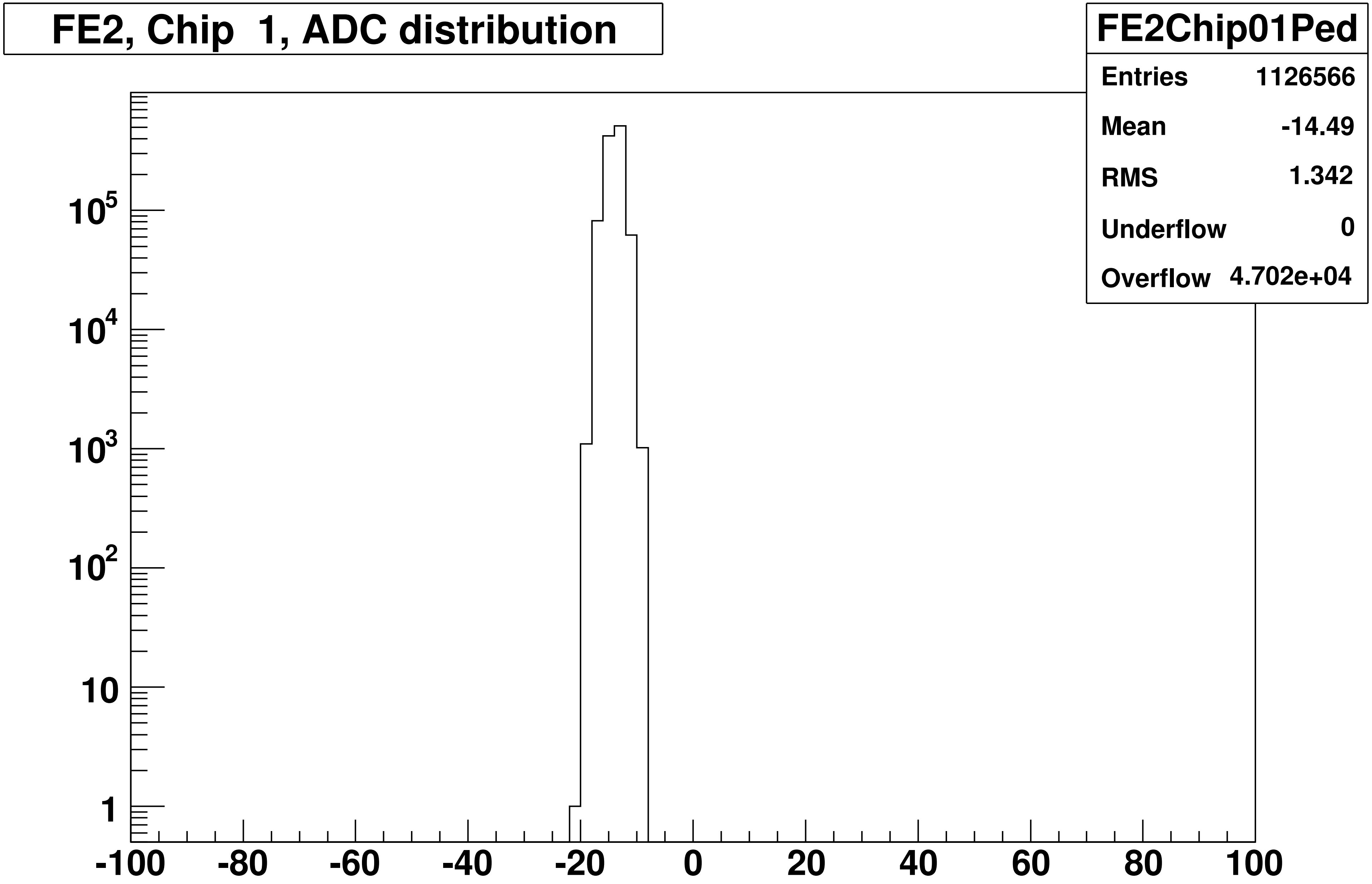
FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 1126566
Mean -14.49
RMS 1.342
Underflow 0
Overflow 4.702e+04
10^5
10^4
10^3
10^2
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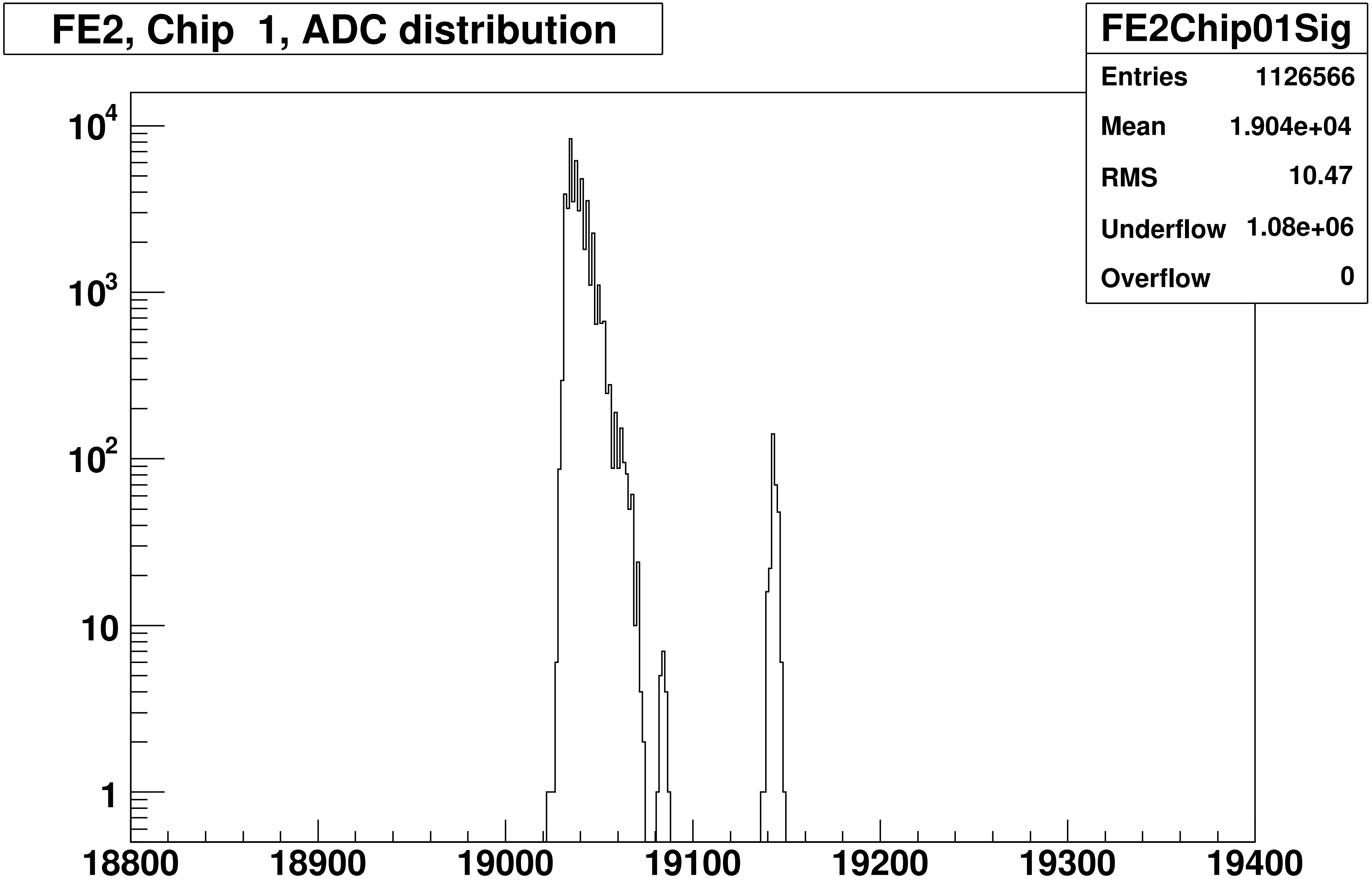
FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 1126566
Mean 1.904e+04
RMS 10.47
Underflow 1.08e+06
Overflow 0
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10^3
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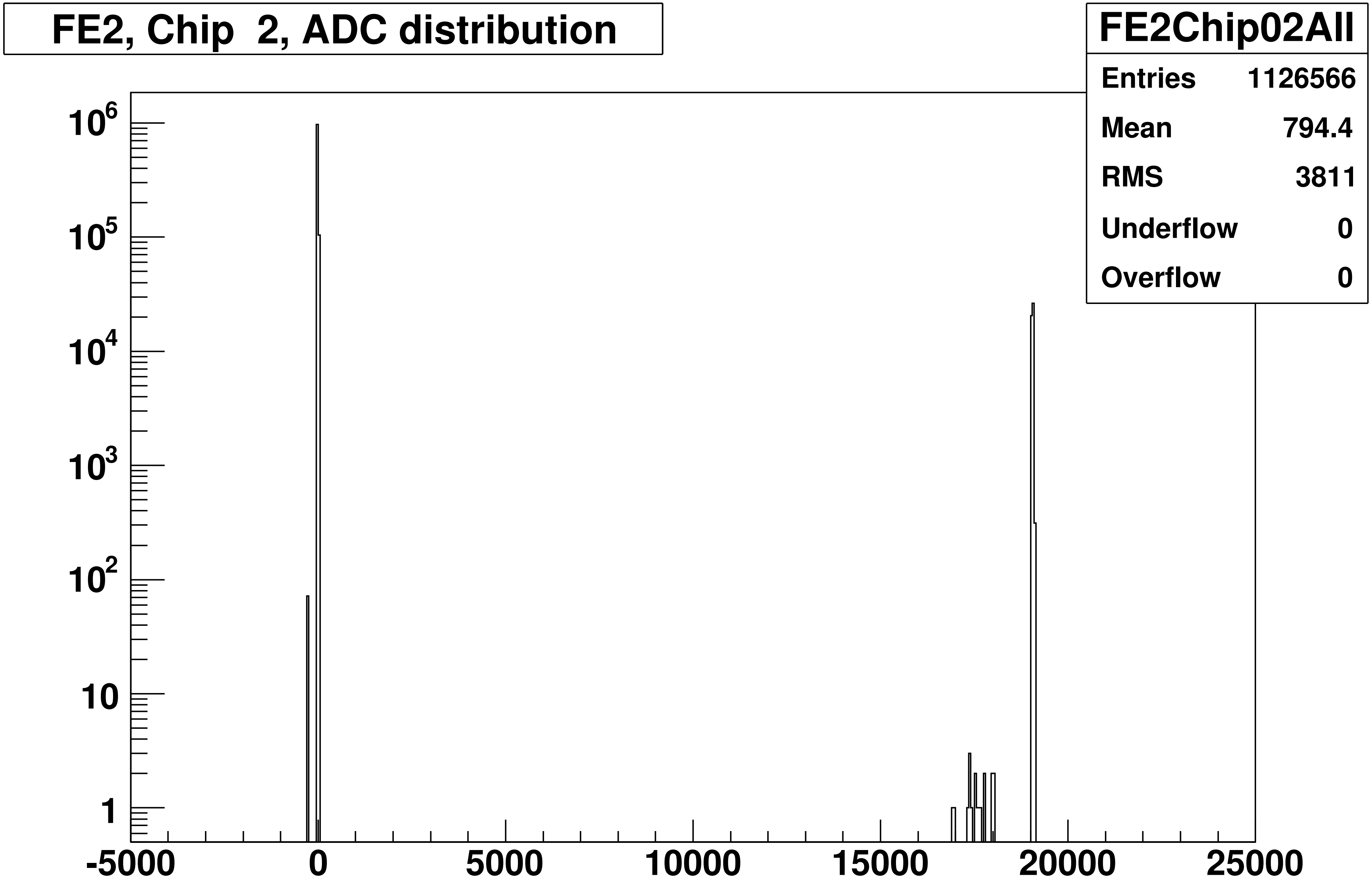

FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 1126566
Mean 794.4
RMS 3811
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
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10000
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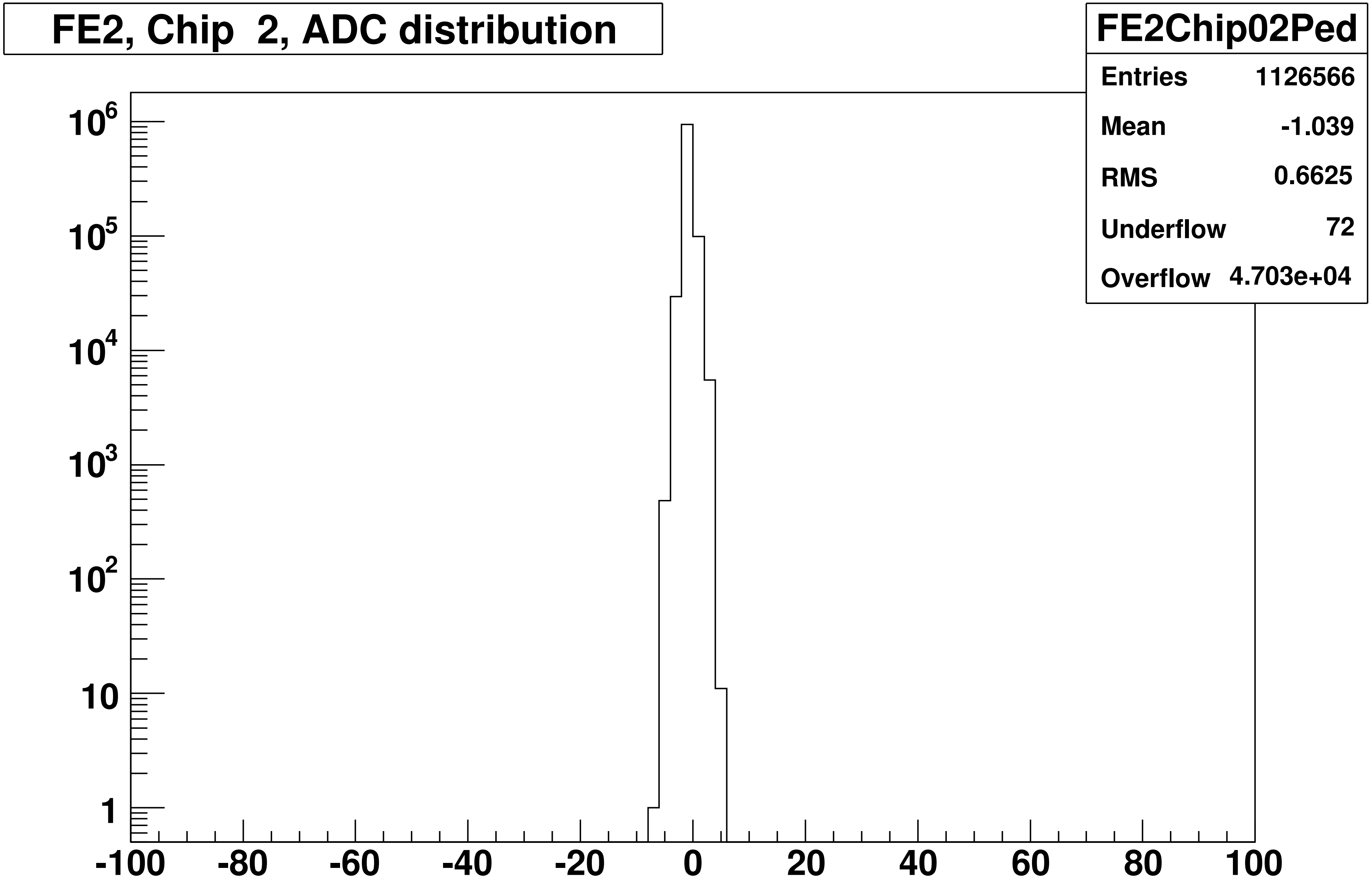
20000
25000


FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 1126566
Mean -1.039
RMS 0.6625
Underflow 72
Overflow 4.703e+04
10^6
10^5
10^4
10^3
10^2
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1
-100
-80
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-40
-20
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20
40
60
80
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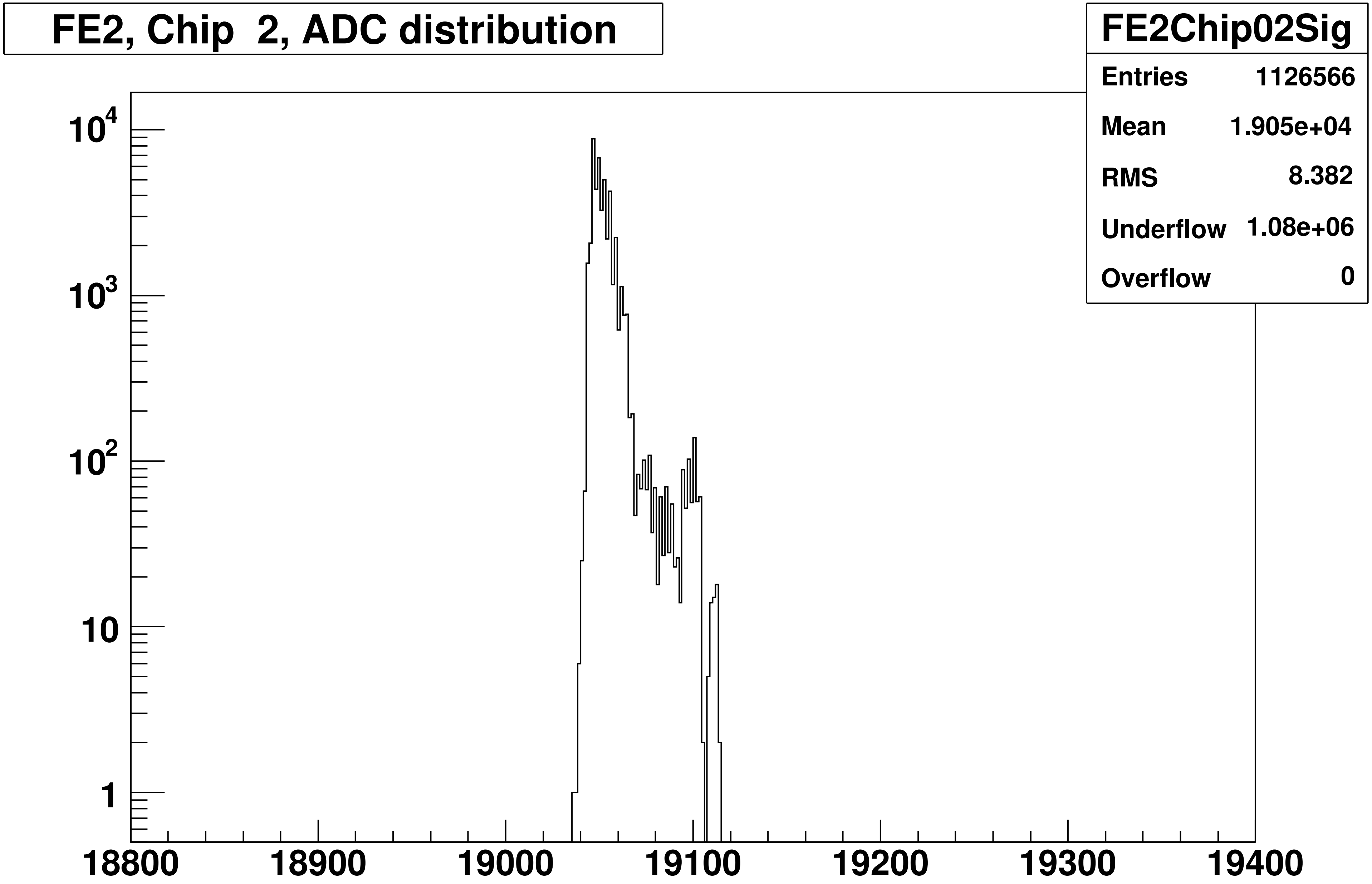
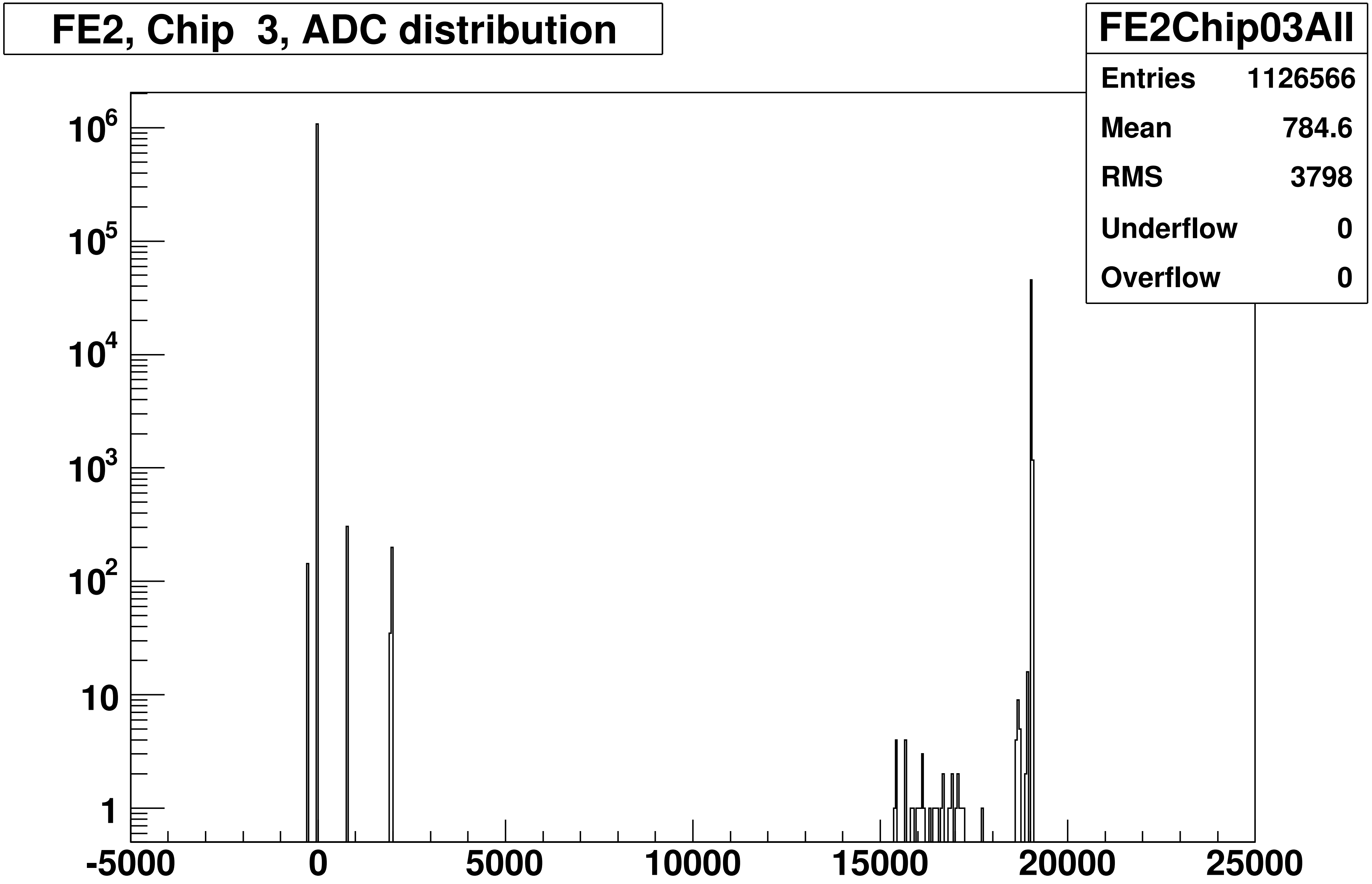

FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 1126566
Mean 1.905e+04
RMS 8.382
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 1126566
Mean 784.6
RMS 3798
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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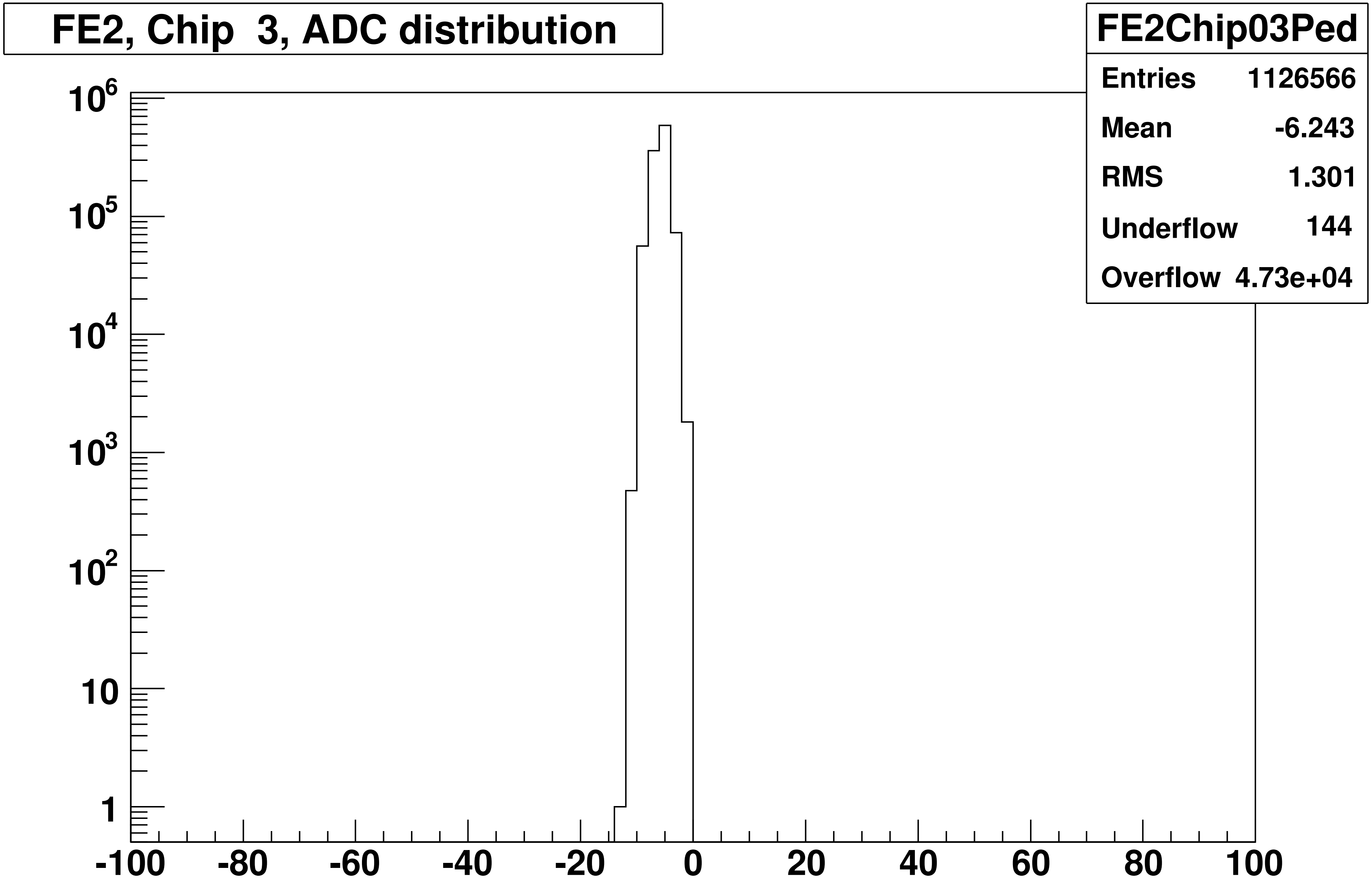
10000
15000
20000
25000


FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 1126566
Mean -6.243
RMS 1.301
Underflow 144
Overflow 4.73e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
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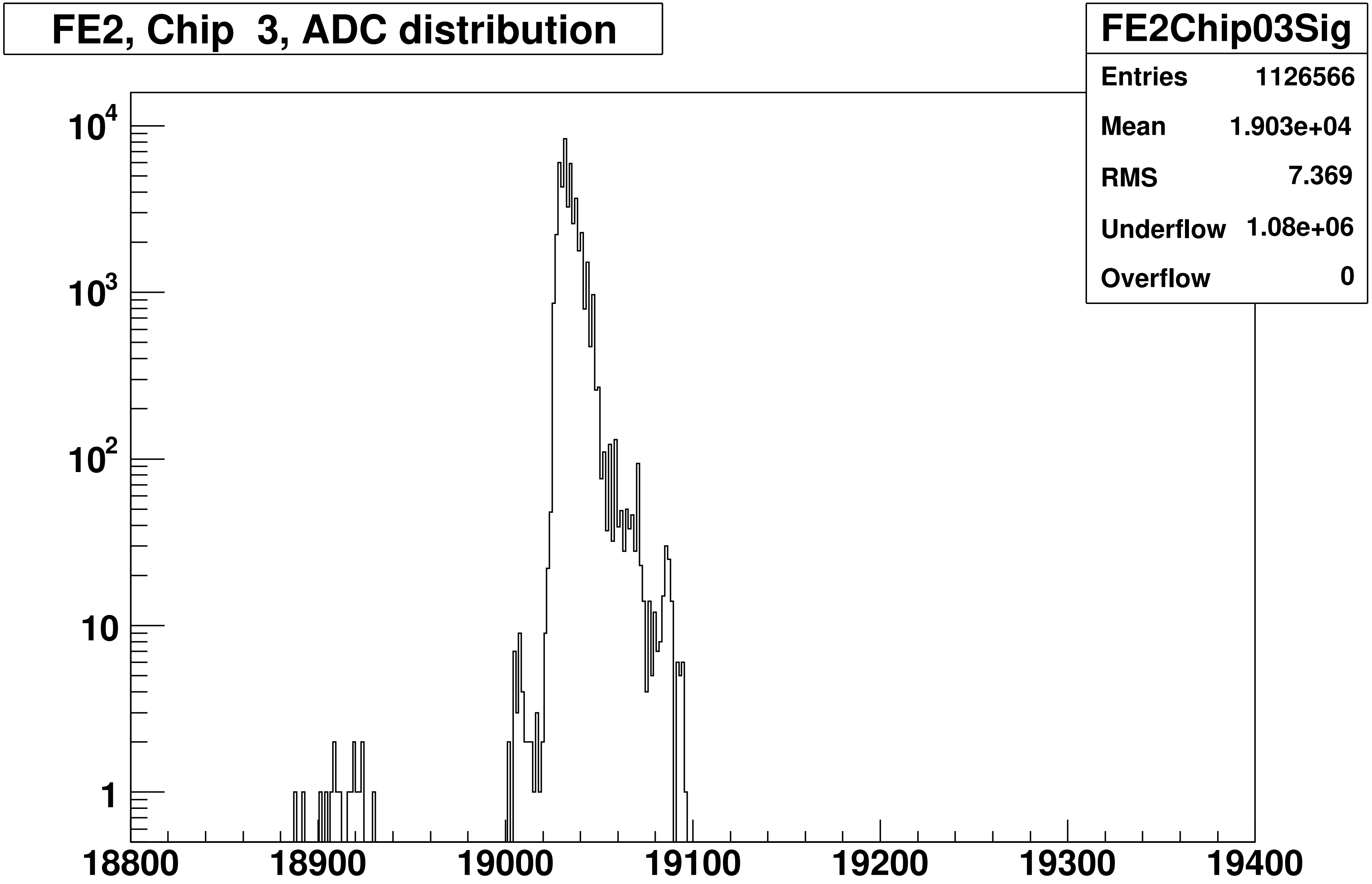
40
60
80
100



FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 1126566
Mean 1.903e+04
RMS 7.369
Underflow 1.08e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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19100
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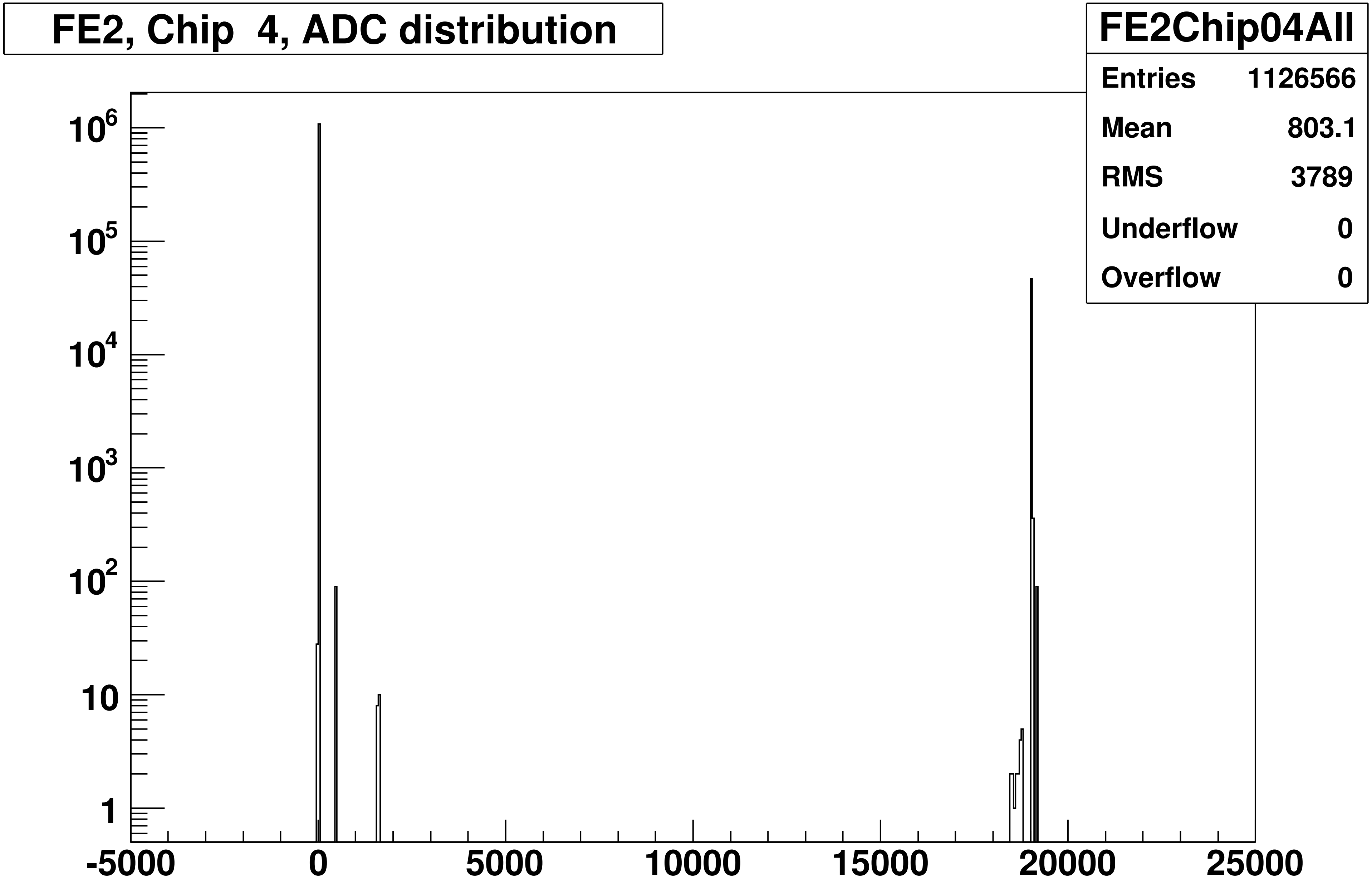
19400


FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 1126566
Mean 803.1
RMS 3789
Underflow 0
Overflow 0
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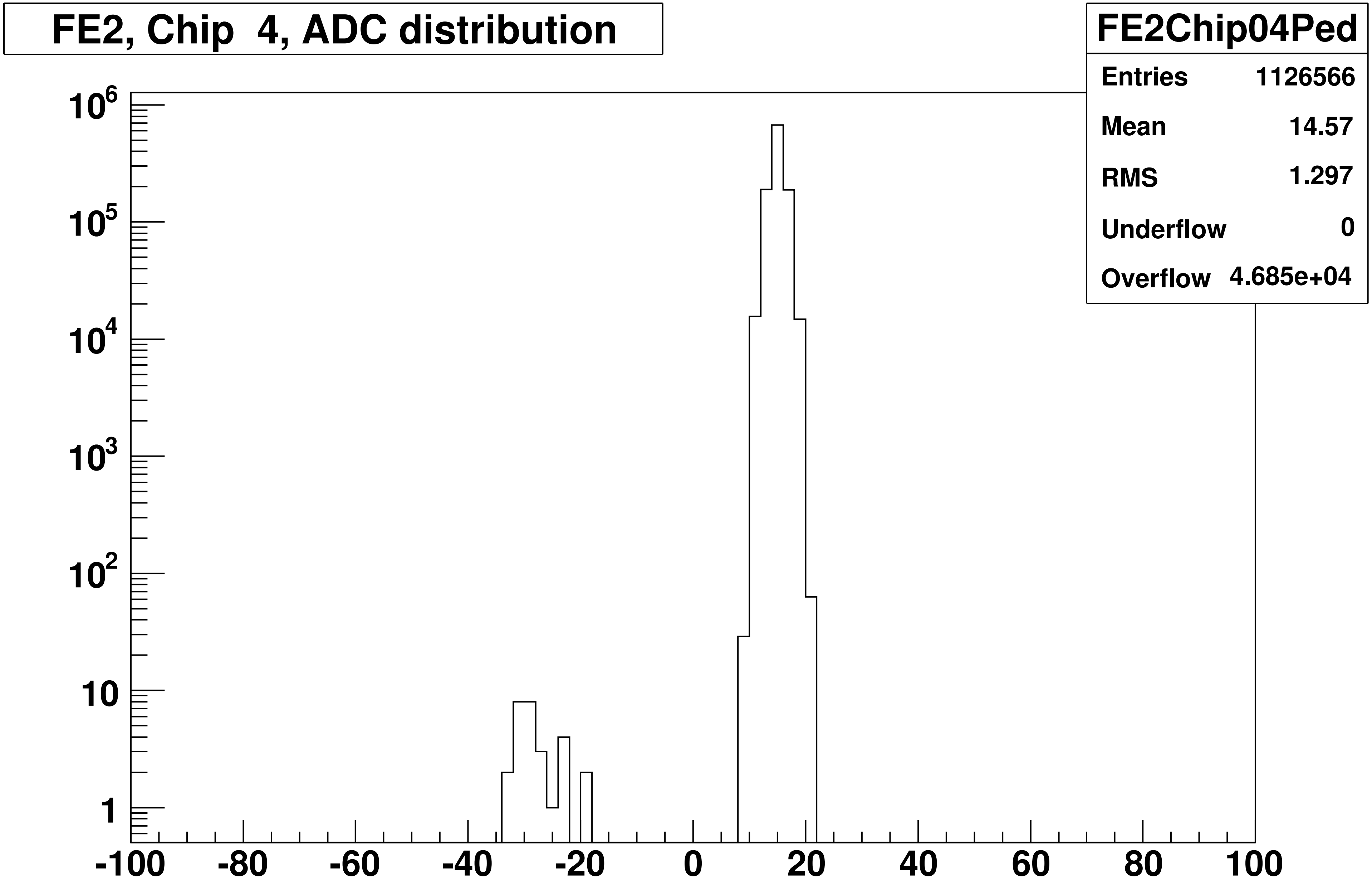
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 1126566
Mean 14.57
RMS 1.297
Underflow 0
Overflow 4.685e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
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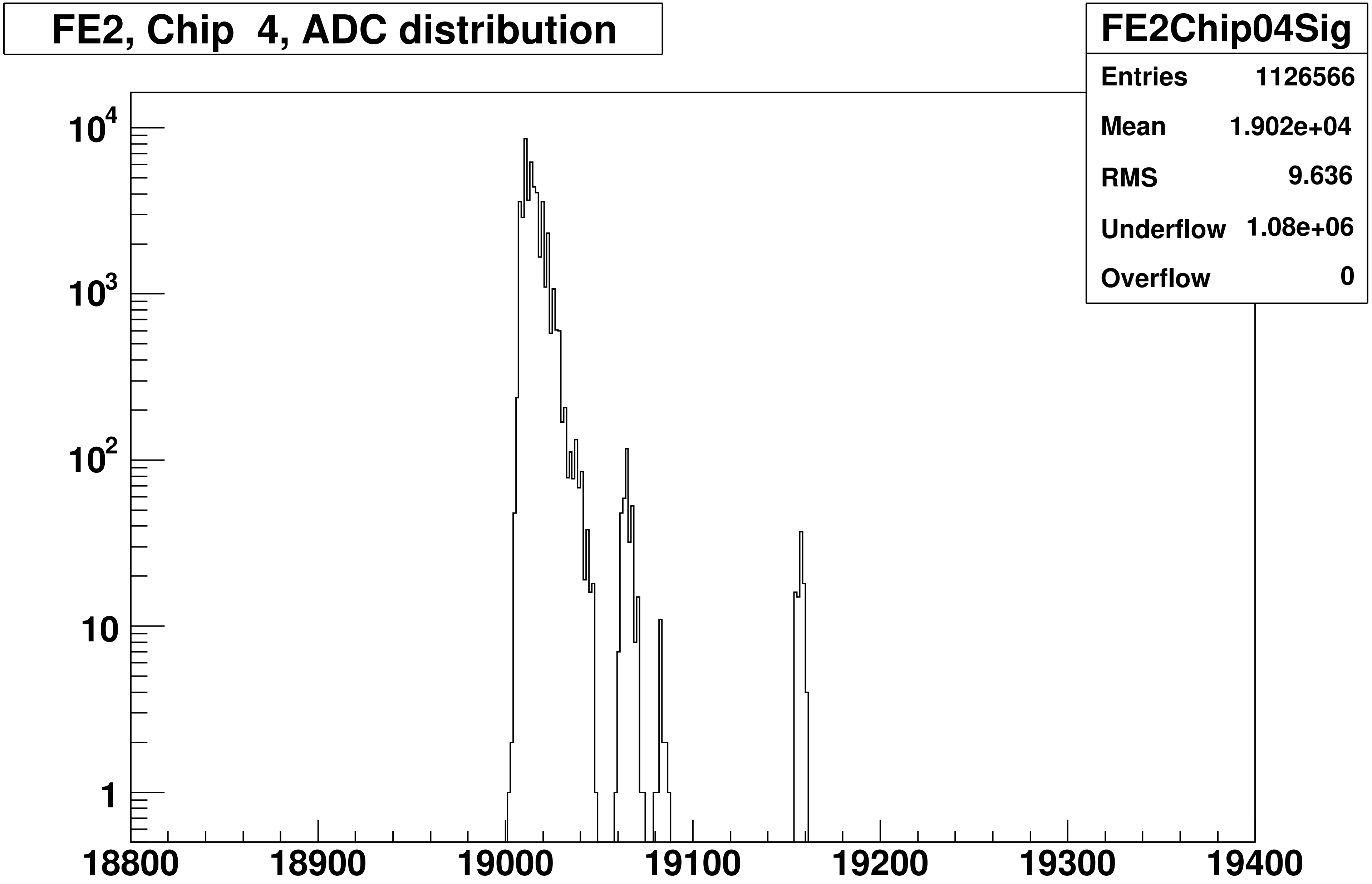
-20
0
20
40
60
80
100


FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 1126566
Mean 1.902e+04
RMS 9.636
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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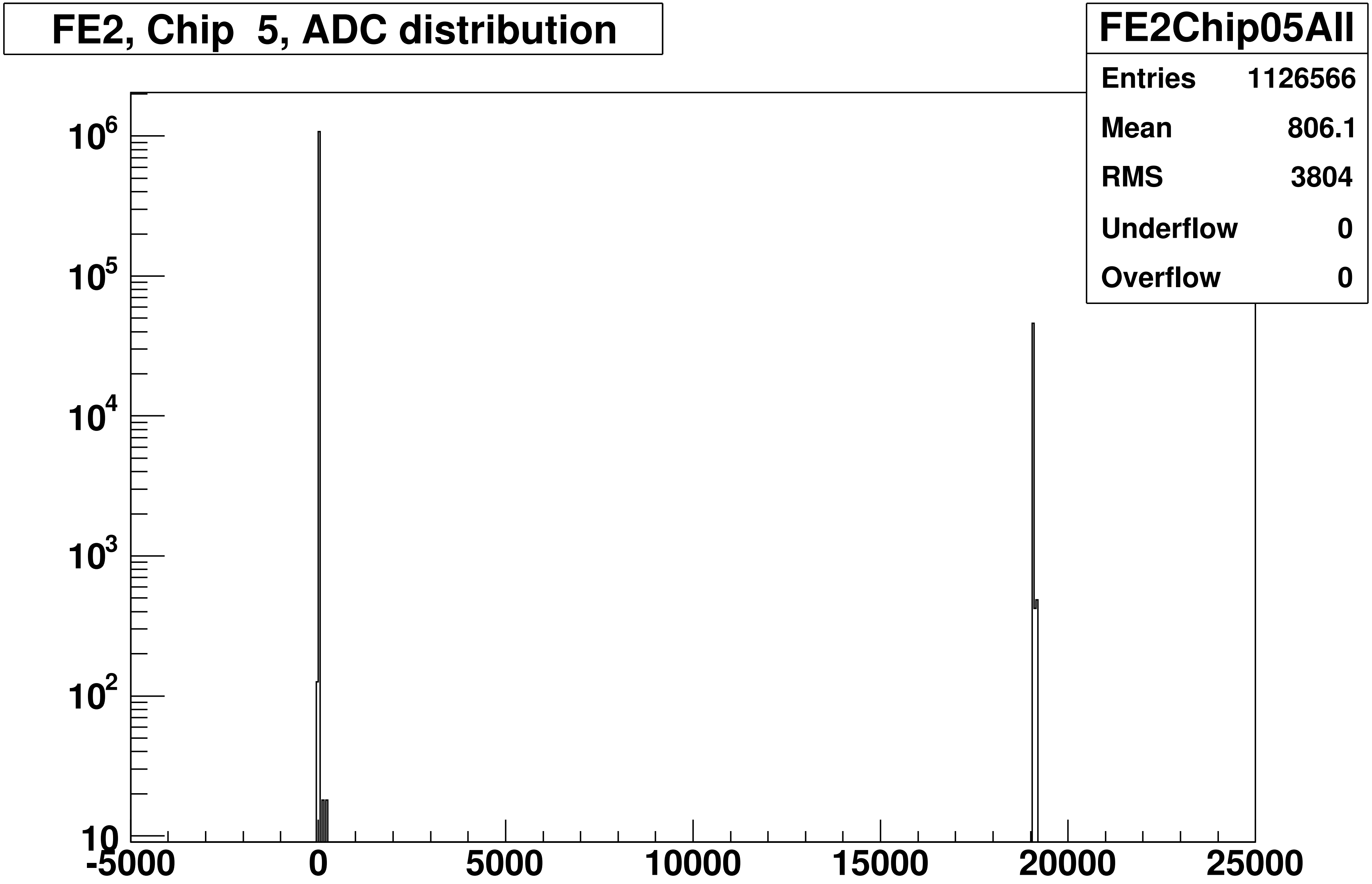
19100
19200
19300
19400



FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 1126566
Mean 806.1
RMS 3804
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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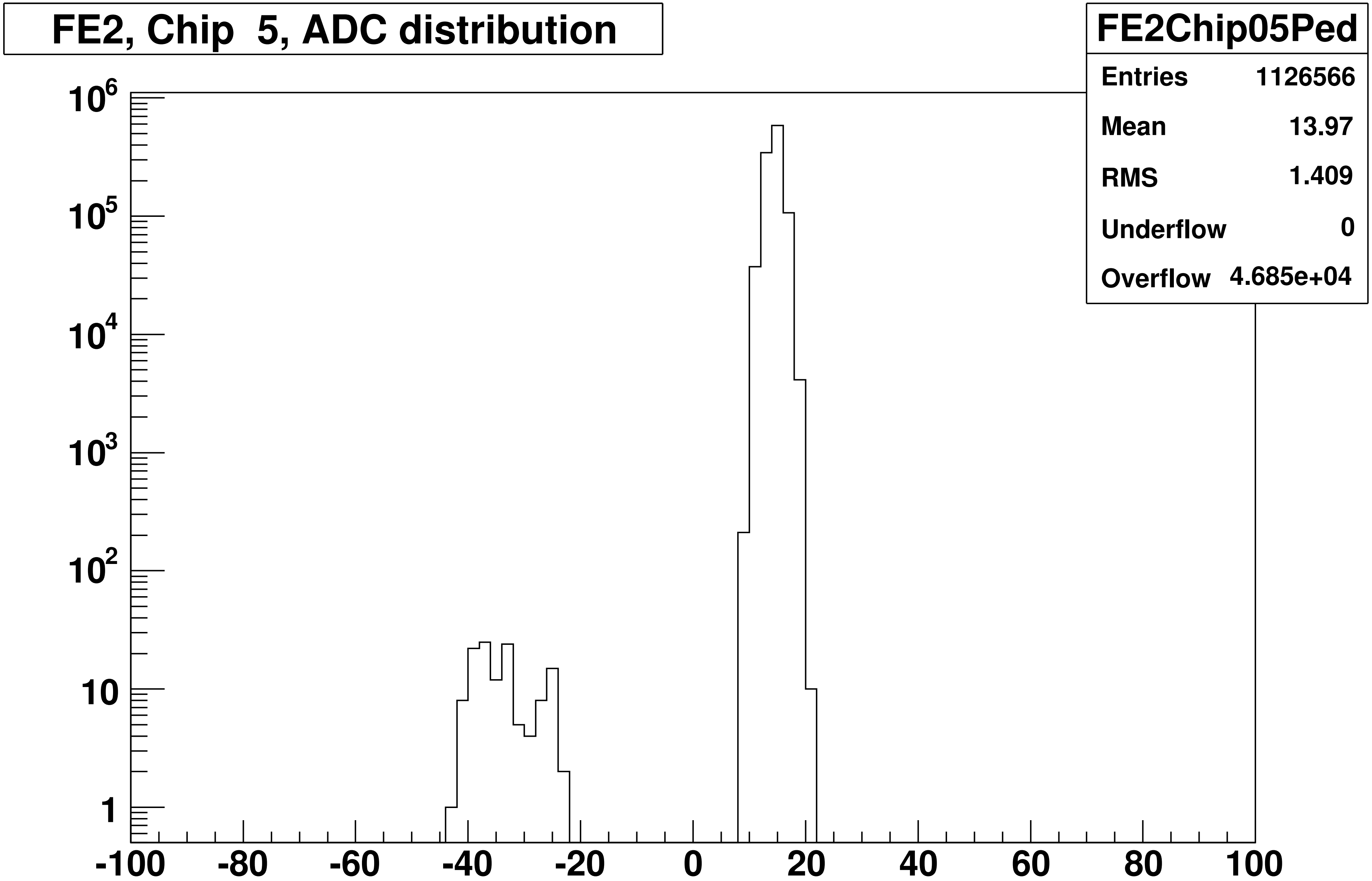
10000
15000
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 1126566
Mean 13.97
RMS 1.409
Underflow 0
Overflow 4.685e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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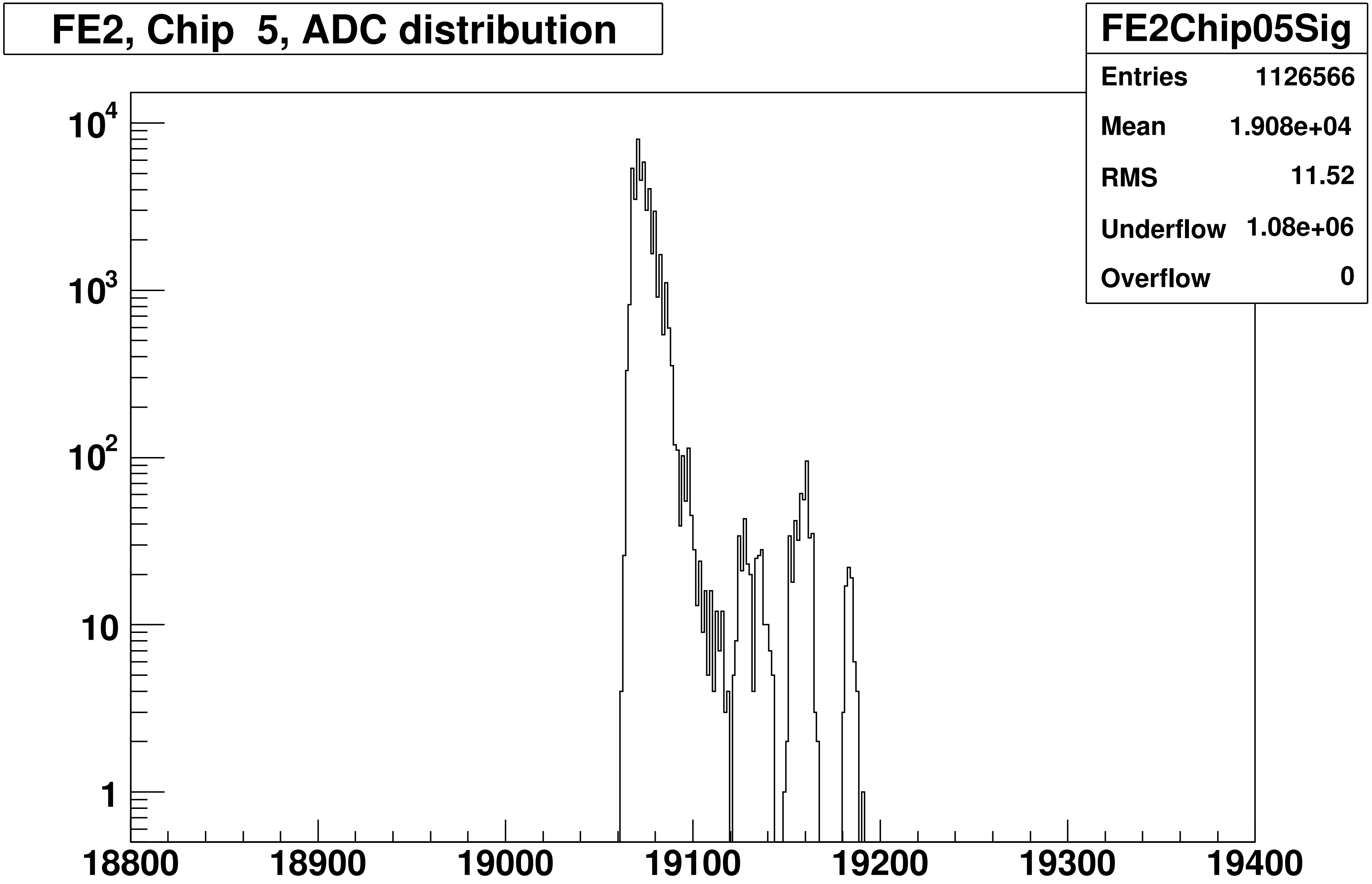
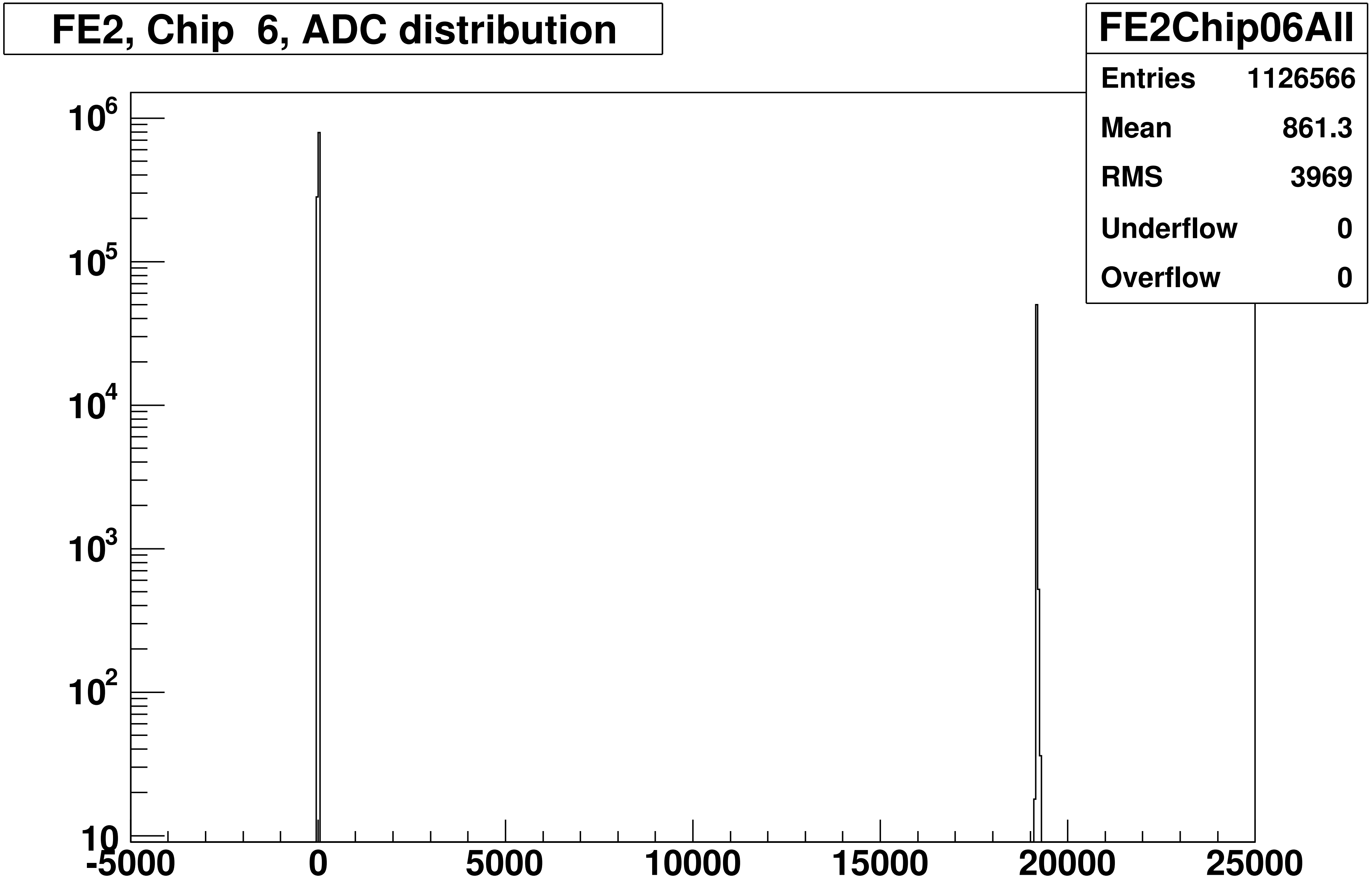
-20
0
20
40
60
80
100



FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 1126566
Mean 1.908e+04
RMS 11.52
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 1126566
Mean 861.3
RMS 3969
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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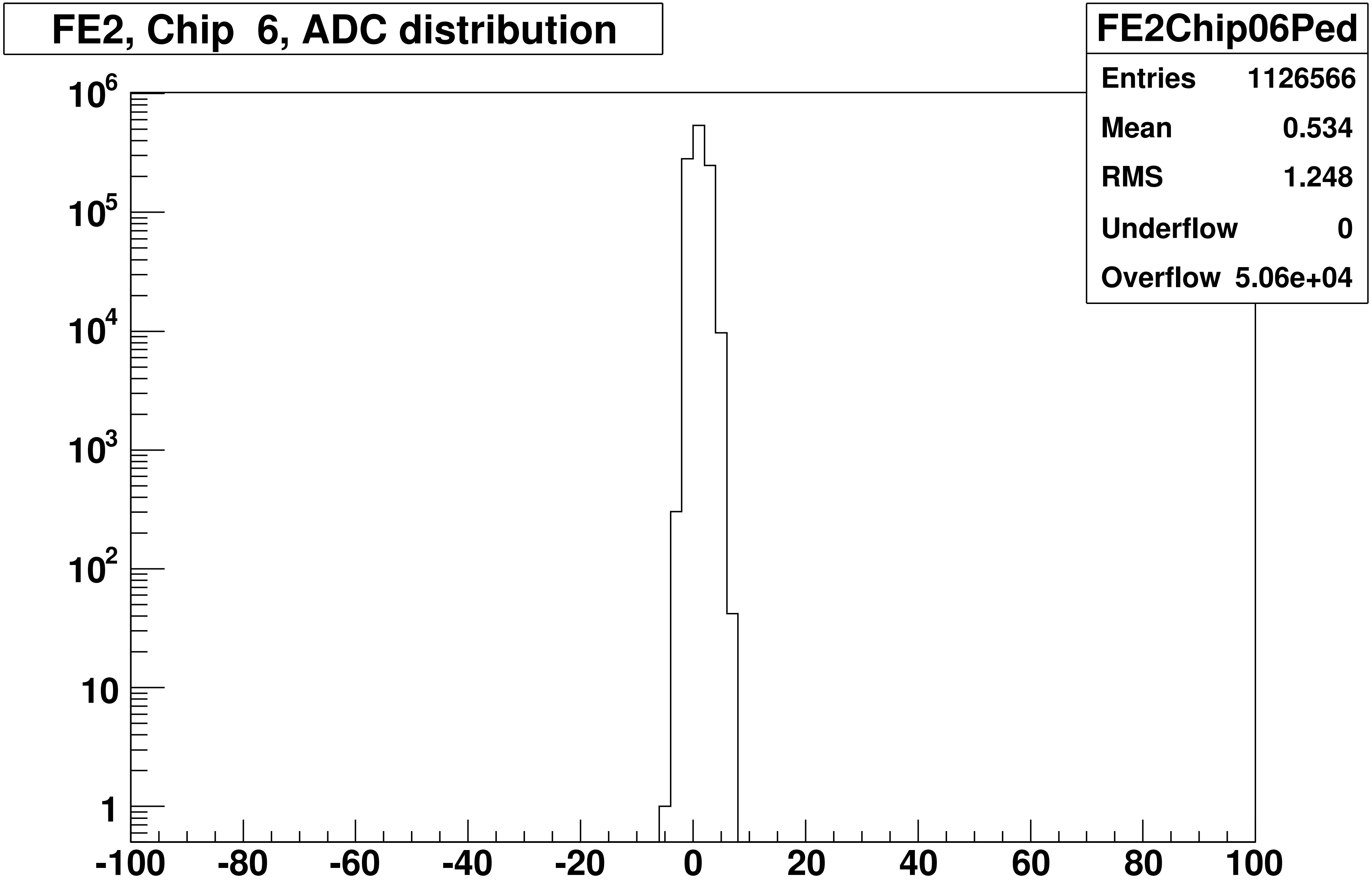
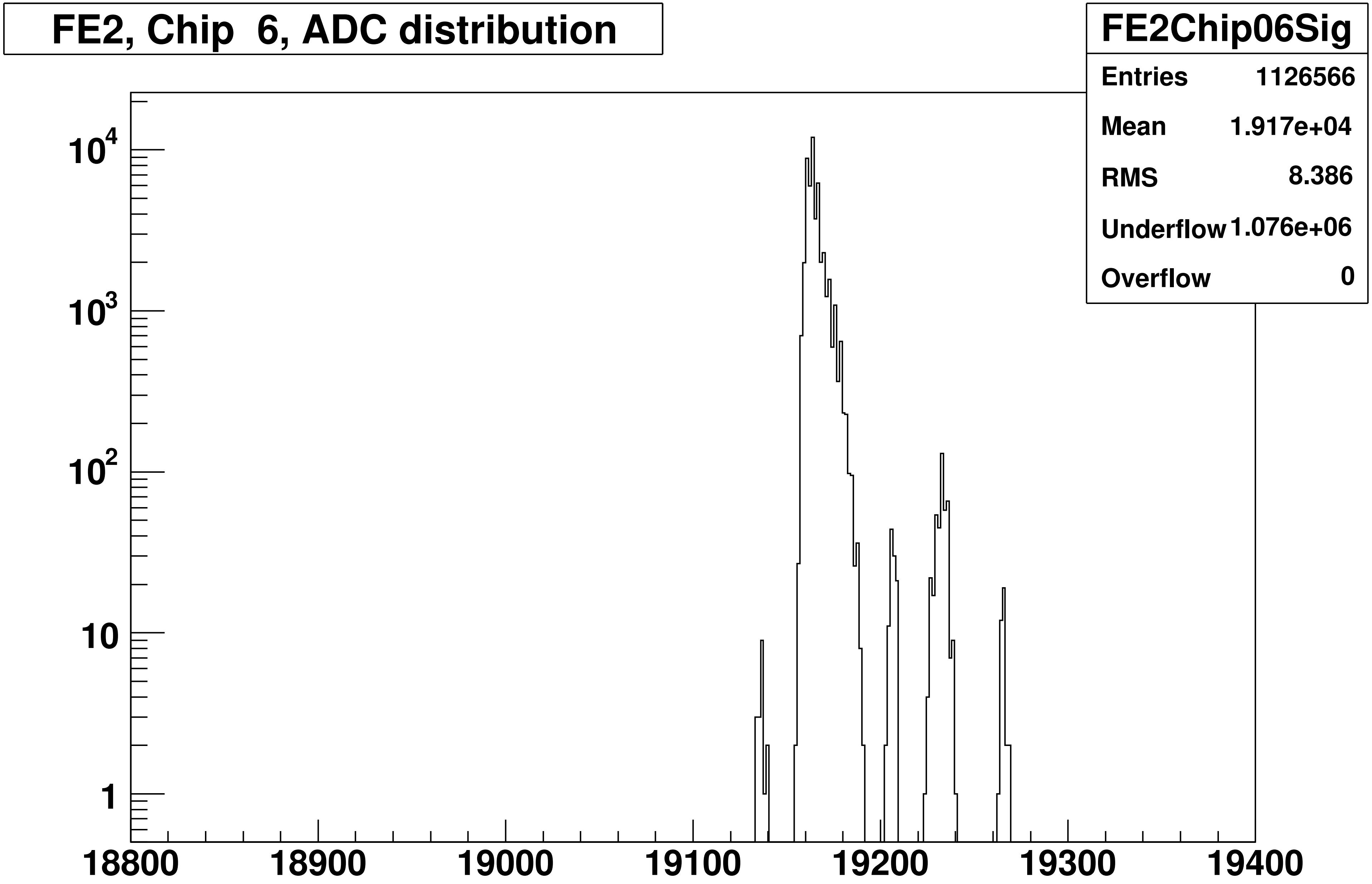
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 1126566
Mean 0.534
RMS 1.248
Underflow 0
Overflow 5.06e+04
1
10
10^2
10^3
10^4
10^5
10^6
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 1126566
Mean 1.917e+04
RMS 8.386
Underflow 1.076e+06
Overflow 0
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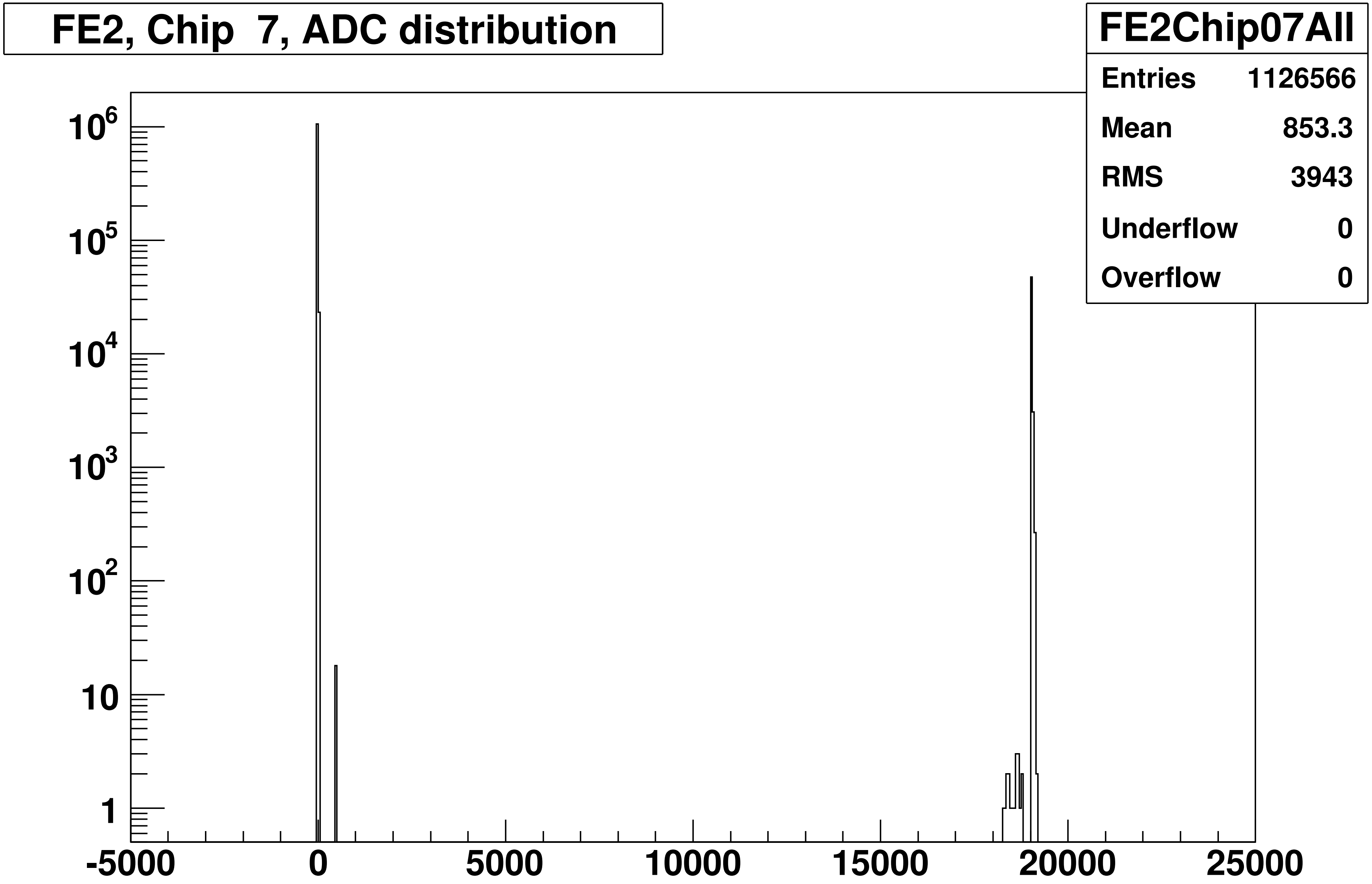
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 1126566
Mean 853.3
RMS 3943
Underflow 0
Overflow 0
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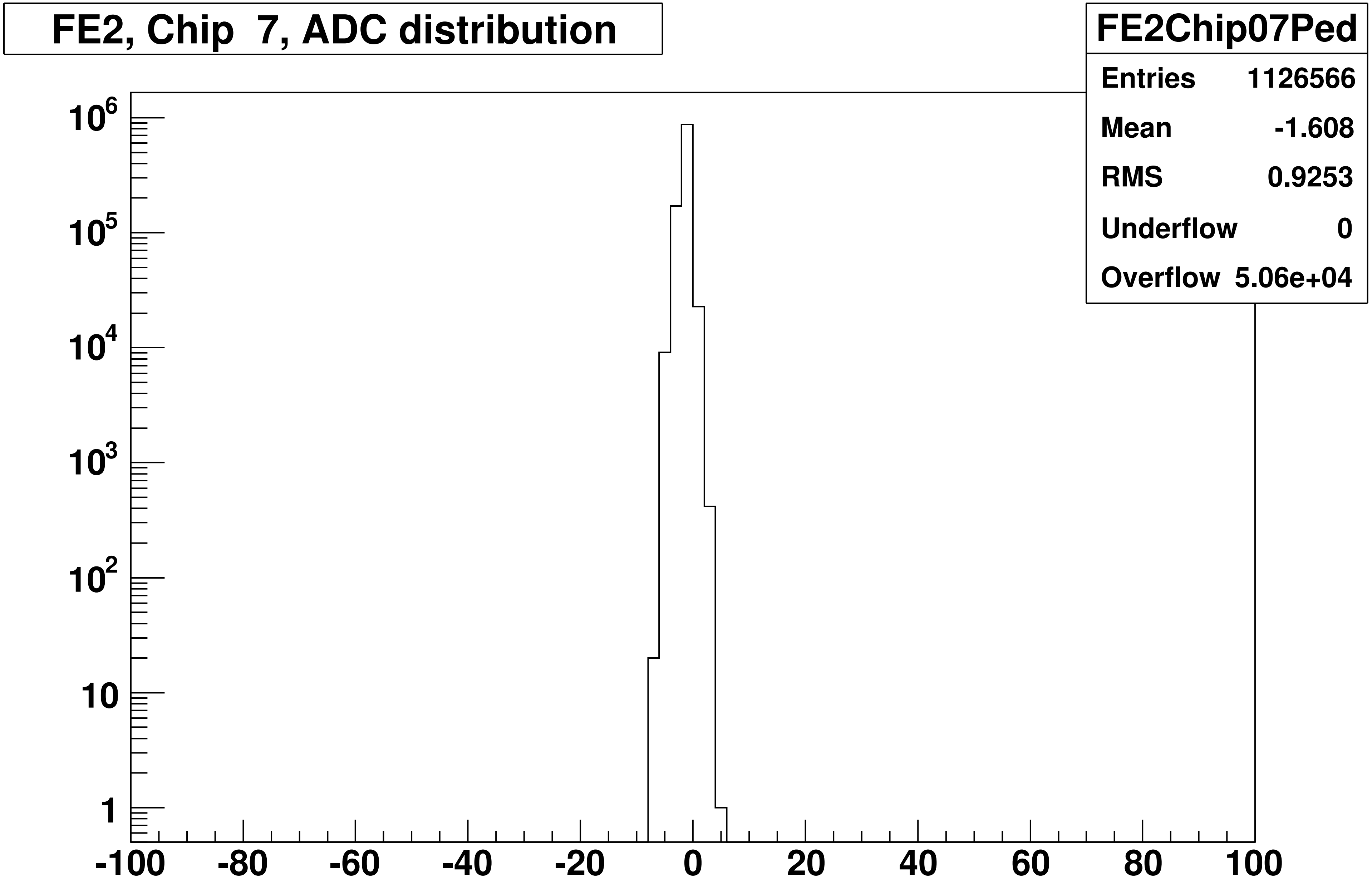
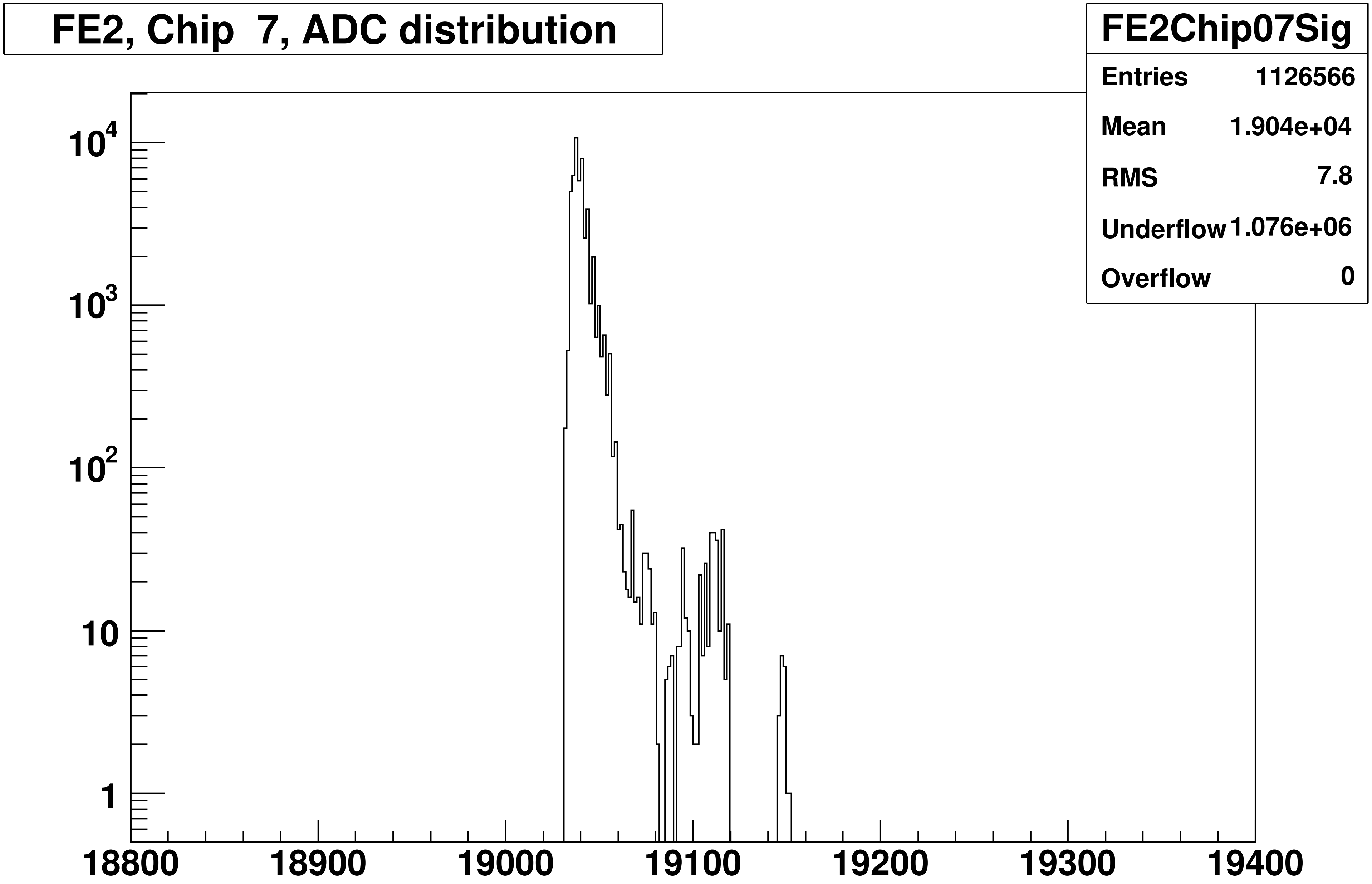
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 1126566
Mean -1.608
RMS 0.9253
Underflow 0
Overflow 5.06e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 1126566
Mean 1.904e+04
RMS 7.8
Underflow 1.076e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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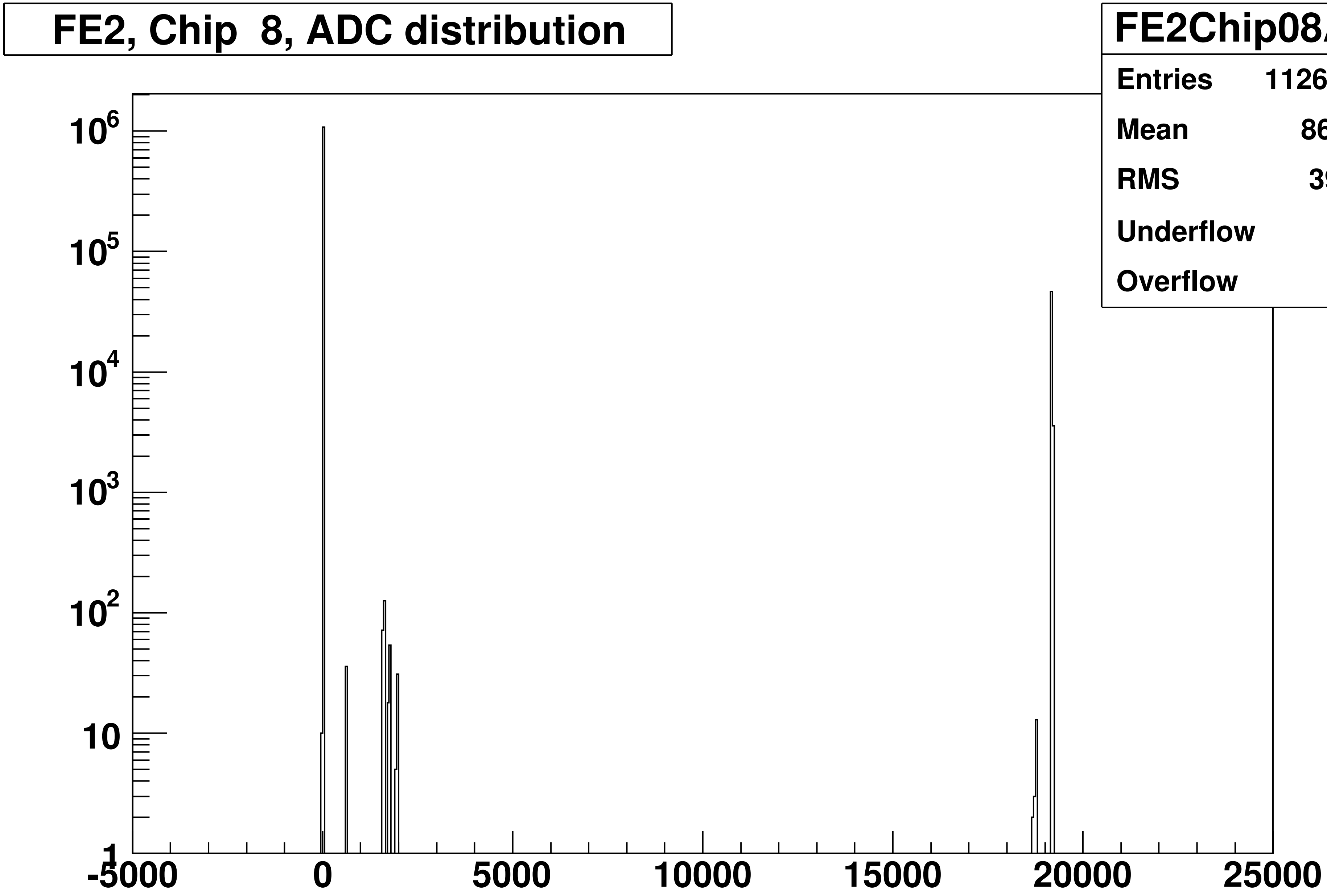
19100
19200
19300
19400



FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 1126566
Mean 860.7
RMS 3959
Underflow 0
Overflow 0
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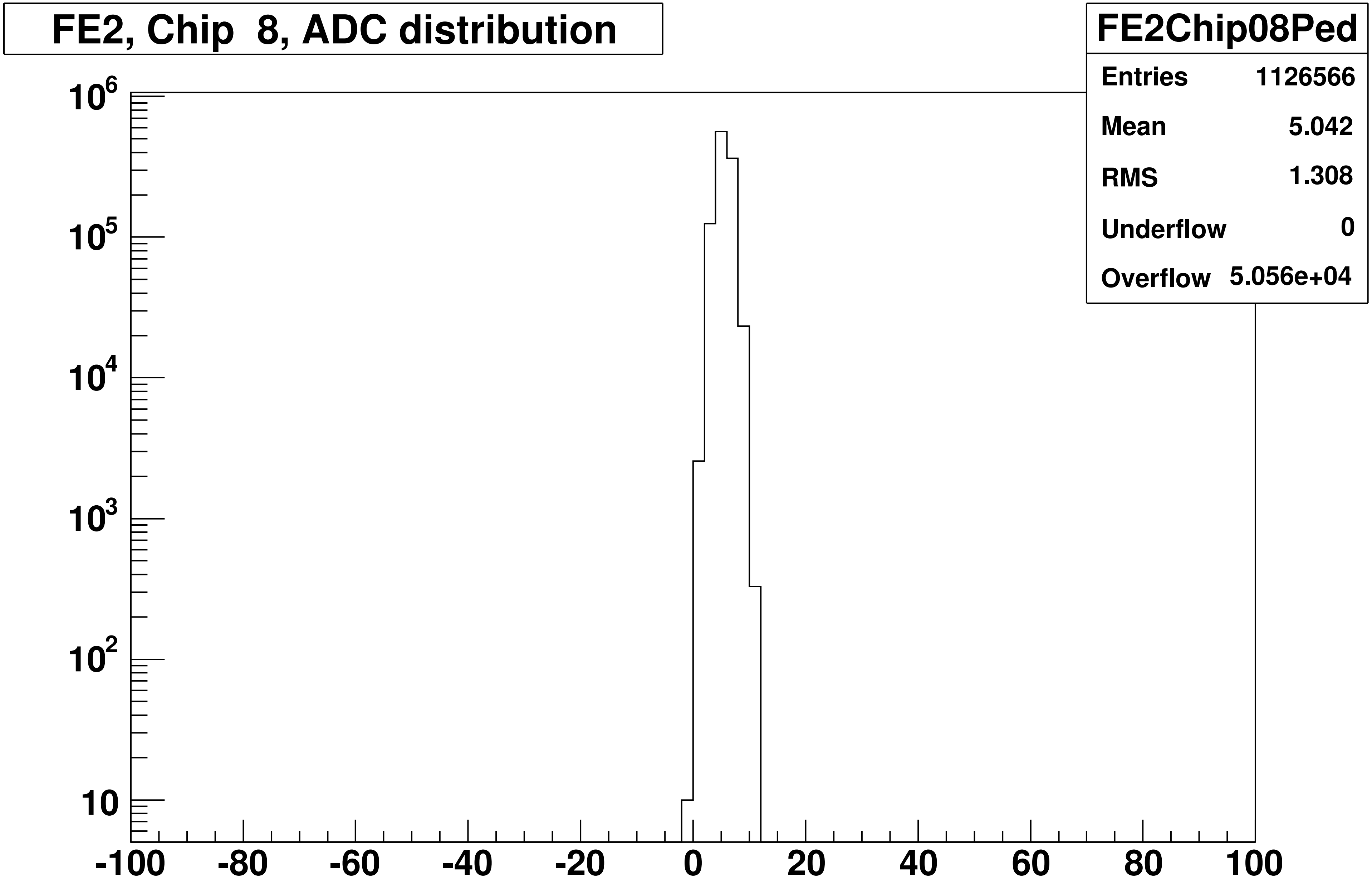
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 1126566
Mean 5.042
RMS 1.308
Underflow 0
Overflow 5.056e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
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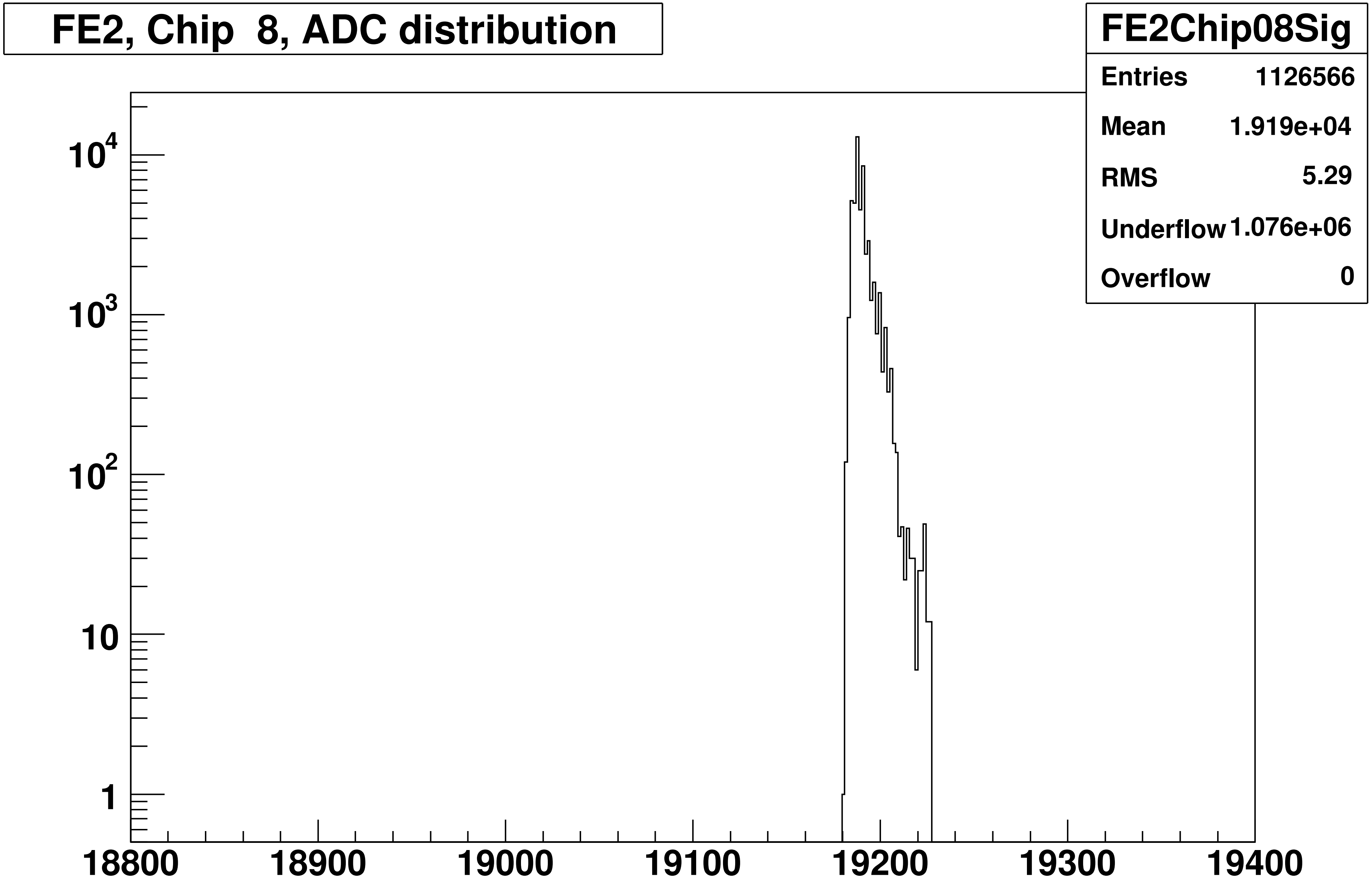
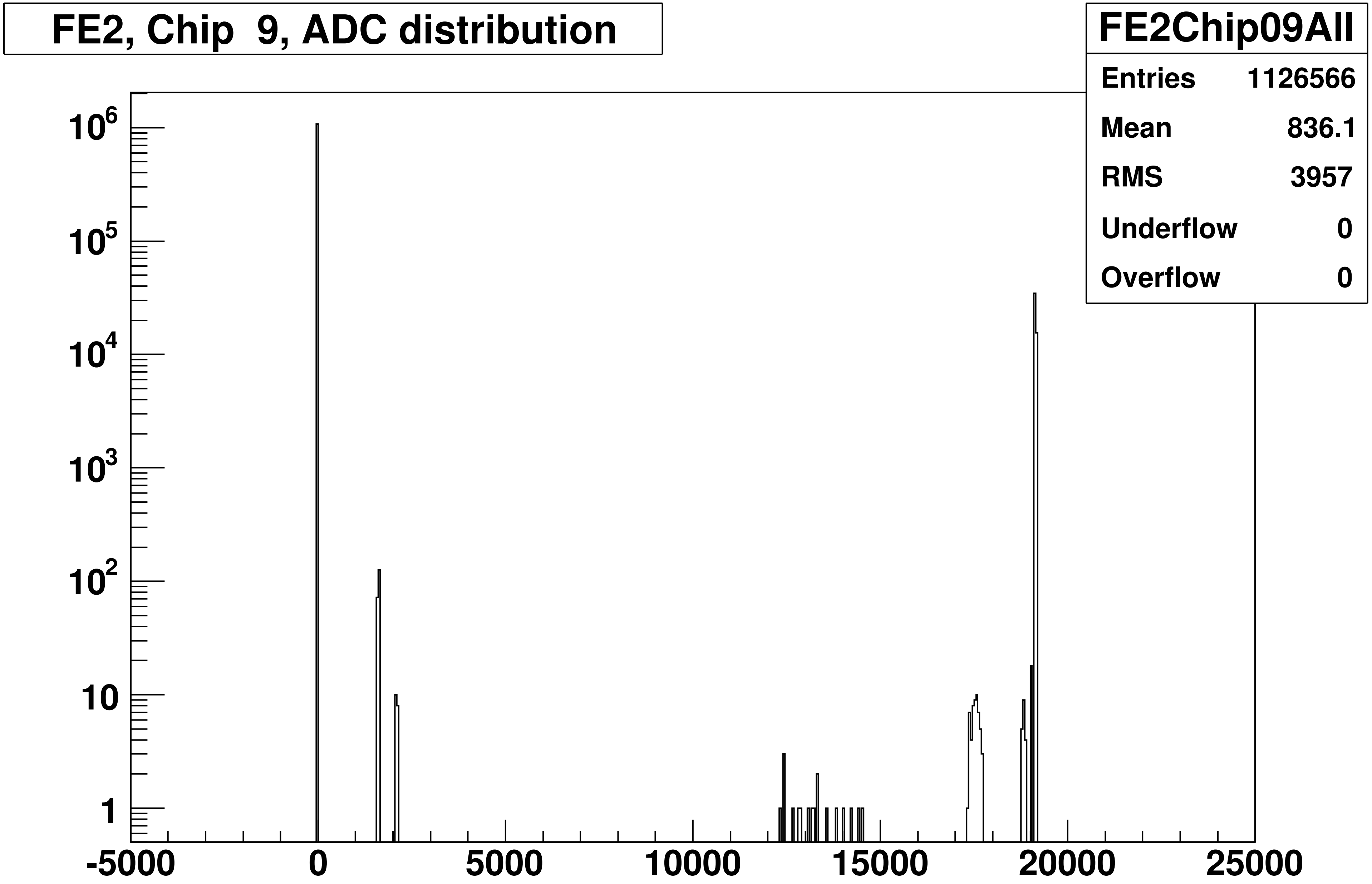


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 1126566
Mean 1.919e+04
RMS 5.29
Underflow 1.076e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 1126566
Mean 836.1
RMS 3957
Underflow 0
Overflow 0
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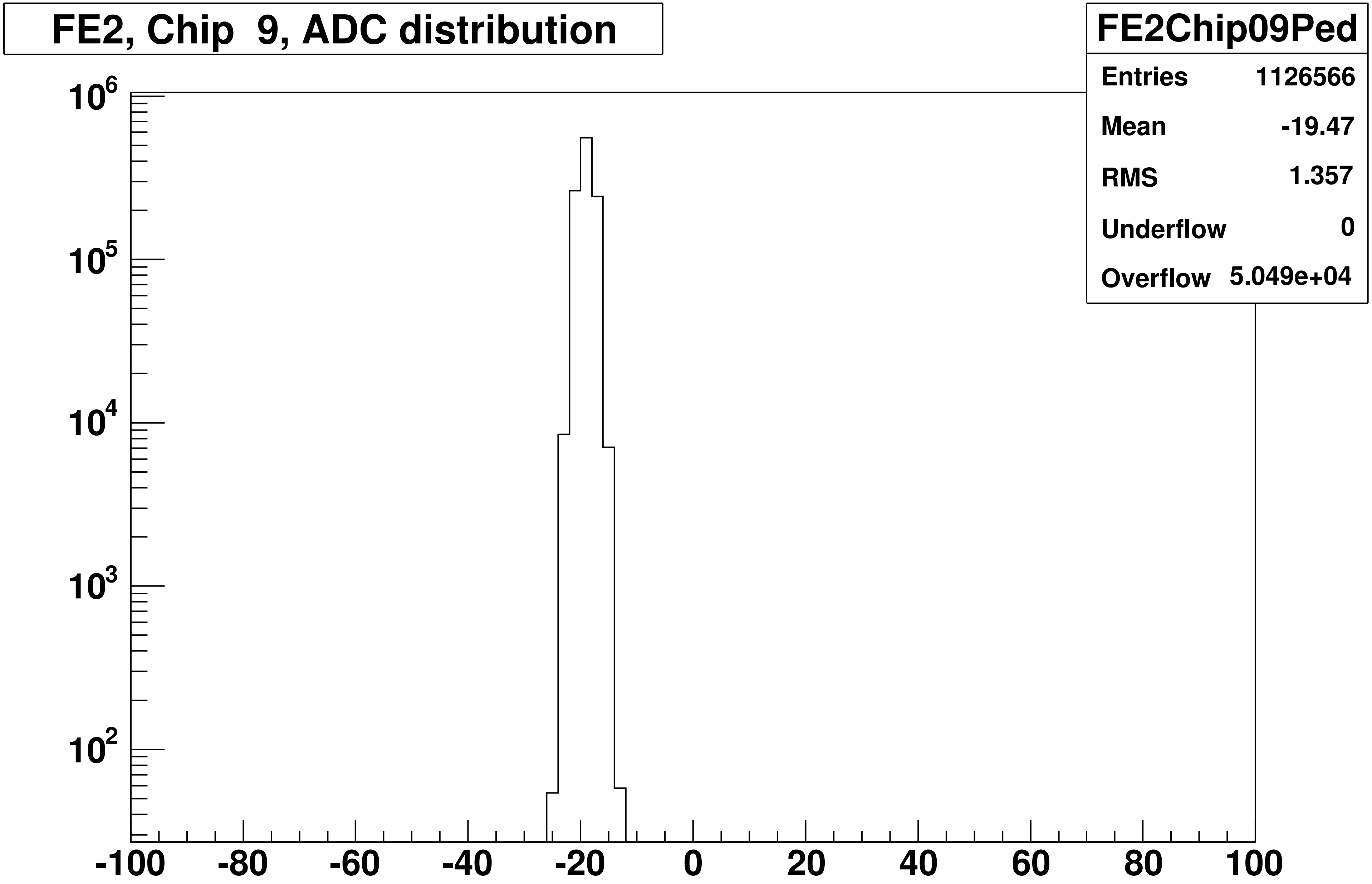
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 1126566
Mean -19.47
RMS 1.357
Underflow 0
Overflow 5.049e+04
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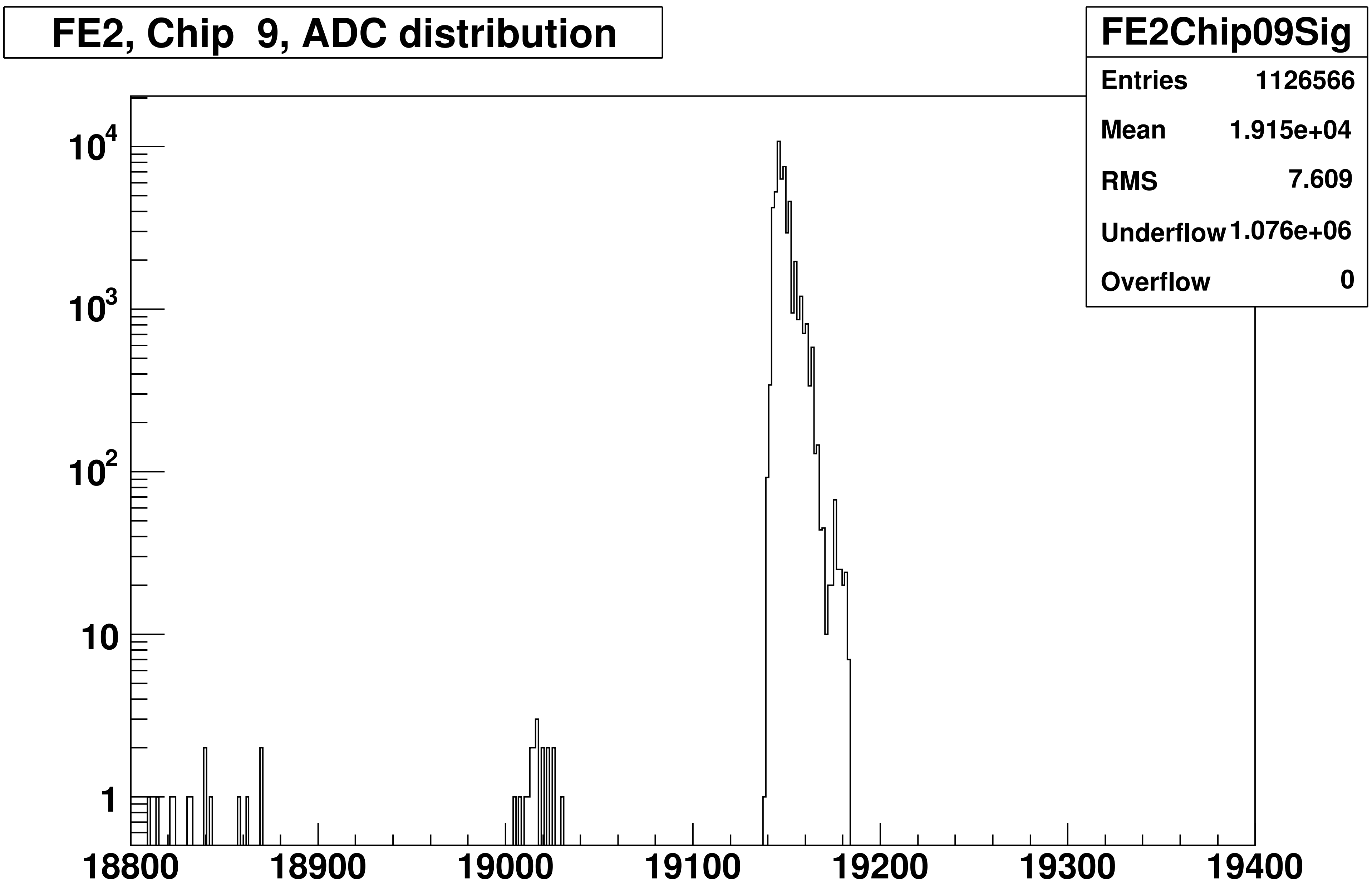
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 1126566
Mean 1.915e+04
RMS 7.609
Underflow 1.076e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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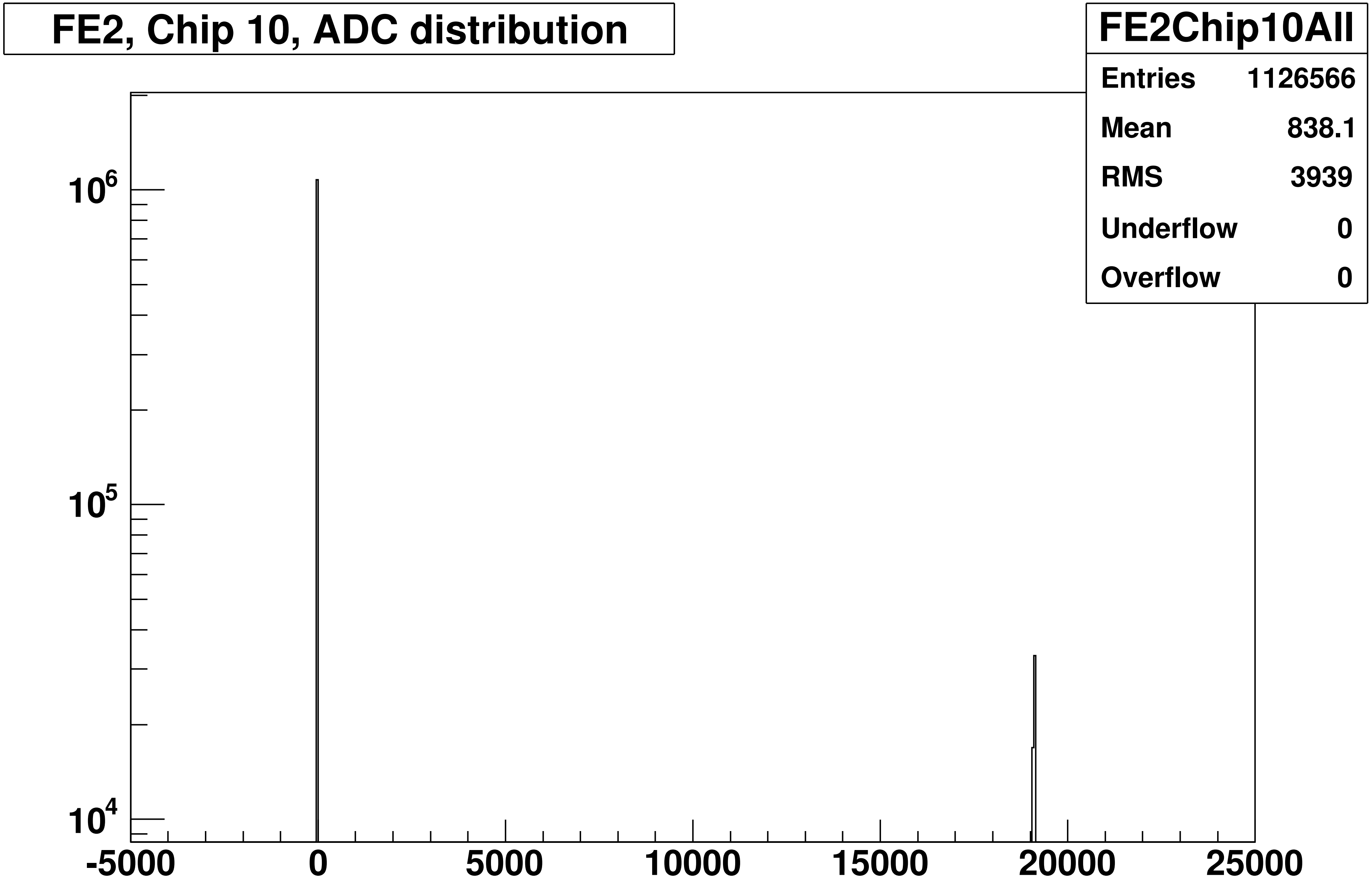

FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 1126566
Mean 838.1
RMS 3939
Underflow 0
Overflow 0
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10^5
10^4
-5000
0
5000
10000
15000
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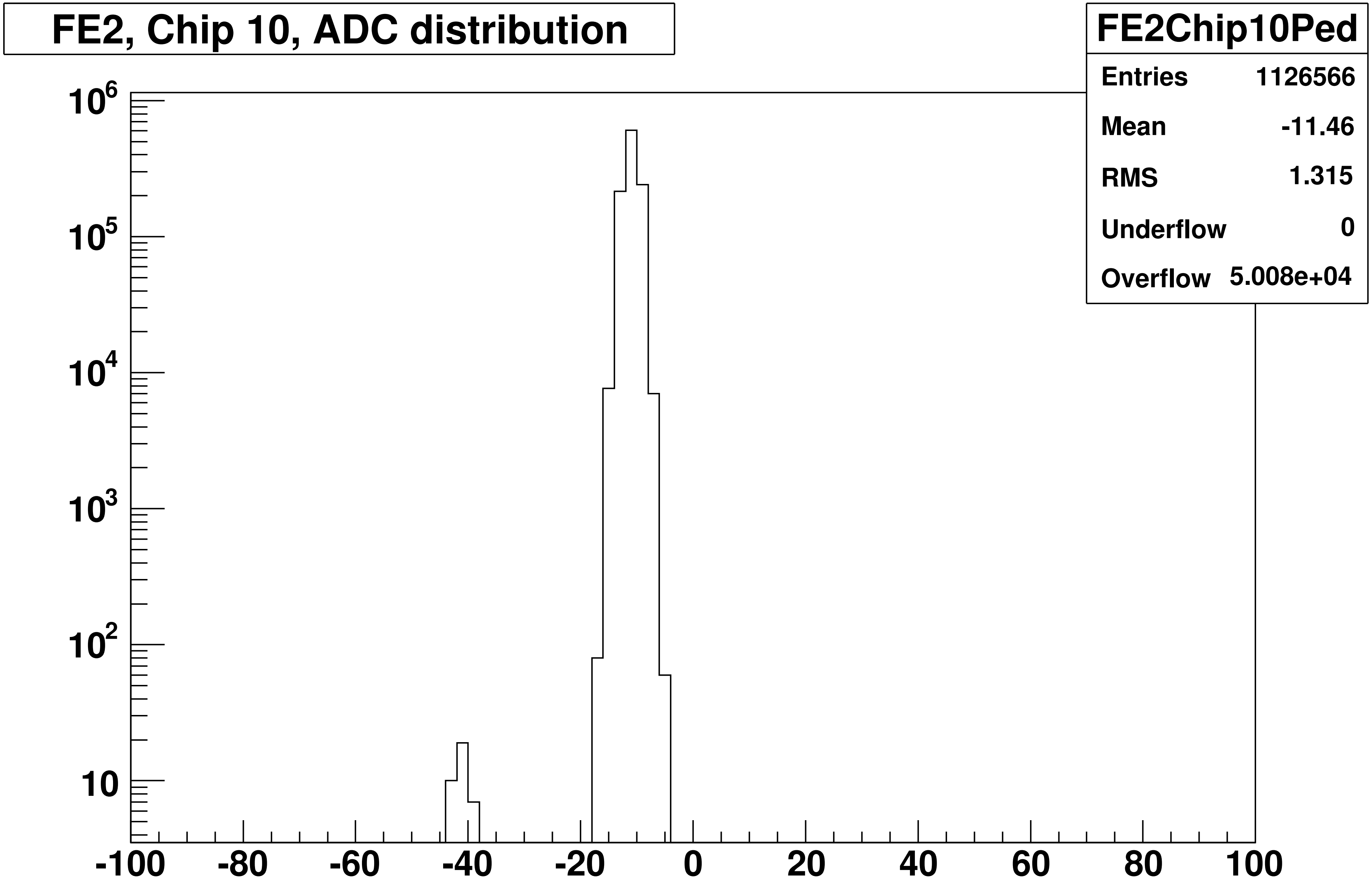
25000


FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 1126566
Mean -11.46
RMS 1.315
Underflow 0
Overflow 5.008e+04
10
10^2
10^3
10^4
10^5
10^6
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-40
-20
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80
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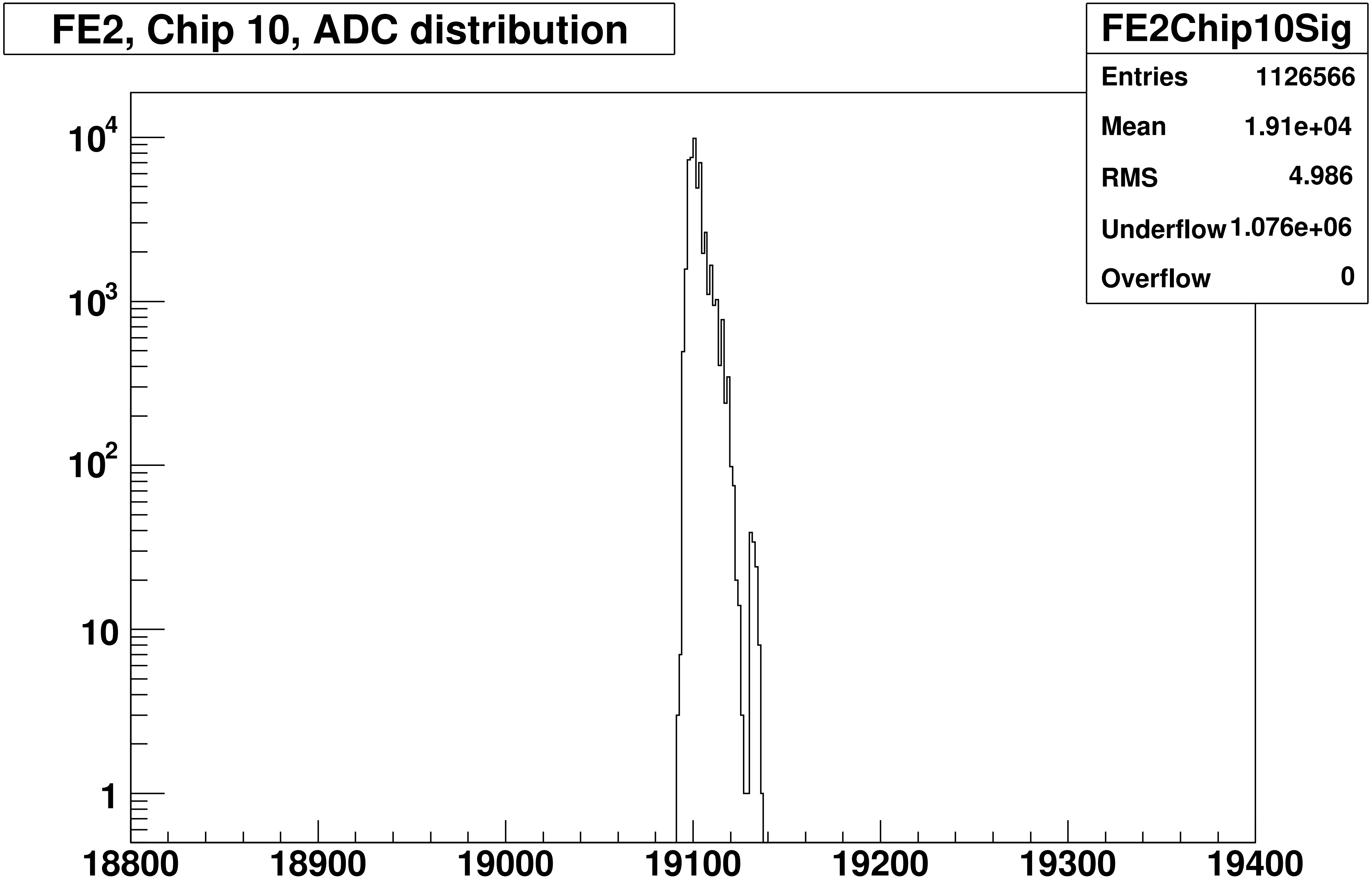
FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 1126566
Mean 1.91e+04
RMS 4.986
Underflow 1.076e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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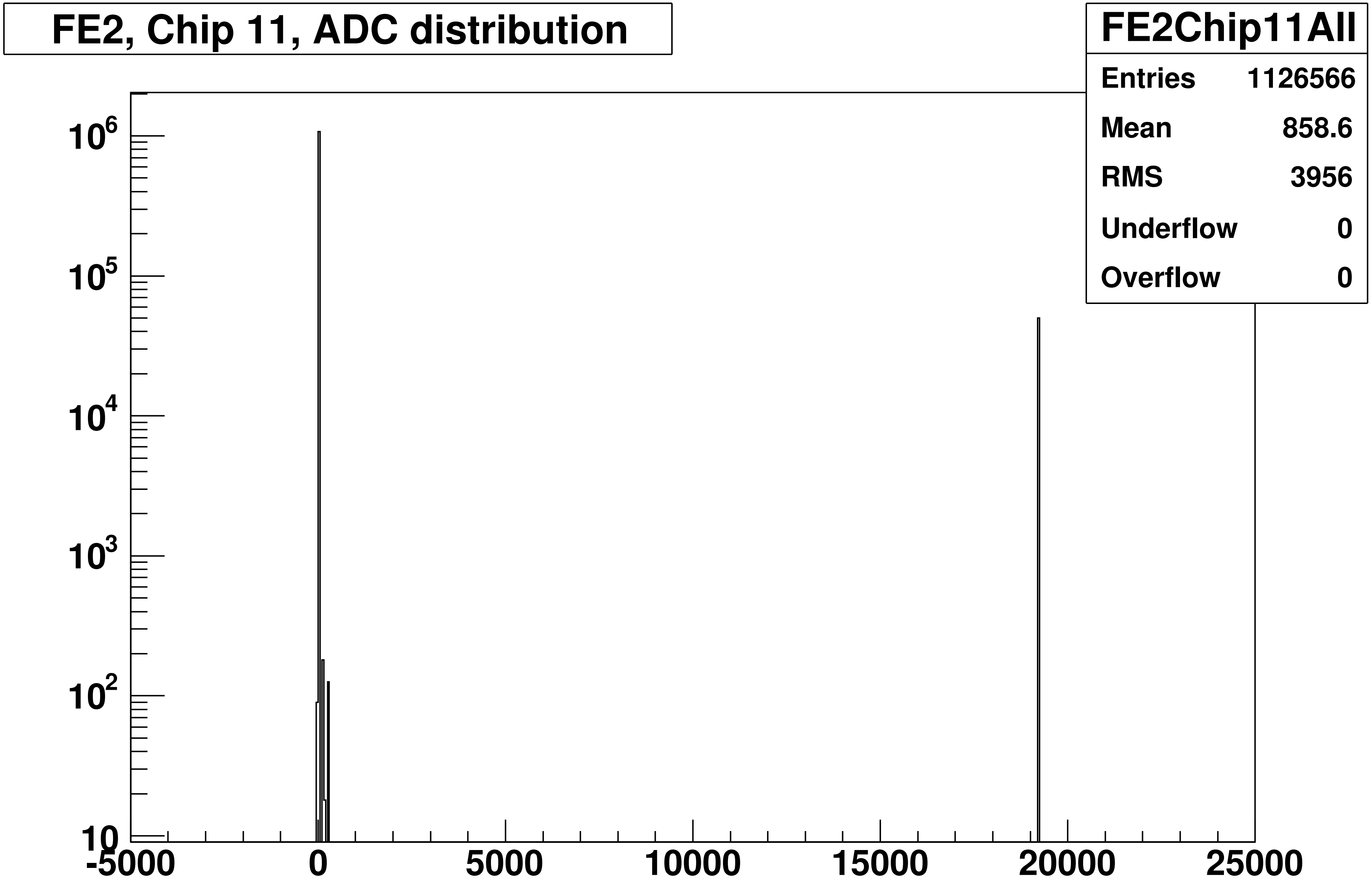
19400



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 1126566
Mean 858.6
RMS 3956
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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20000
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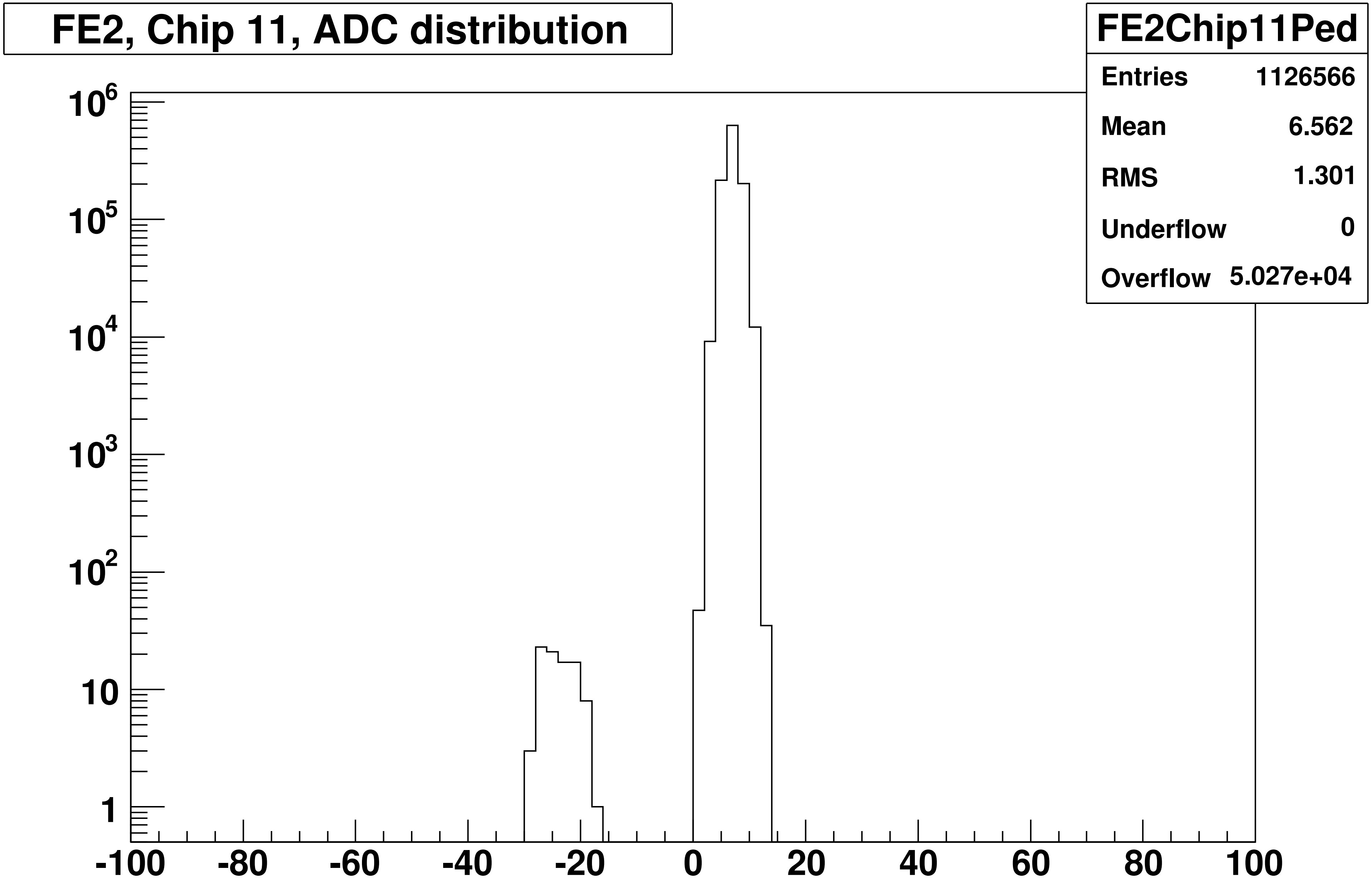
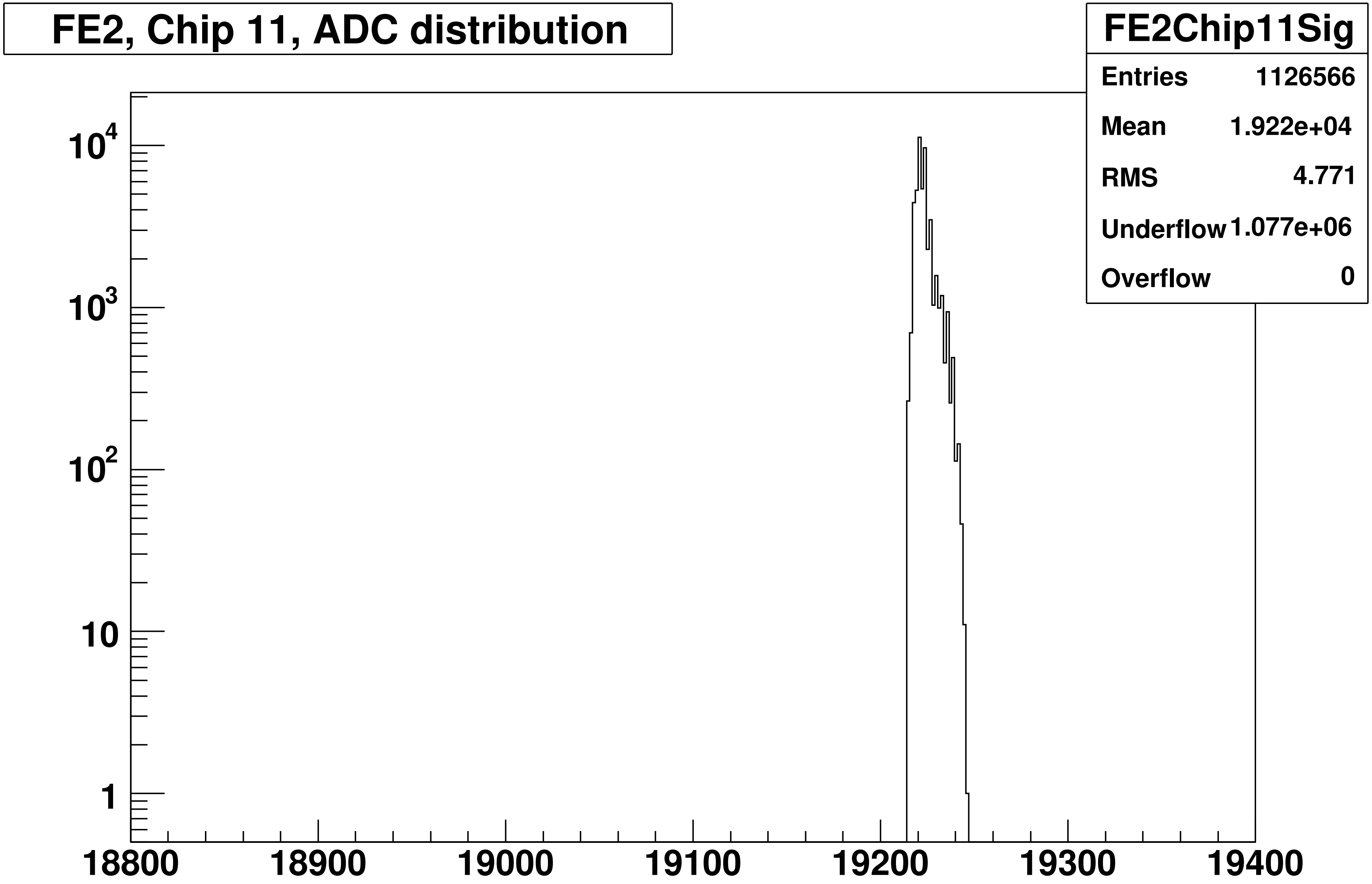

FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 1126566
Mean 6.562
RMS 1.301
Underflow 0
Overflow 5.027e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 1126566
Mean 1.922e+04
RMS 4.771
Underflow 1.077e+06
Overflow 0
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10^3
10^2
10
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18900
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19200
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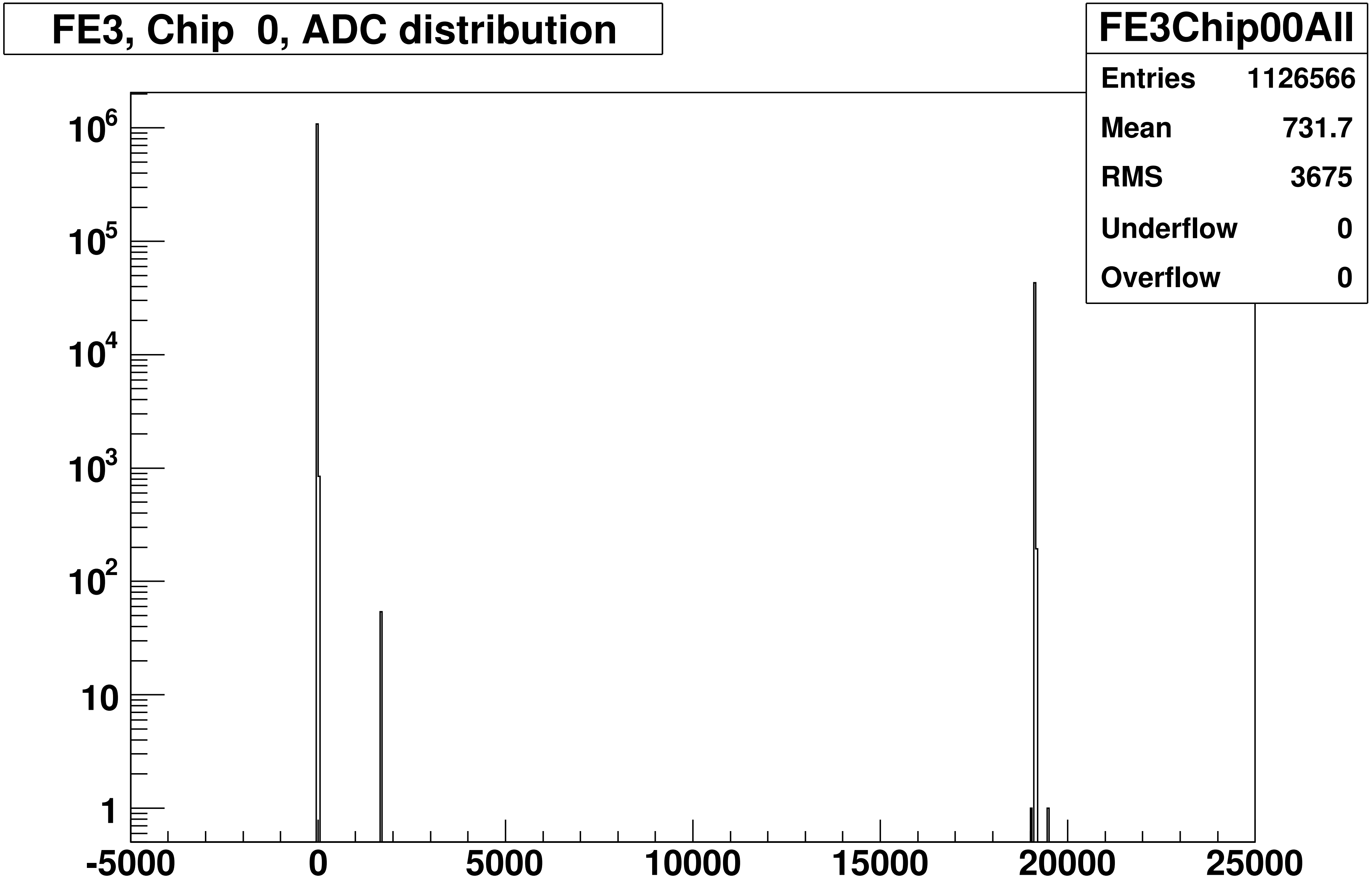
FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 1126566
Mean 731.7
RMS 3675
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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0
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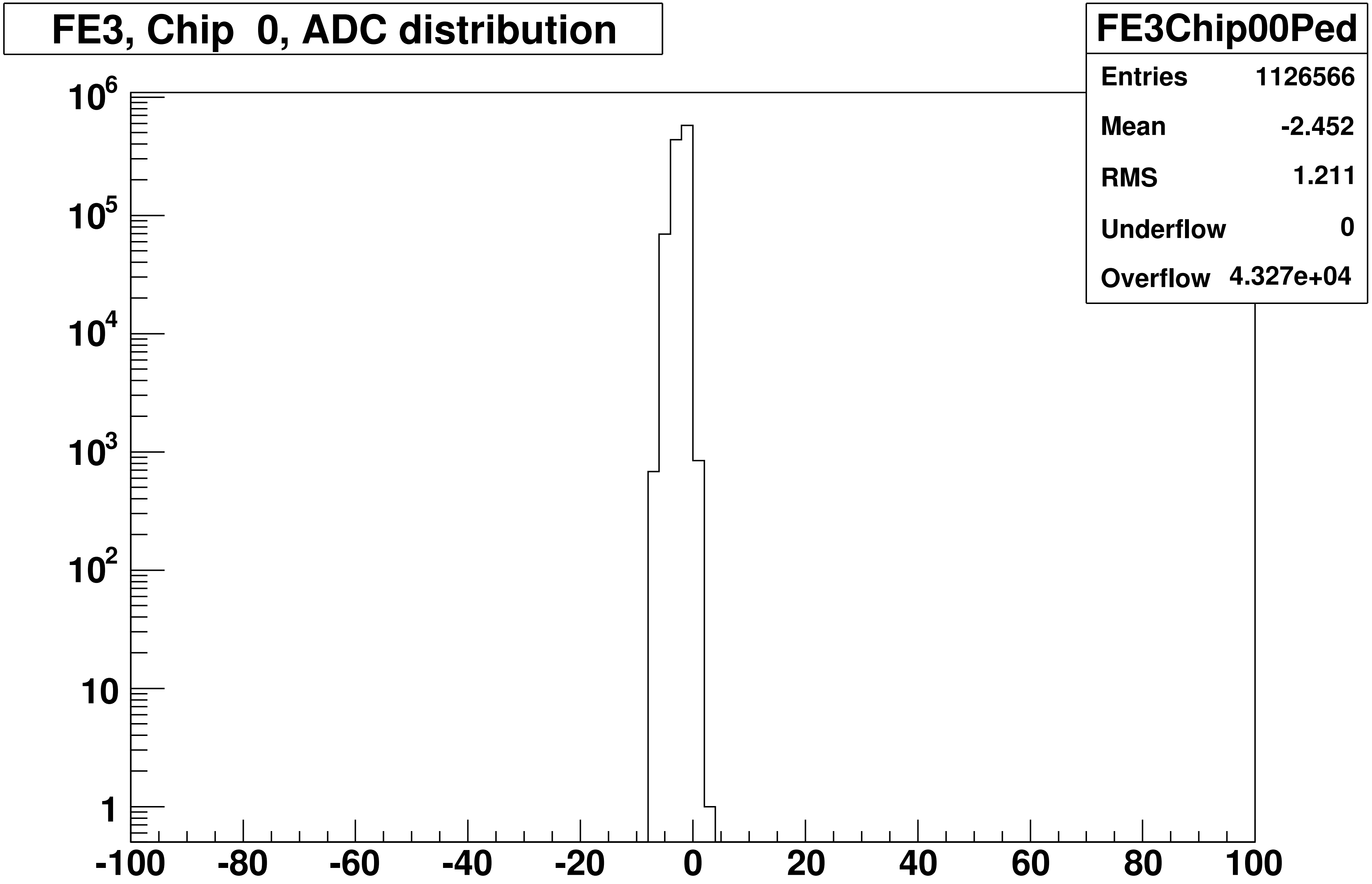
FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 1126566
Mean -2.452
RMS 1.211
Underflow 0
Overflow 4.327e+04
10^6
10^5
10^4
10^3
10^2
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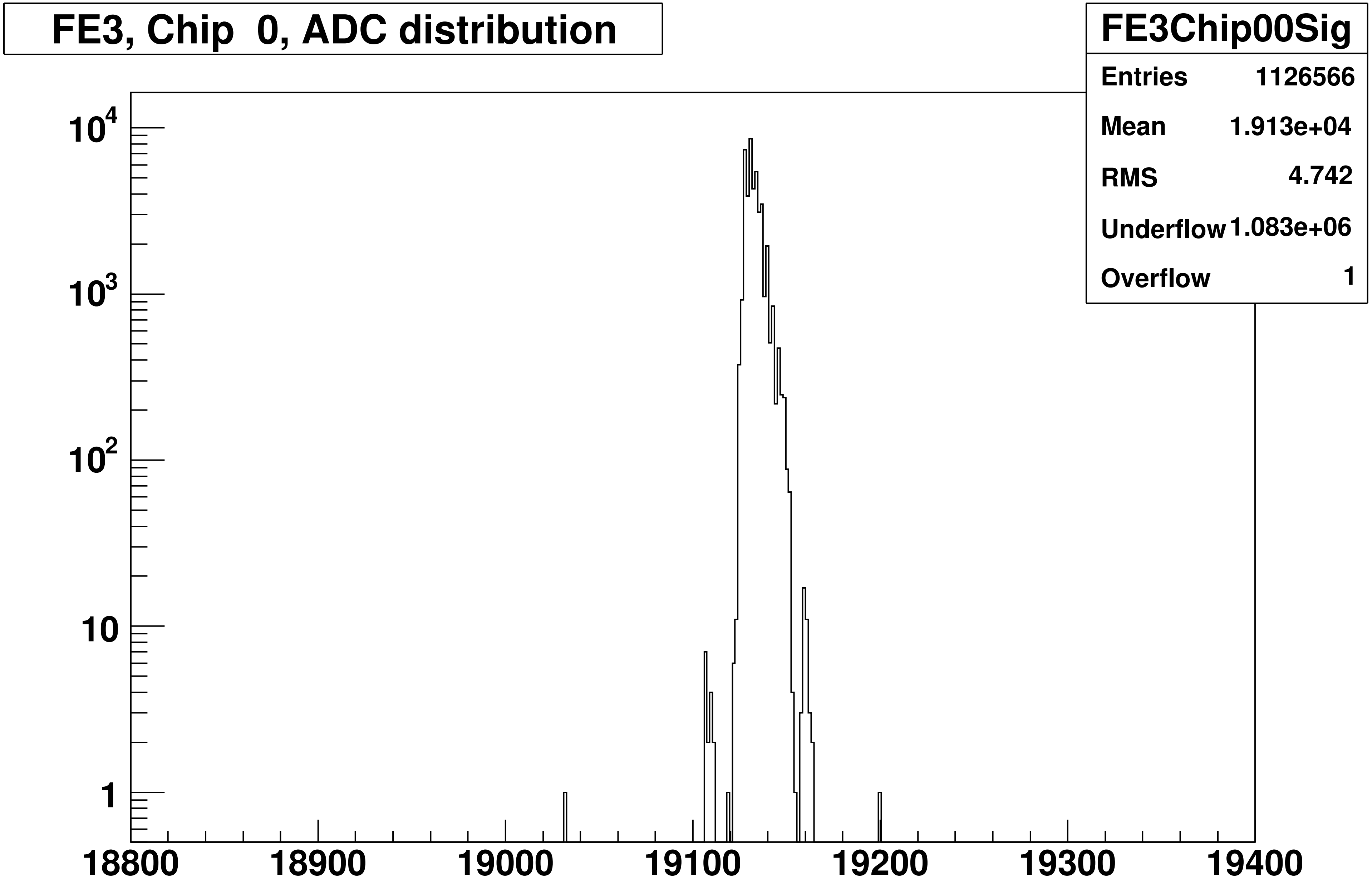
FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 1126566
Mean 1.913e+04
RMS 4.742
Underflow 1.083e+06
Overflow 1
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10^3
10^2
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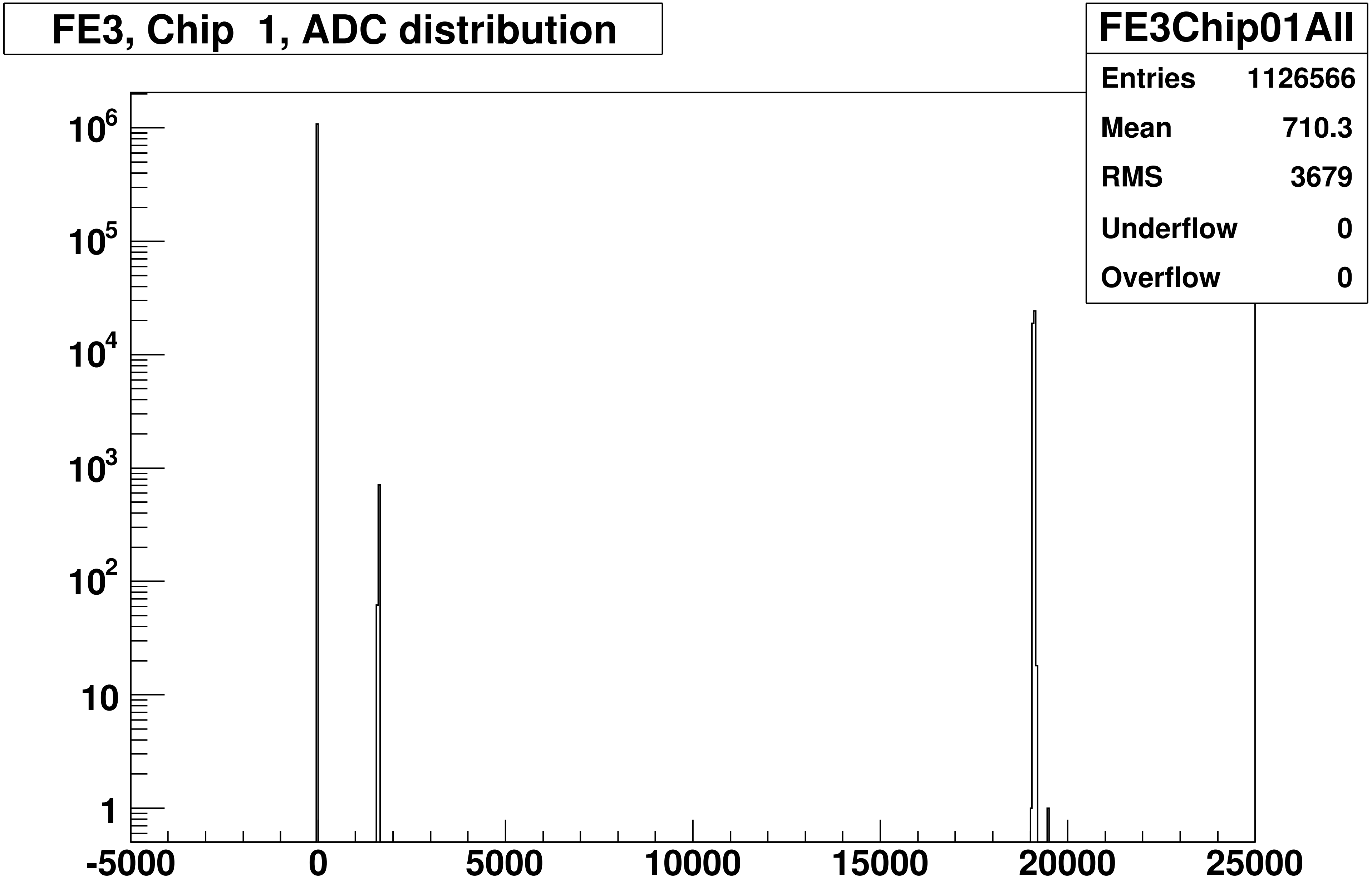

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 1126566
Mean 710.3
RMS 3679
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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20000
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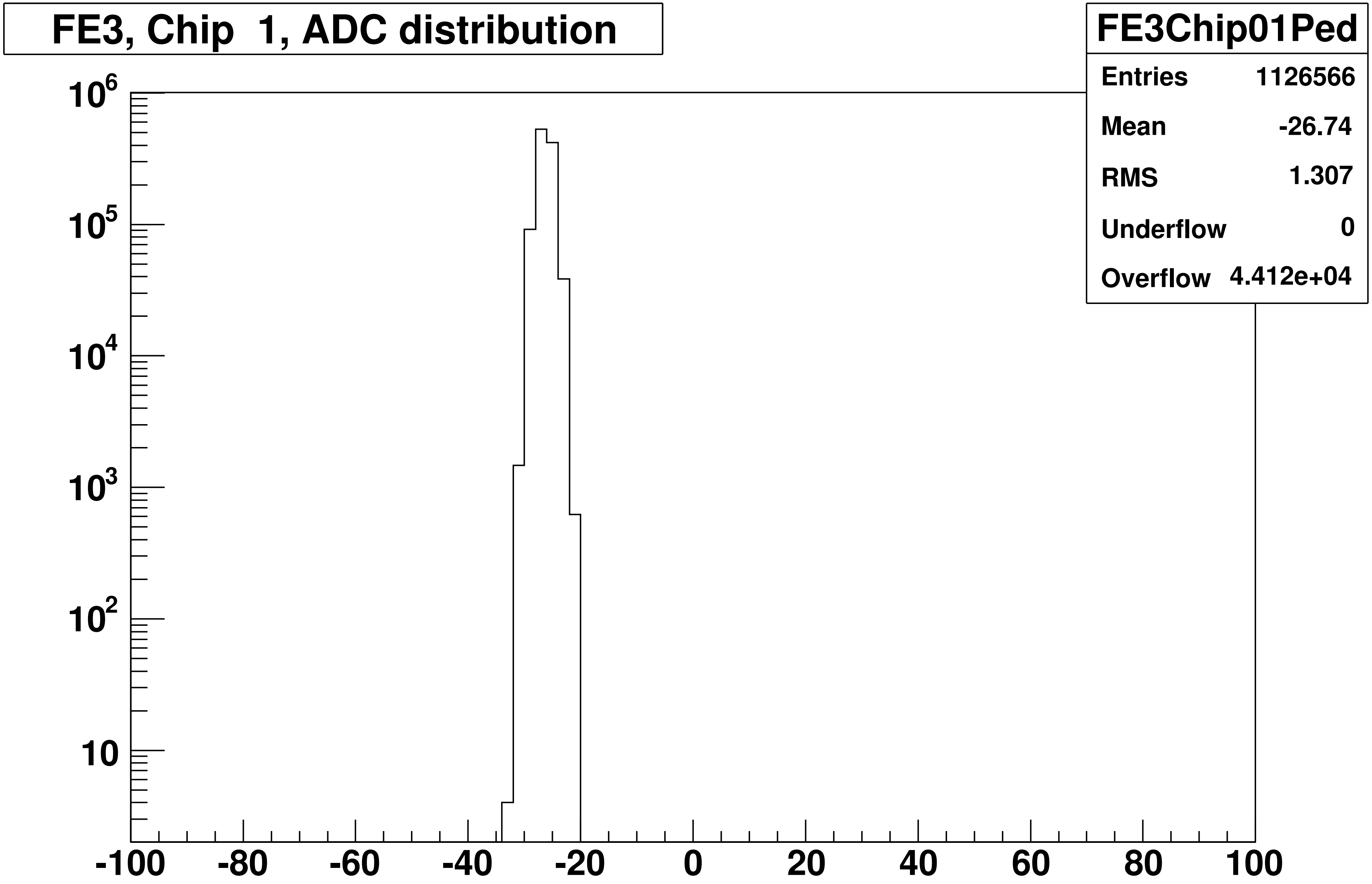

FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 1126566
Mean -26.74
RMS 1.307
Underflow 0
Overflow 4.412e+04
10^6
10^5
10^4
10^3
10^2
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-100
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-20
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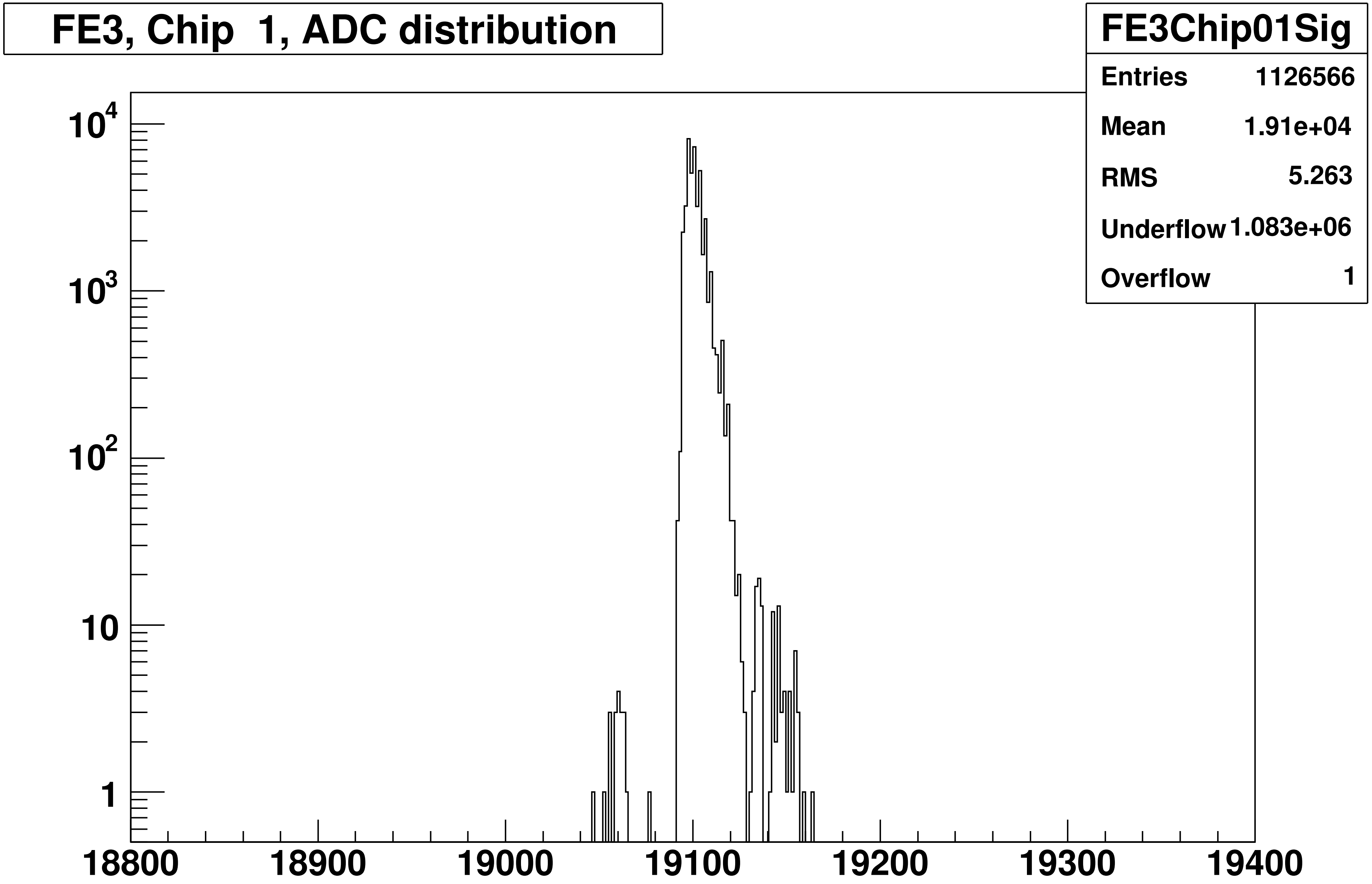
FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 1126566
Mean 1.91e+04
RMS 5.263
Underflow 1.083e+06
Overflow 1
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10^3
10^2
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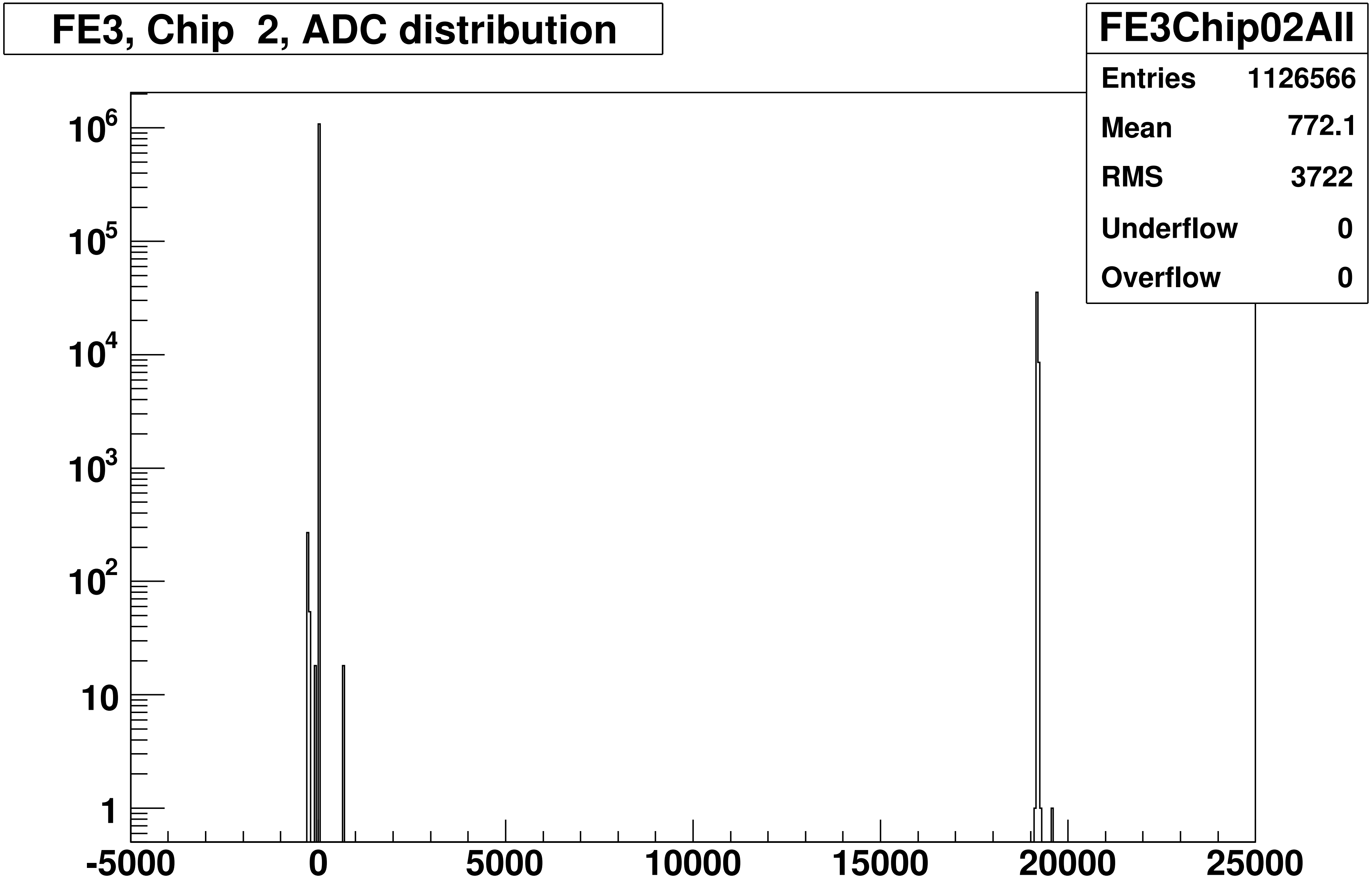

FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 1126566
Mean 772.1
RMS 3722
Underflow 0
Overflow 0
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10^4
10^3
10^2
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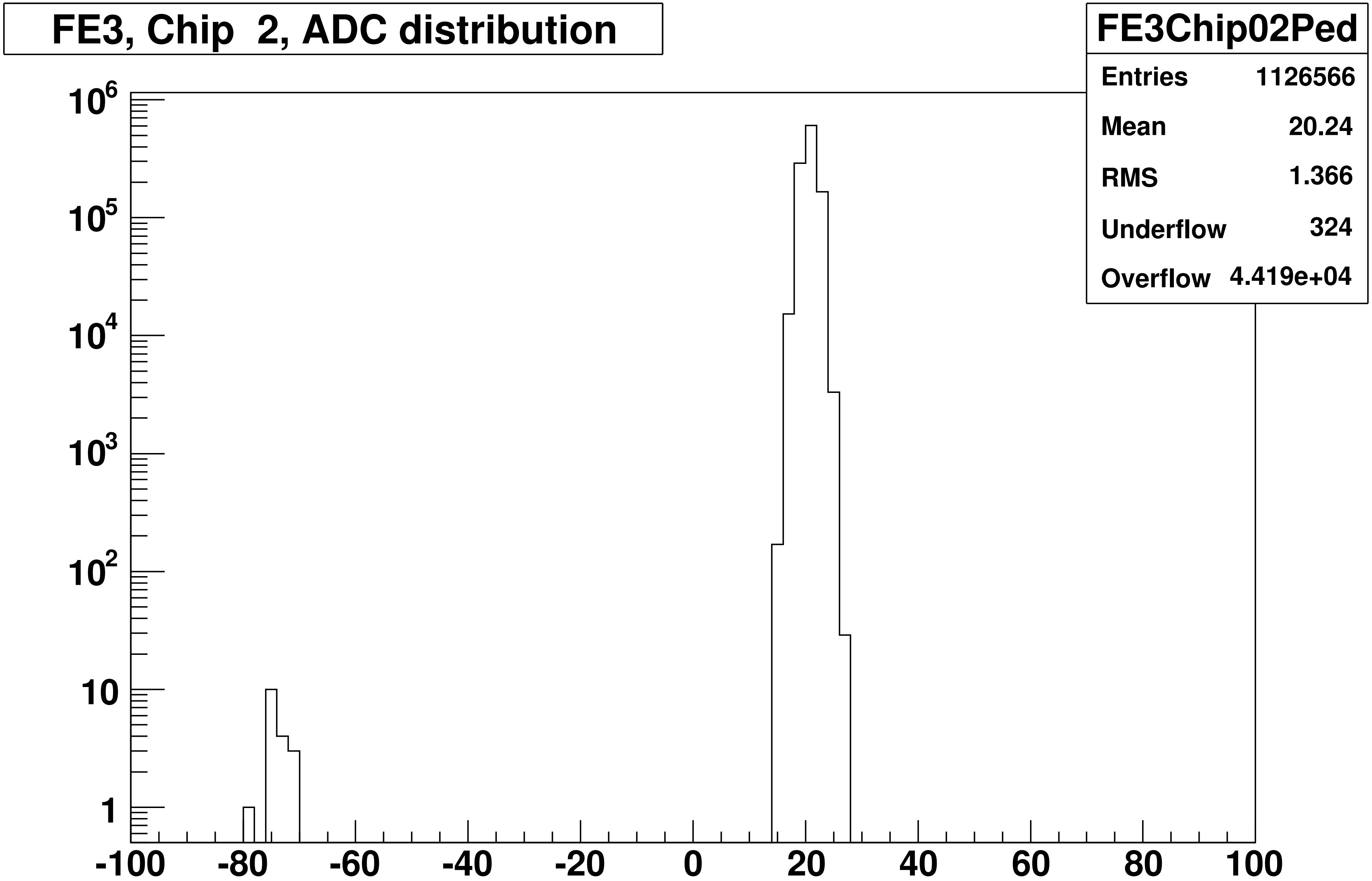
FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 1126566
Mean 20.24
RMS 1.366
Underflow 324
Overflow 4.419e+04
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10^5
10^4
10^3
10^2
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80
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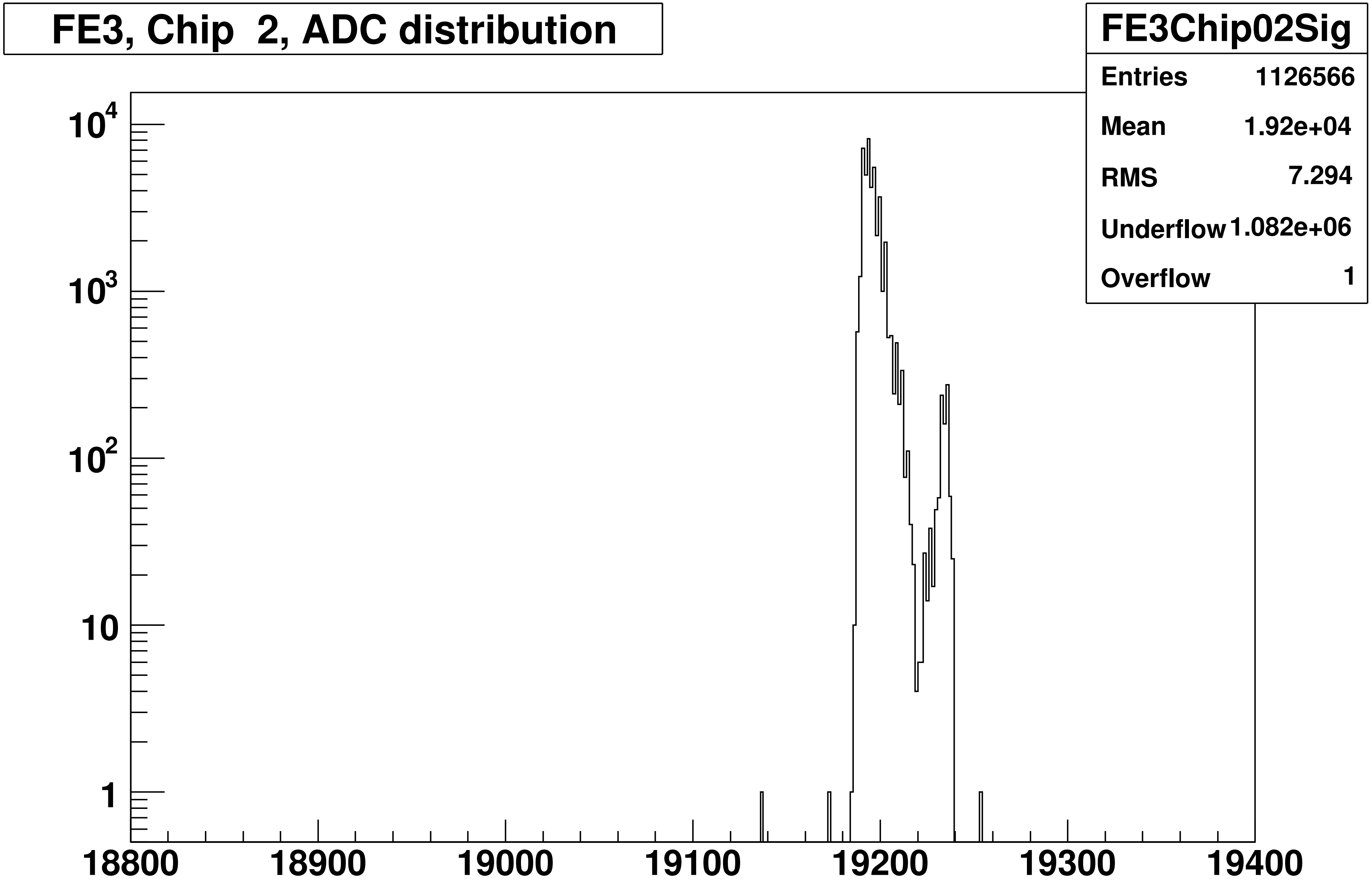
FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 1126566
Mean 1.92e+04
RMS 7.294
Underflow 1.082e+06
Overflow 1
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10^3
10^2
10
1
18800
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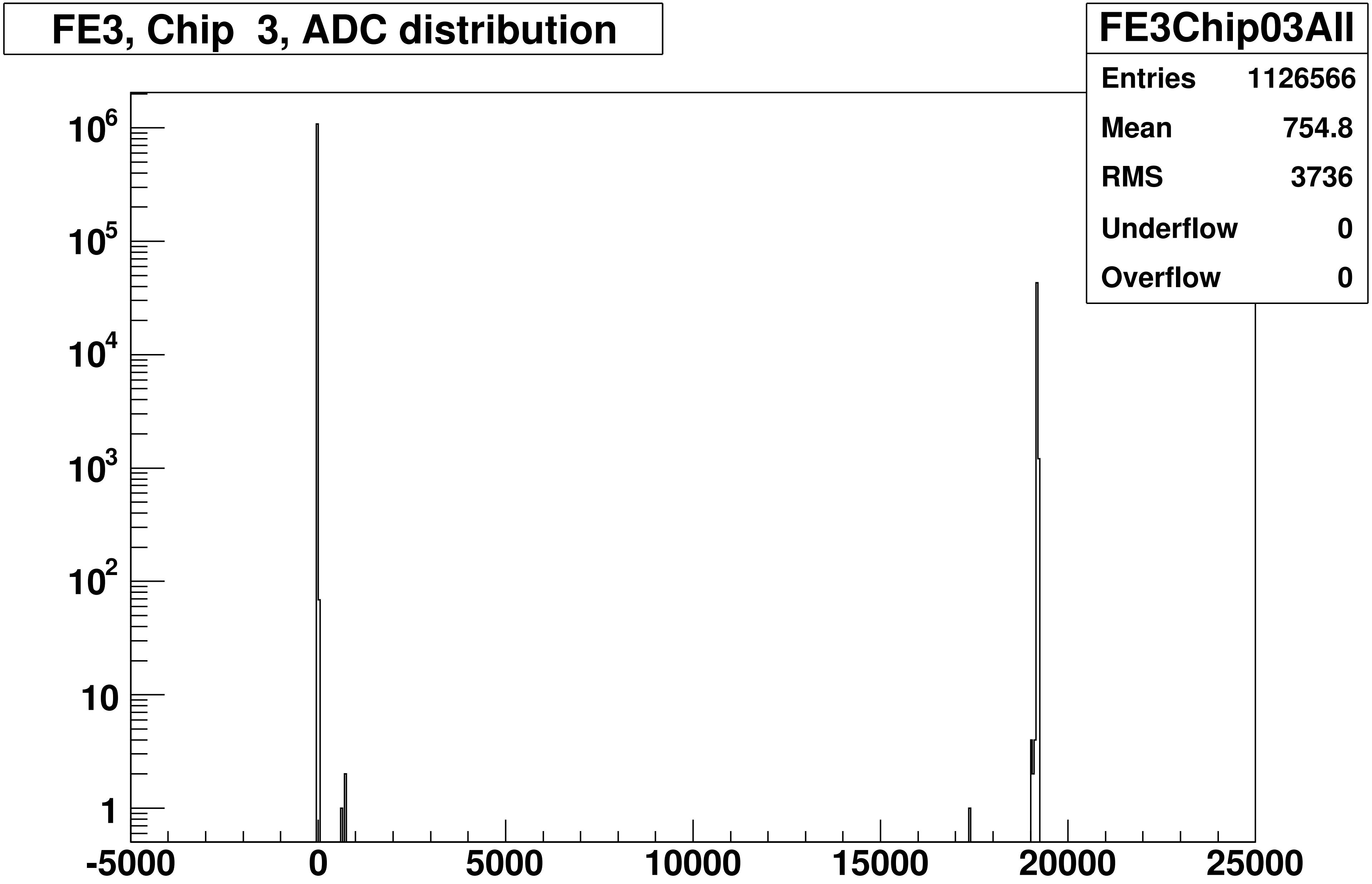
19100
19200
19300
19400


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 1126566
Mean 754.8
RMS 3736
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
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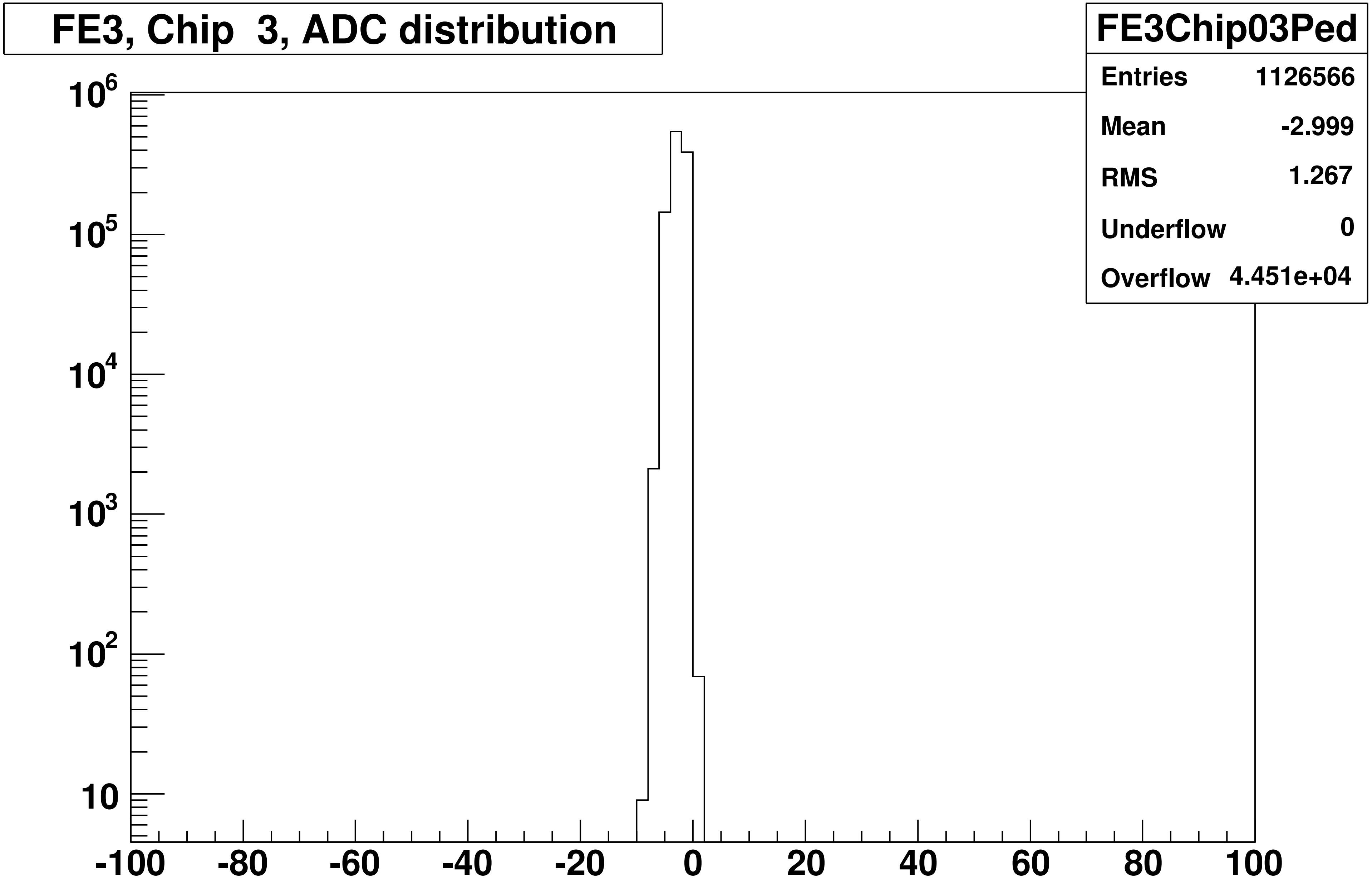
20000
25000



FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 1126566
Mean -2.999
RMS 1.267
Underflow 0
Overflow 4.451e+04
10^6
10^5
10^4
10^3
10^2
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-100
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-60
-40
-20
0
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40
60
80
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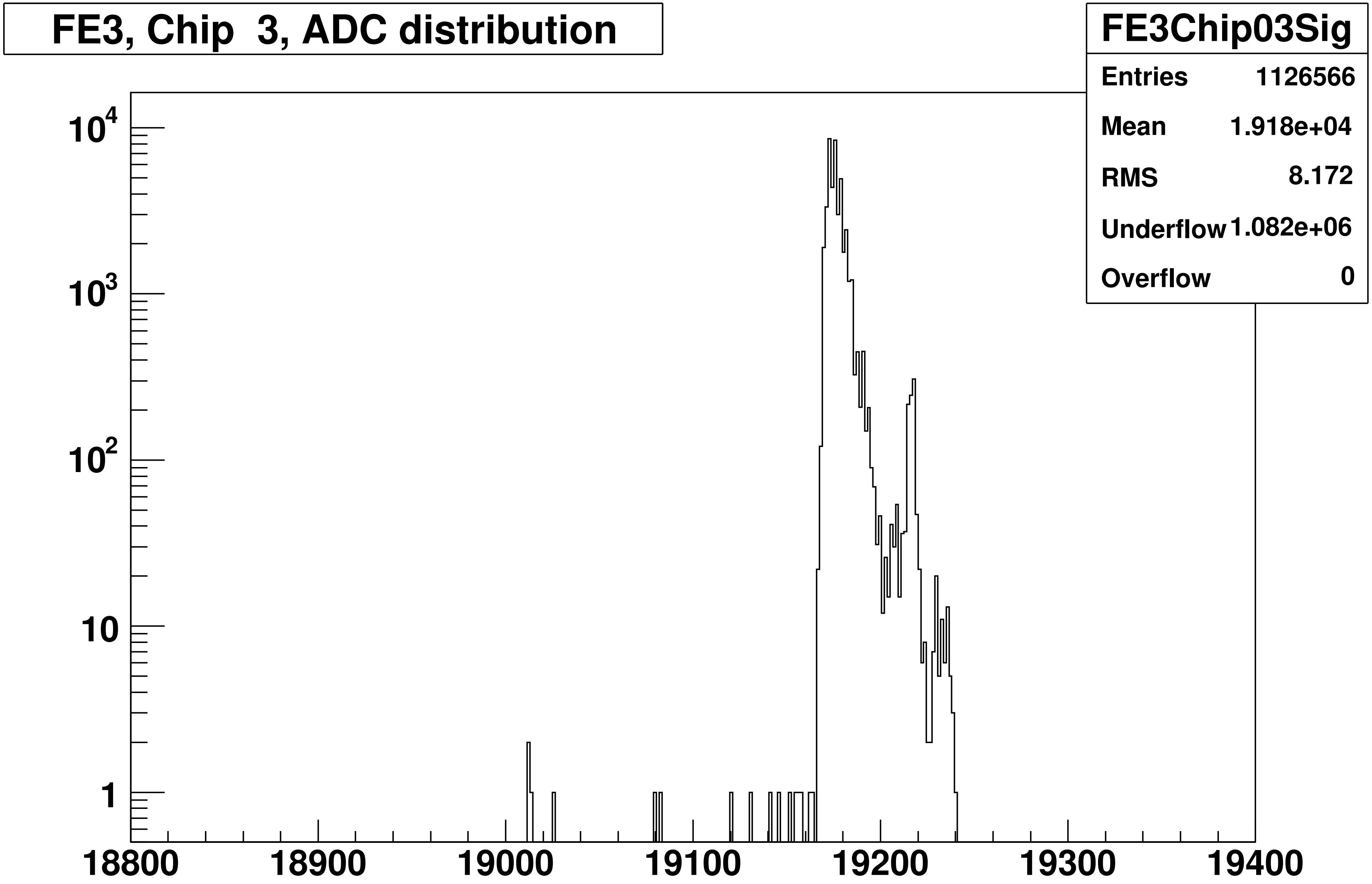

FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 1126566
Mean 1.918e+04
RMS 8.172
Underflow 1.082e+06
Overflow 0
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10^3
10^2
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18900
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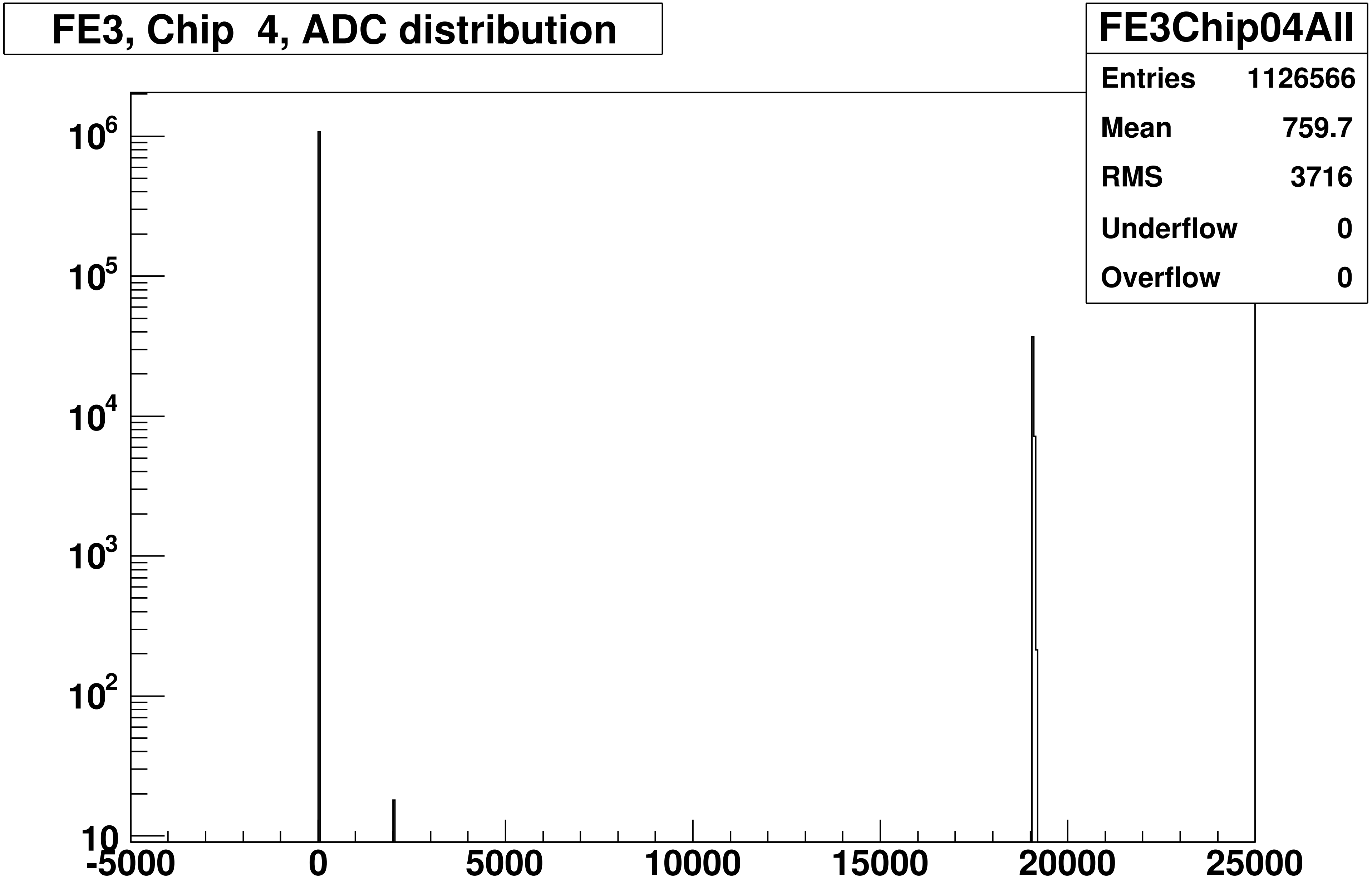

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 1126566
Mean 759.7
RMS 3716
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
-5000
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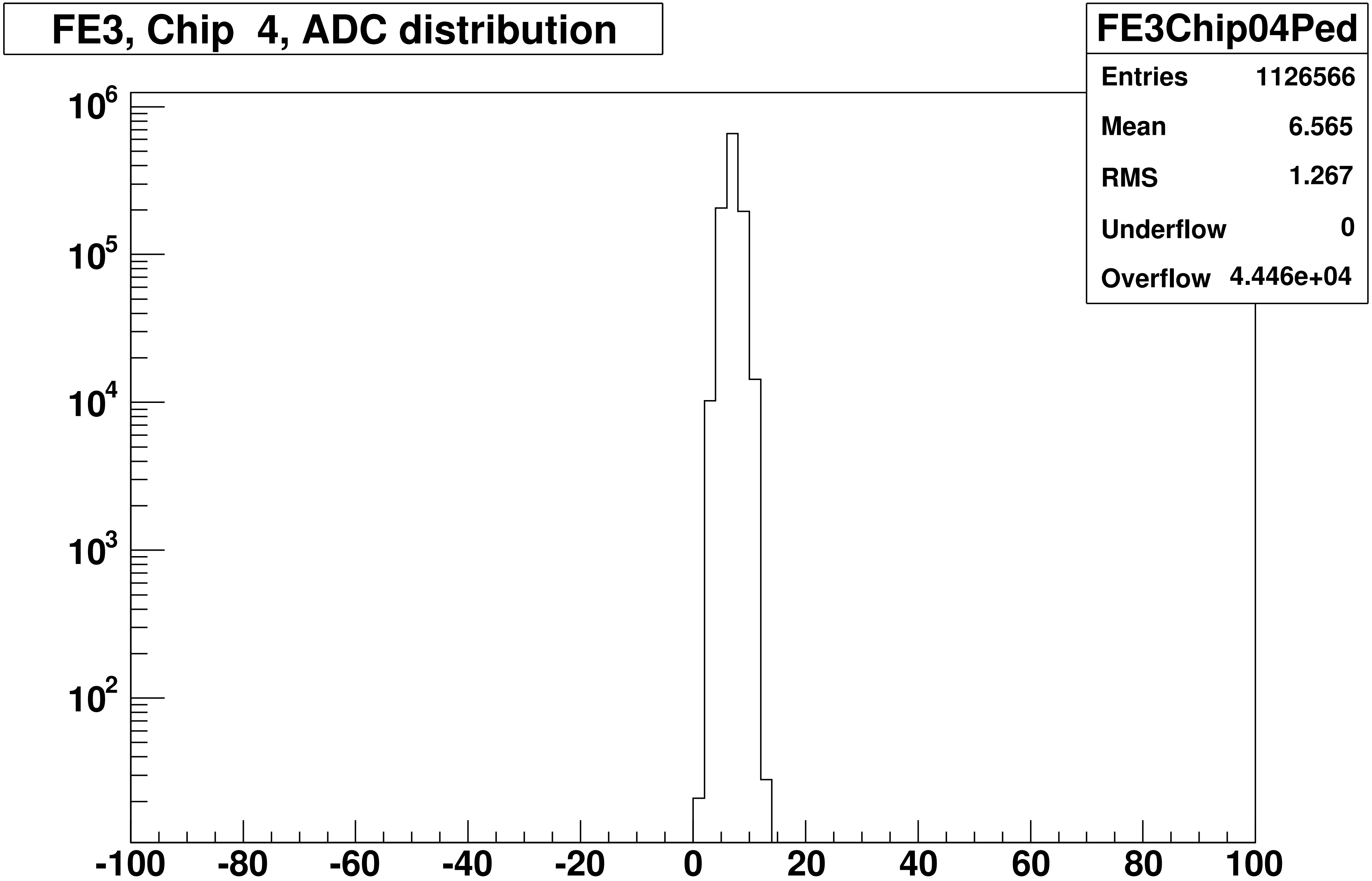
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 1126566
Mean 6.565
RMS 1.267
Underflow 0
Overflow 4.446e+04
10^6
10^5
10^4
10^3
10^2
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-80
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-20
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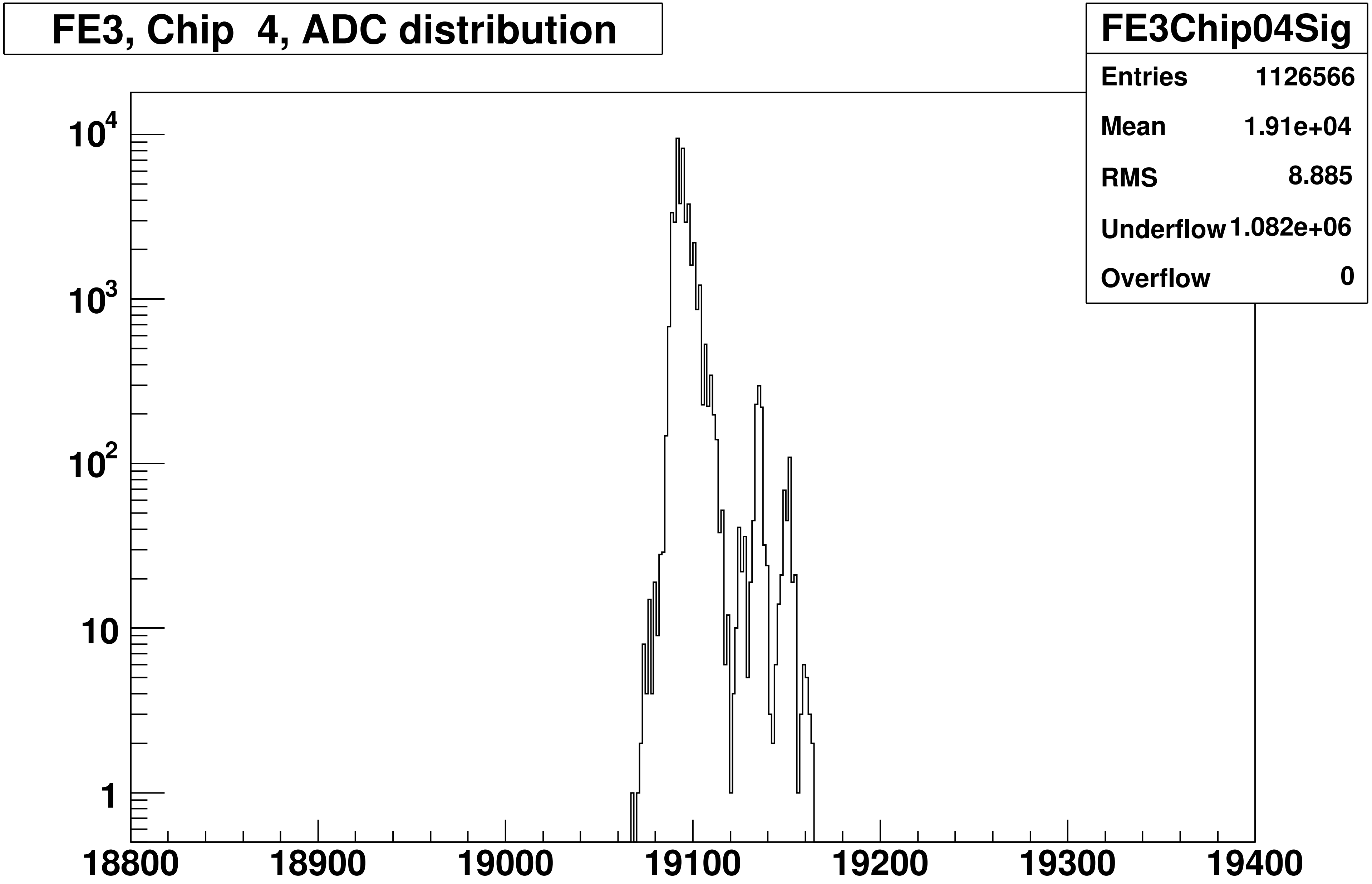

FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 1126566
Mean 1.91e+04
RMS 8.885
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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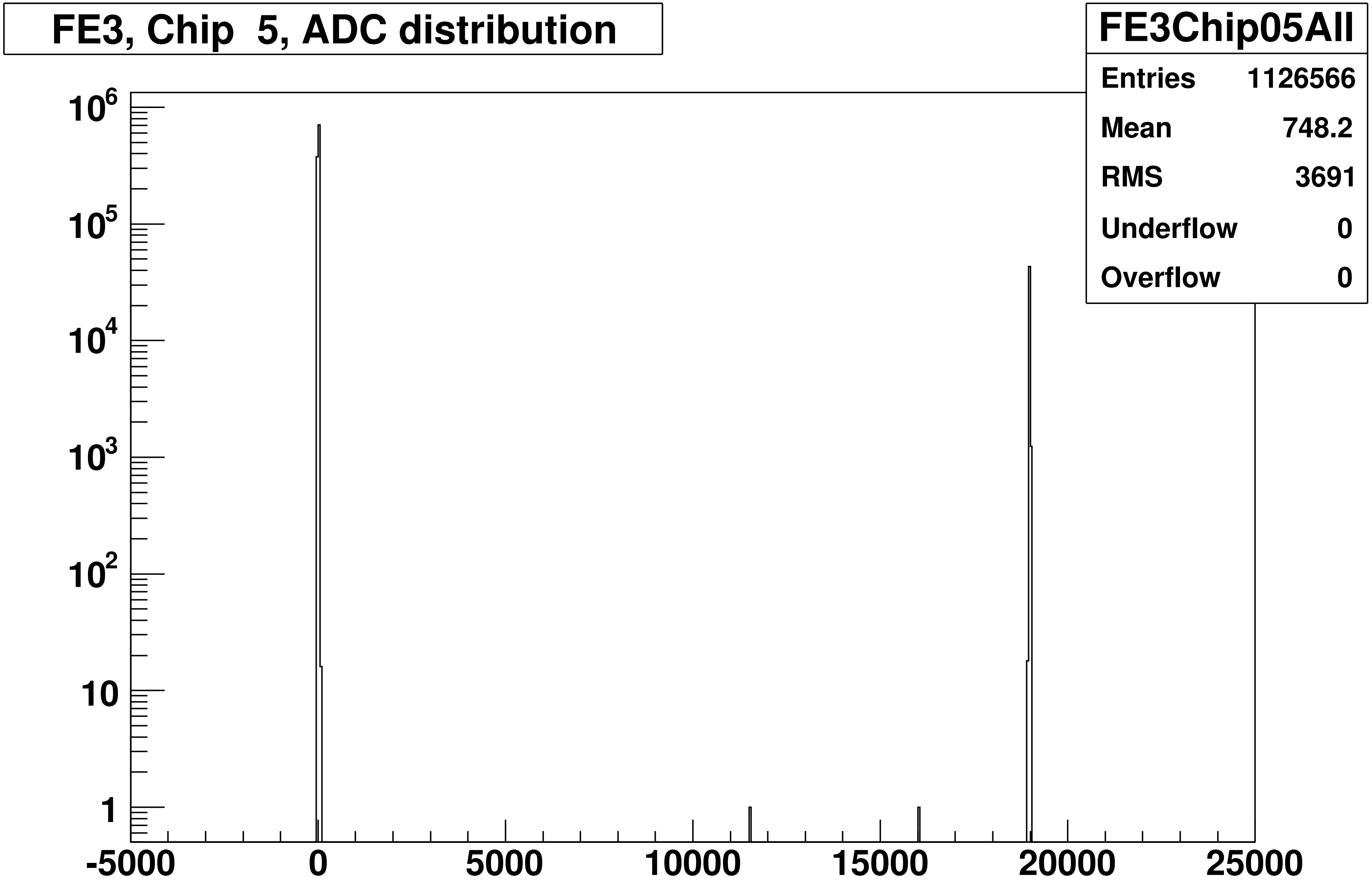

FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 1126566
Mean 748.2
RMS 3691
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
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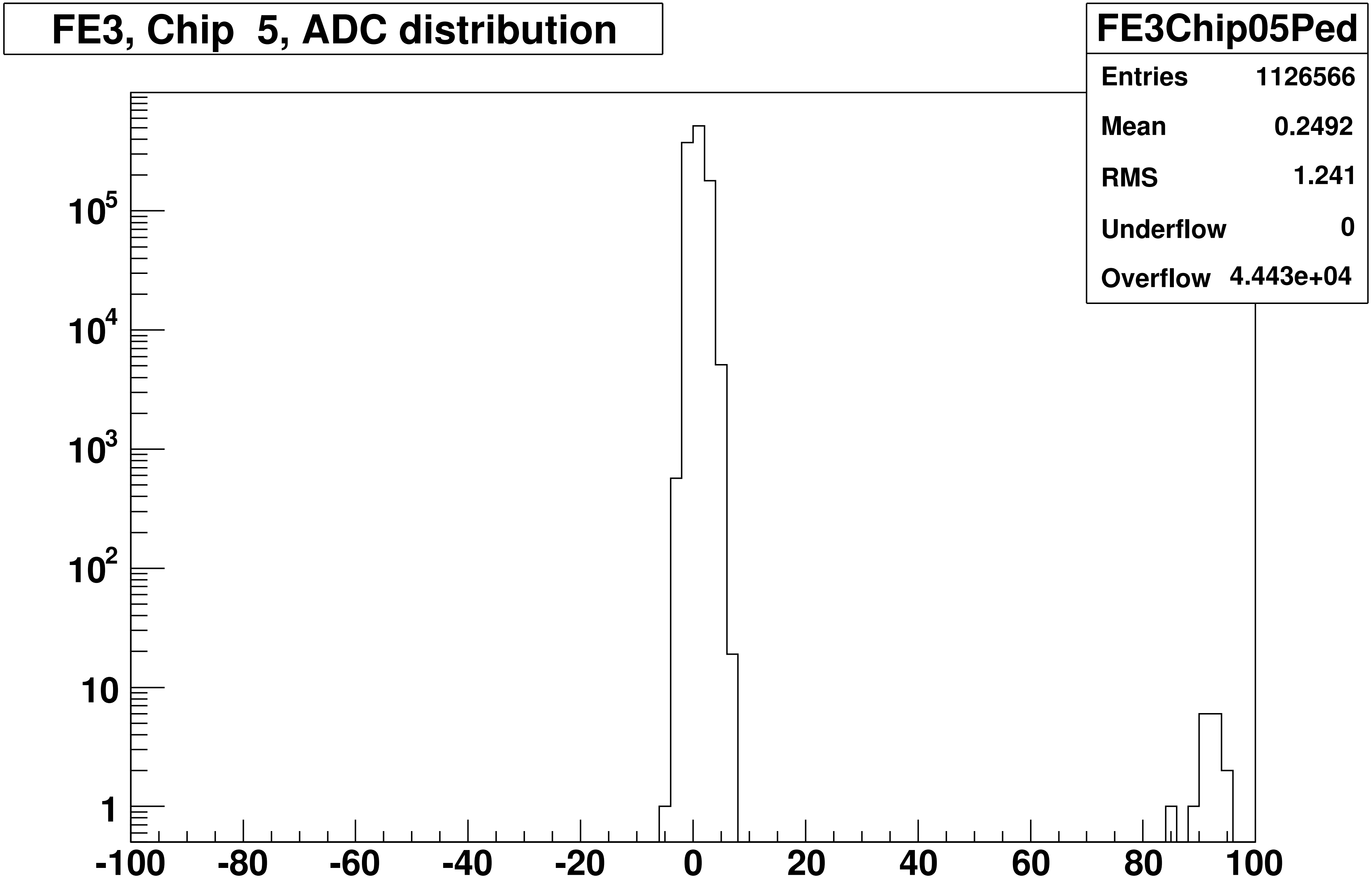

FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 1126566
Mean 0.2492
RMS 1.241
Underflow 0
Overflow 4.443e+04
10^5
10^4
10^3
10^2
10
1
-100
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-60
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-20
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40
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80
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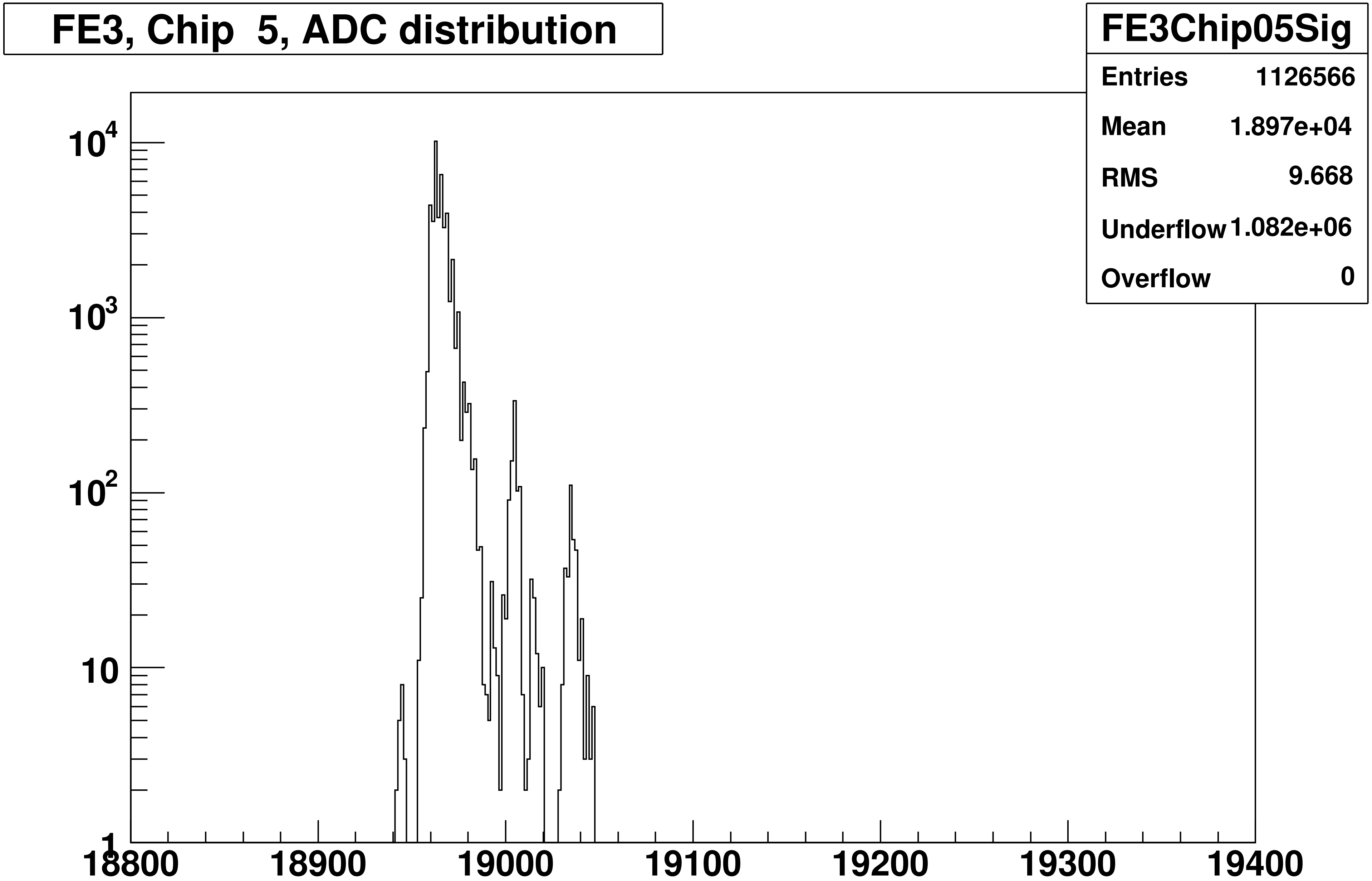

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 1126566
Mean 1.897e+04
RMS 9.668
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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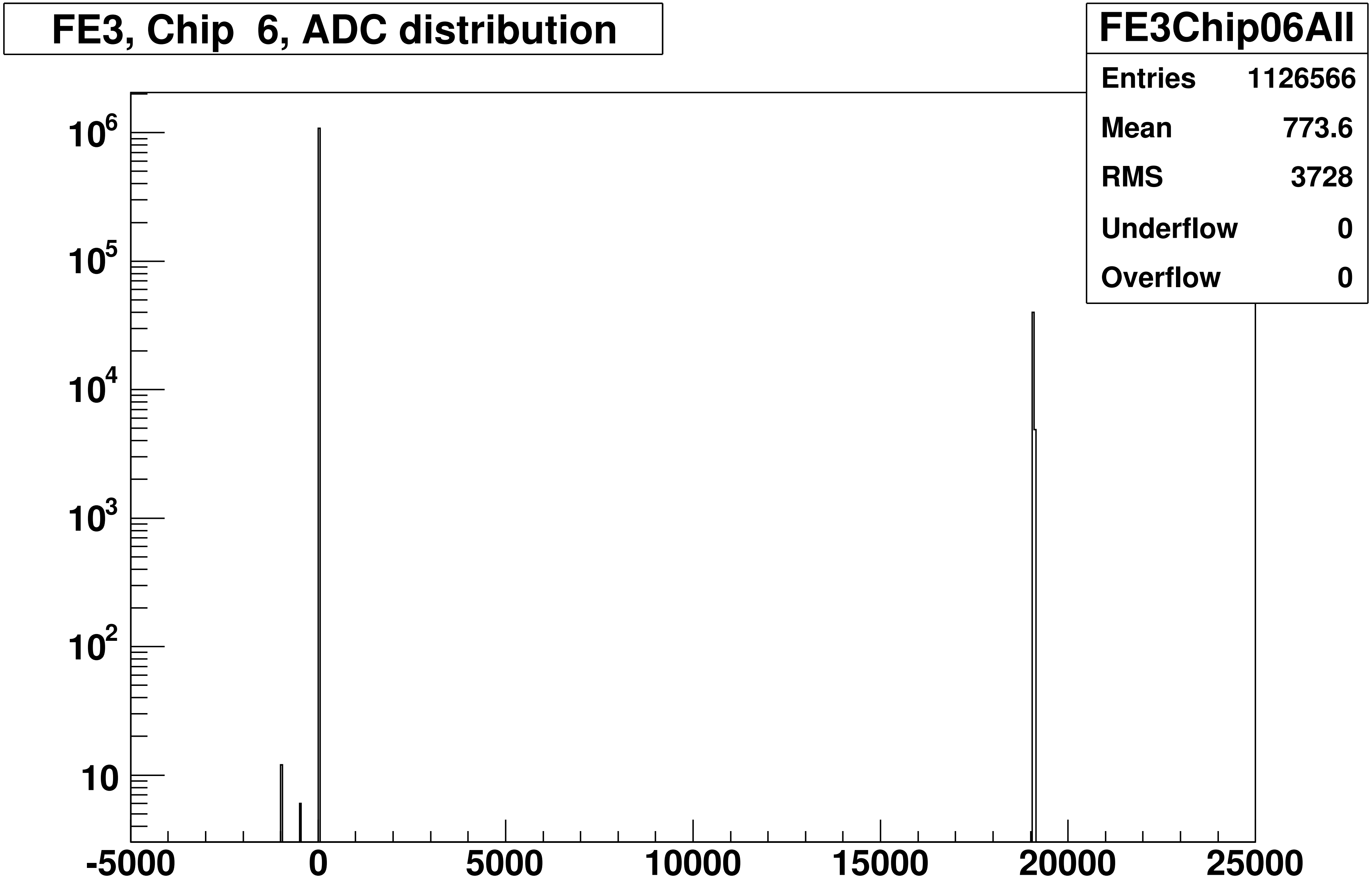
FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 1126566
Mean 773.6
RMS 3728
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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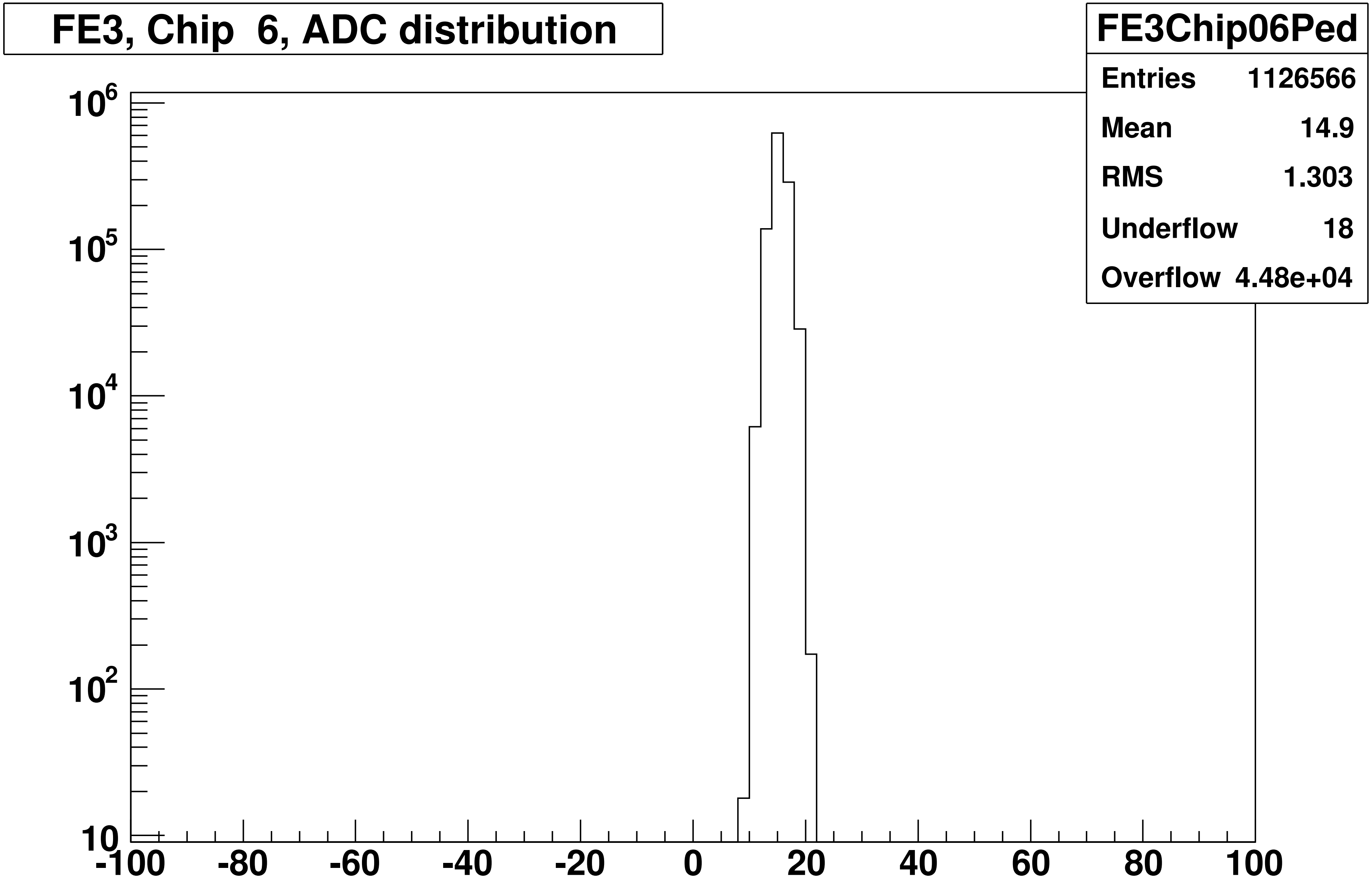
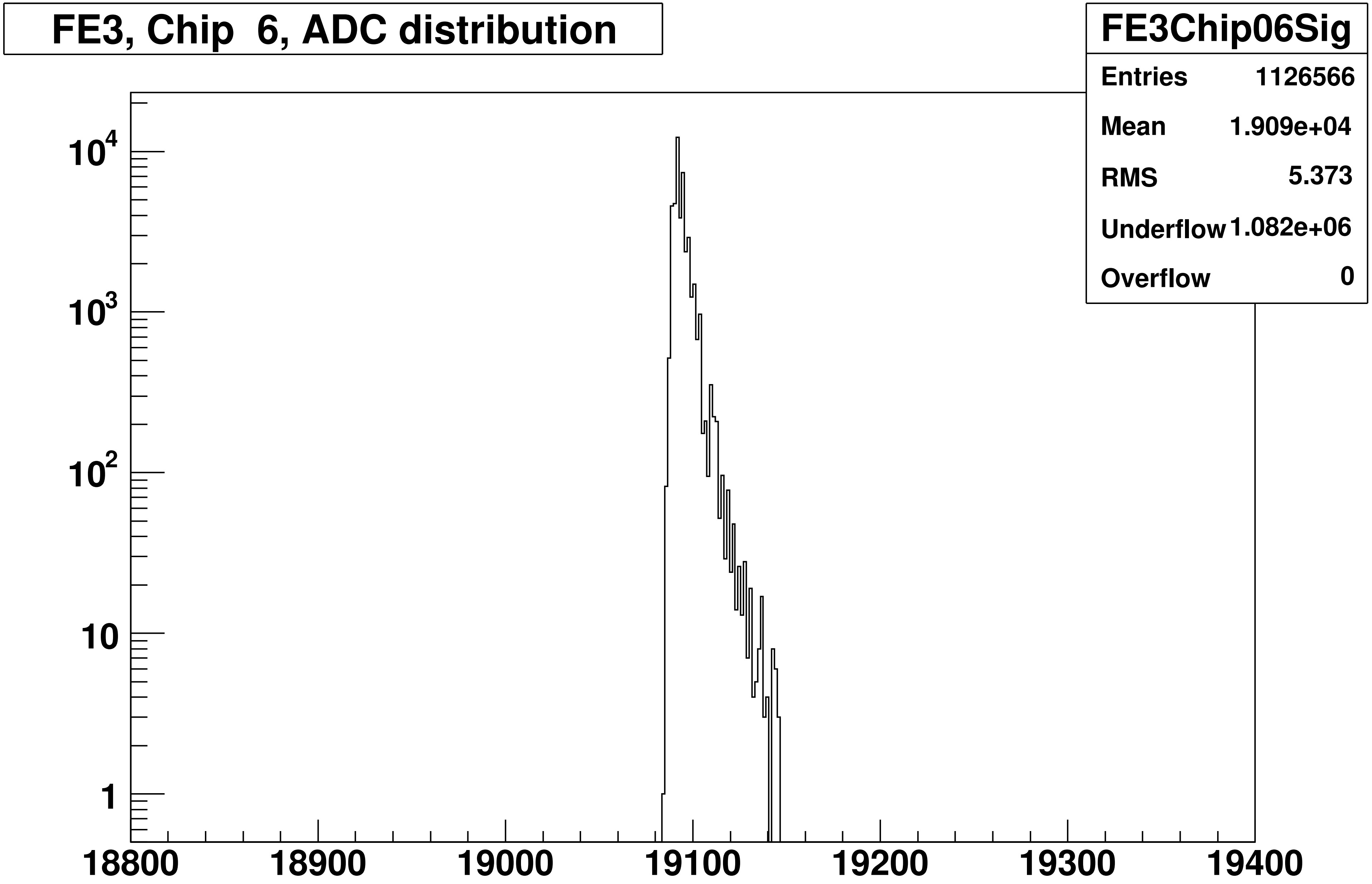
10000
15000
20000
25000


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 1126566
Mean 14.9
RMS 1.303
Underflow 18
Overflow 4.48e+04
10
10^2
10^3
10^4
10^5
10^6
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 1126566
Mean 1.909e+04
RMS 5.373
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
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18900
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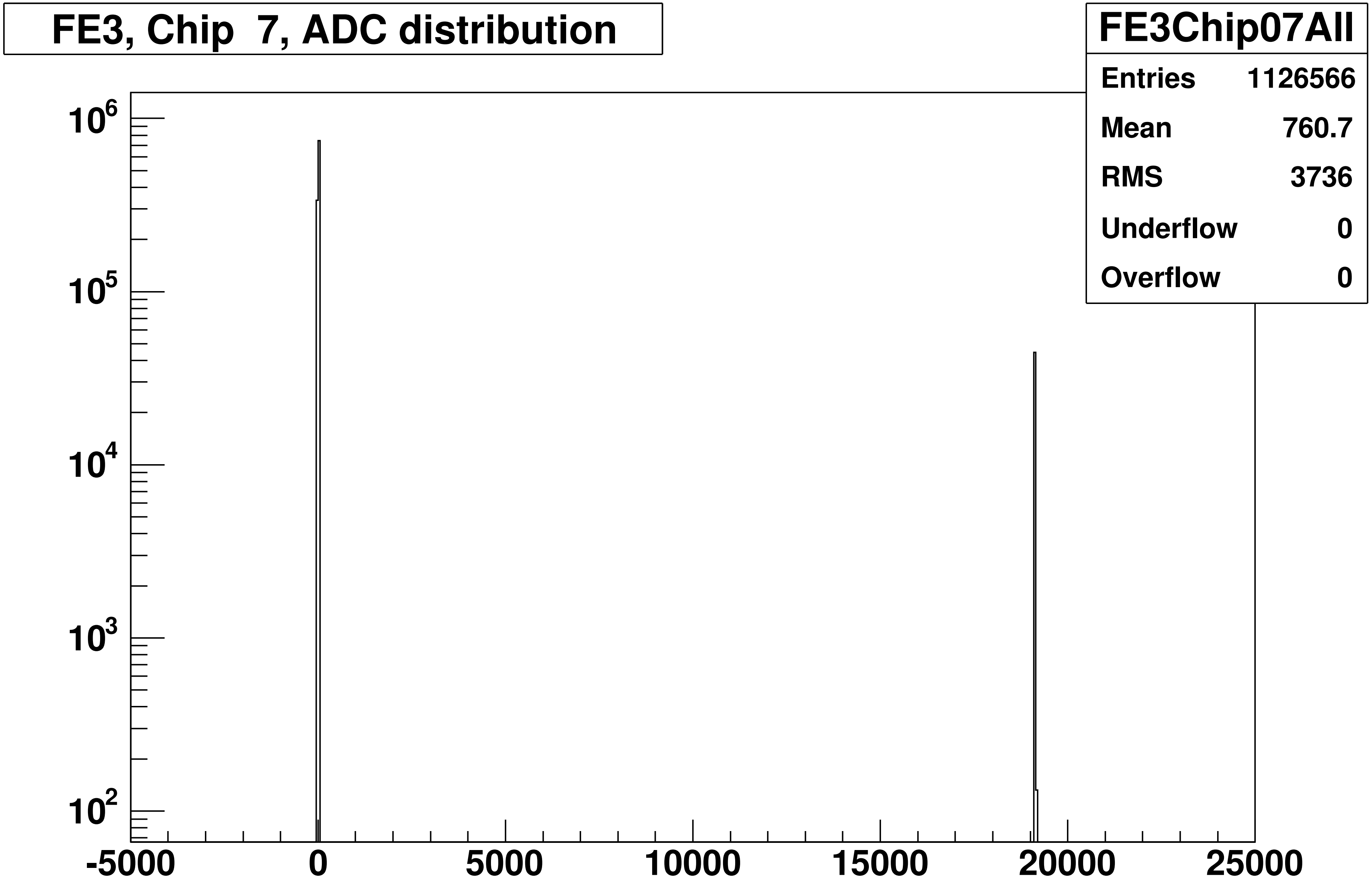
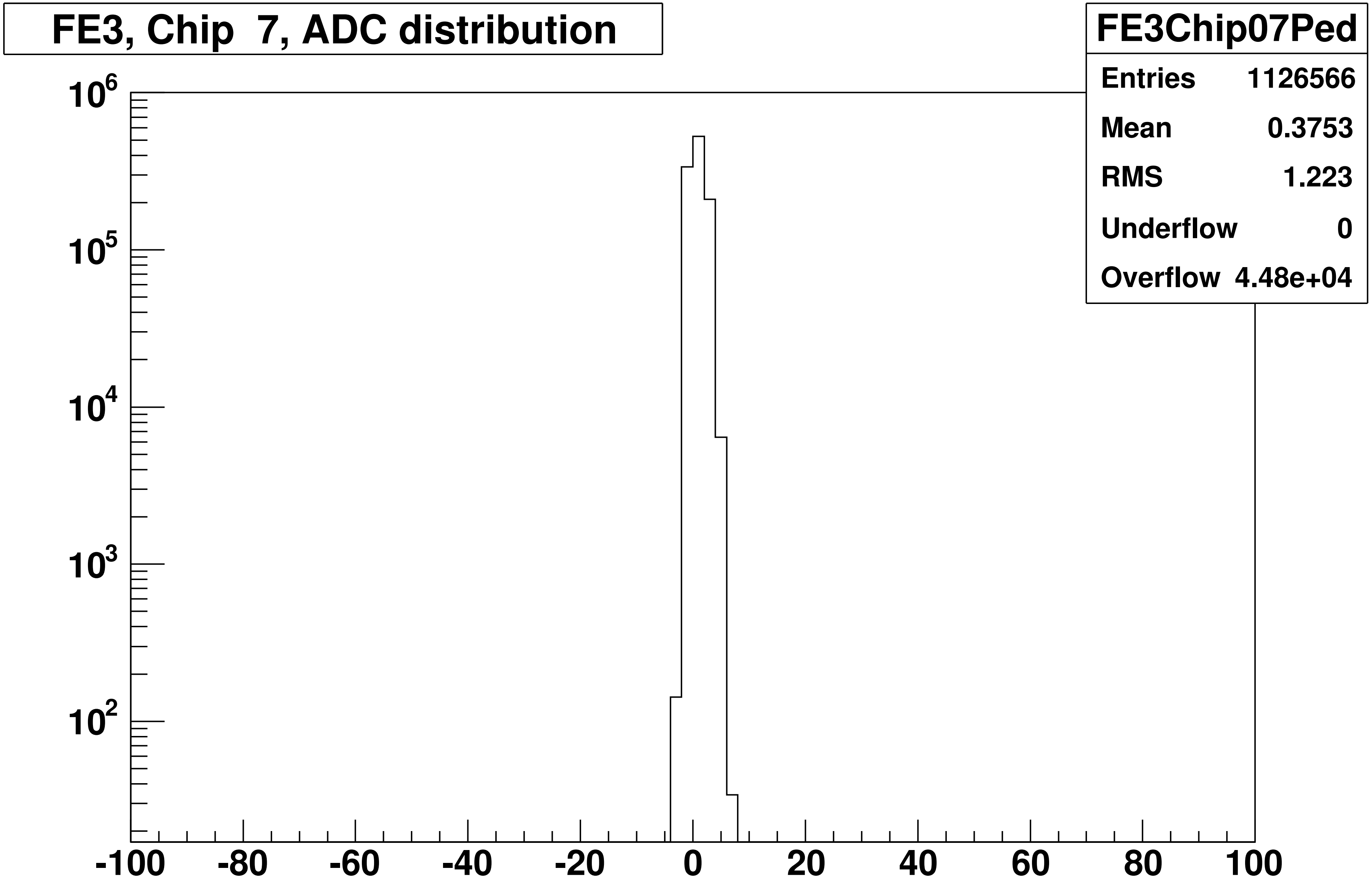
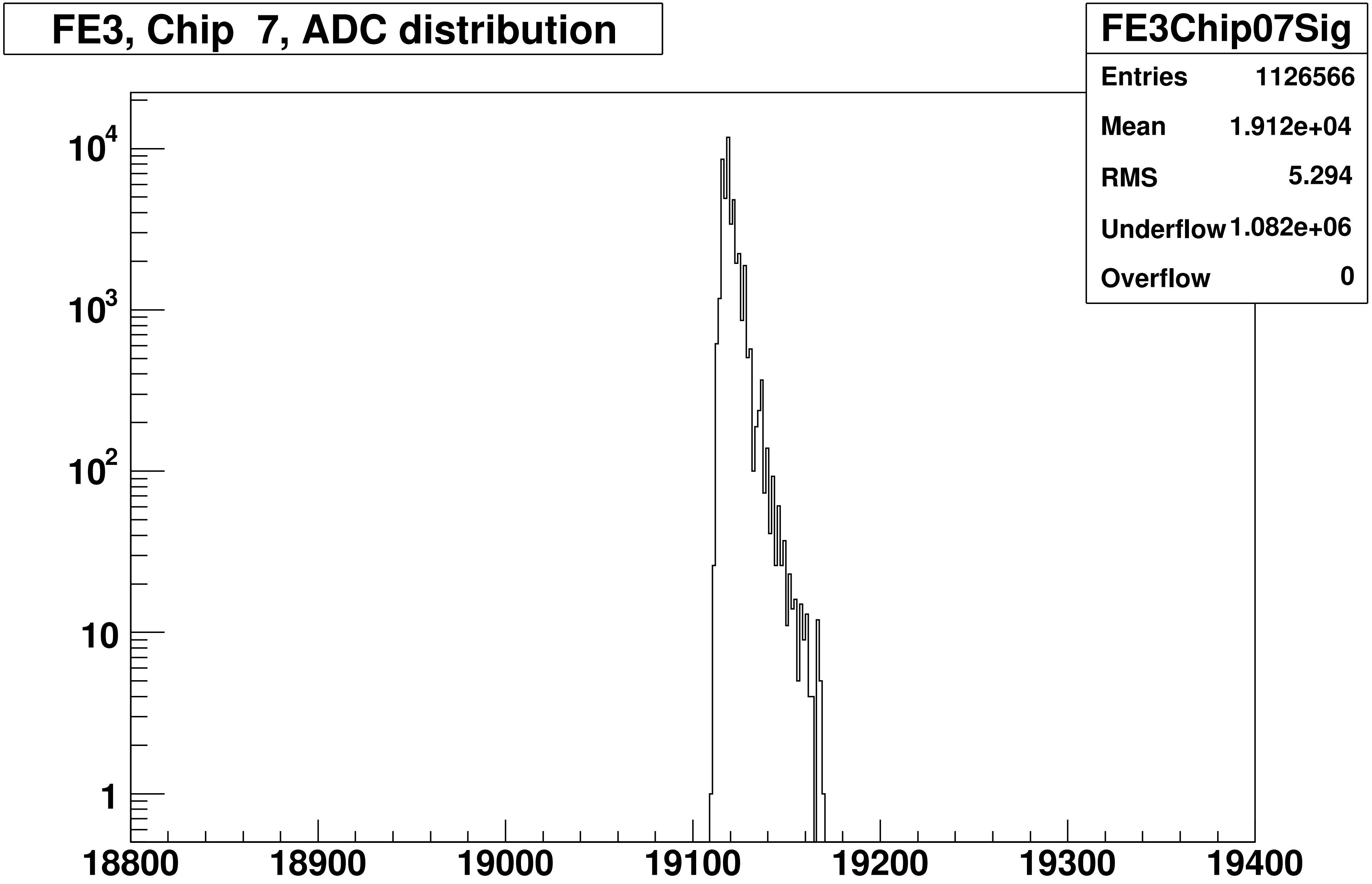
19100
19200
19300
19400


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 1126566
Mean 760.7
RMS 3736
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 1126566
Mean 0.3753
RMS 1.223
Underflow 0
Overflow 4.48e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 1126566
Mean 1.912e+04
RMS 5.294
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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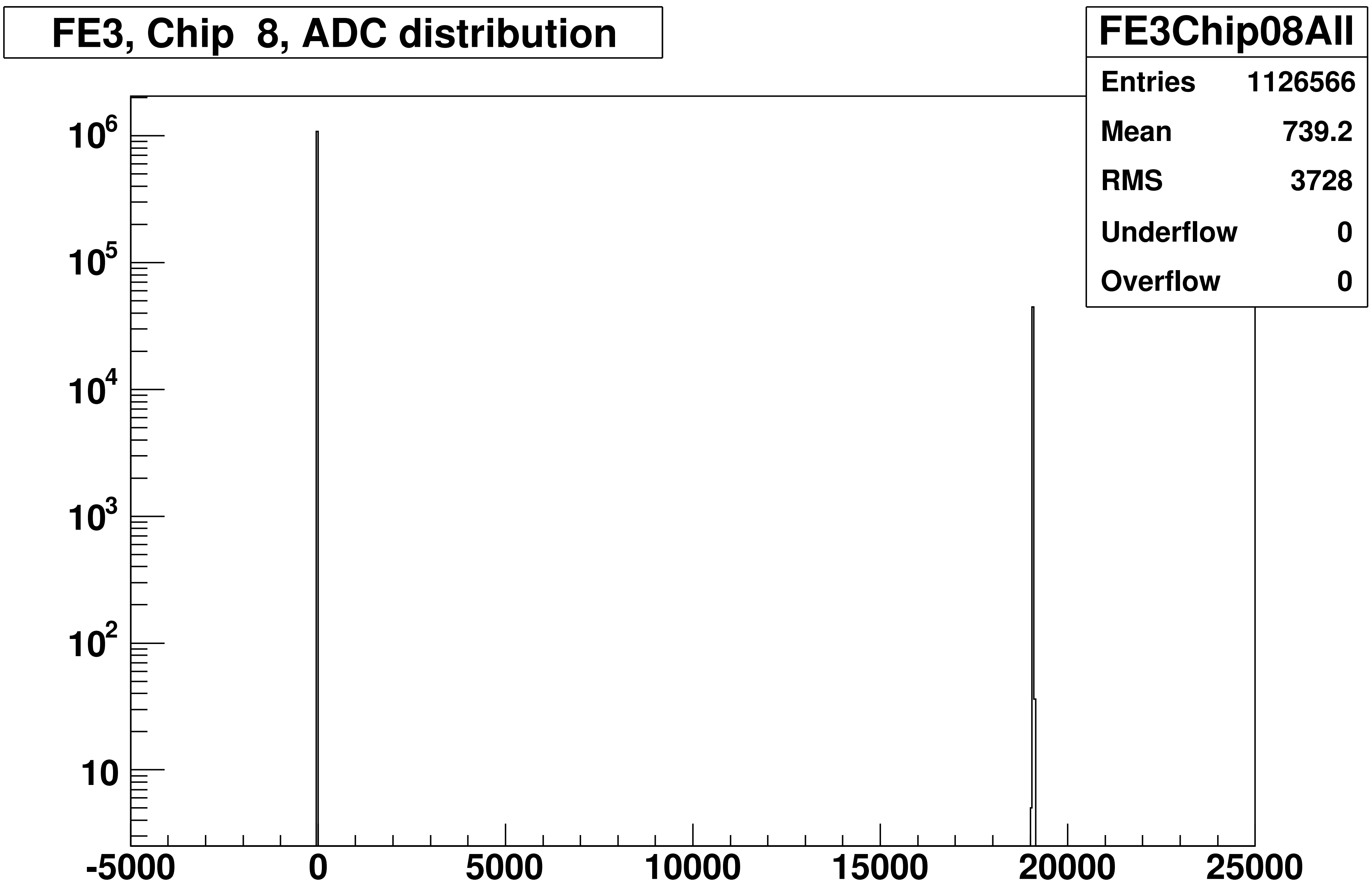
19100
19200
19300
19400



FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 1126566
Mean 739.2
RMS 3728
Underflow 0
Overflow 0
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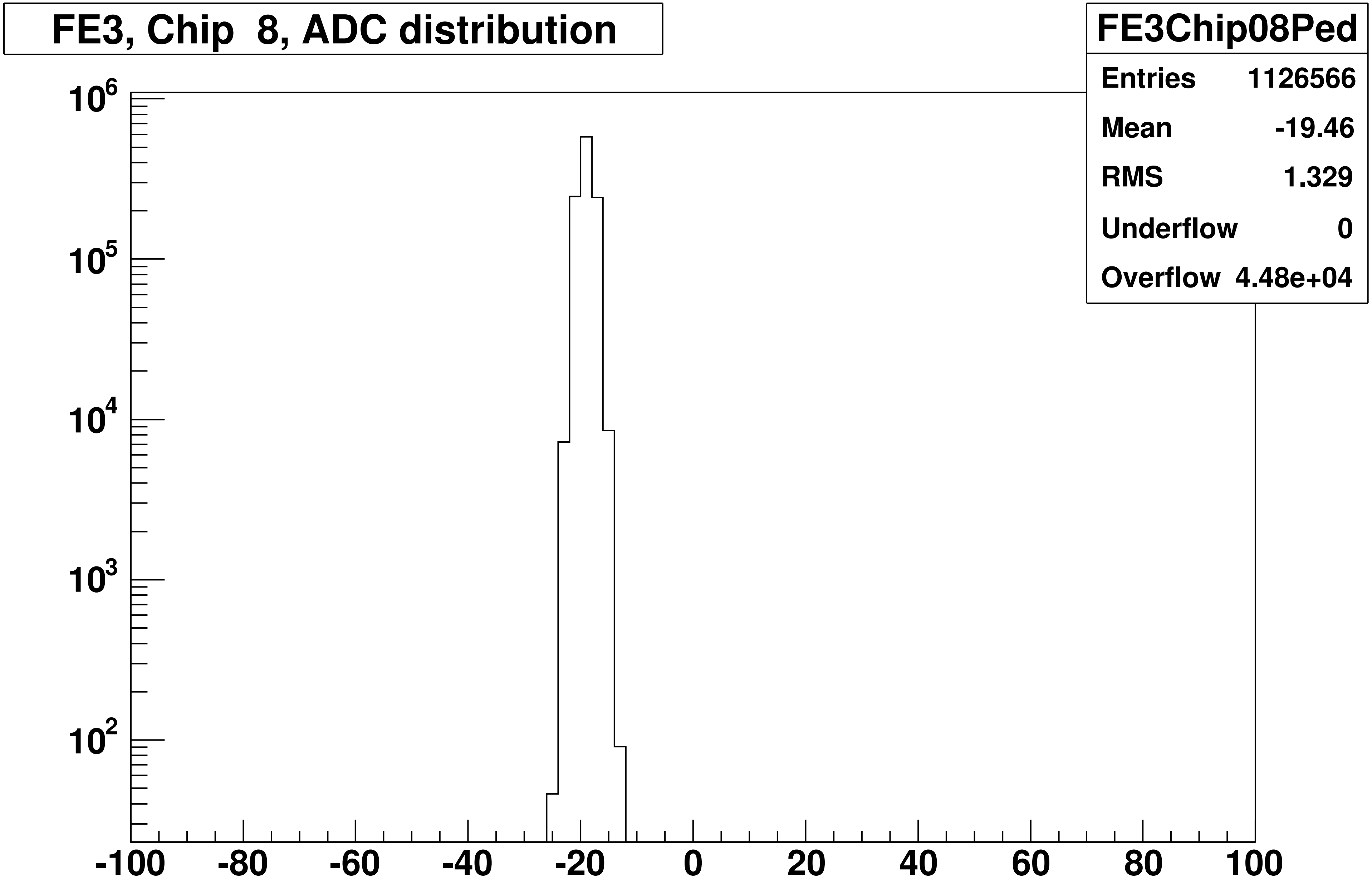
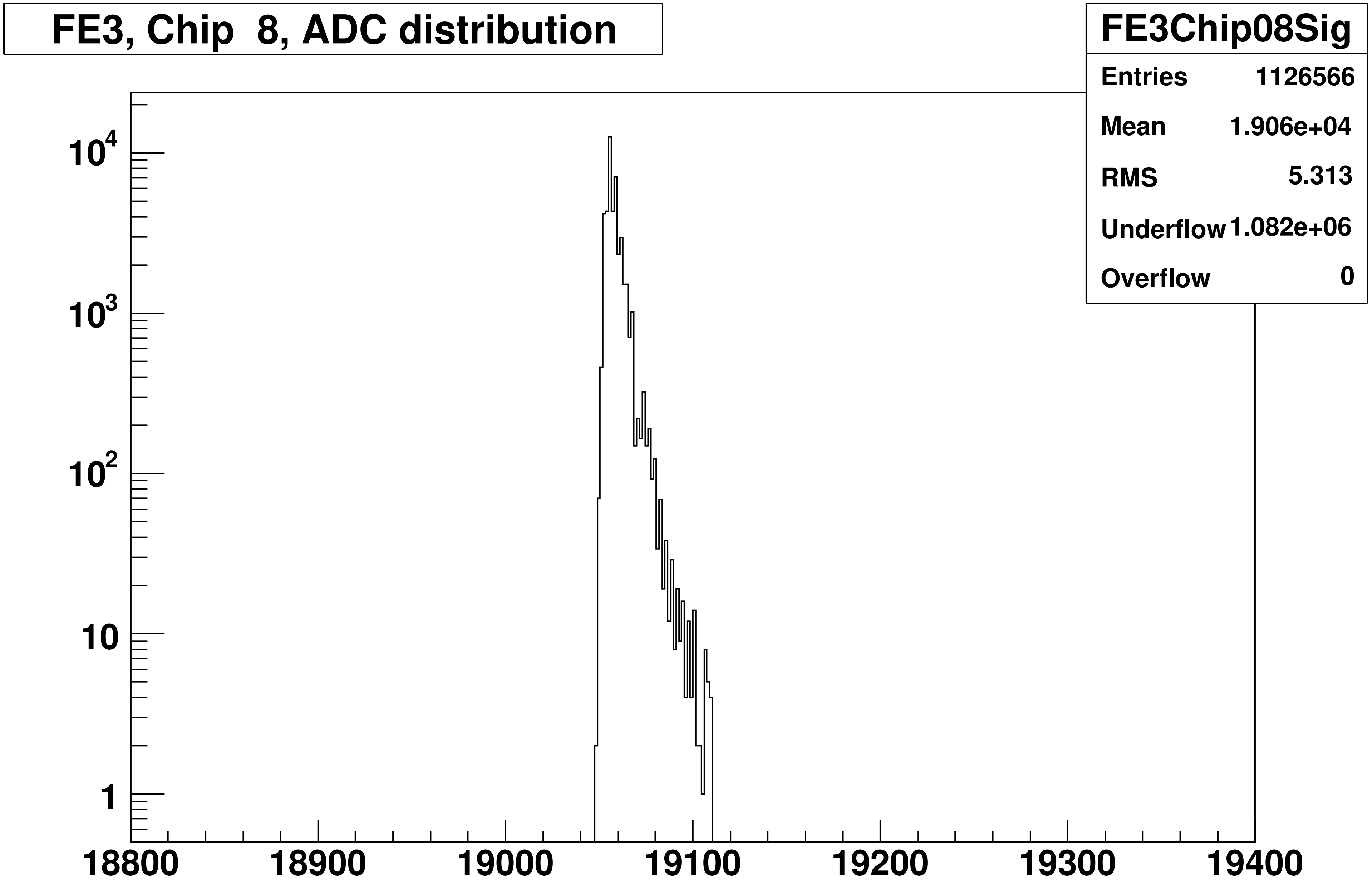
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 1126566
Mean -19.46
RMS 1.329
Underflow 0
Overflow 4.48e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 1126566
Mean 1.906e+04
RMS 5.313
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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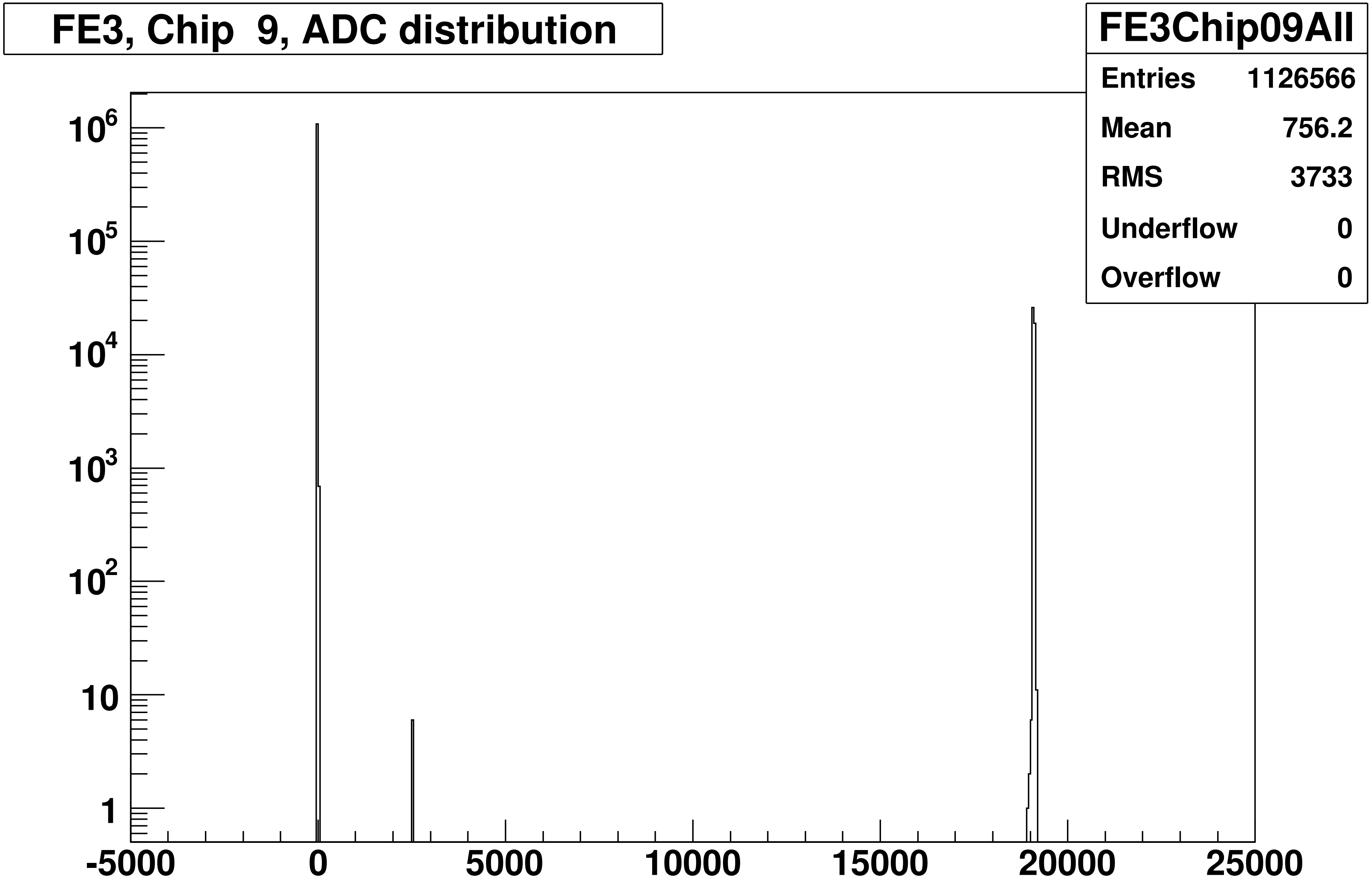
19100
19200
19300
19400


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 1126566
Mean 756.2
RMS 3733
Underflow 0
Overflow 0
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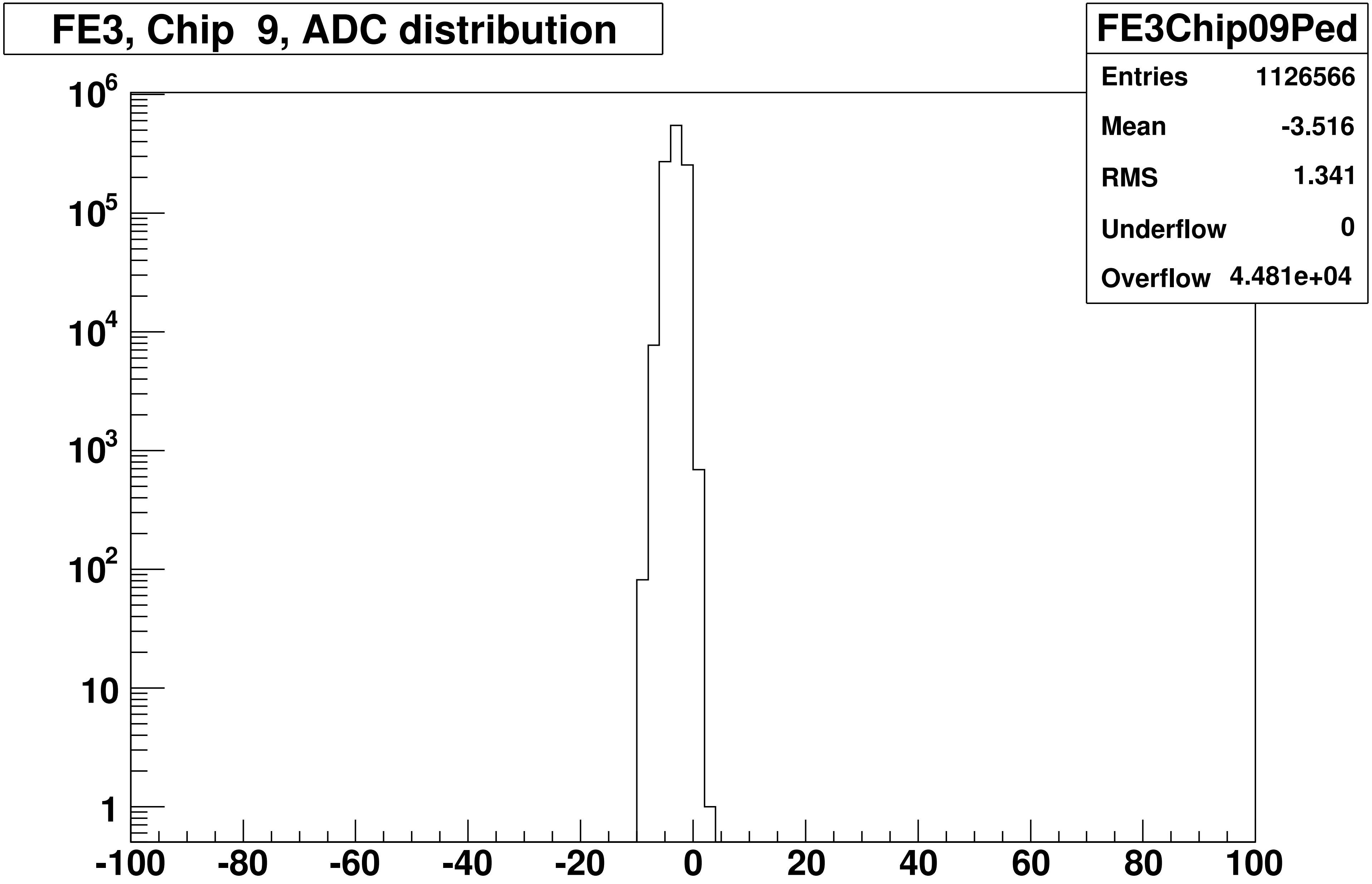
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 1126566
Mean -3.516
RMS 1.341
Underflow 0
Overflow 4.481e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
0
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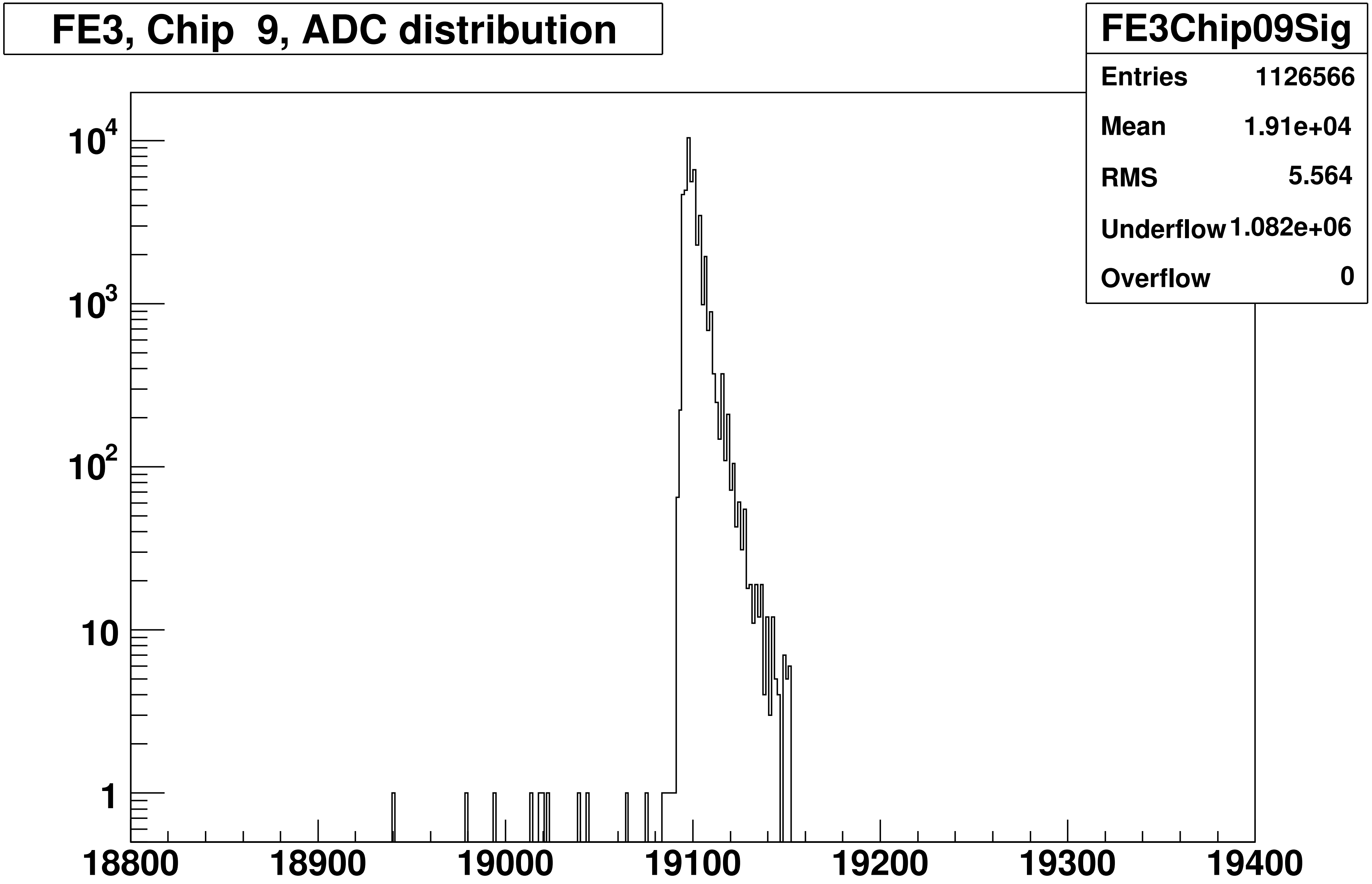
40
60
80
100



FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 1126566
Mean 1.91e+04
RMS 5.564
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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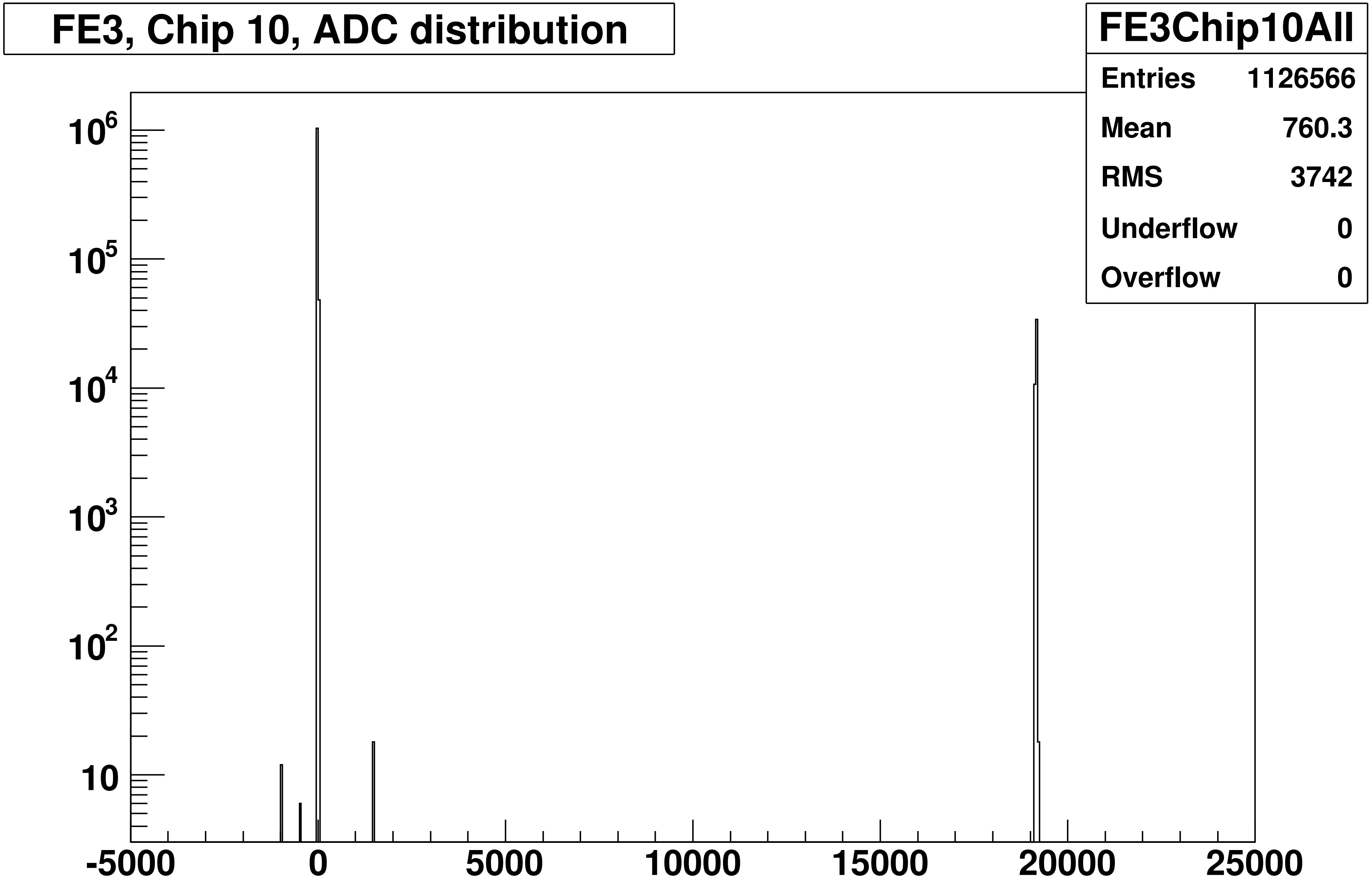
19100
19200
19300
19400



FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 1126566
Mean 760.3
RMS 3742
Underflow 0
Overflow 0
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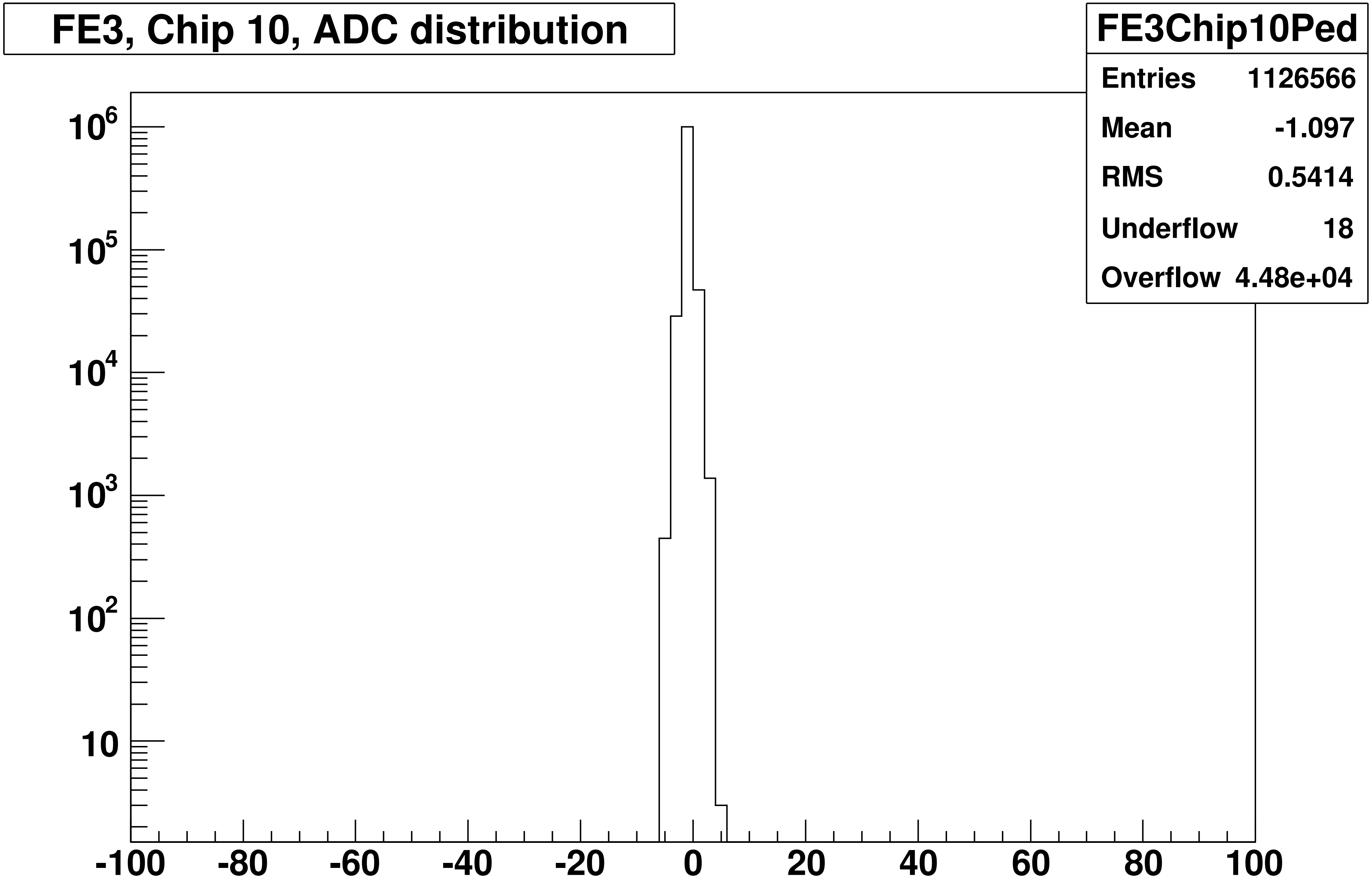
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 1126566
Mean -1.097
RMS 0.5414
Underflow 18
Overflow 4.48e+04
10^6
10^5
10^4
10^3
10^2
10
-100
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-60
-40
-20
0
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80
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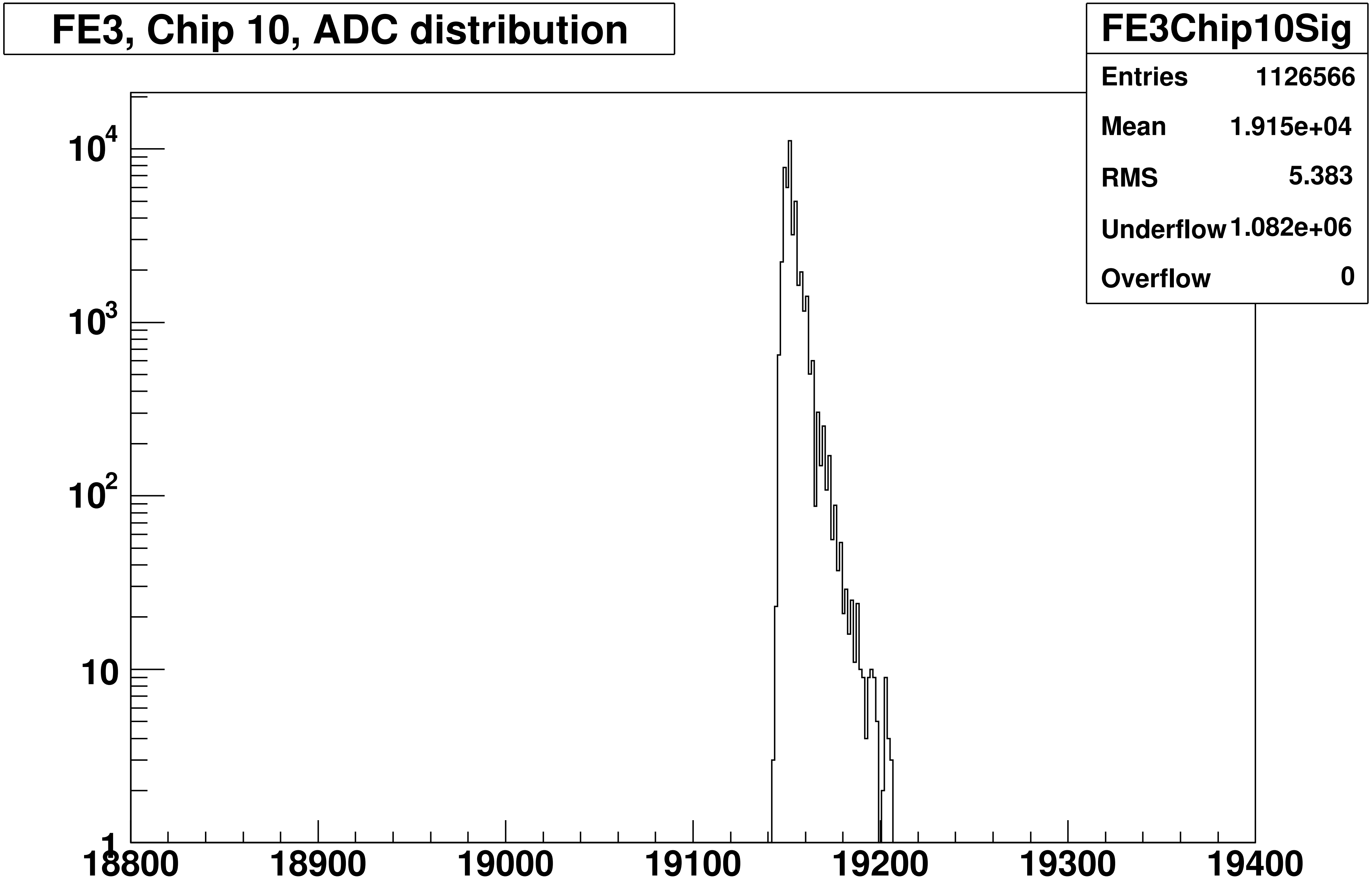

FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 1126566
Mean 1.915e+04
RMS 5.383
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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19100
19200
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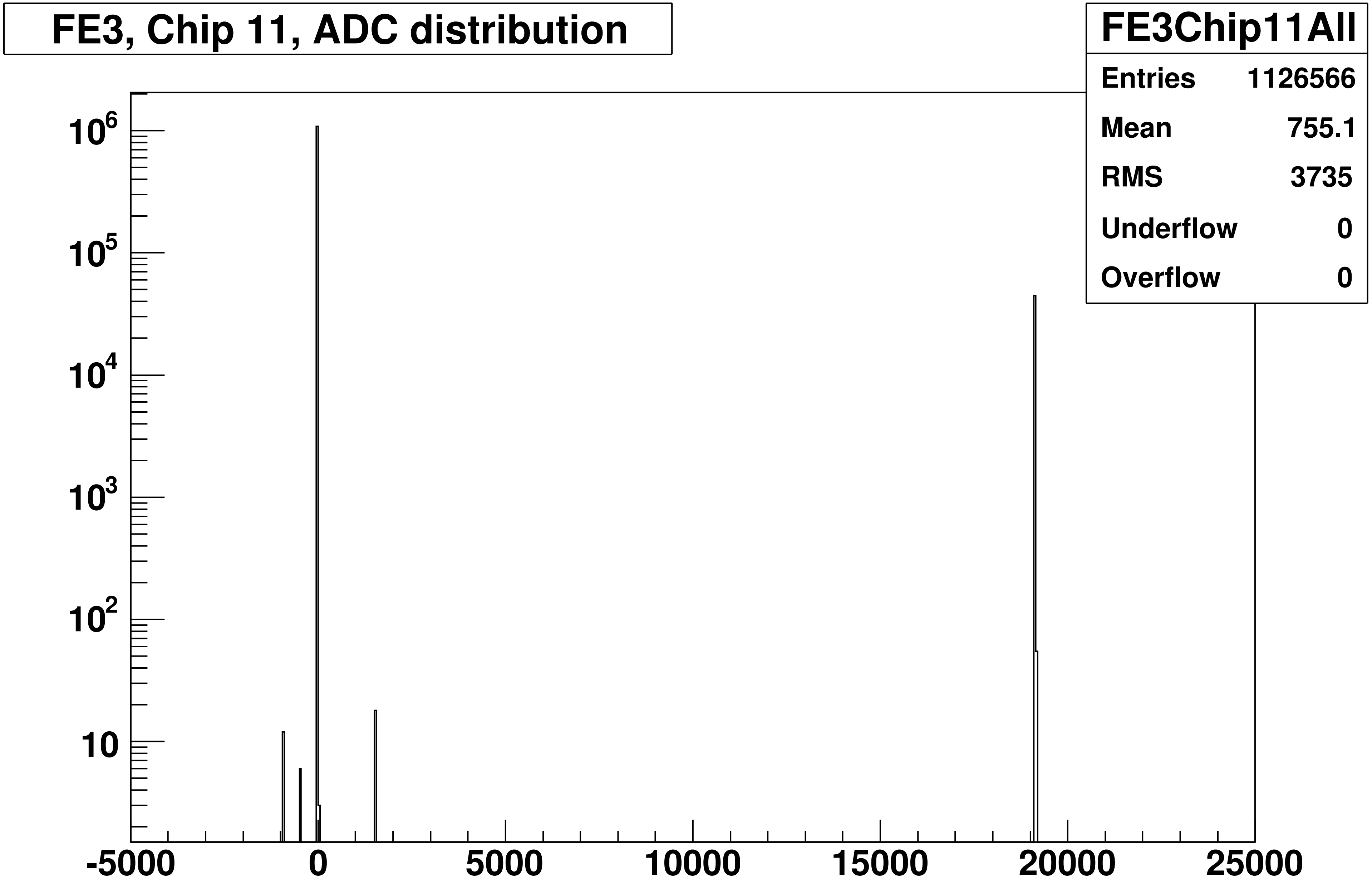
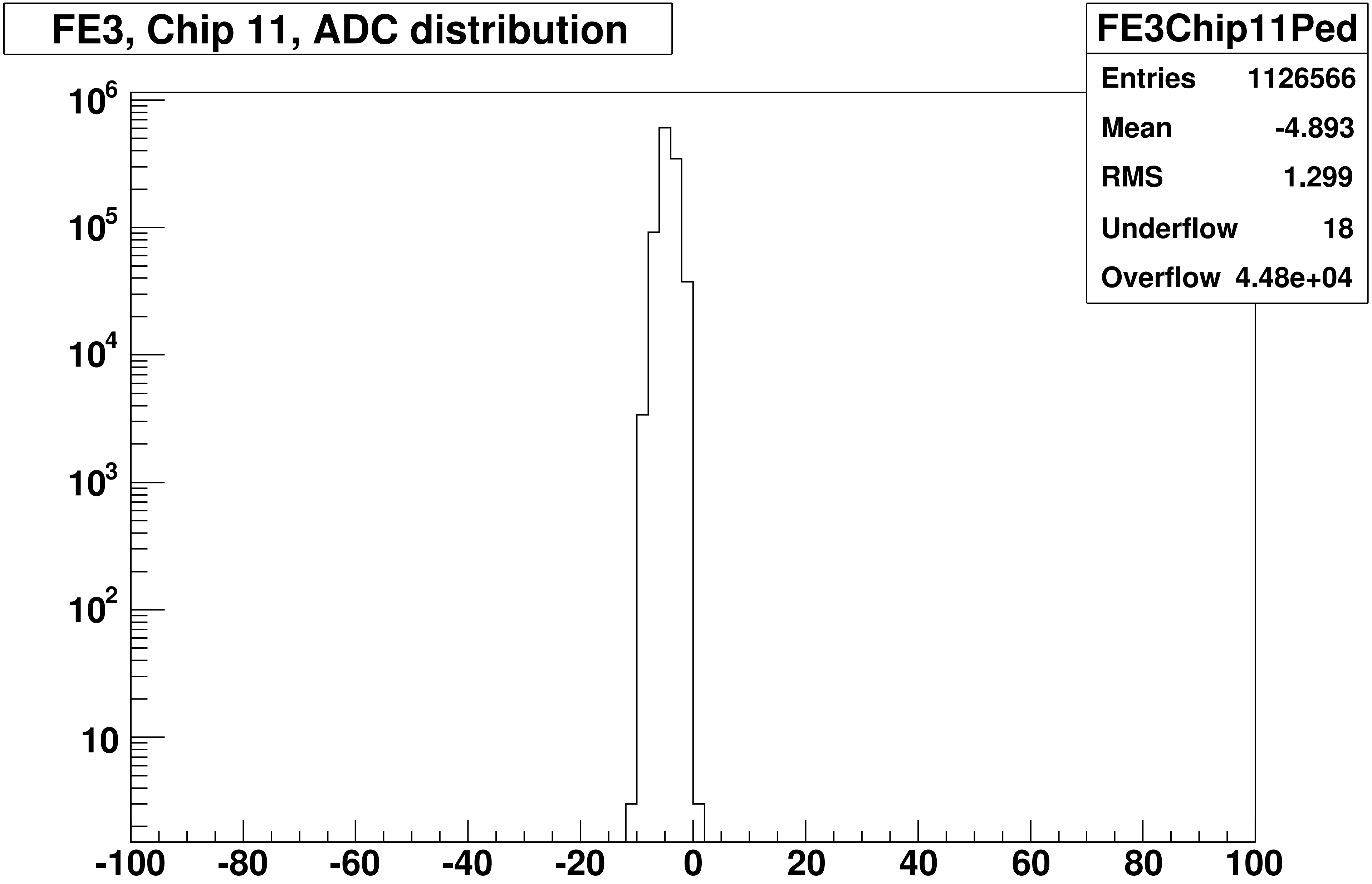
19400


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 1126566
Mean 755.1
RMS 3735
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 1126566
Mean -4.893
RMS 1.299
Underflow 18
Overflow 4.48e+04
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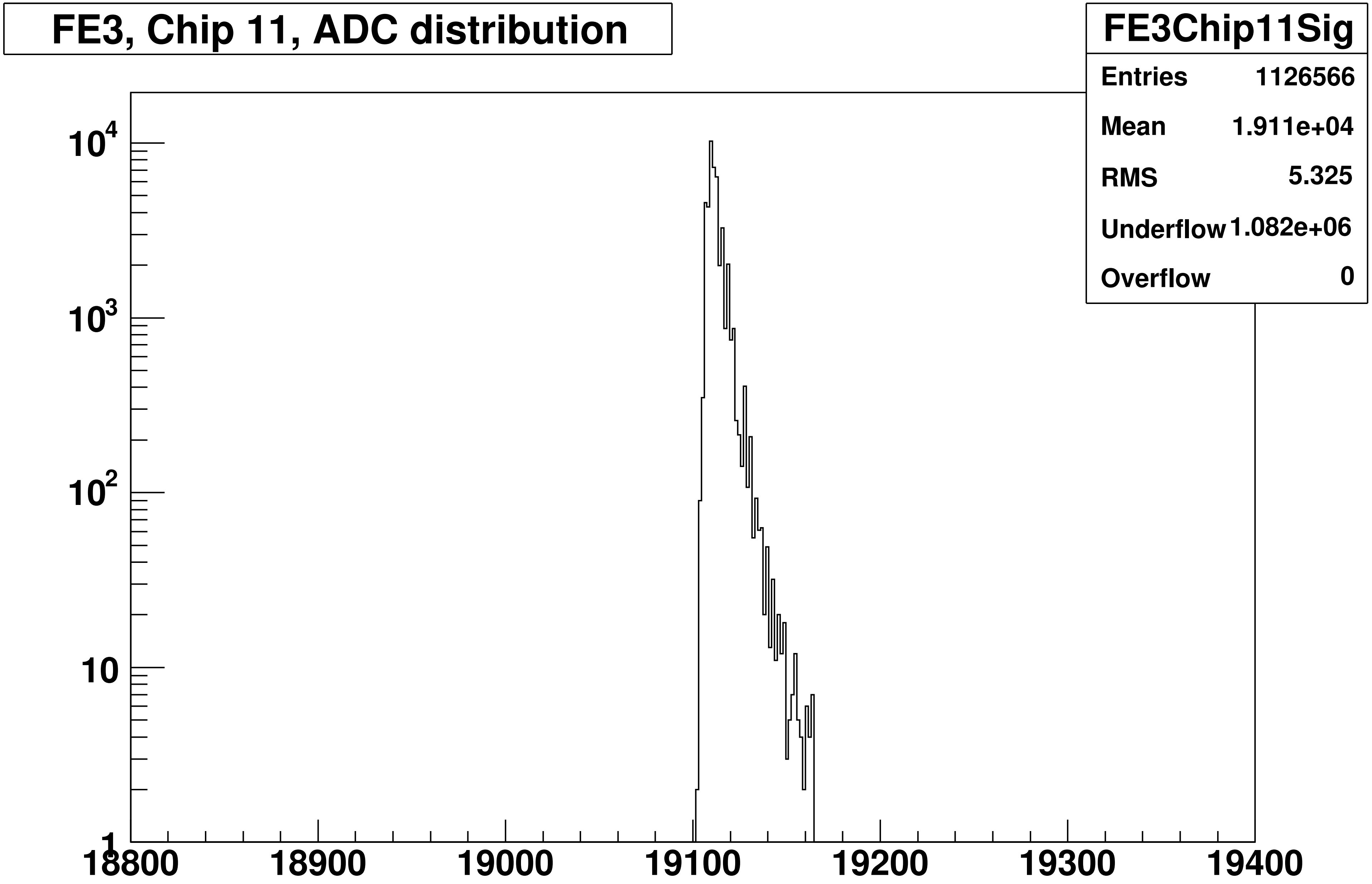
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 1126566
Mean 1.911e+04
RMS 5.325
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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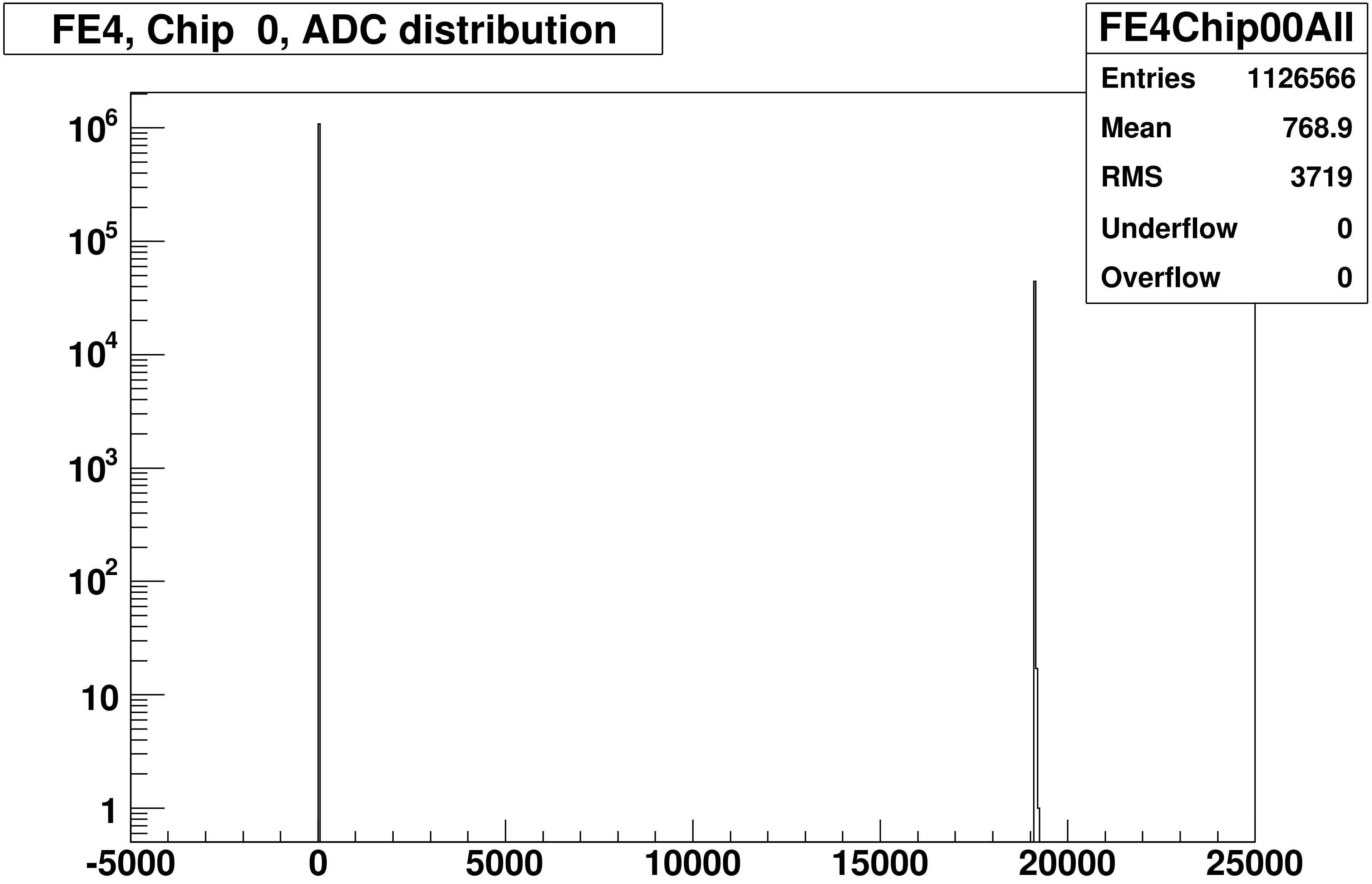
19200
19300
19400


FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 1126566
Mean 768.9
RMS 3719
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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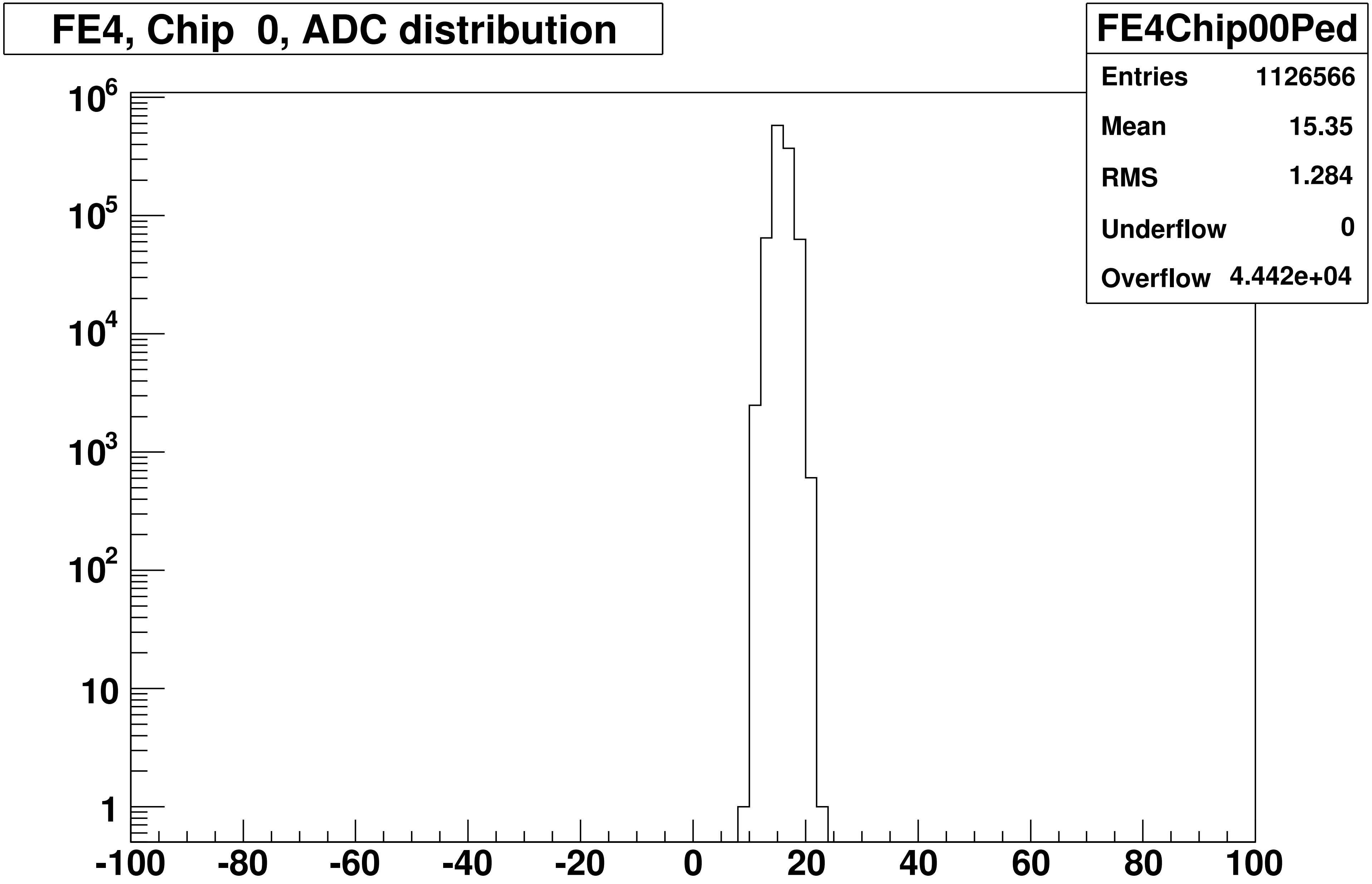
10000
15000
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 1126566
Mean 15.35
RMS 1.284
Underflow 0
Overflow 4.442e+04
10^6
10^5
10^4
10^3
10^2
10
1
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-40
-20
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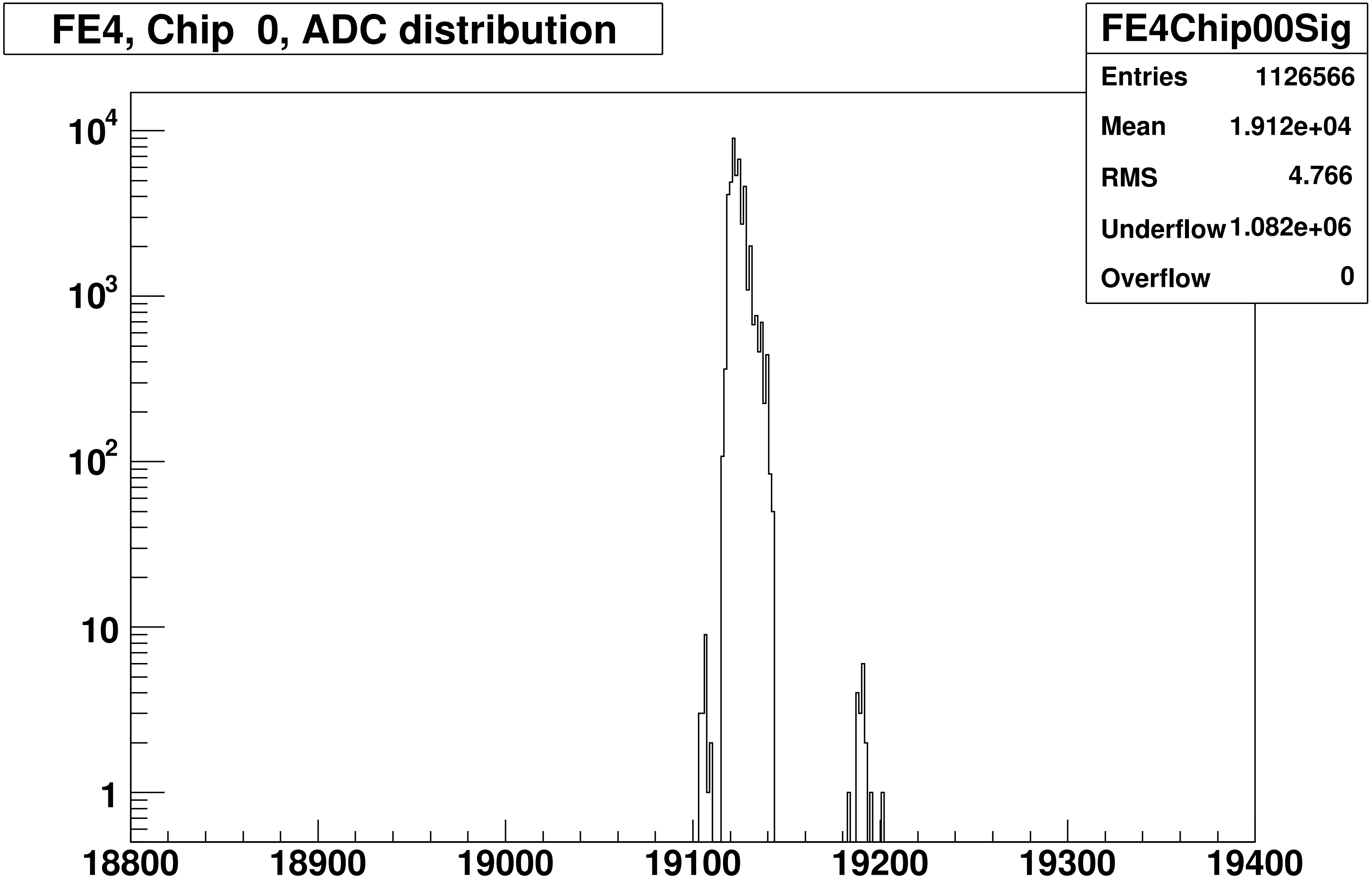
80
100


FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 1126566
Mean 1.912e+04
RMS 4.766
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
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18900
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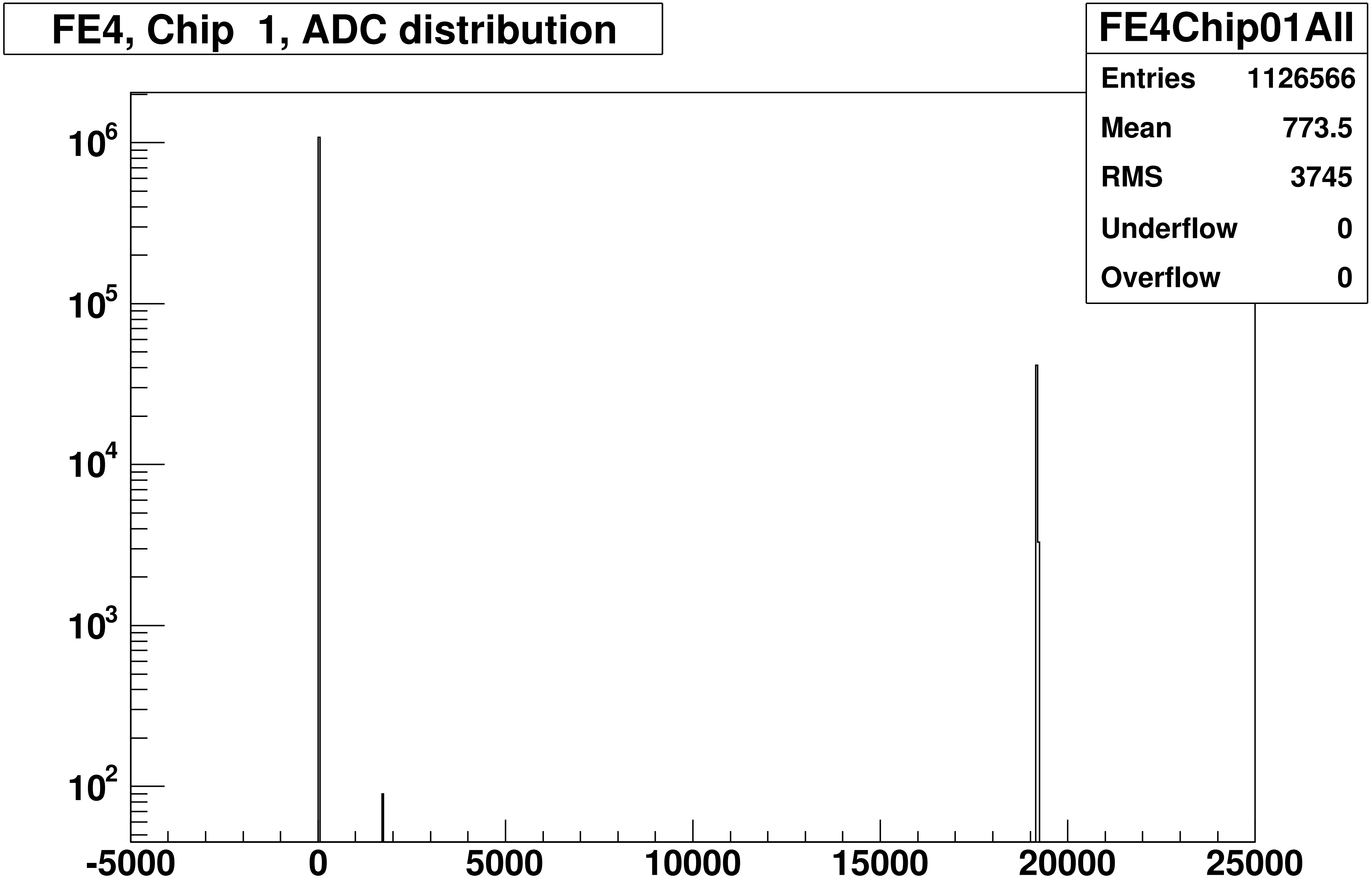

FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 1126566
Mean 773.5
RMS 3745
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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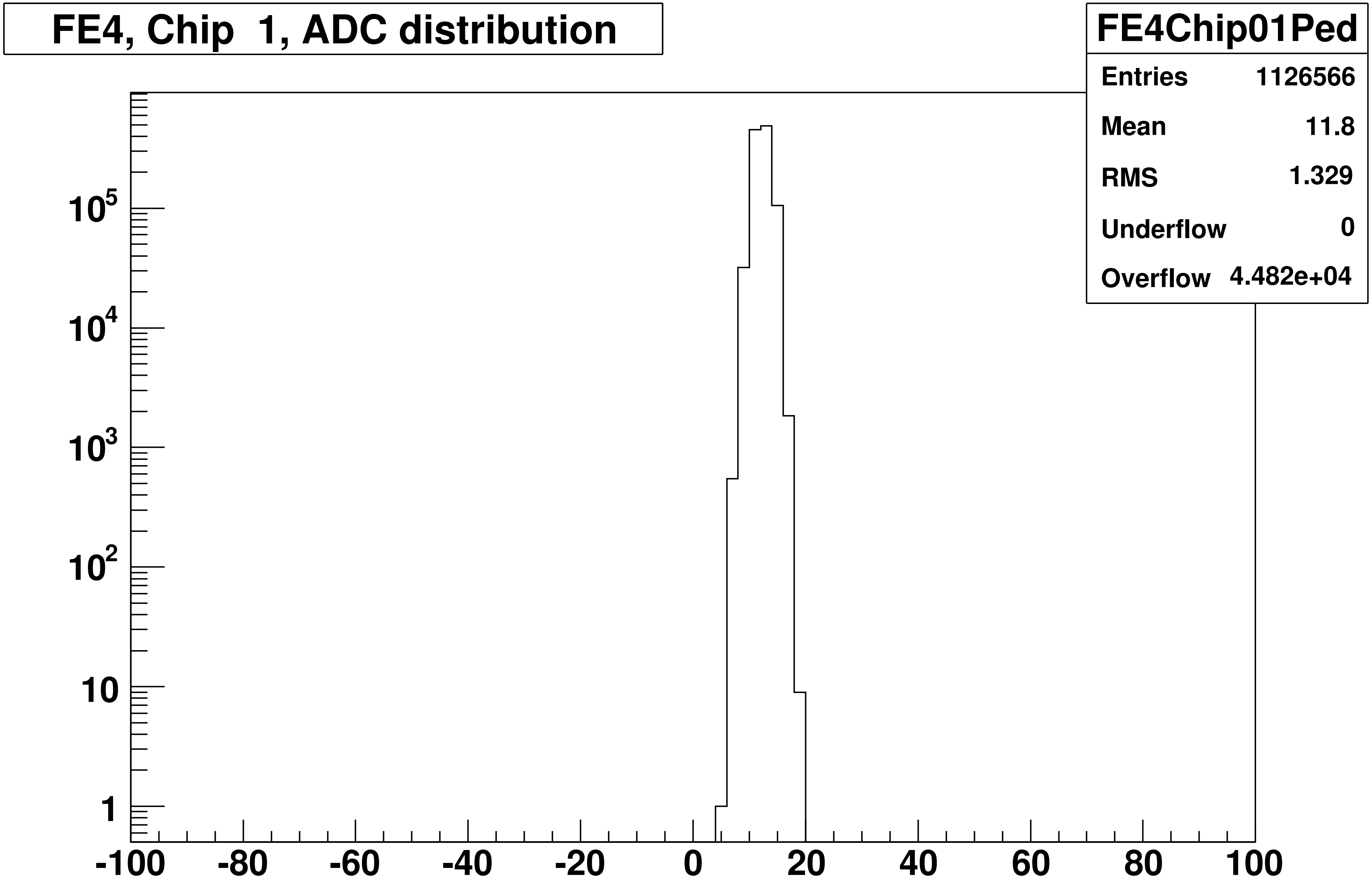
10000
15000
20000
25000



FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 1126566
Mean 11.8
RMS 1.329
Underflow 0
Overflow 4.482e+04
10^5
10^4
10^3
10^2
10
1
-100
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-20
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80
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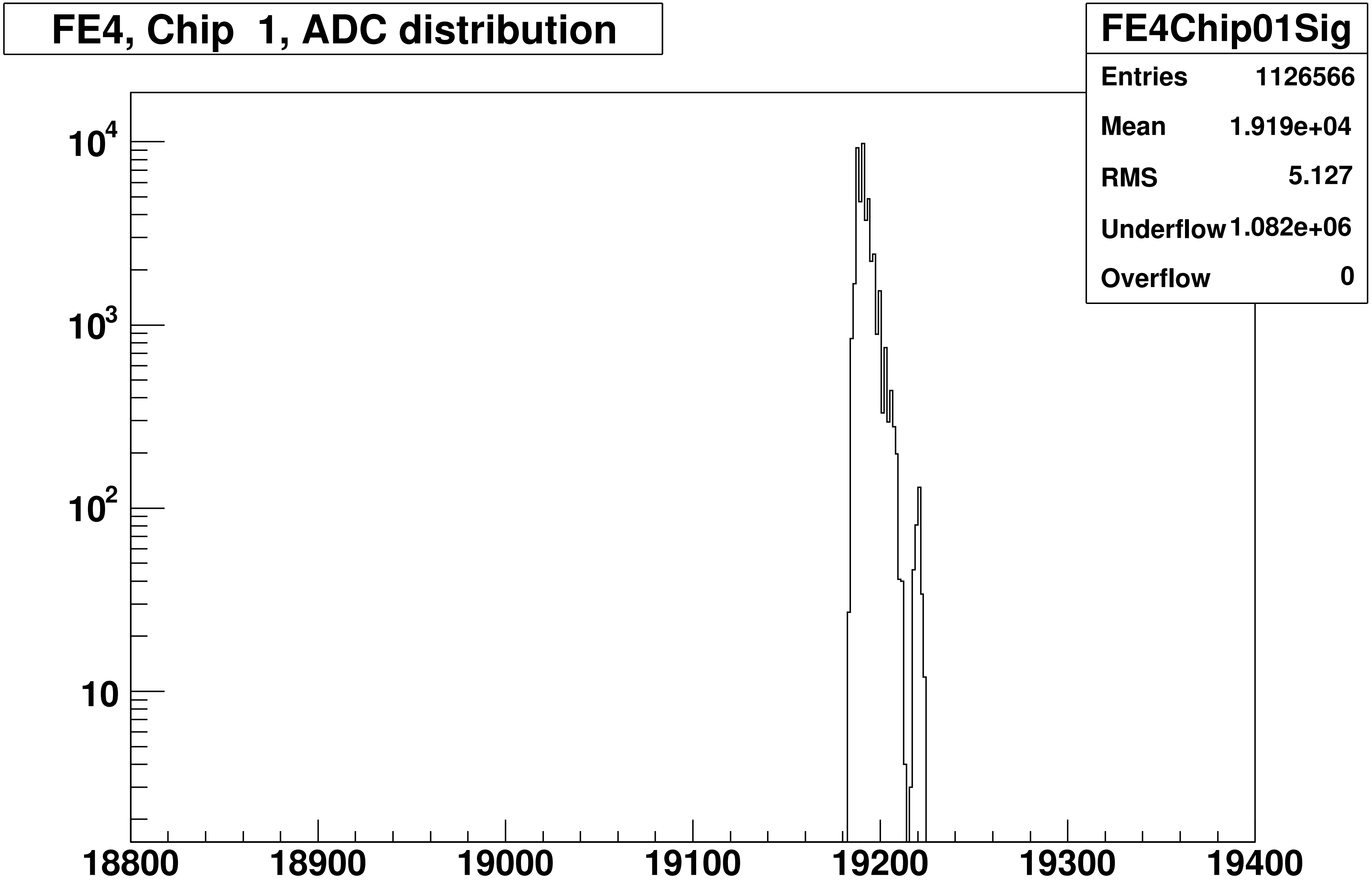
FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 1126566
Mean 1.919e+04
RMS 5.127
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
18800
18900
19000
19100
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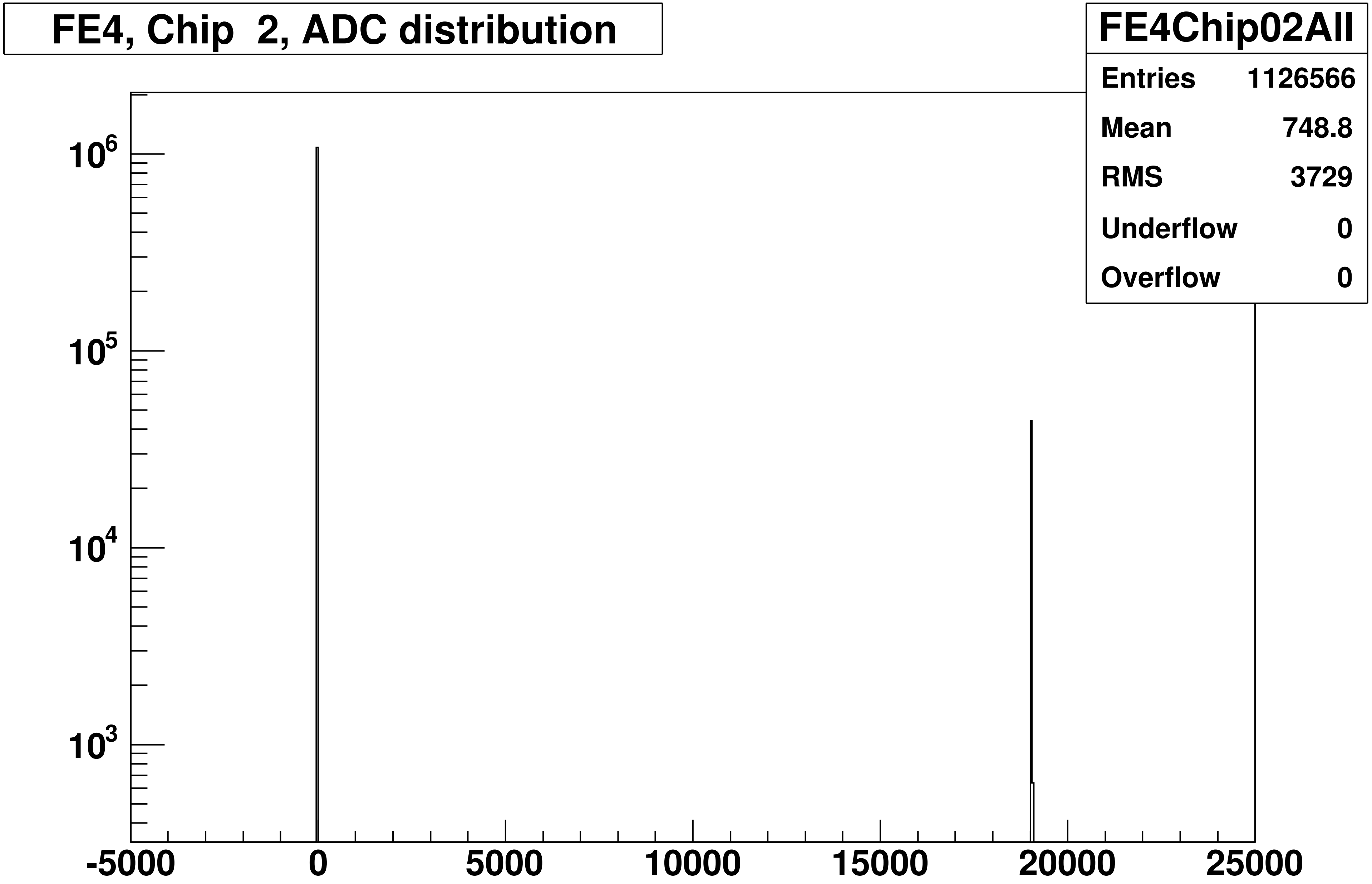
19400



FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 1126566
Mean 748.8
RMS 3729
Underflow 0
Overflow 0
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10^5
10^4
10^3
-5000
0
5000
10000
15000
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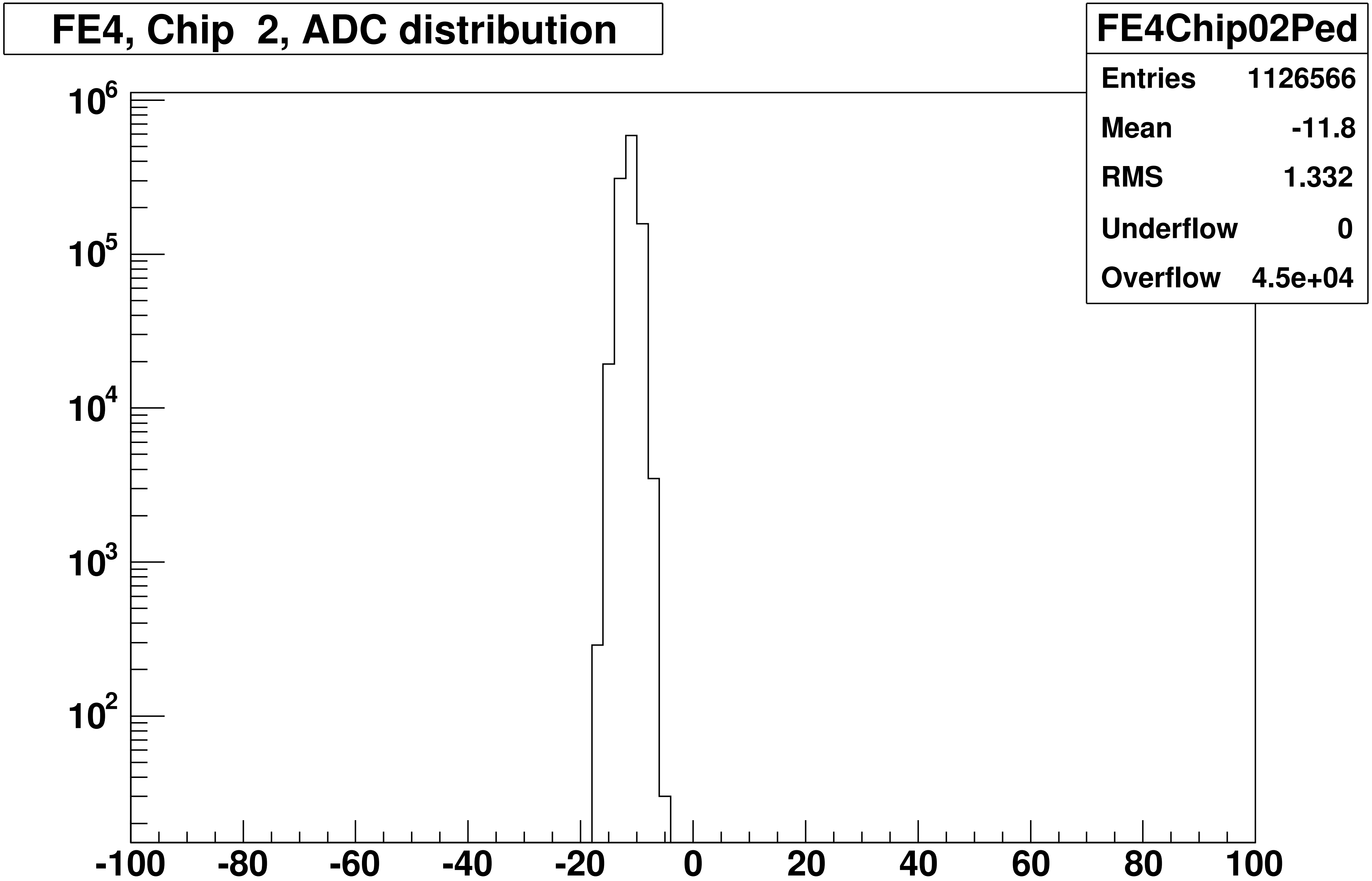
25000



FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 1126566
Mean -11.8
RMS 1.332
Underflow 0
Overflow 4.5e+04
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10^5
10^4
10^3
10^2
-100
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-40
-20
0
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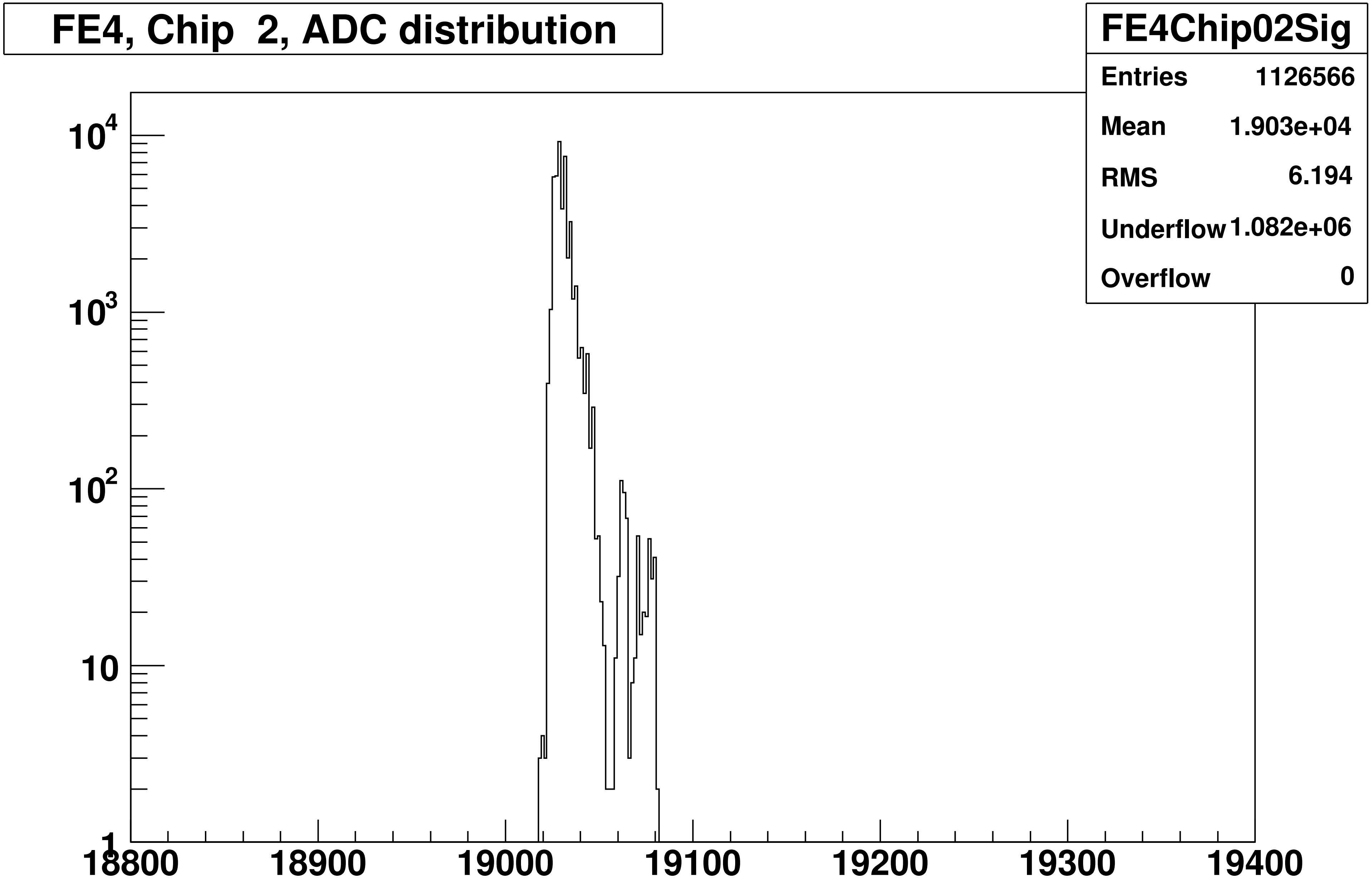
40
60
80
100


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 1126566
Mean 1.903e+04
RMS 6.194
Underflow 1.082e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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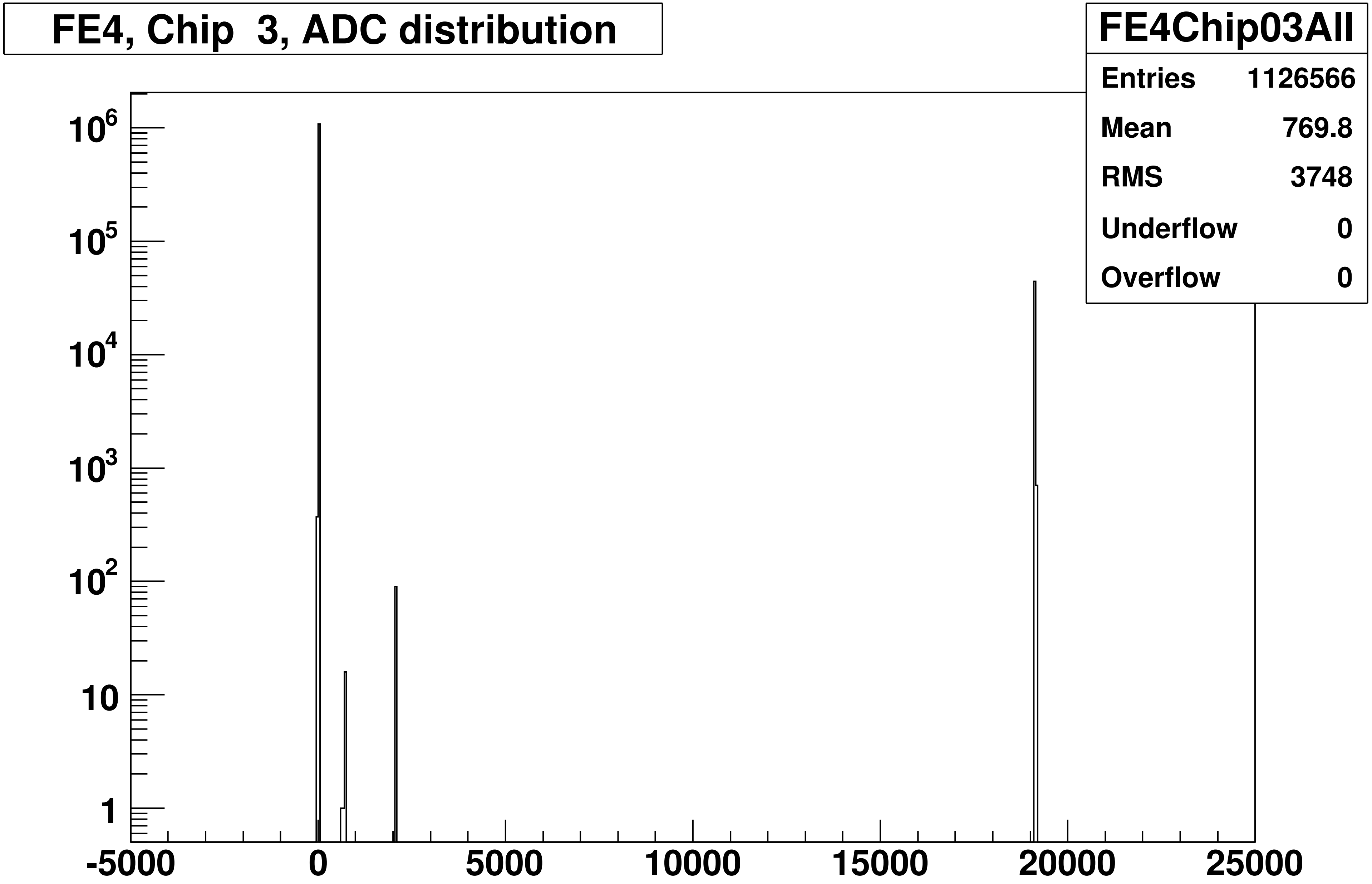
19400



FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 1126566
Mean 769.8
RMS 3748
Underflow 0
Overflow 0
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10^4
10^3
10^2
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1
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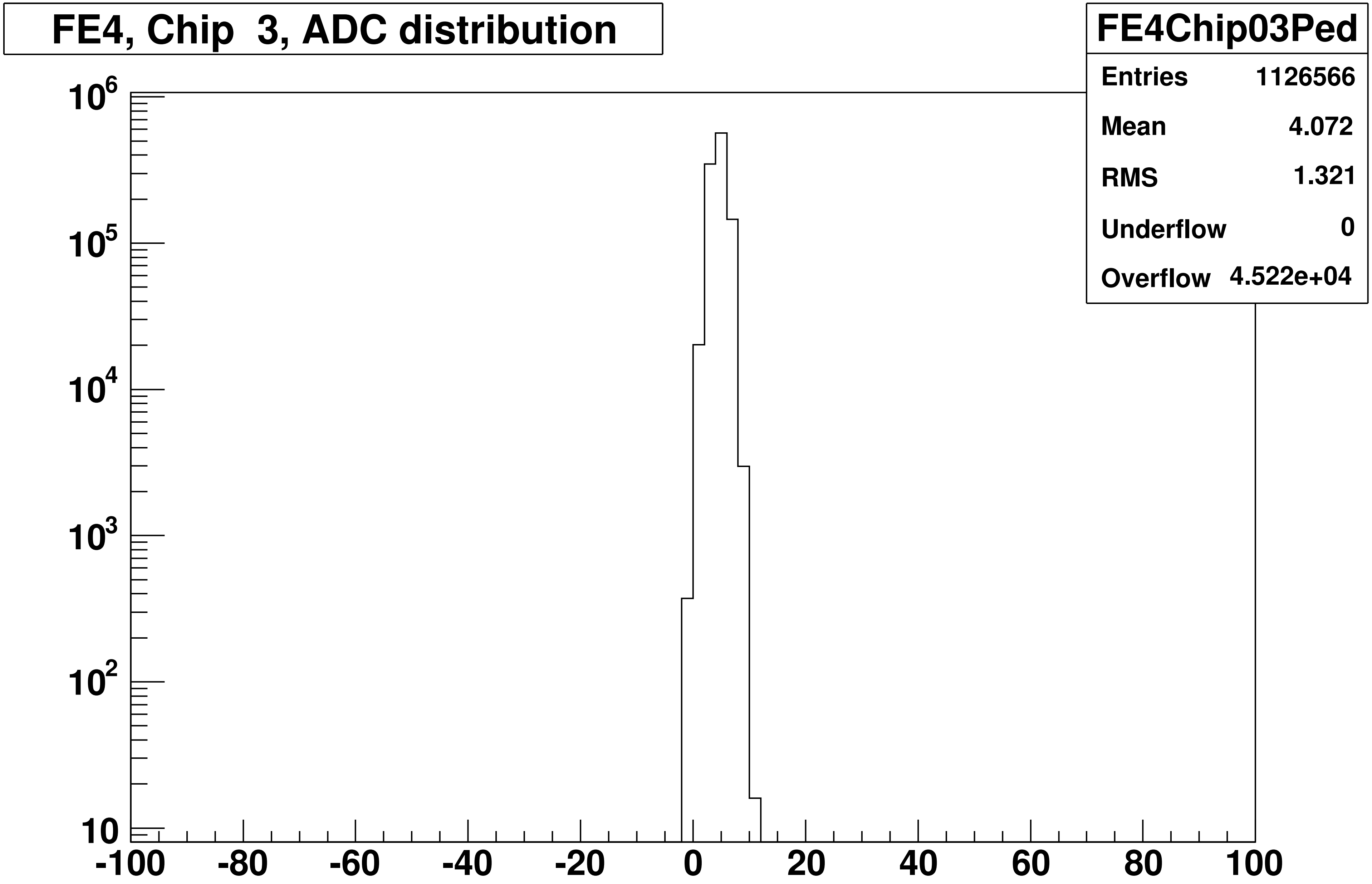
FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 1126566
Mean 4.072
RMS 1.321
Underflow 0
Overflow 4.522e+04
10^6
10^5
10^4
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10^2
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-20
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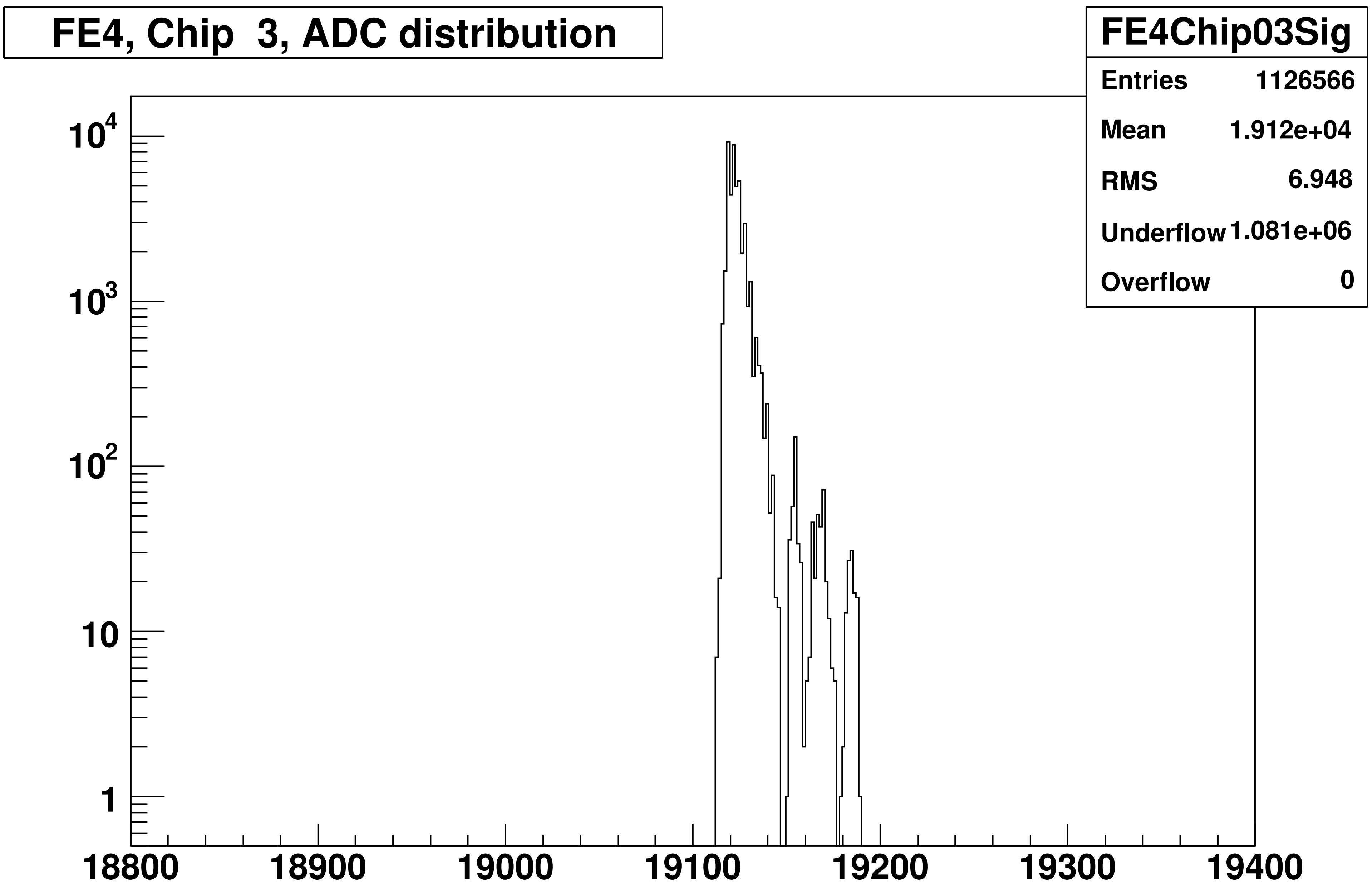
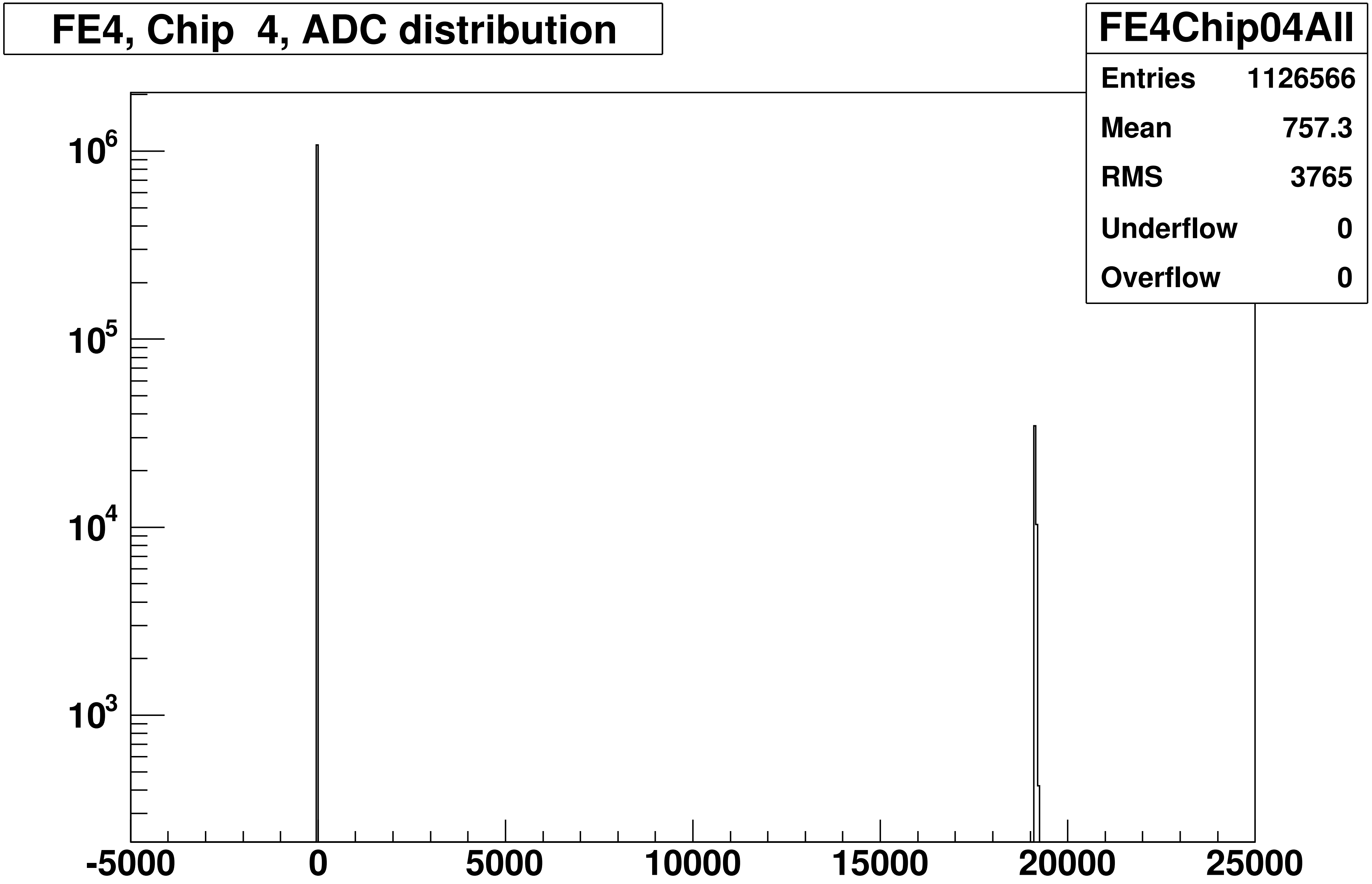
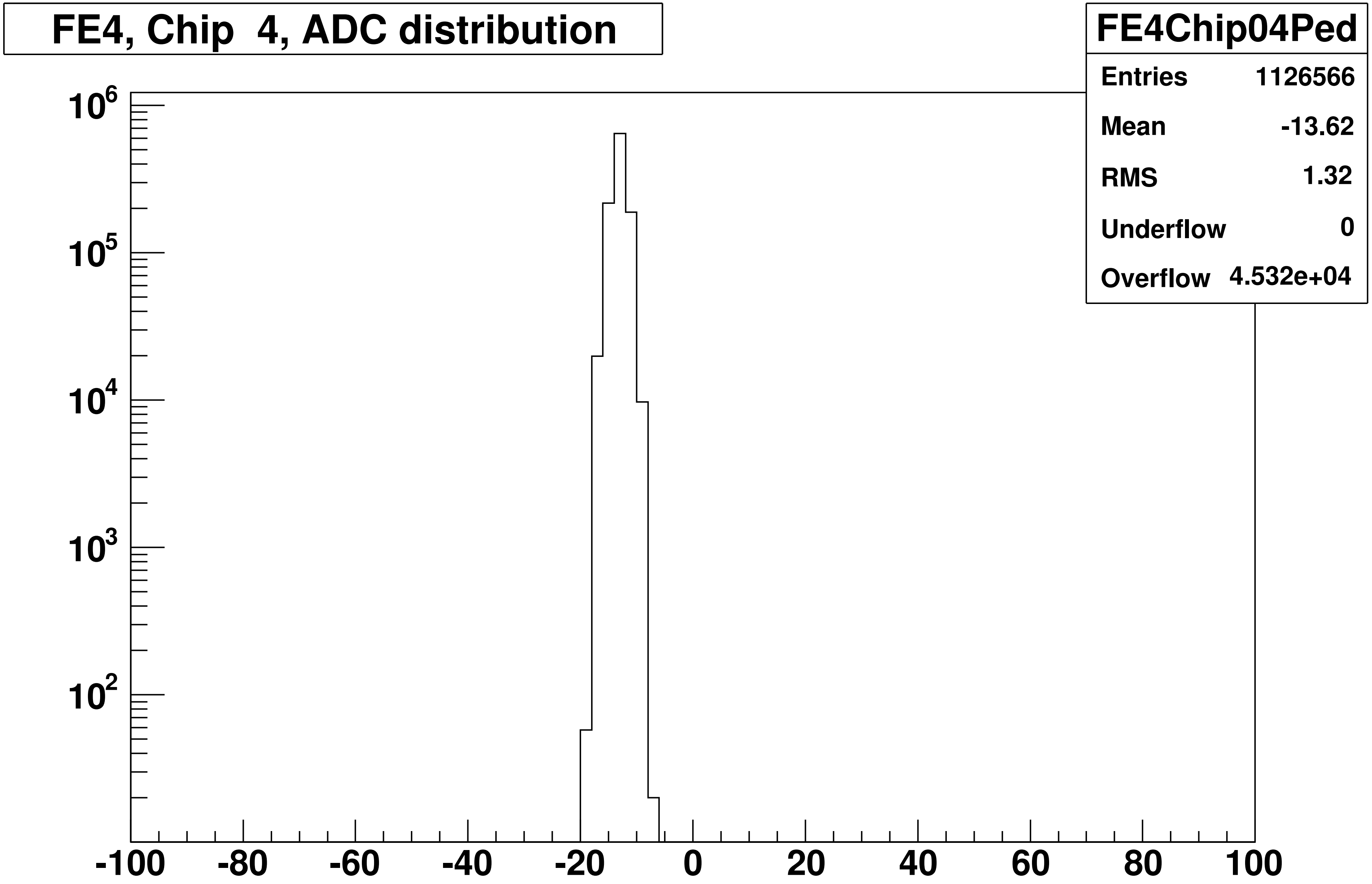
80
100

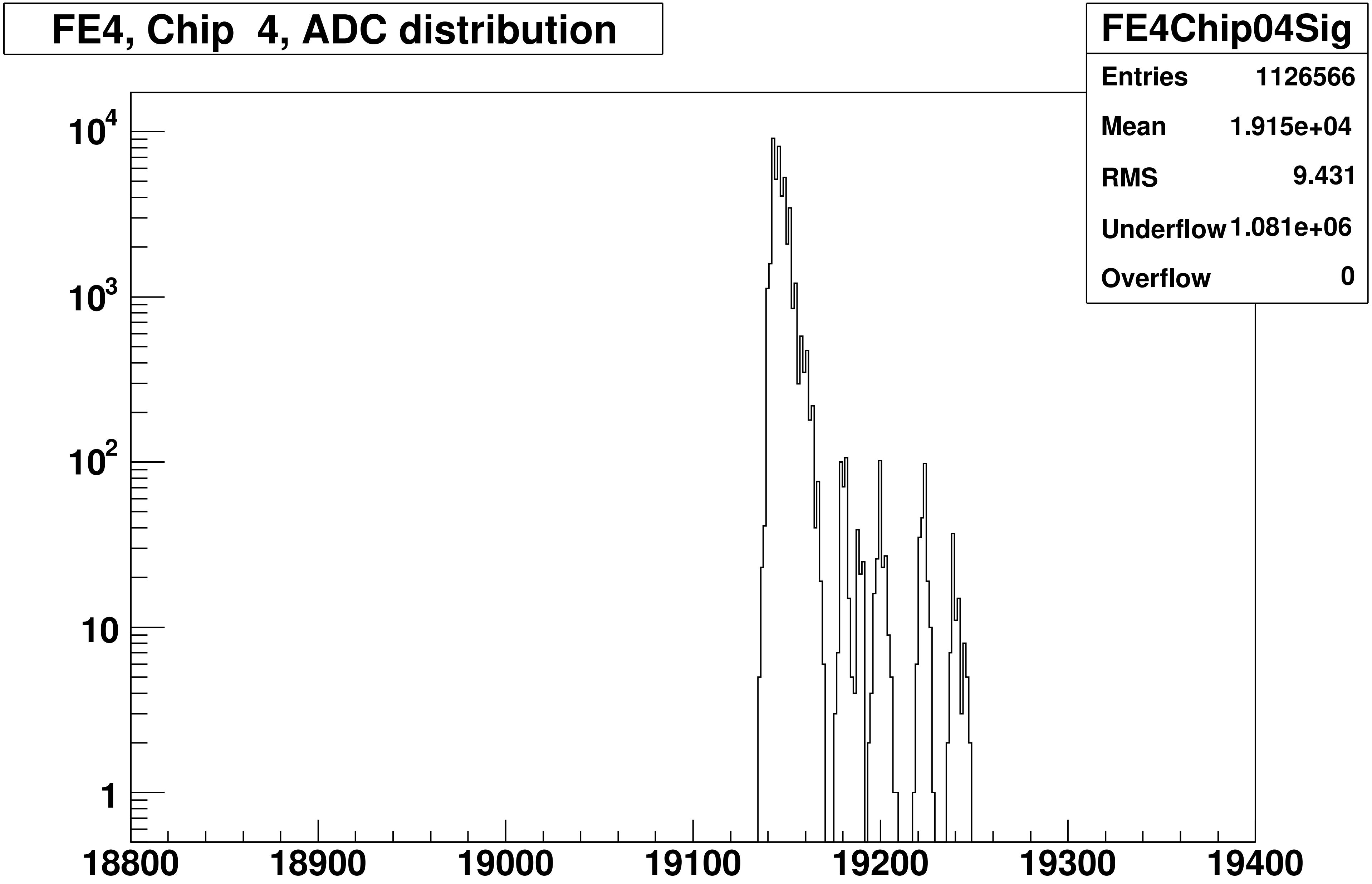

FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 1126566
Mean 1.912e+04
RMS 6.948
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 1126566
Mean 757.3
RMS 3765
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 1126566
Mean -13.62
RMS 1.32
Underflow 0
Overflow 4.532e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 1126566
Mean 1.915e+04
RMS 9.431
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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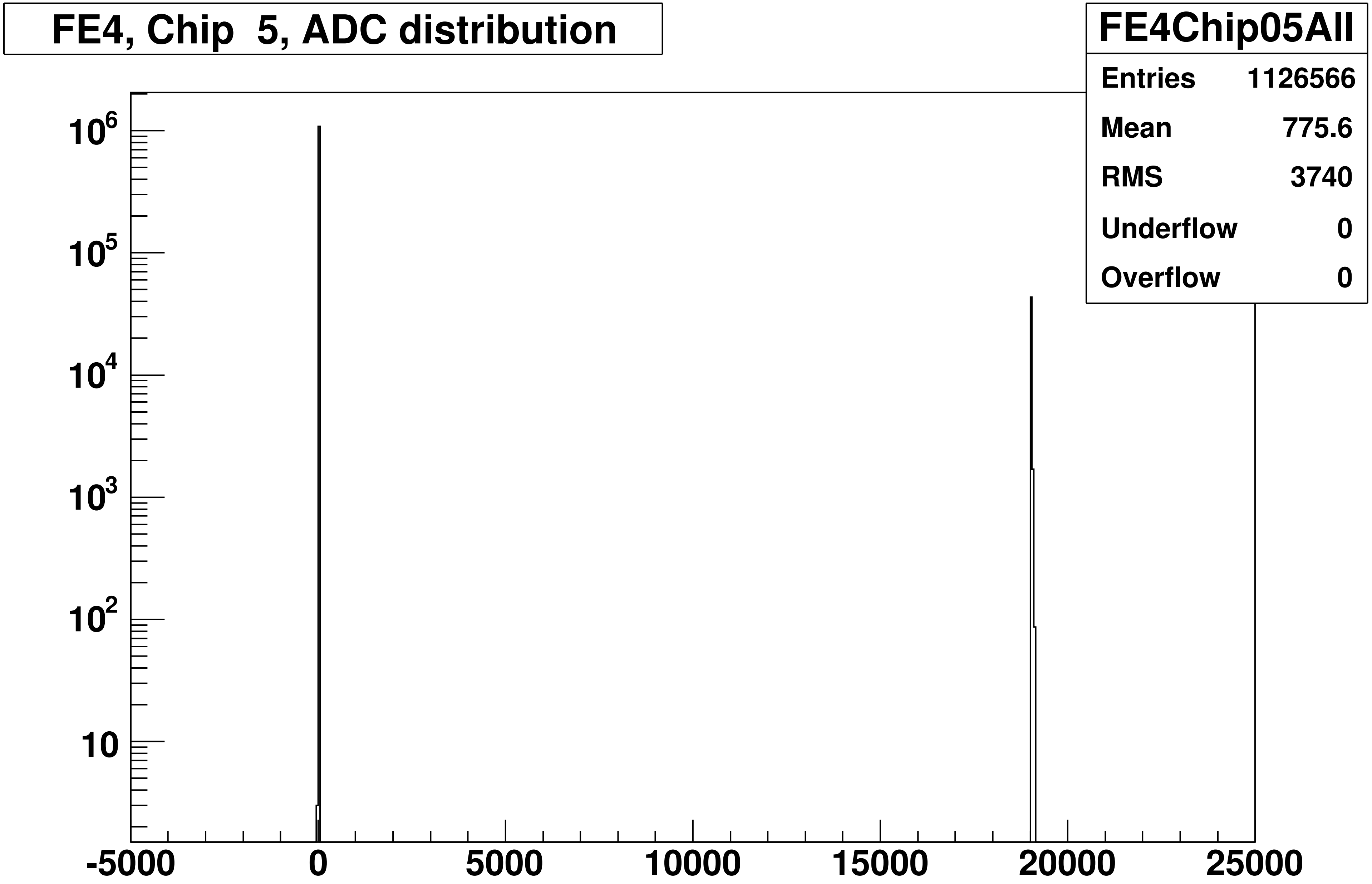
19300
19400



FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 1126566
Mean 775.6
RMS 3740
Underflow 0
Overflow 0
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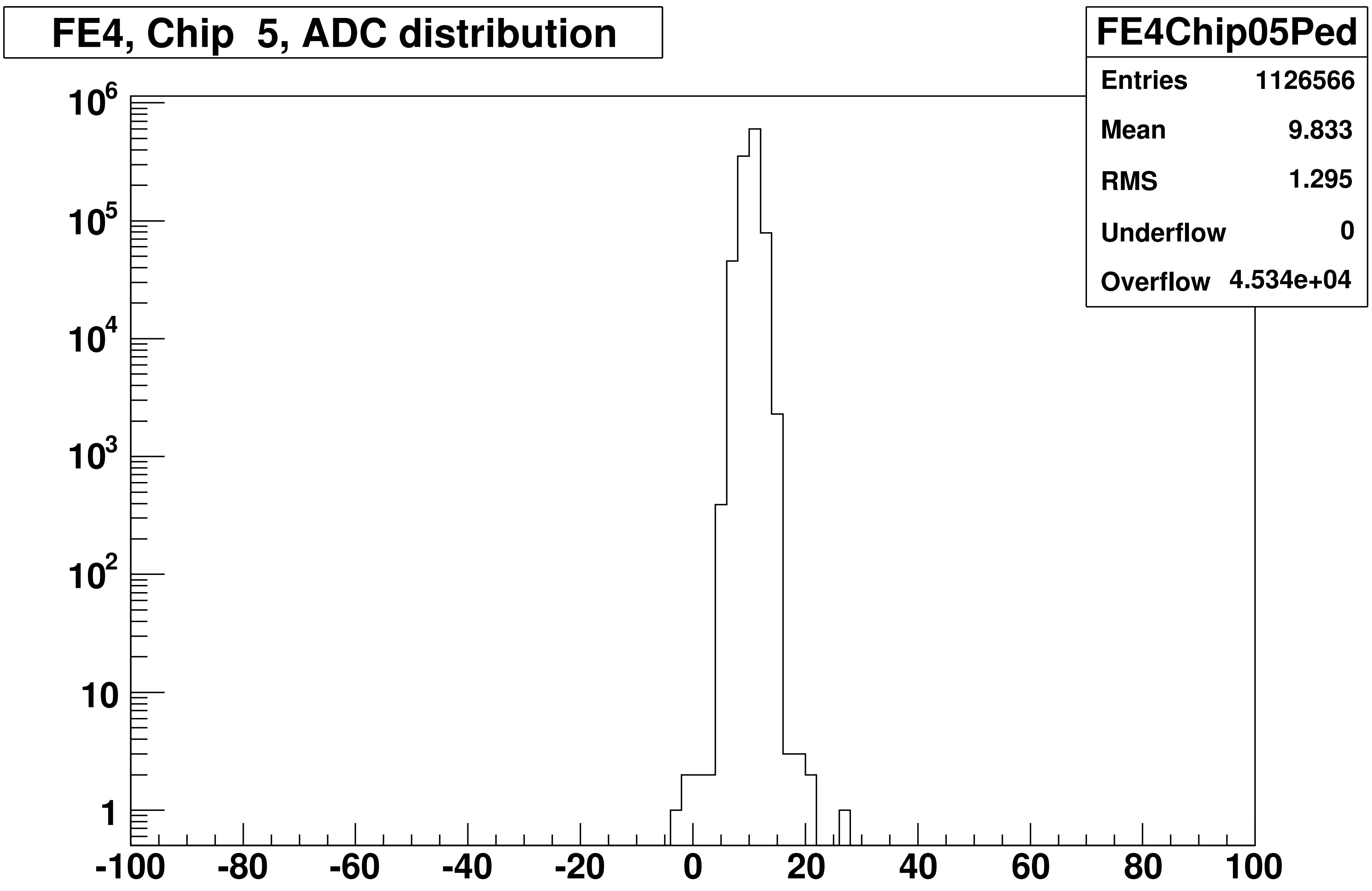
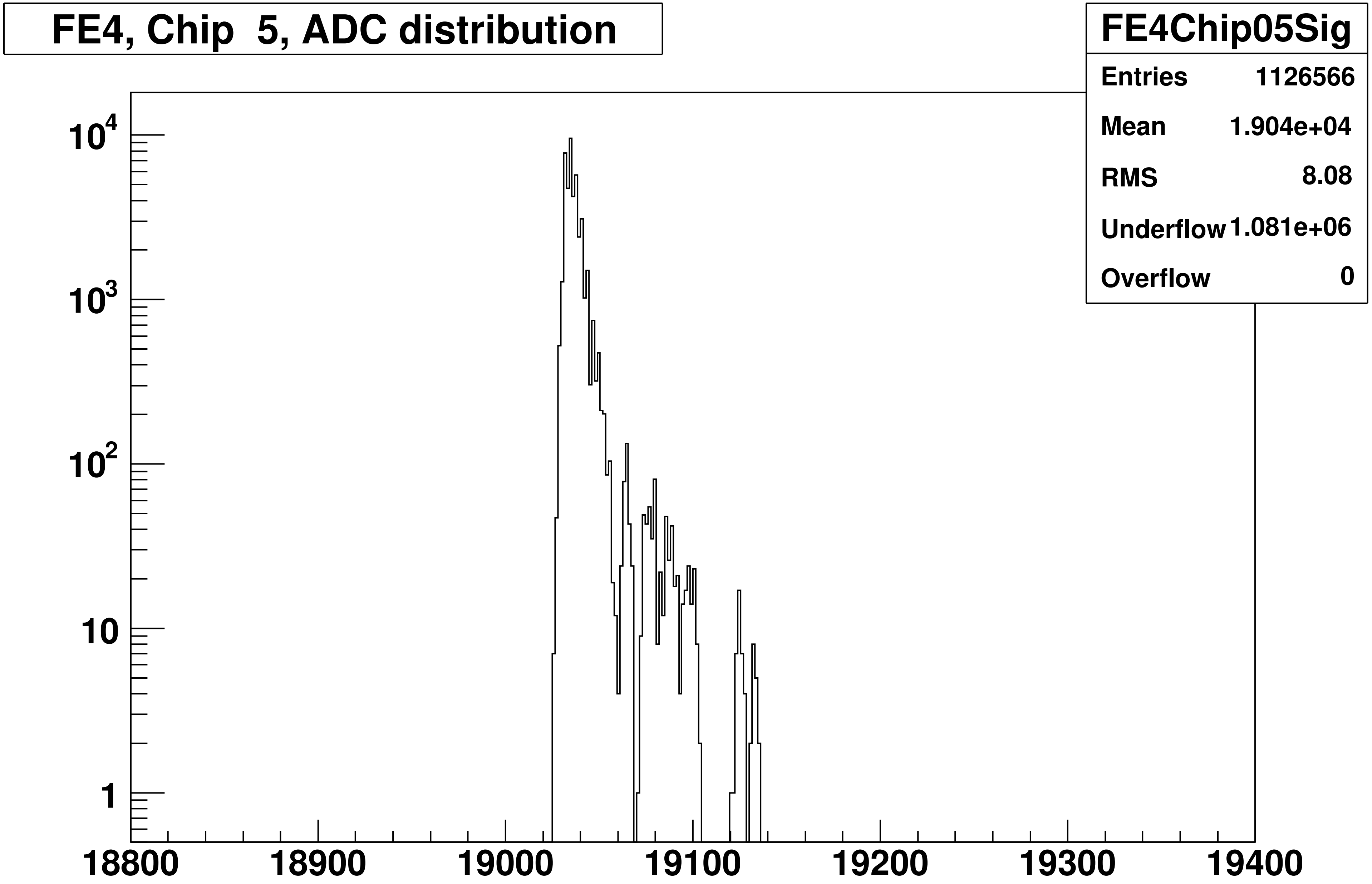
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 1126566
Mean 9.833
RMS 1.295
Underflow 0
Overflow 4.534e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 1126566
Mean 1.904e+04
RMS 8.08
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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19100
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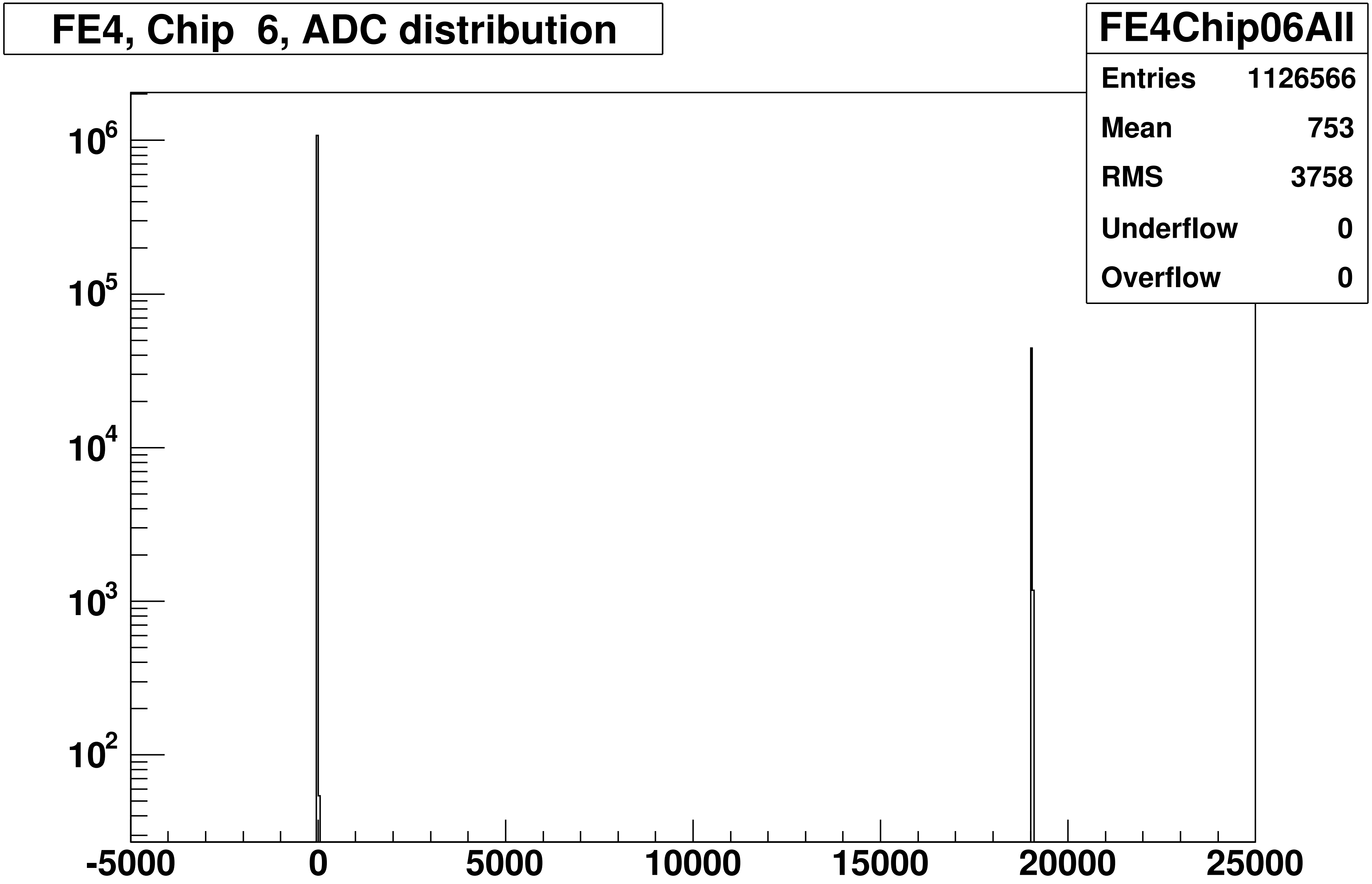
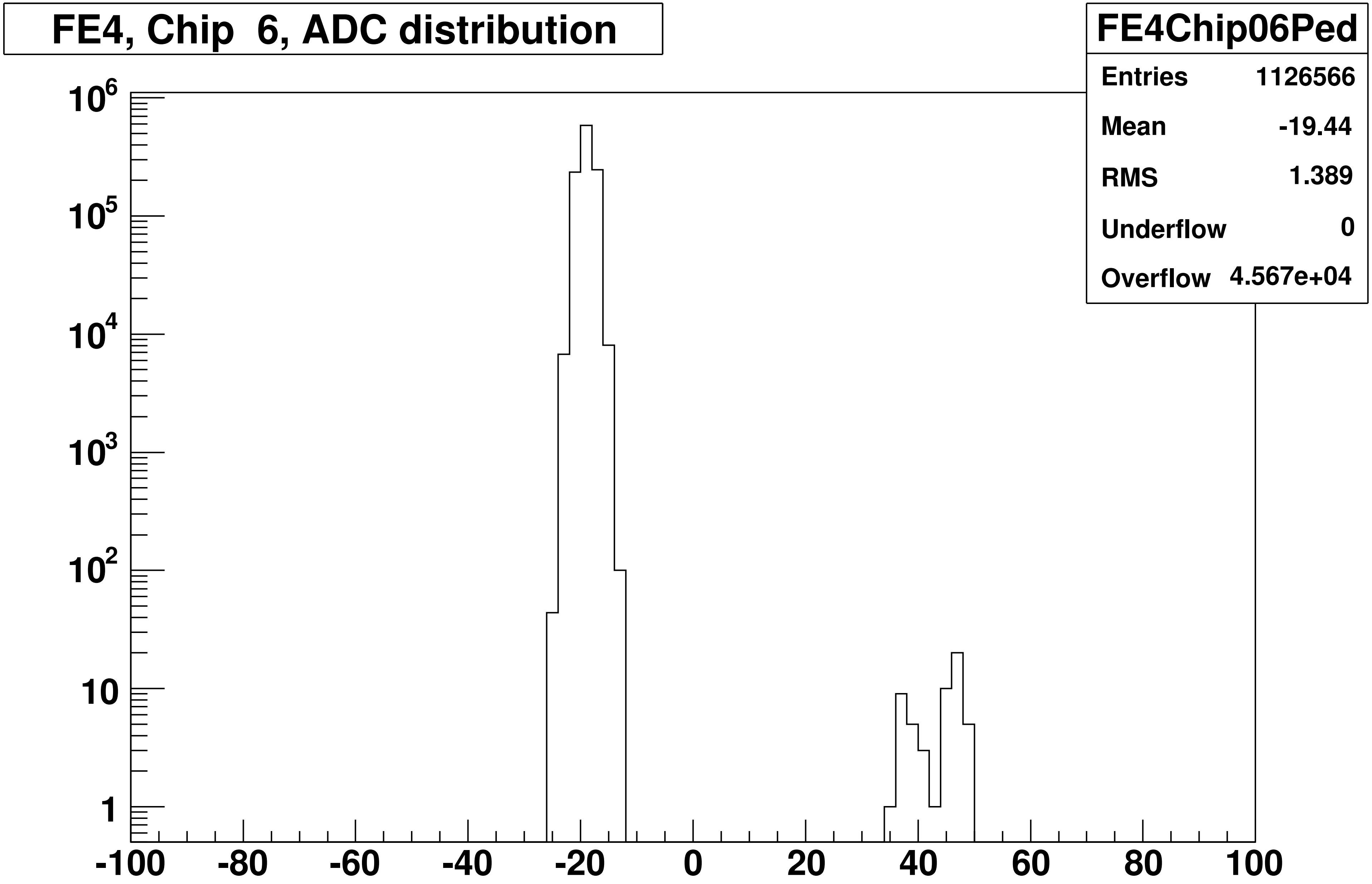
19400


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 1126566
Mean 753
RMS 3758
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 1126566
Mean -19.44
RMS 1.389
Underflow 0
Overflow 4.567e+04
10^6
10^5
10^4
10^3
10^2
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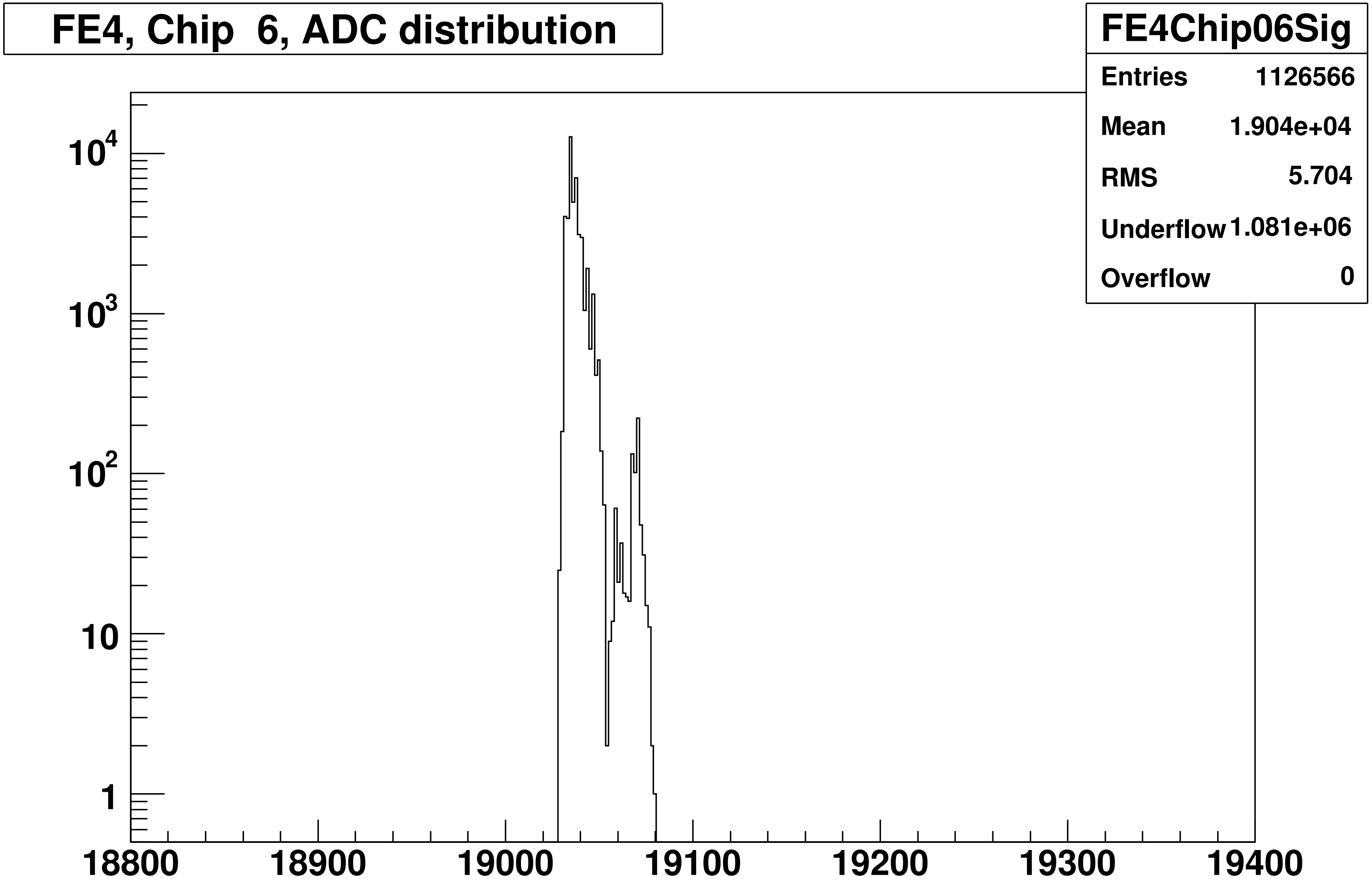
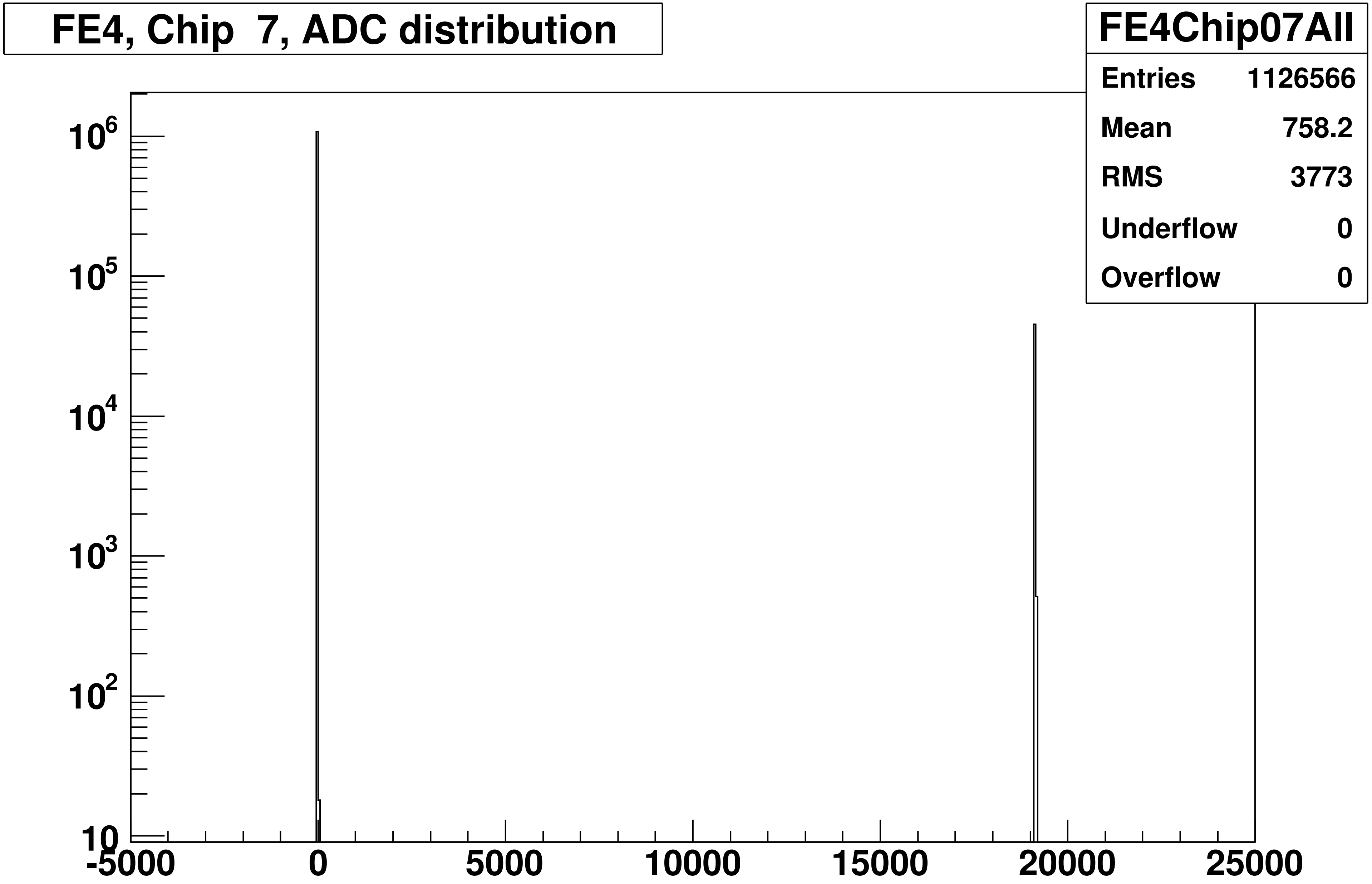
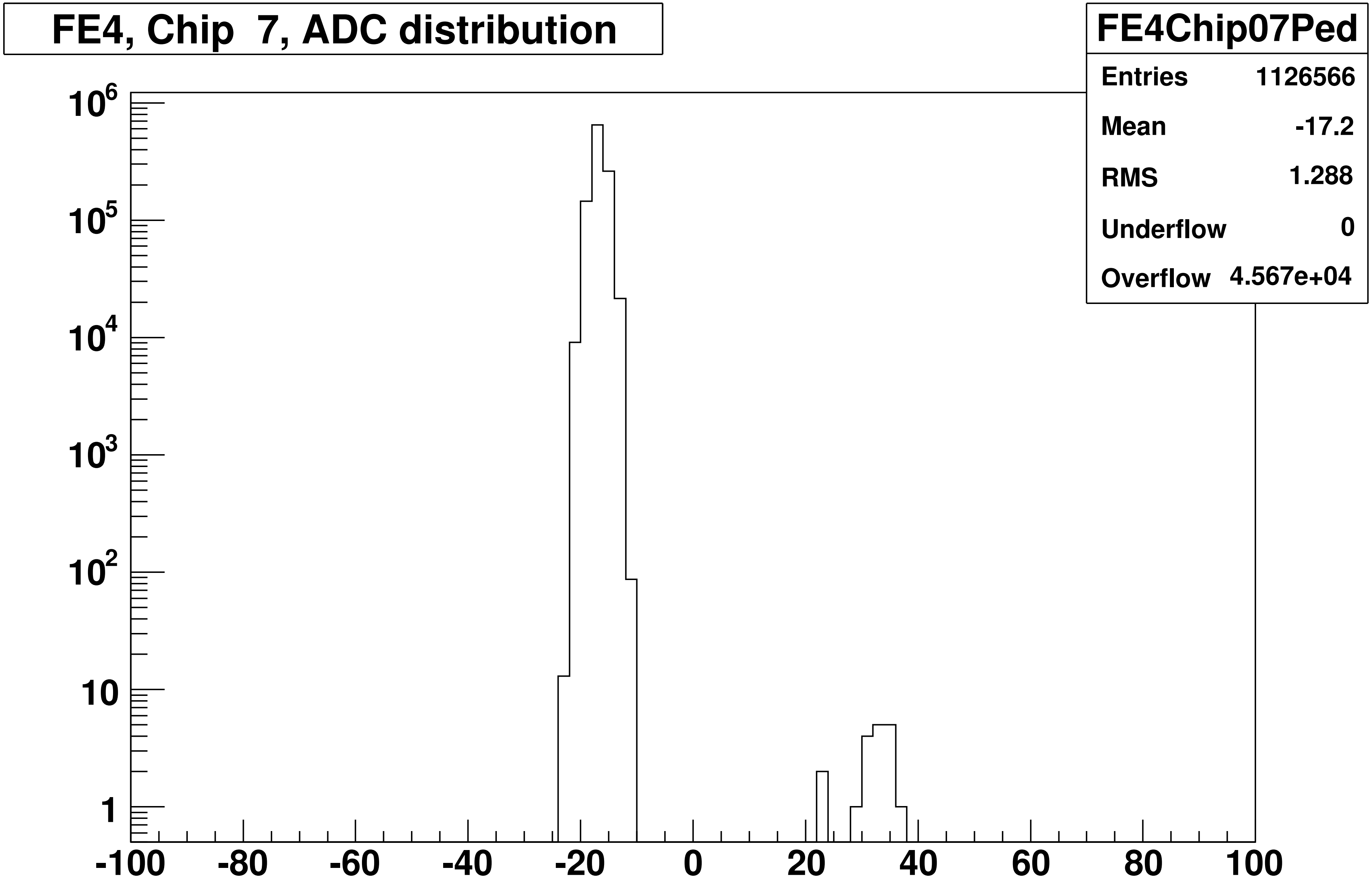
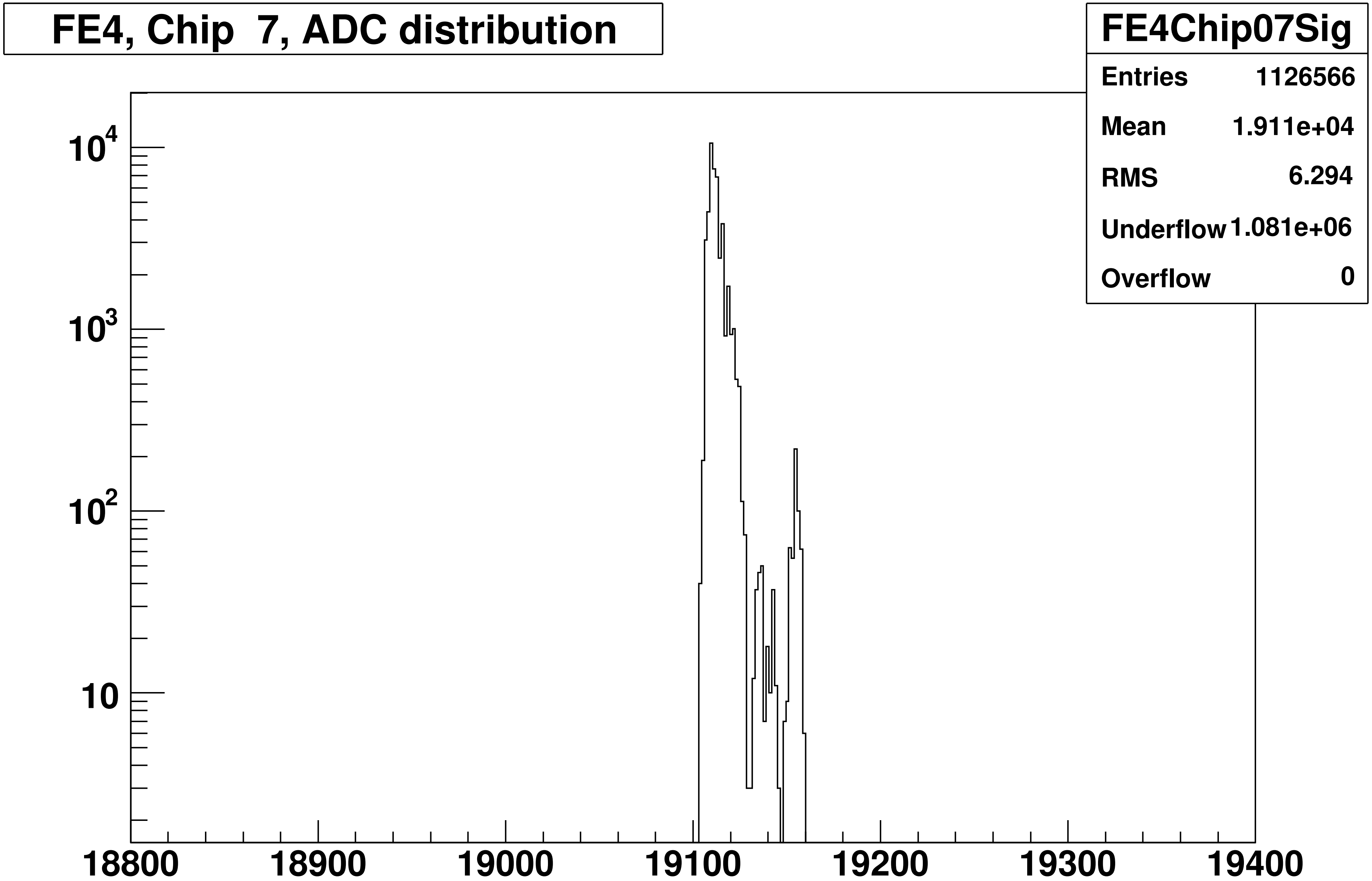
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 1126566
Mean 1.904e+04
RMS 5.704
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

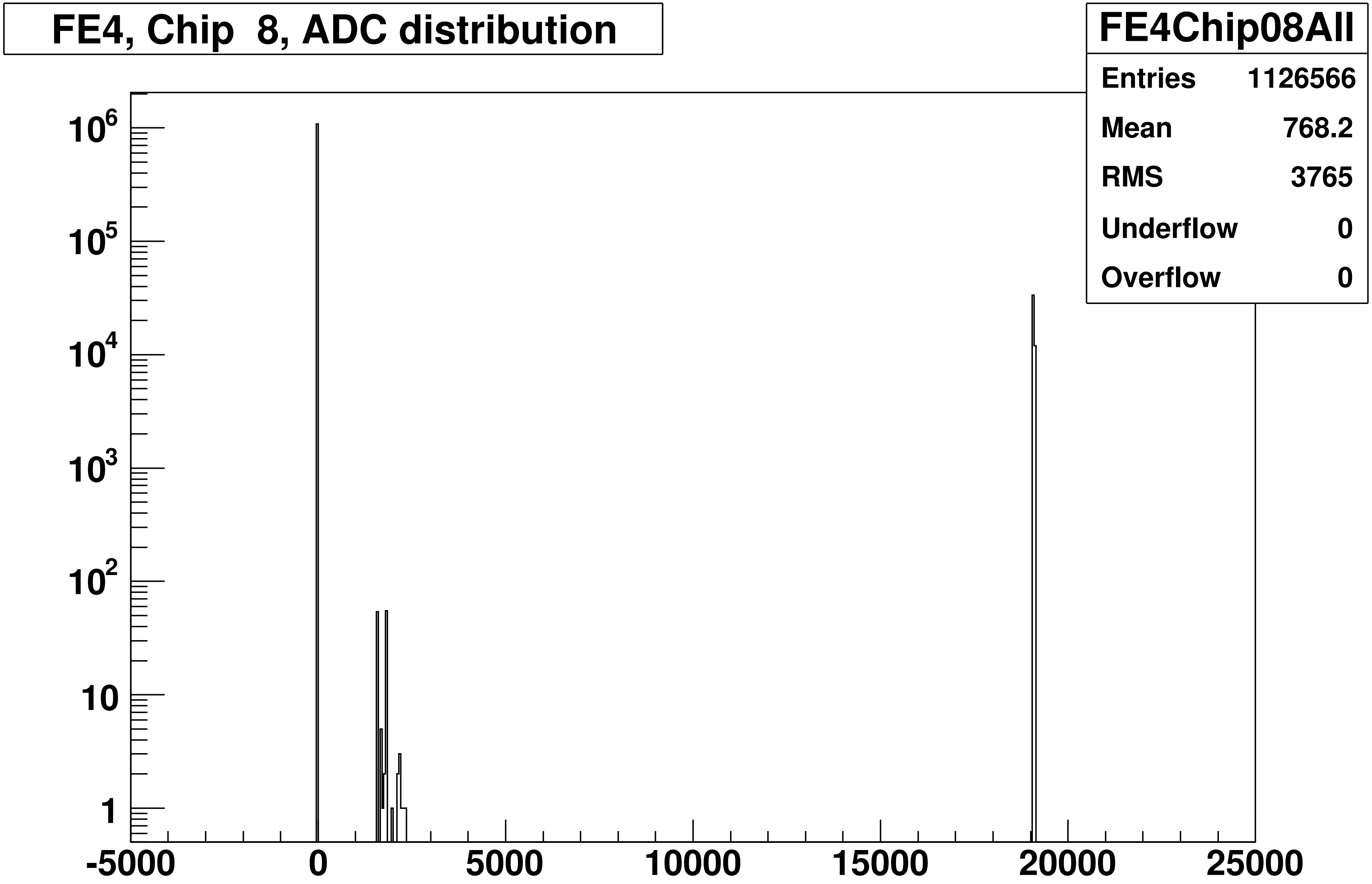


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 1126566
Mean 758.2
RMS 3773
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

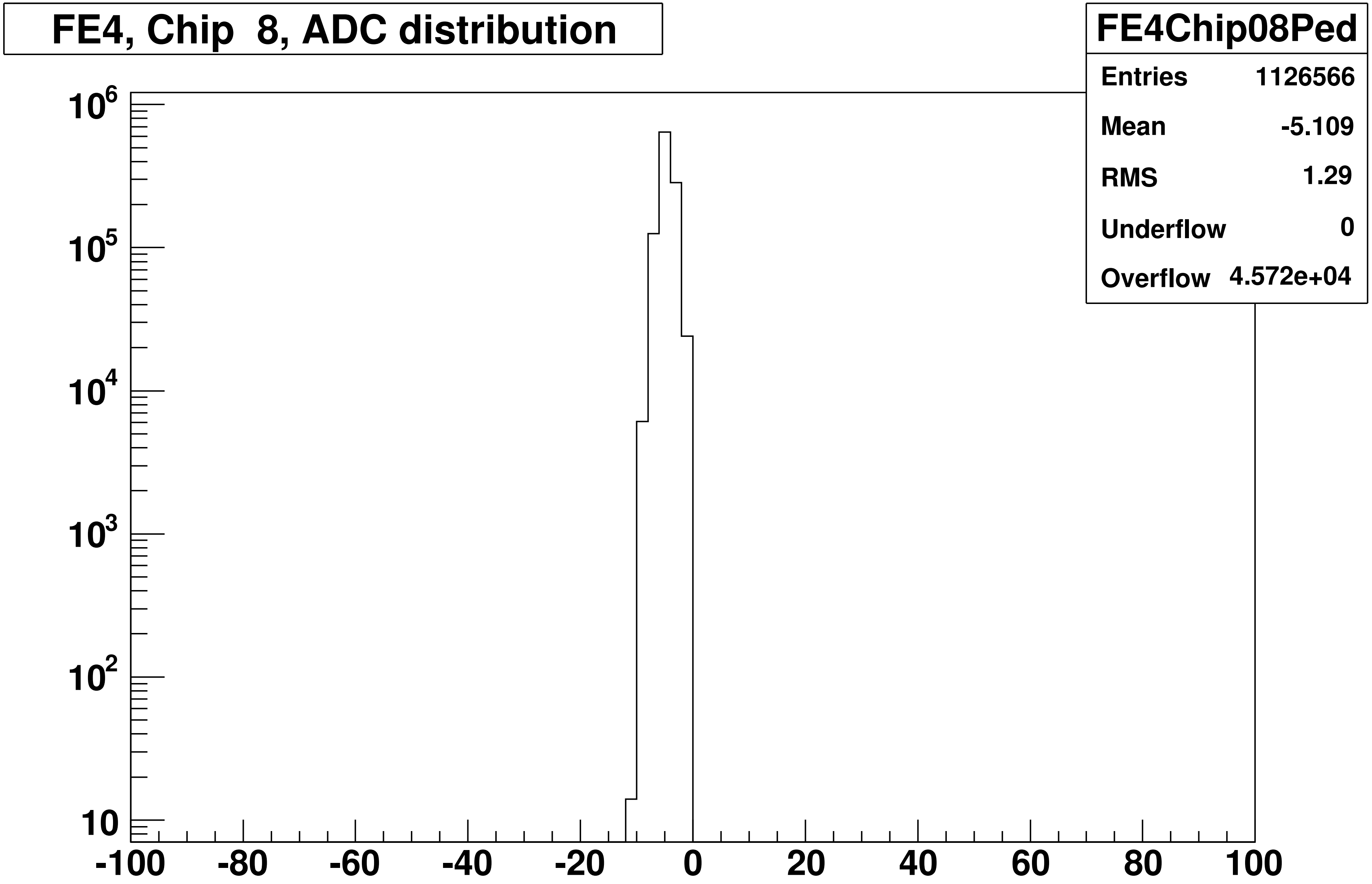

FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 1126566
Mean -17.2
RMS 1.288
Underflow 0
Overflow 4.567e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



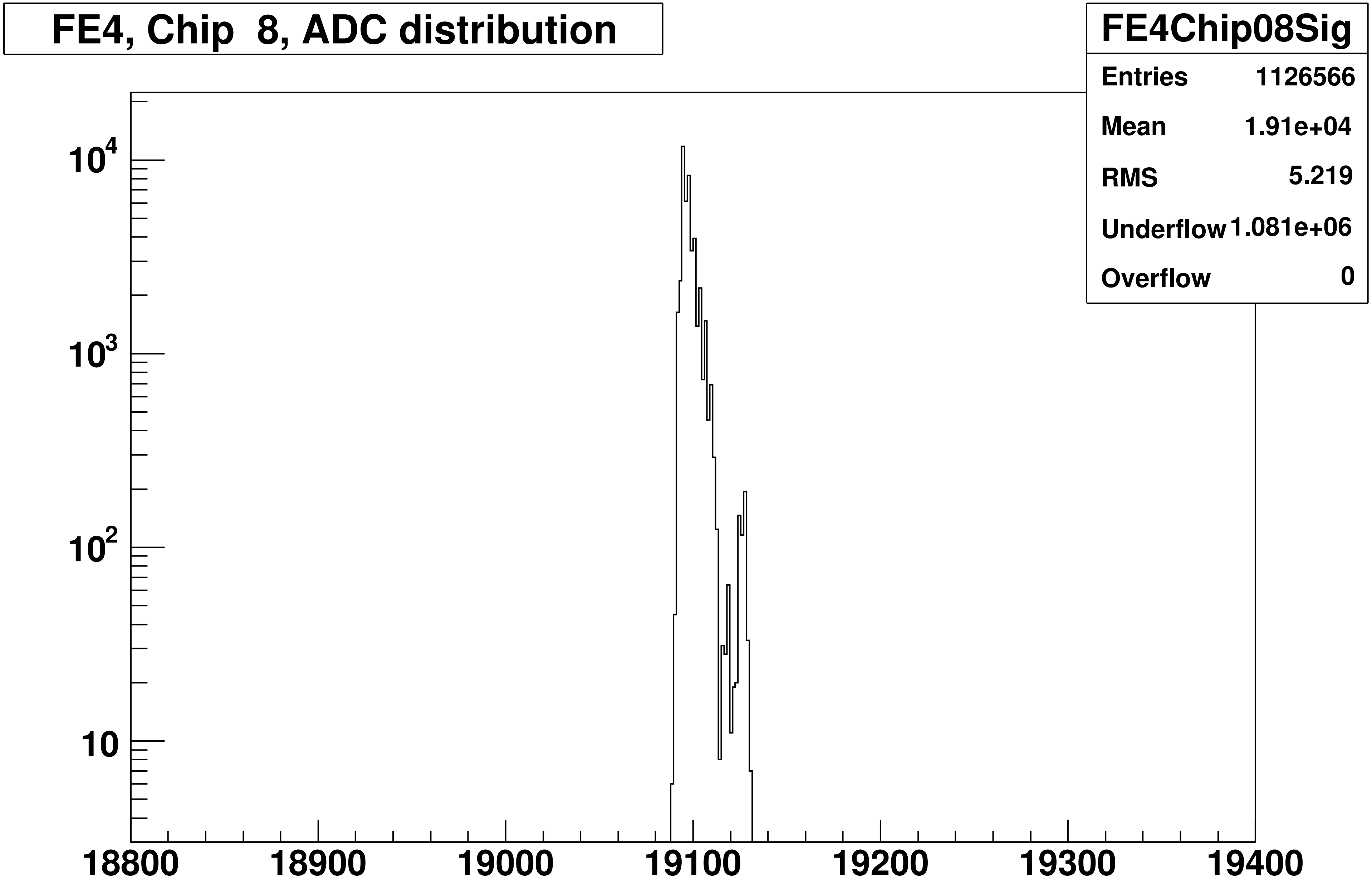
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 1126566
Mean 1.911e+04
RMS 6.294
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



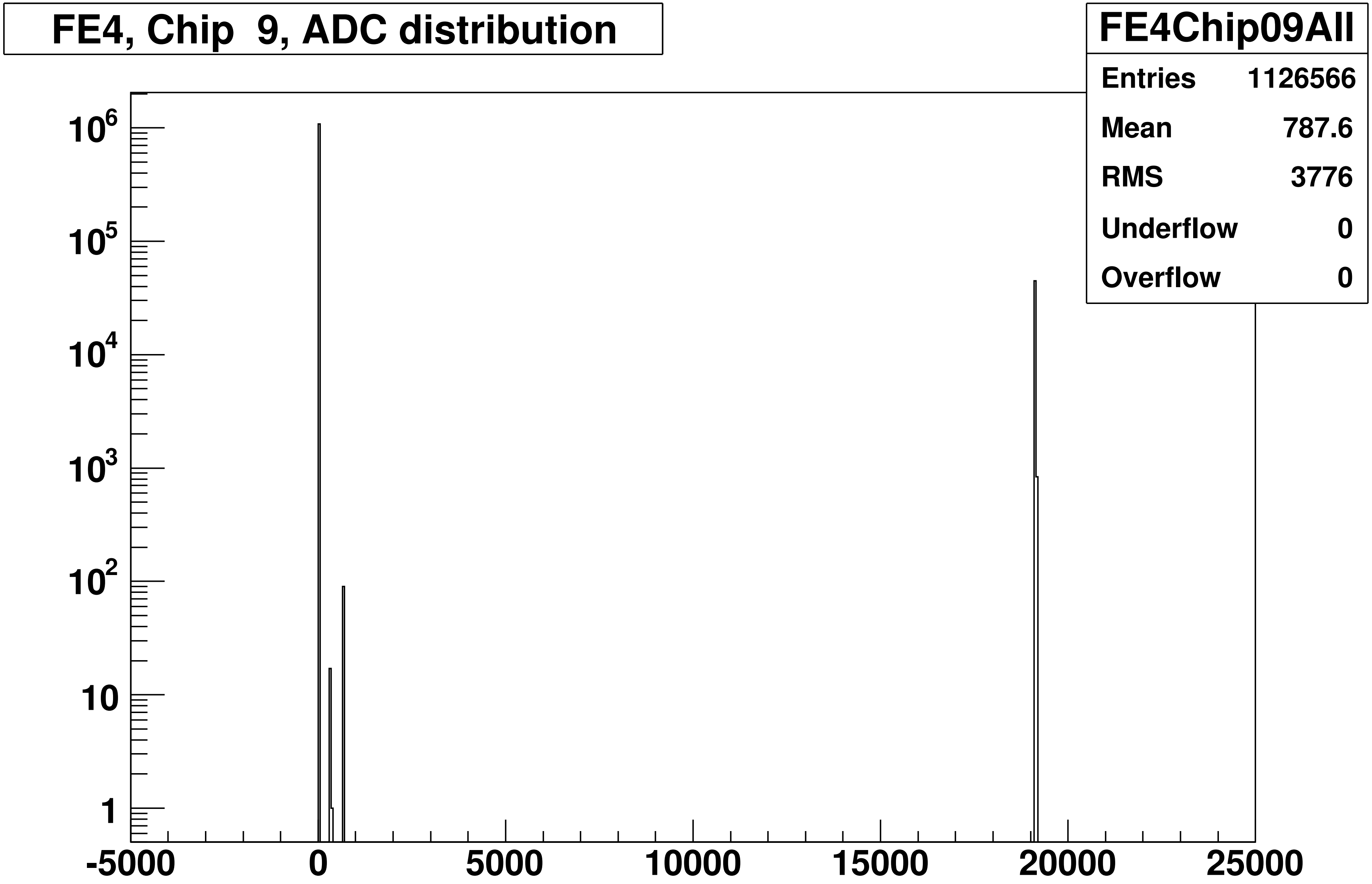
FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 1126566
Mean 768.2
RMS 3765
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



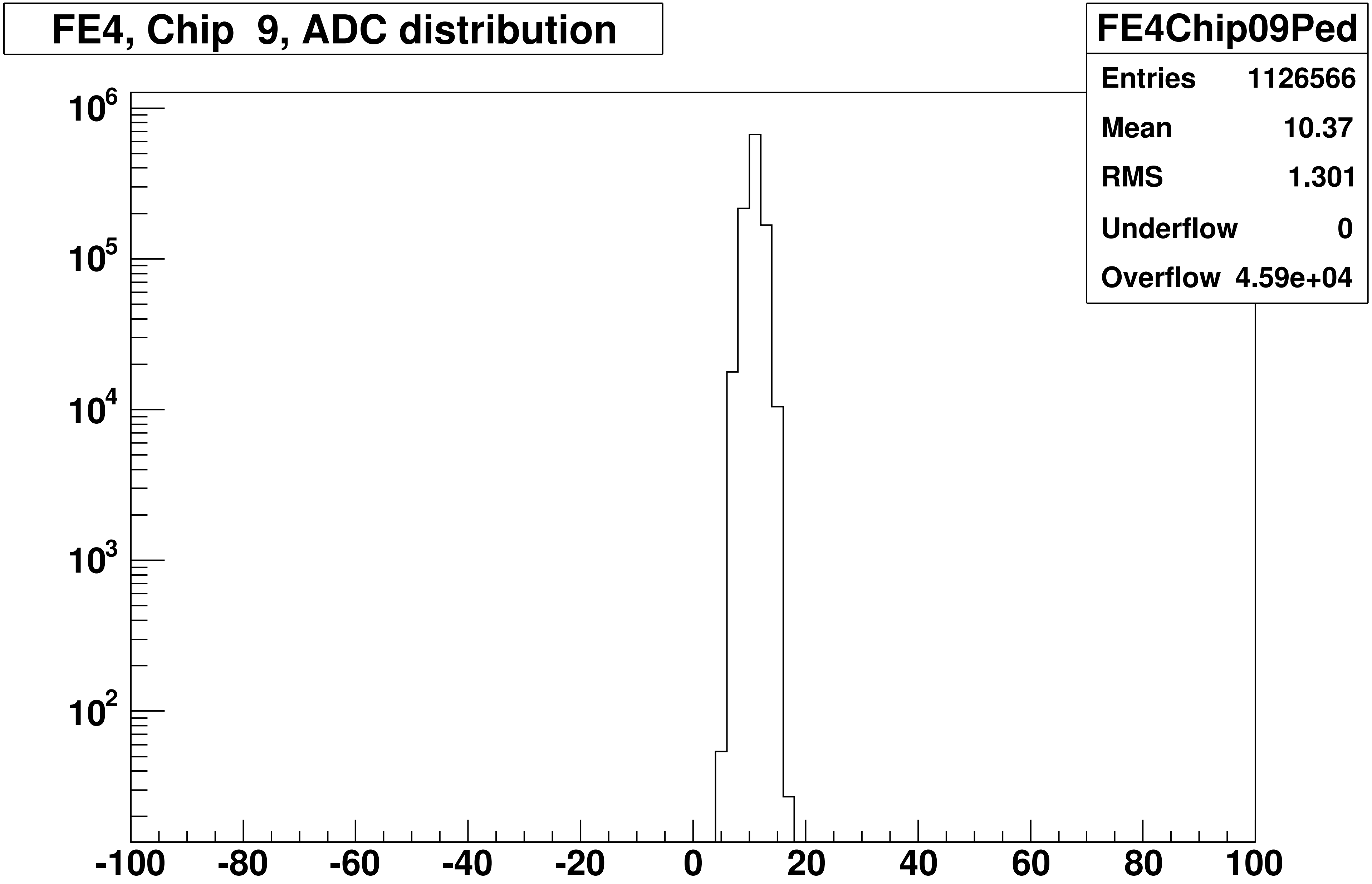
FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 1126566
Mean -5.109
RMS 1.29
Underflow 0
Overflow 4.572e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



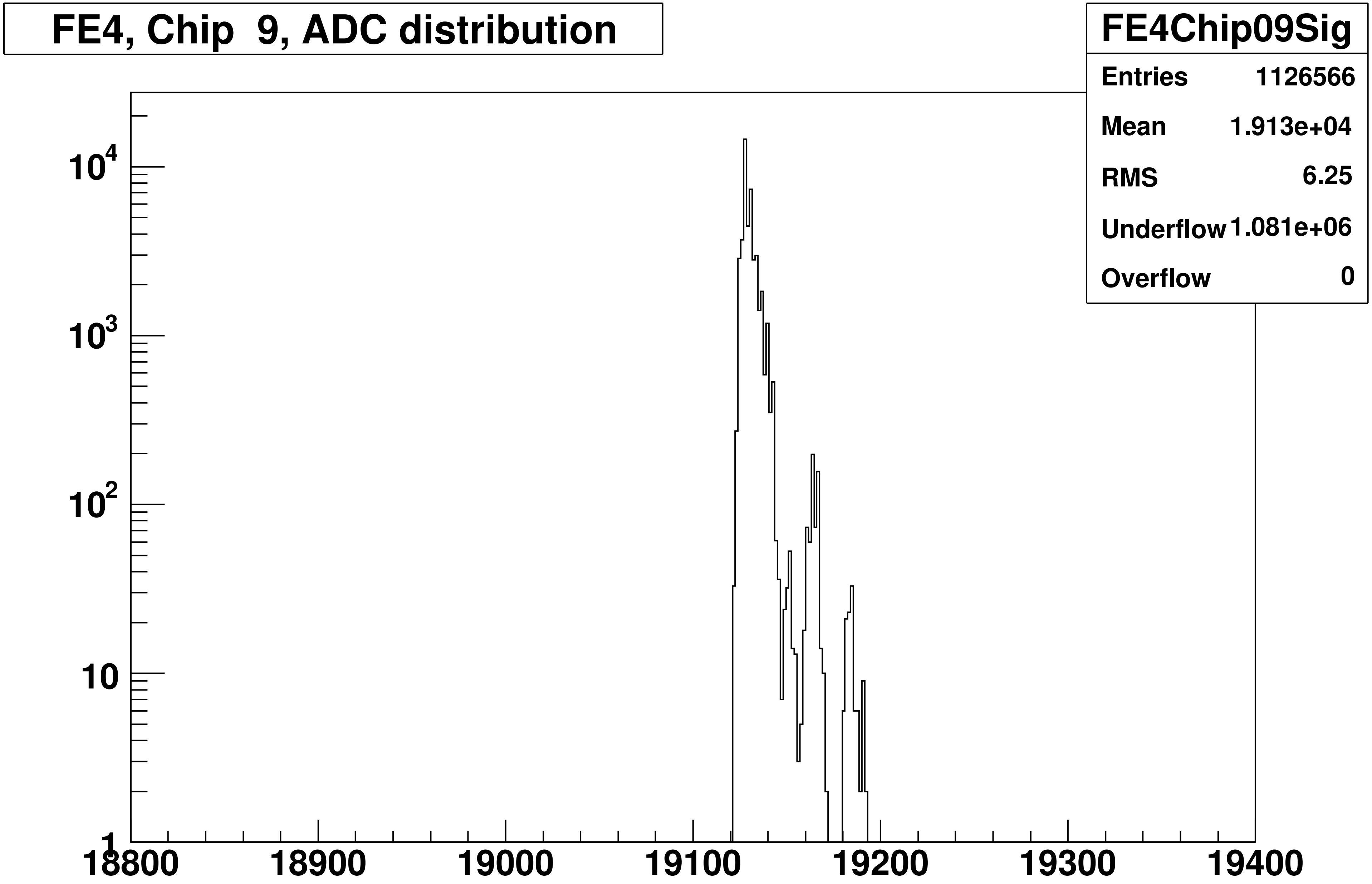
FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 1126566
Mean 1.91e+04
RMS 5.219
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



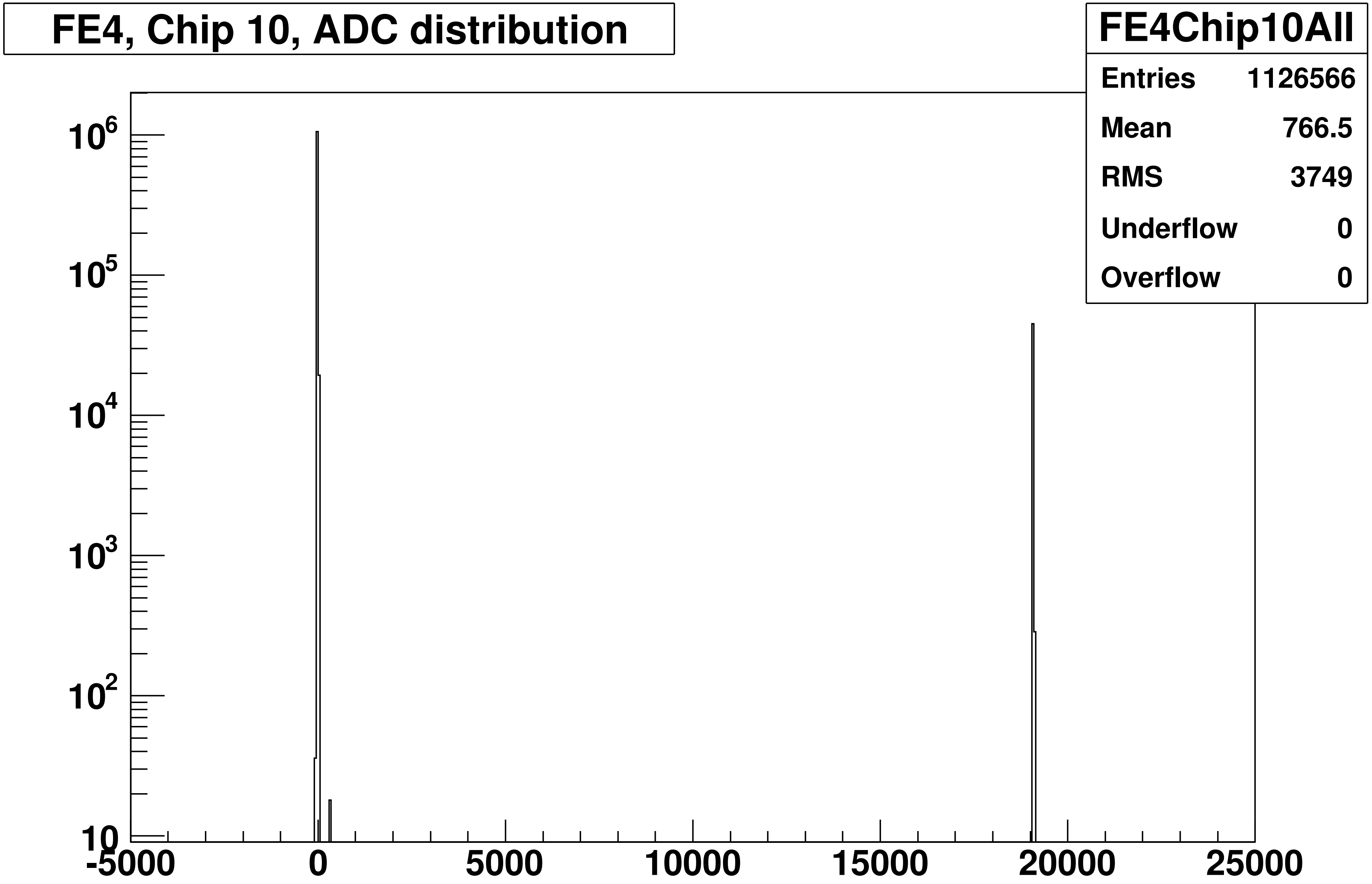
FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 1126566
Mean 787.6
RMS 3776
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 1126566
Mean 10.37
RMS 1.301
Underflow 0
Overflow 4.59e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 1126566
Mean 1.913e+04
RMS 6.25
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 1126566
Mean 766.5
RMS 3749
Underflow 0
Overflow 0
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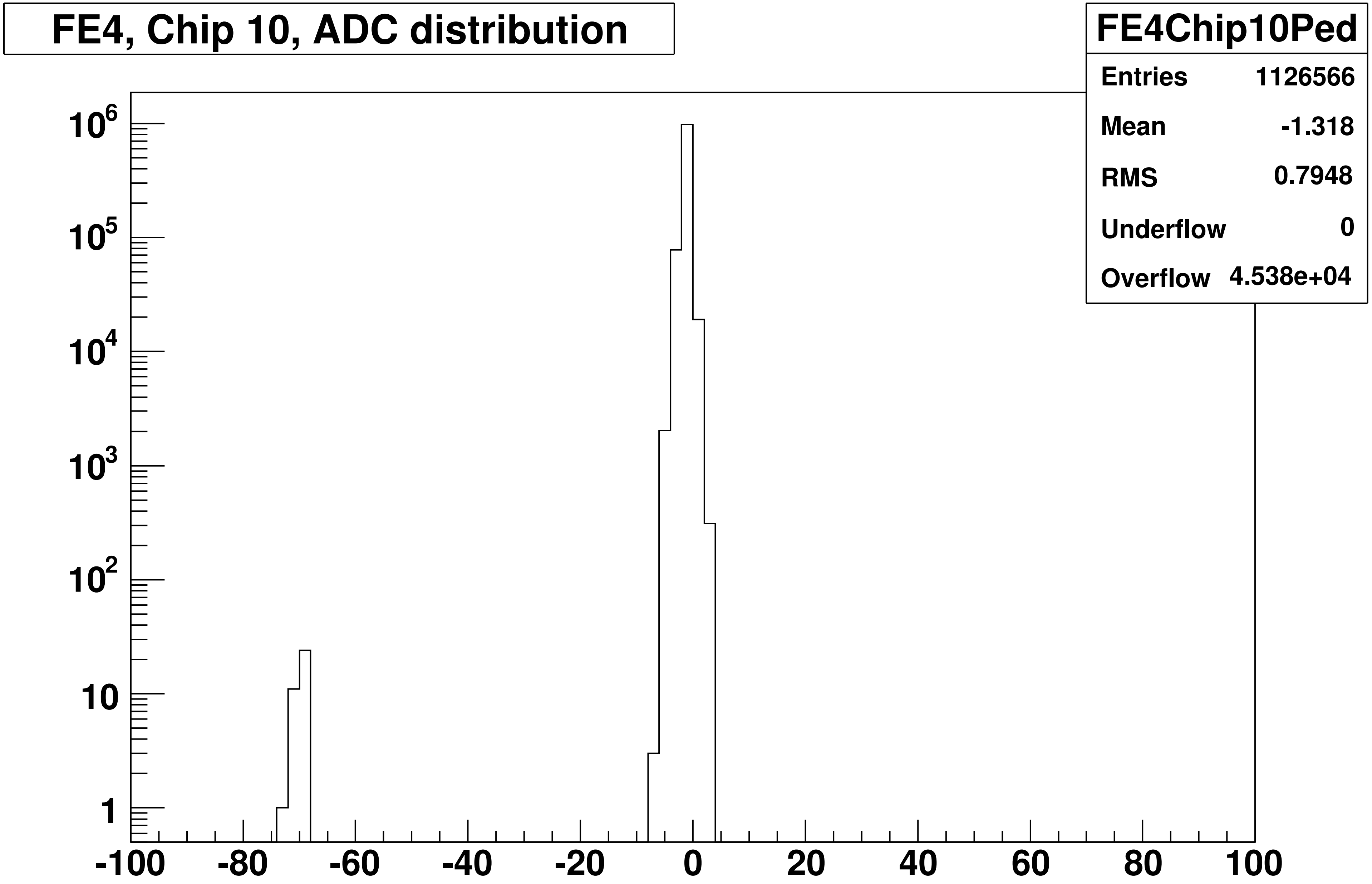
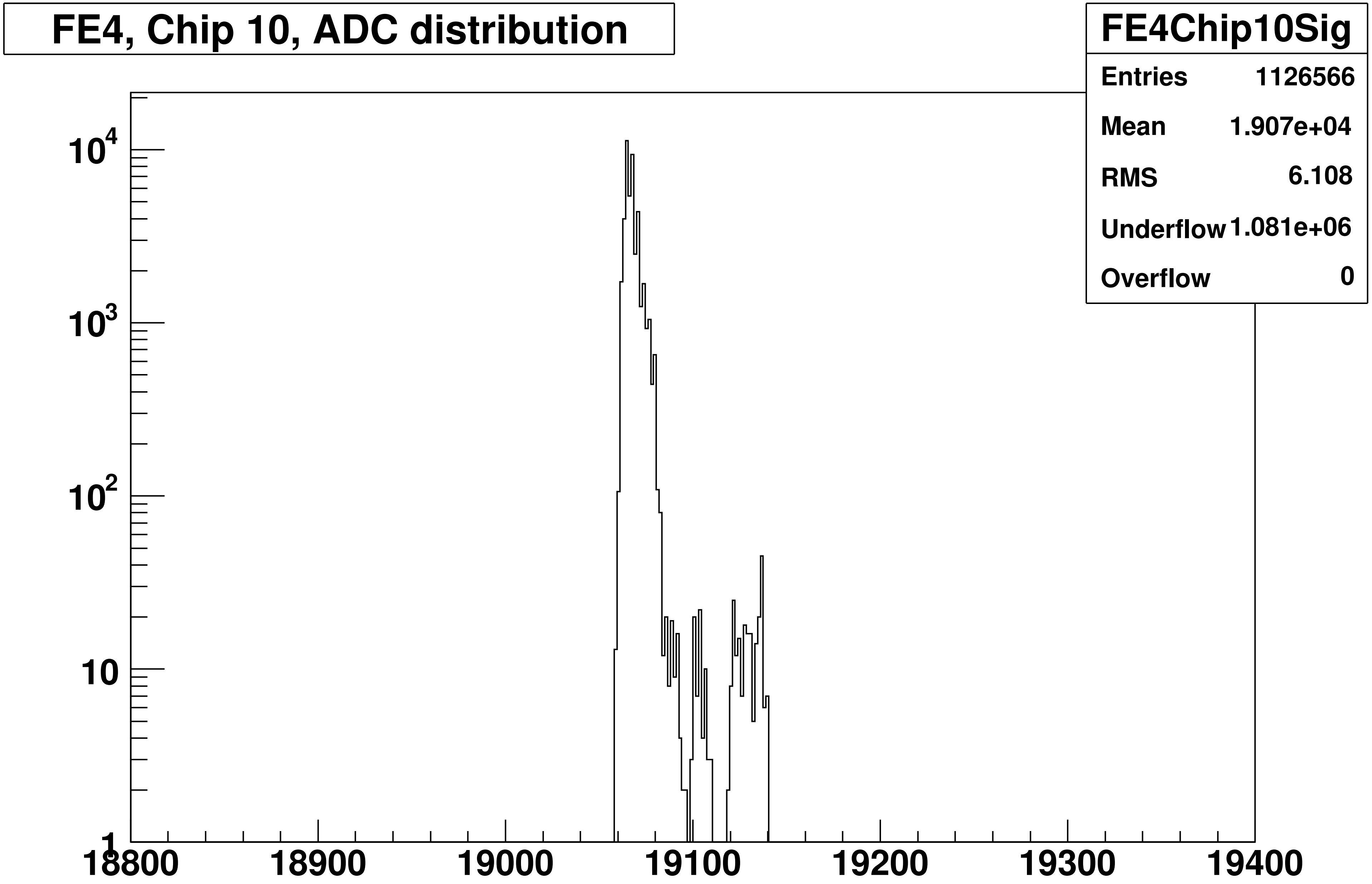
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 1126566
Mean -1.318
RMS 0.7948
Underflow 0
Overflow 4.538e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 1126566
Mean 1.907e+04
RMS 6.108
Underflow 1.081e+06
Overflow 0
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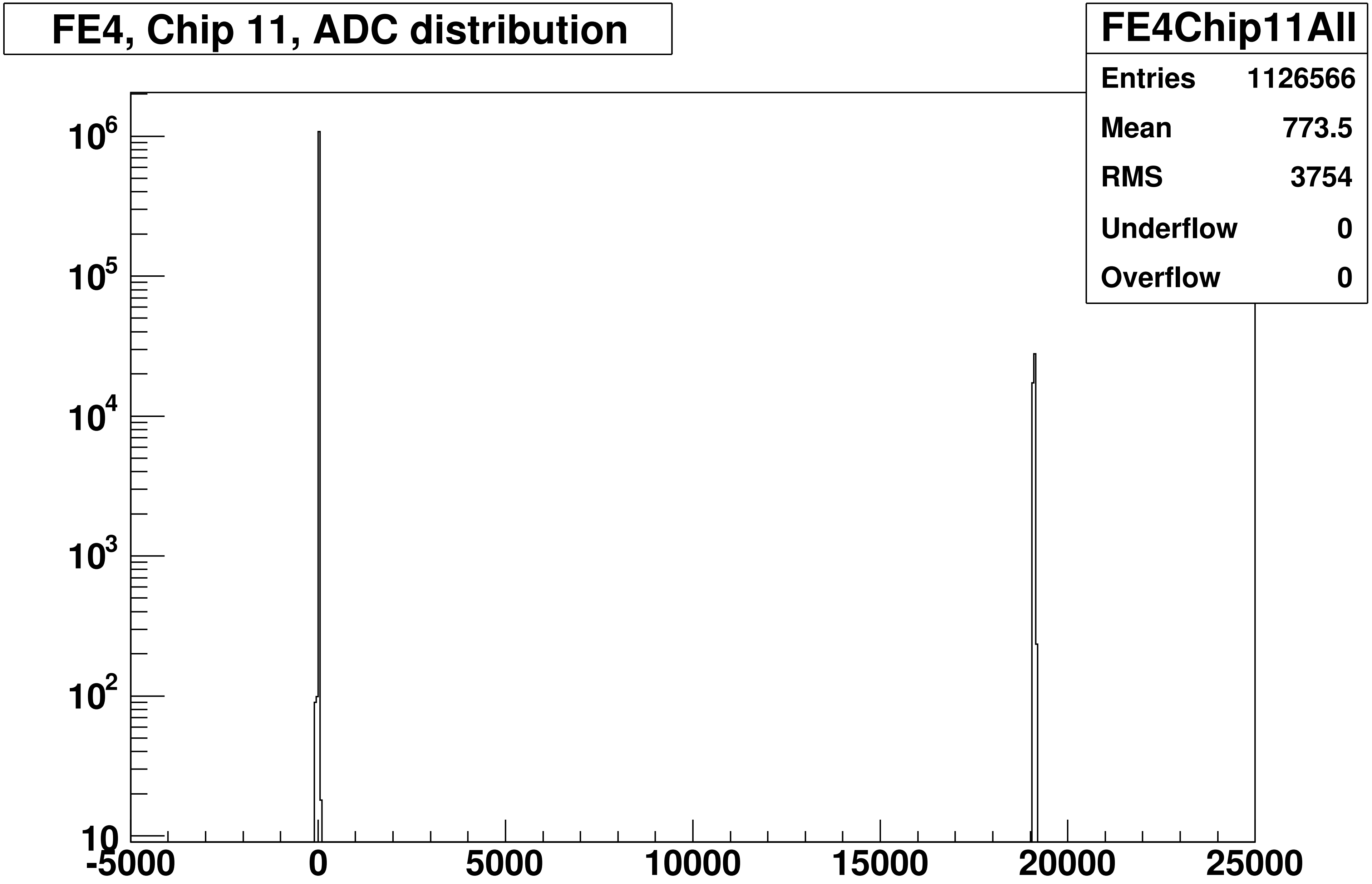
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 1126566
Mean 773.5
RMS 3754
Underflow 0
Overflow 0
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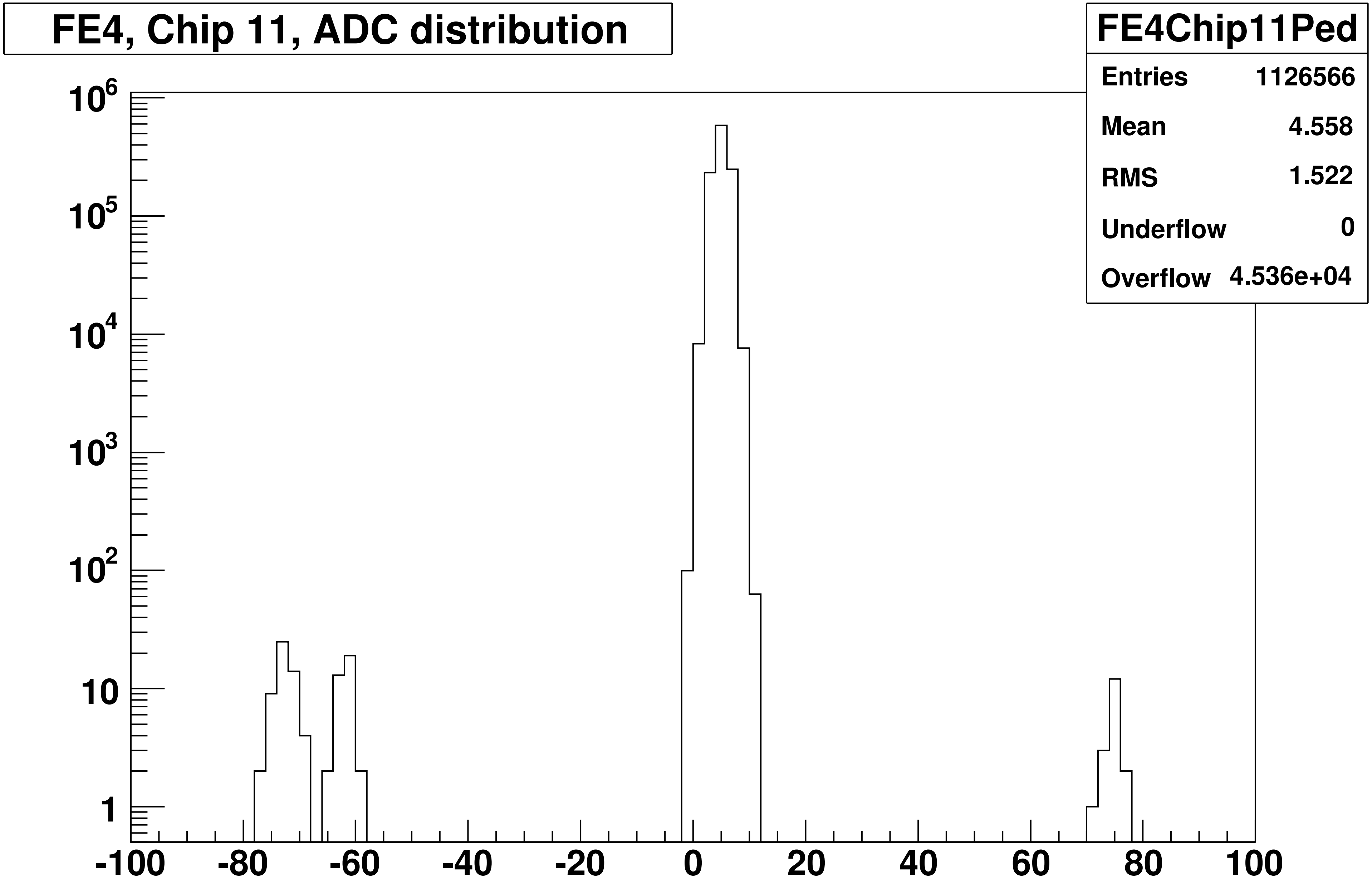
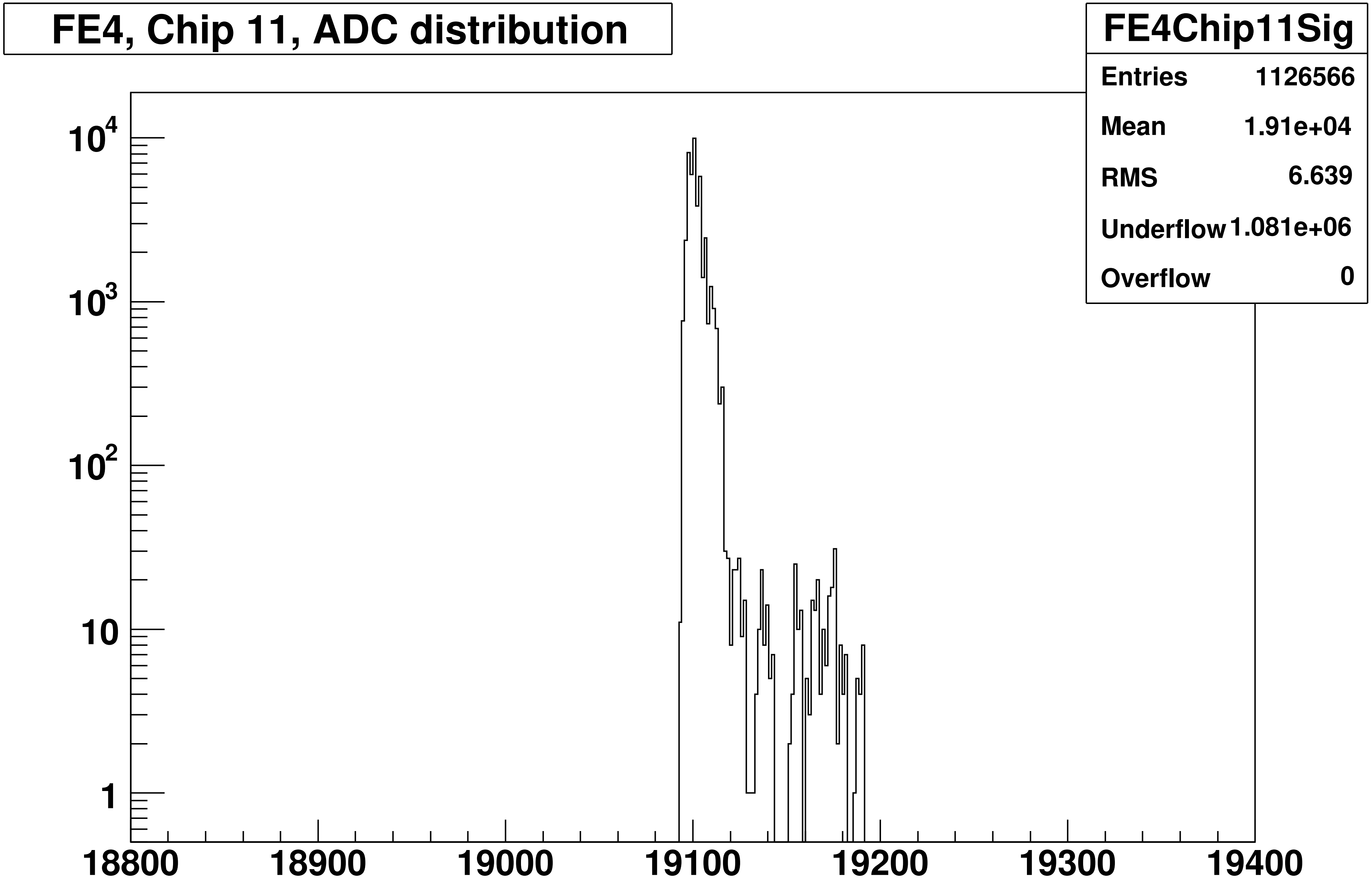
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 1126566
Mean 4.558
RMS 1.522
Underflow 0
Overflow 4.536e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 1126566
Mean 1.91e+04
RMS 6.639
Underflow 1.081e+06
Overflow 0
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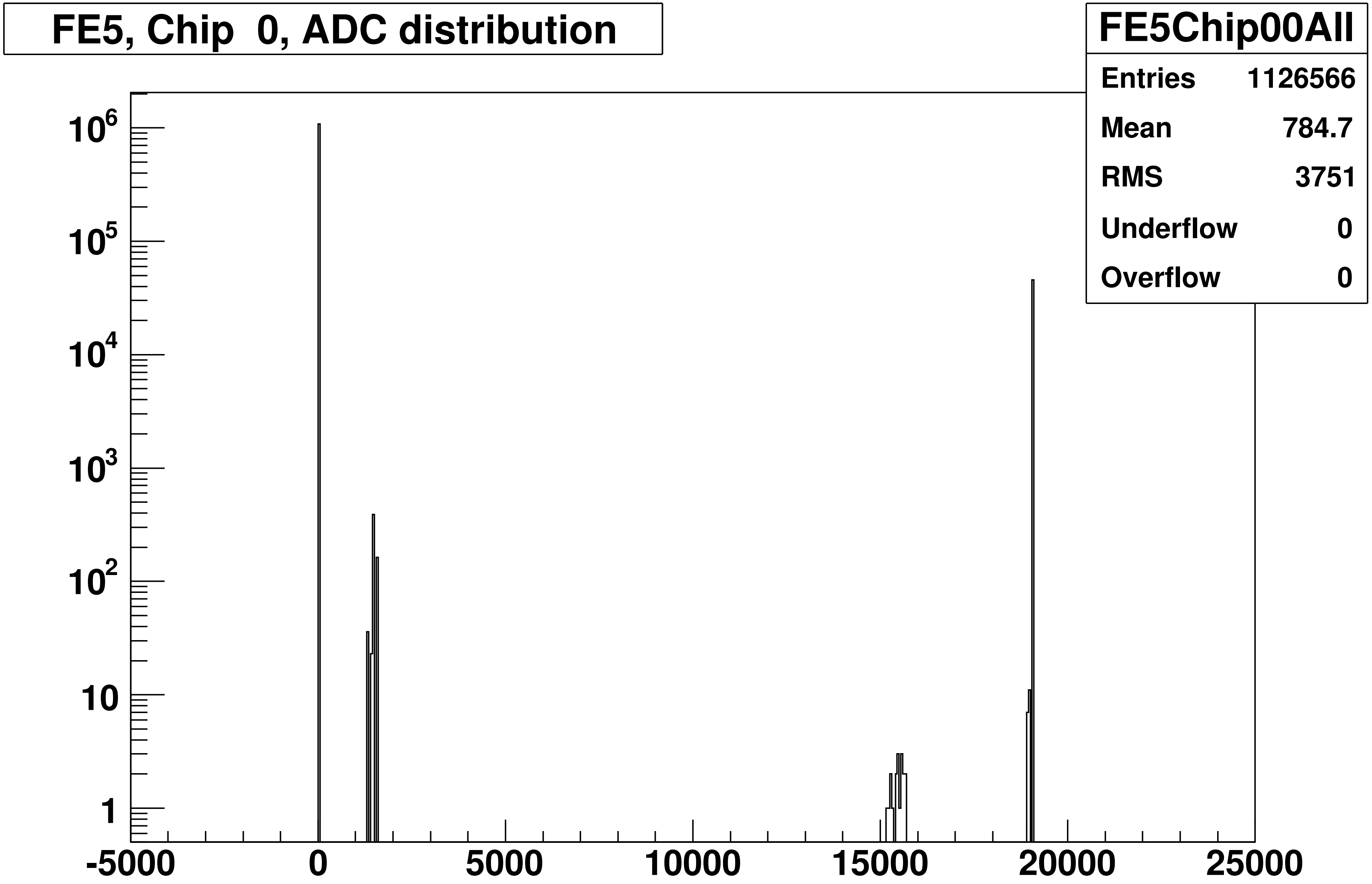
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 1126566
Mean 784.7
RMS 3751
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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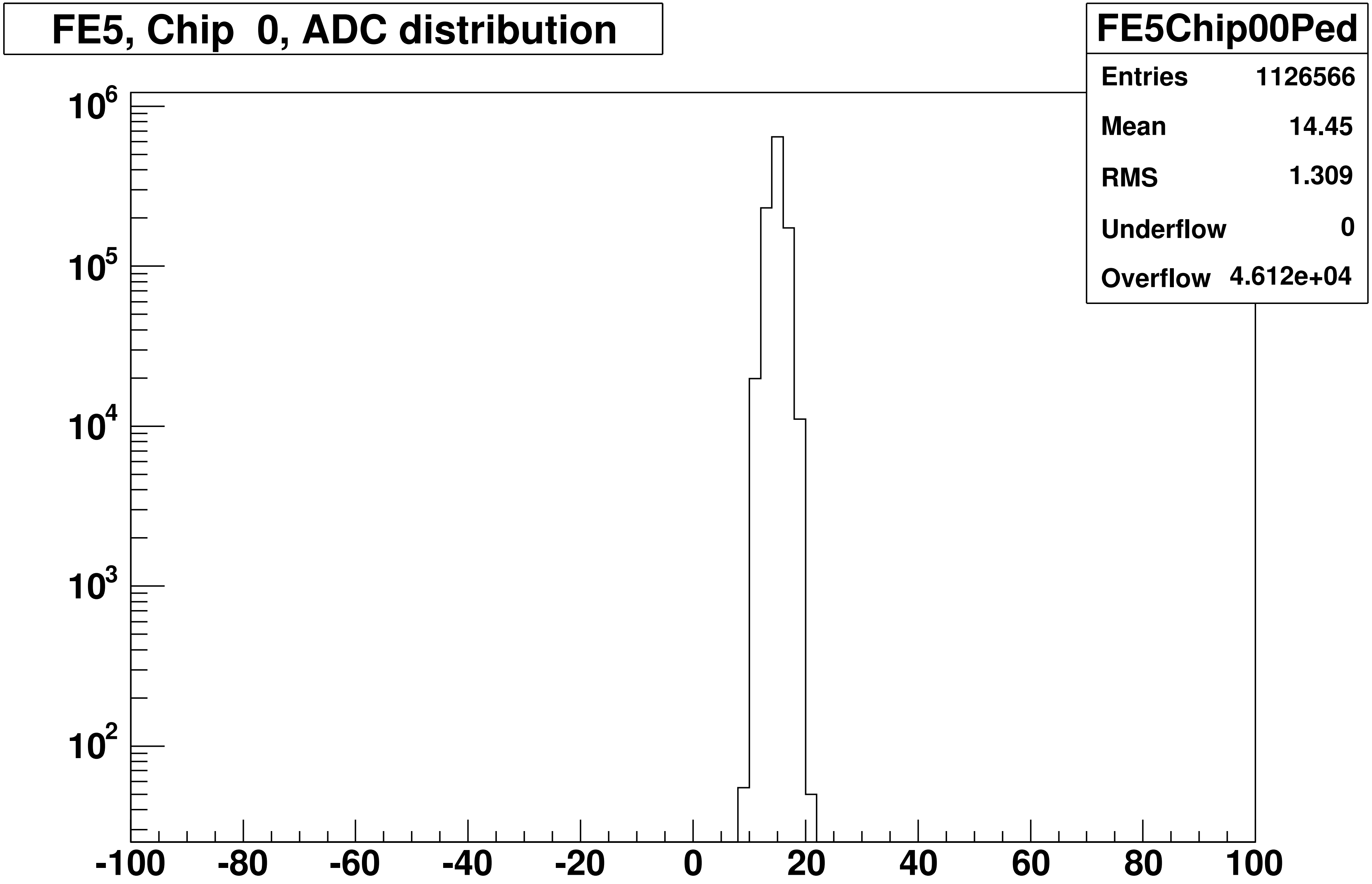
20000
25000



FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 1126566
Mean 14.45
RMS 1.309
Underflow 0
Overflow 4.612e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
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80
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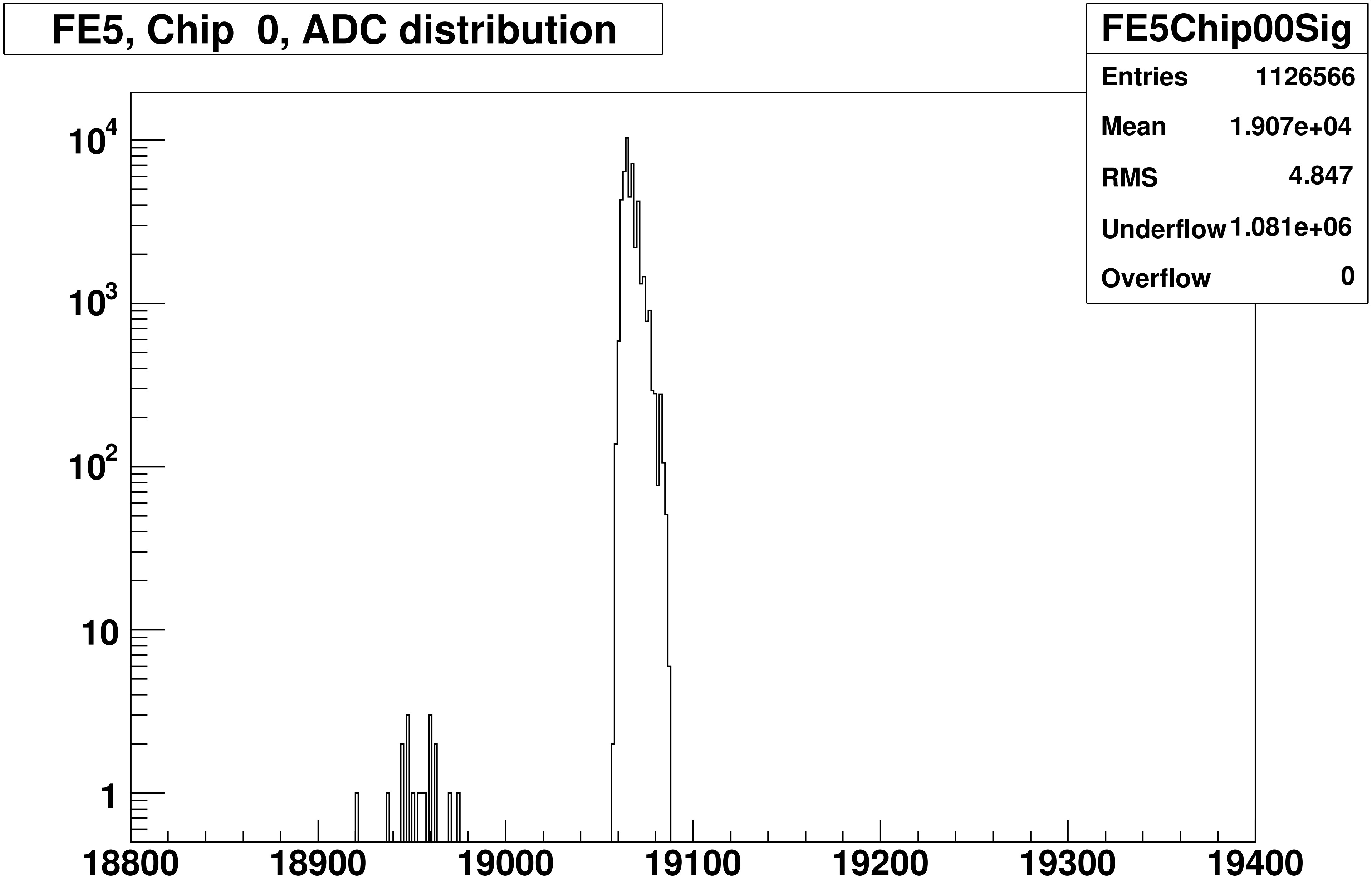
FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 1126566
Mean 1.907e+04
RMS 4.847
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
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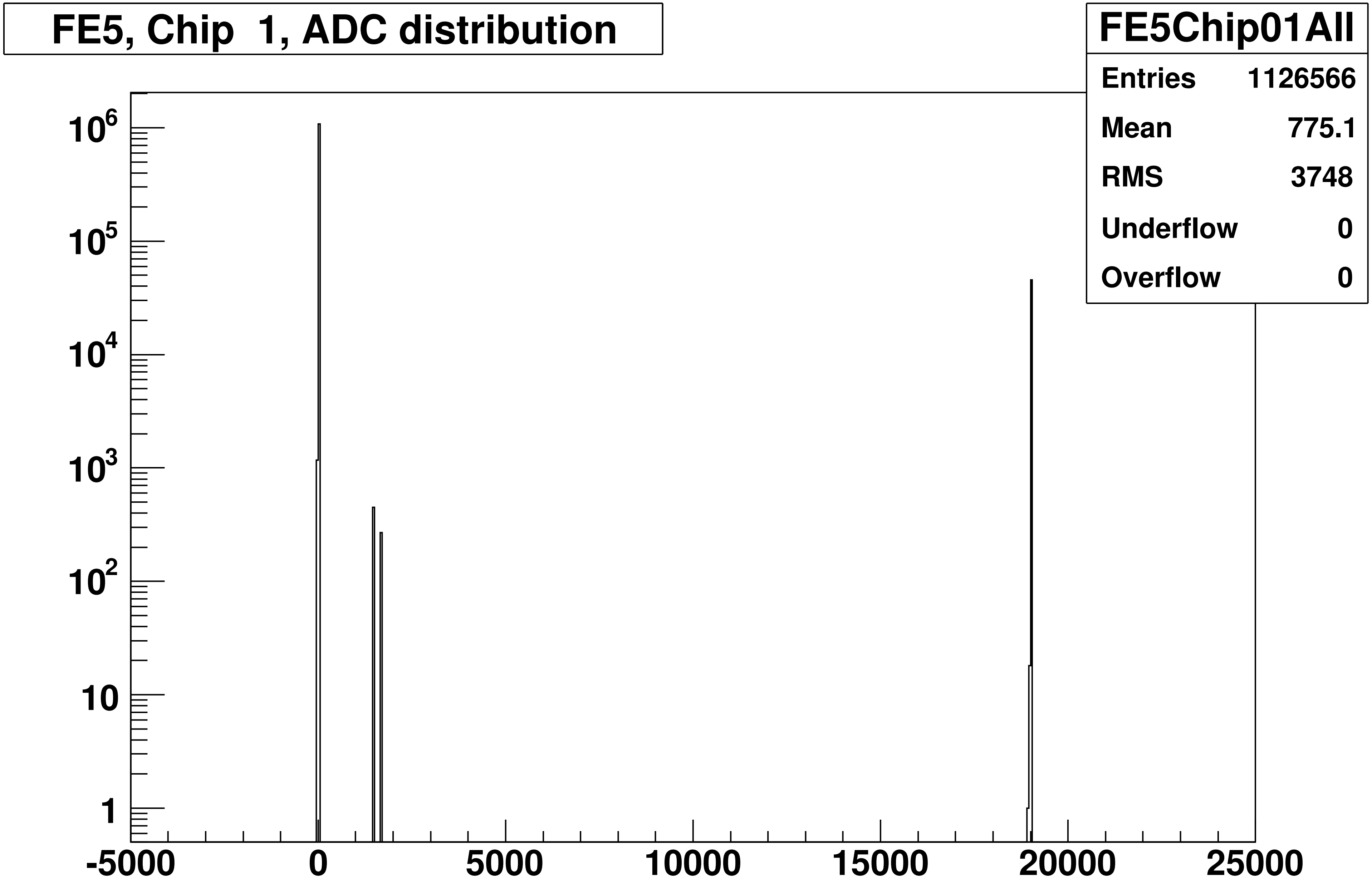
19200
19300
19400


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 1126566
Mean 775.1
RMS 3748
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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15000
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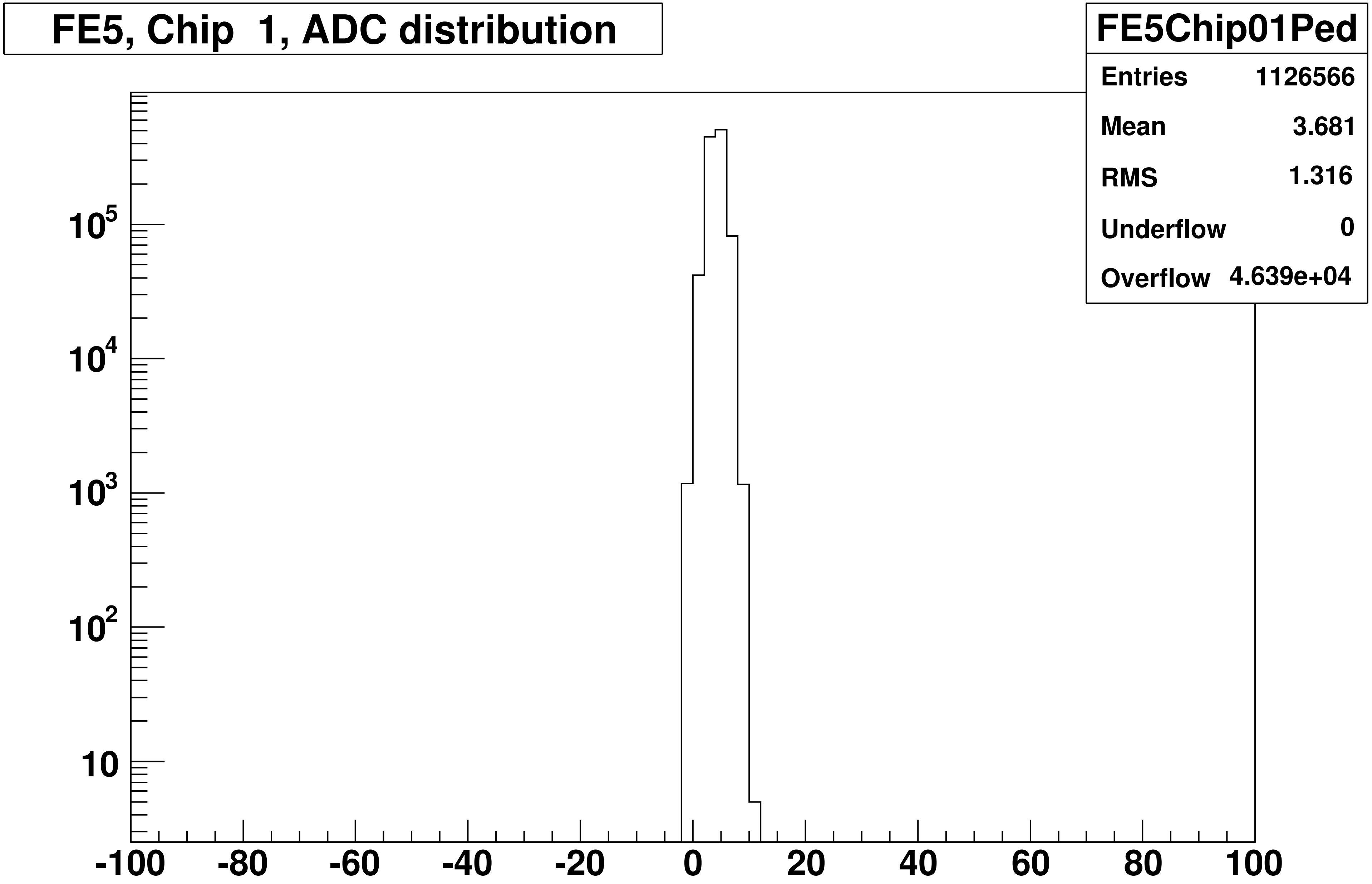
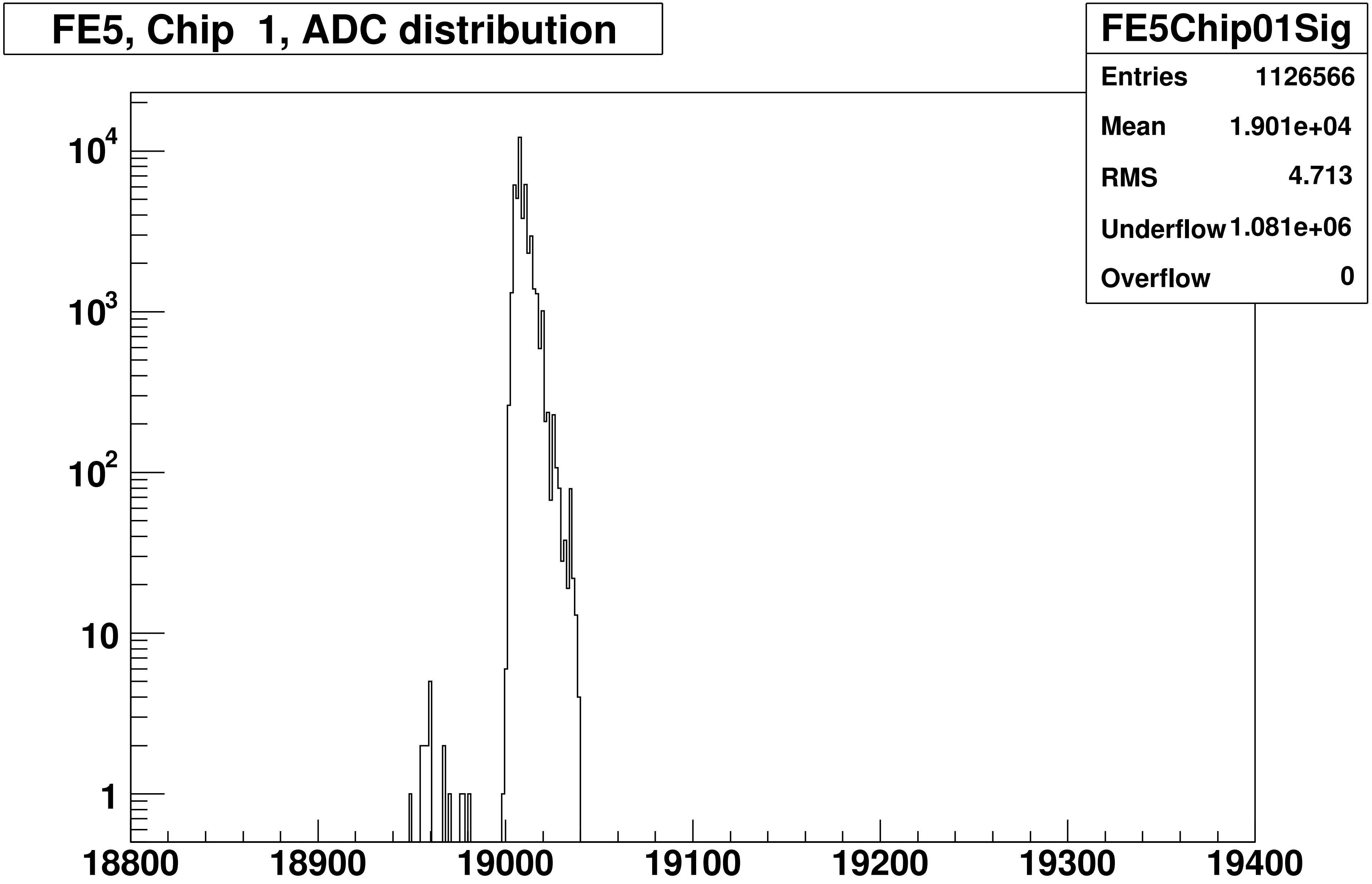
25000


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 1126566
Mean 3.681
RMS 1.316
Underflow 0
Overflow 4.639e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 1126566
Mean 1.901e+04
RMS 4.713
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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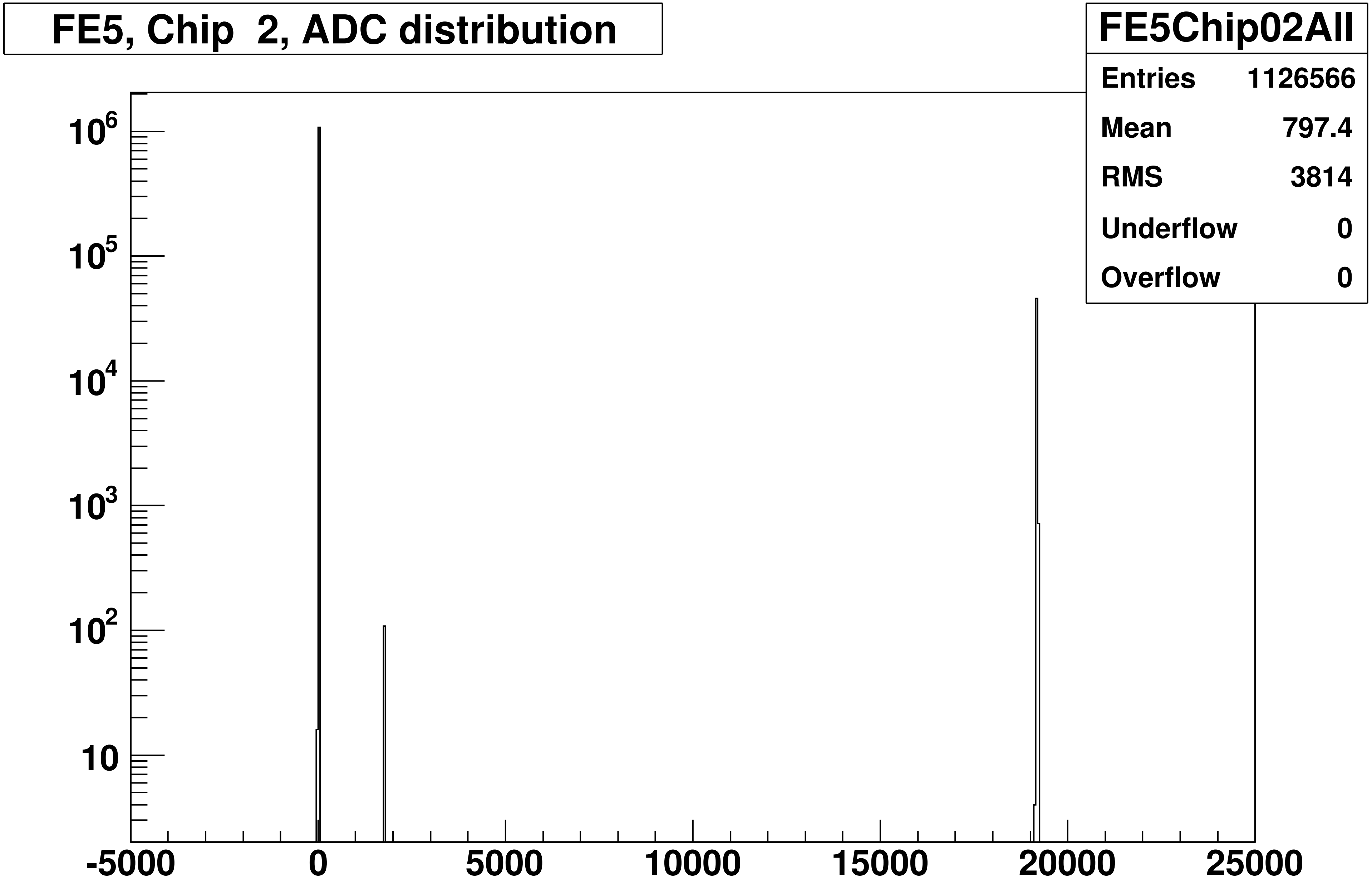
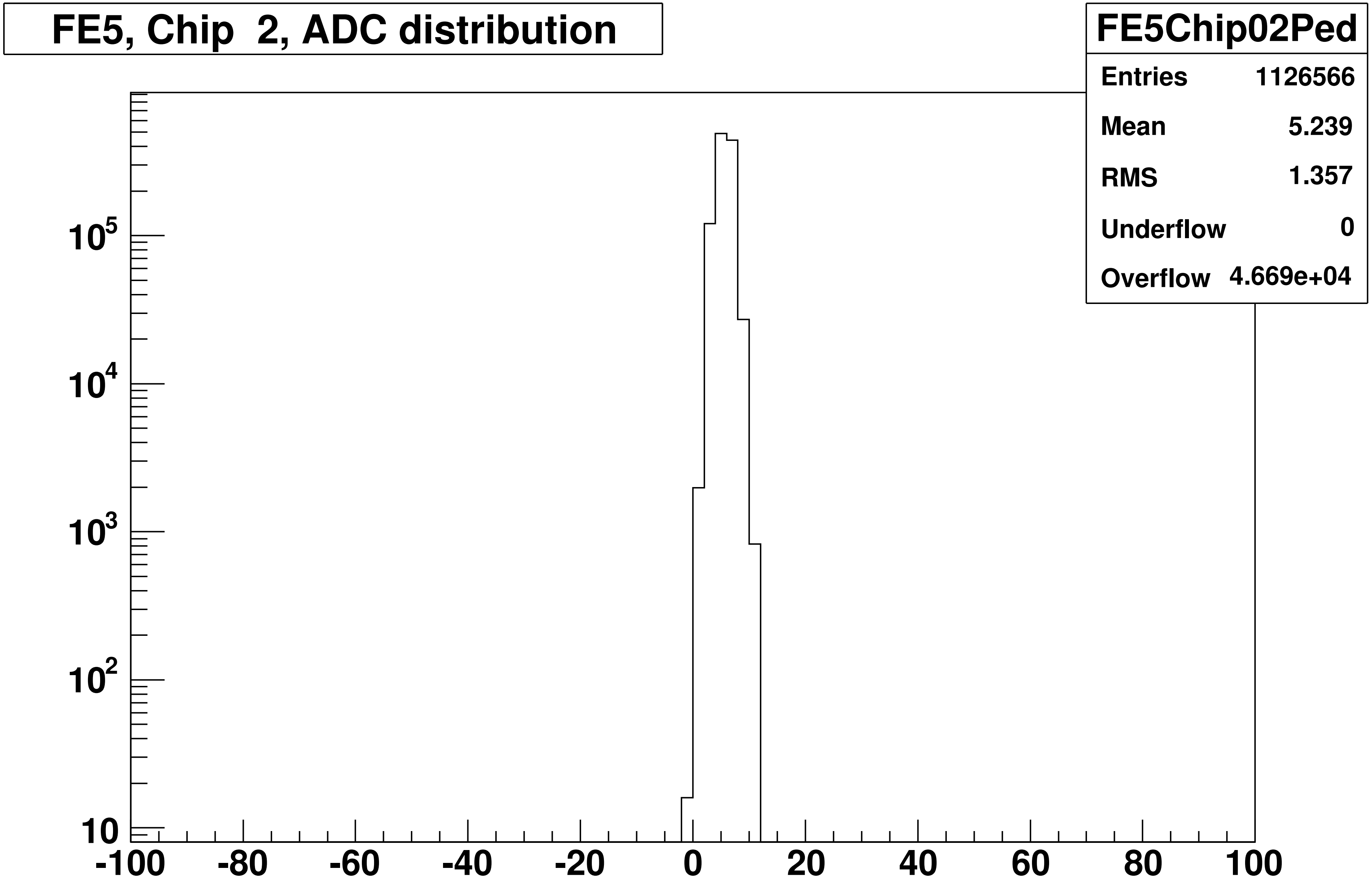
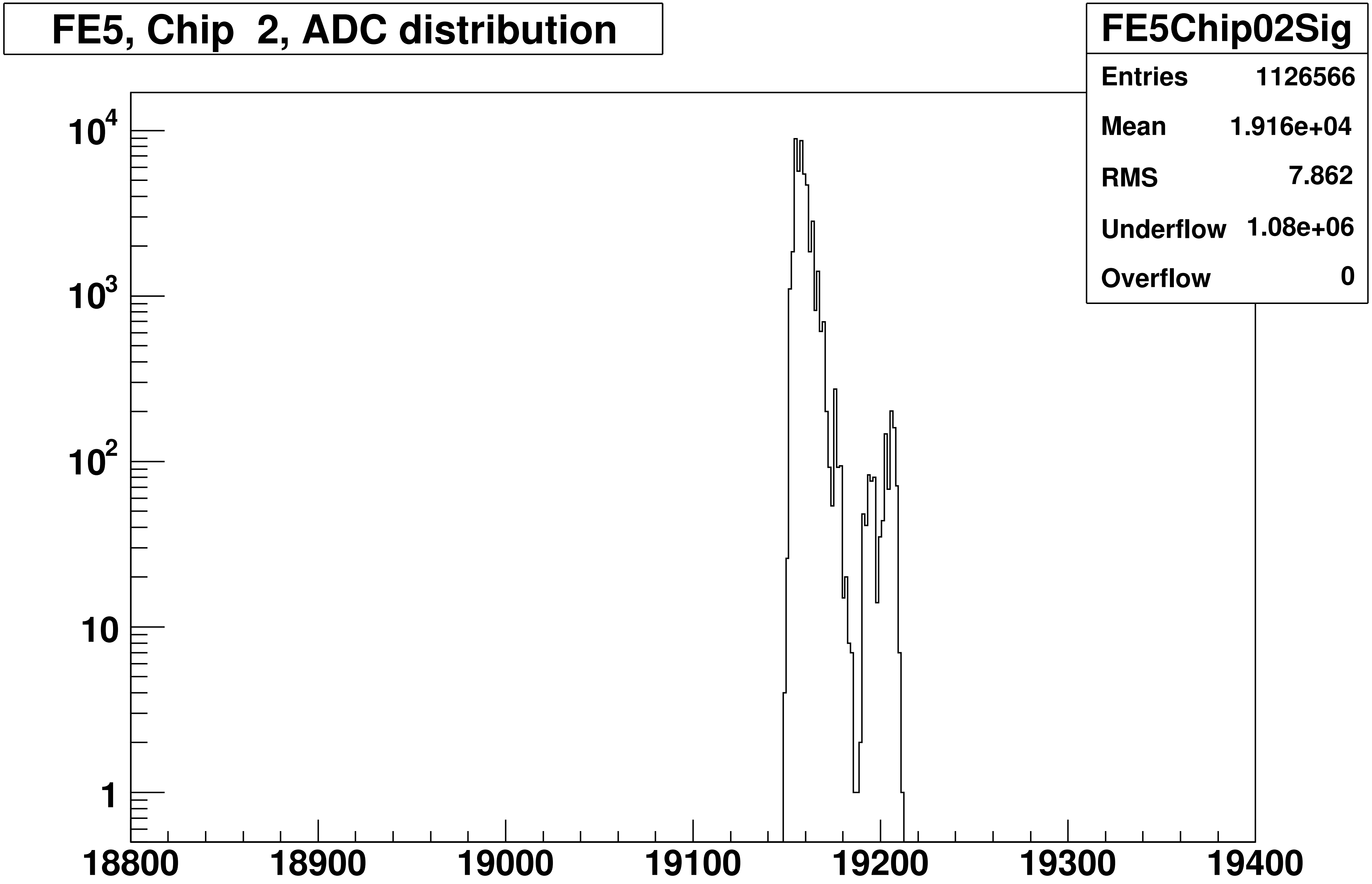
19400


FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 1126566
Mean 797.4
RMS 3814
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 1126566
Mean 5.239
RMS 1.357
Underflow 0
Overflow 4.669e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 1126566
Mean 1.916e+04
RMS 7.862
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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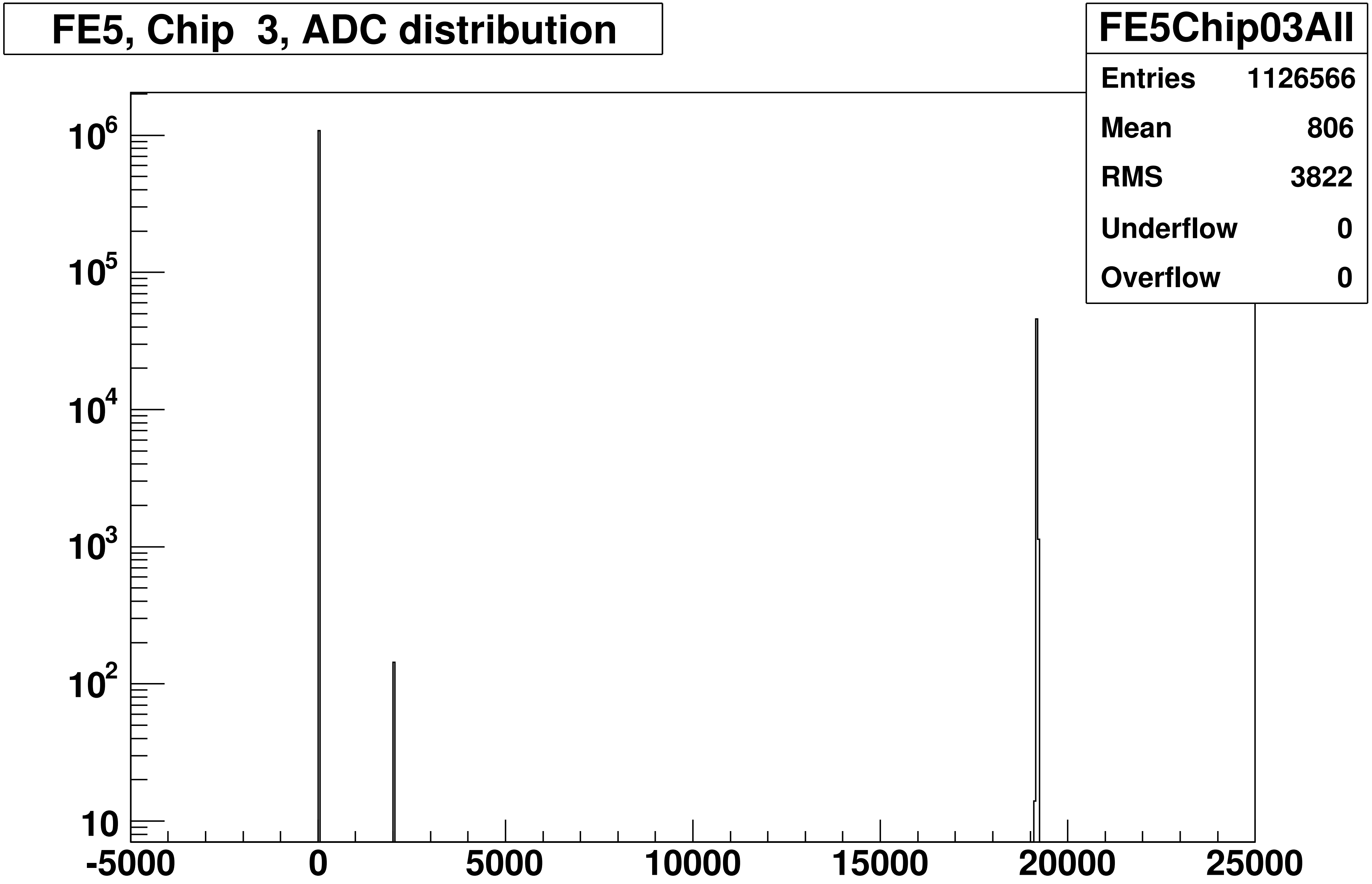
19400


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 1126566
Mean 806
RMS 3822
Underflow 0
Overflow 0
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10^3
10^4
10^5
10^6
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10000
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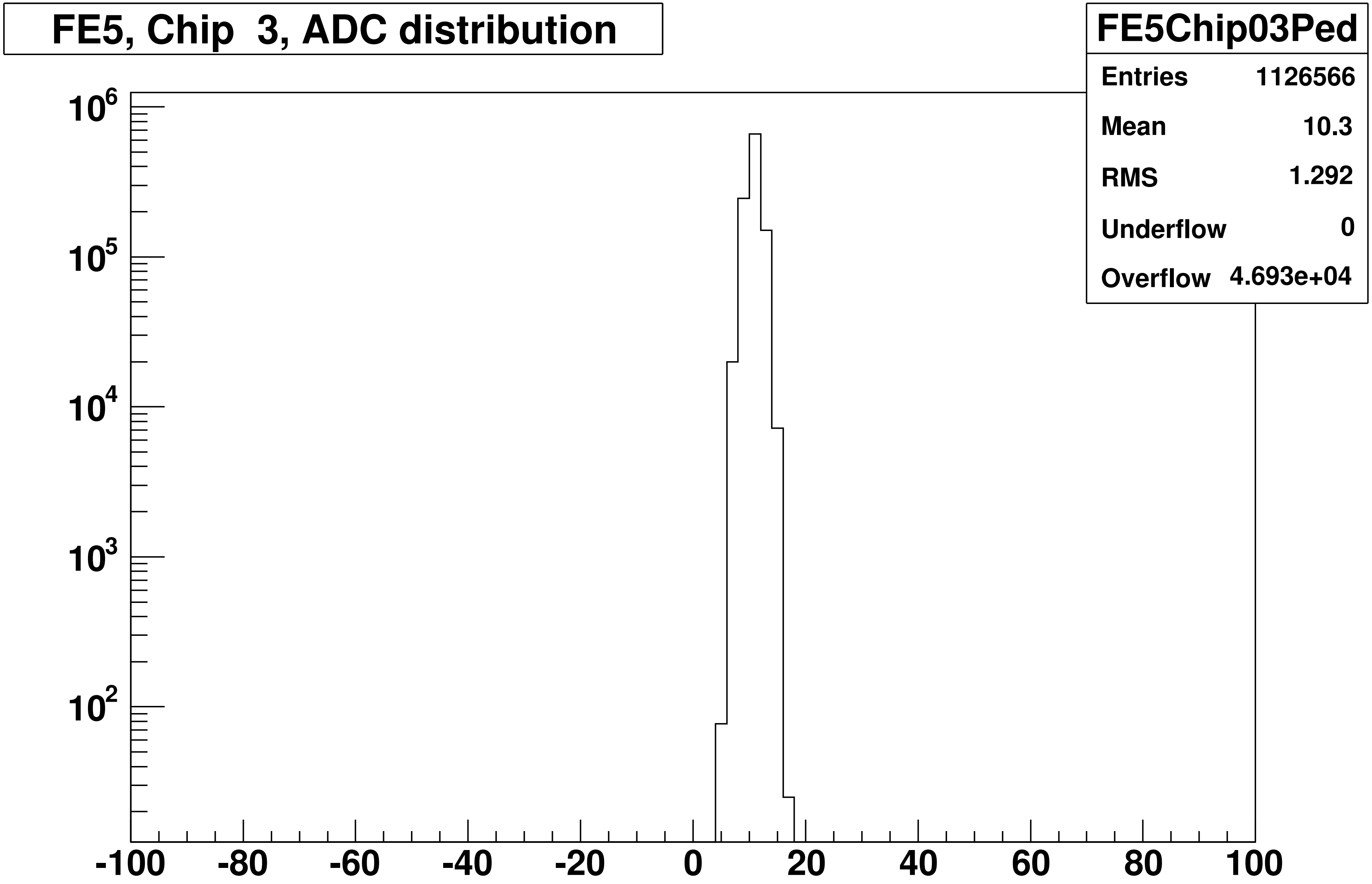
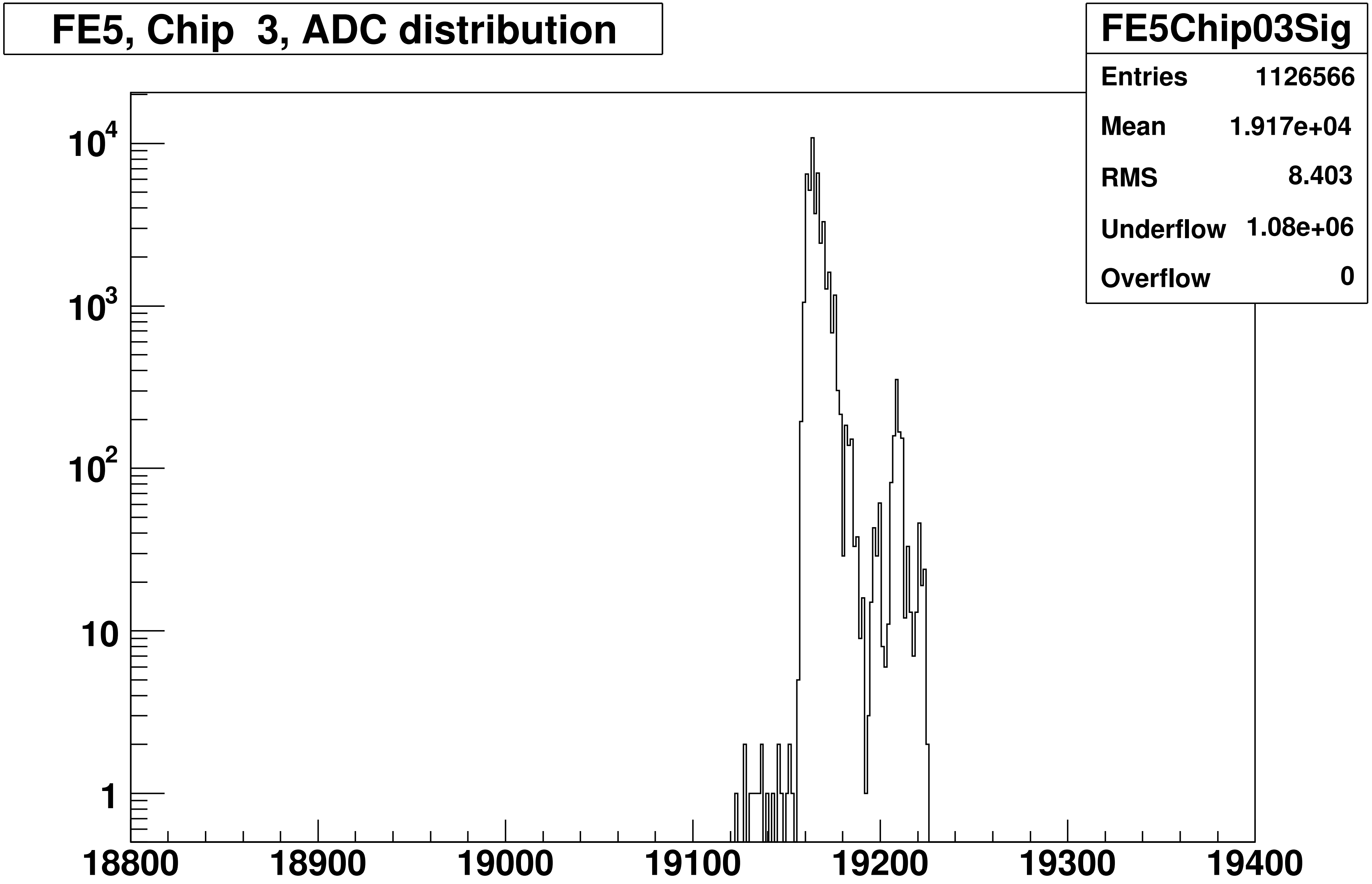
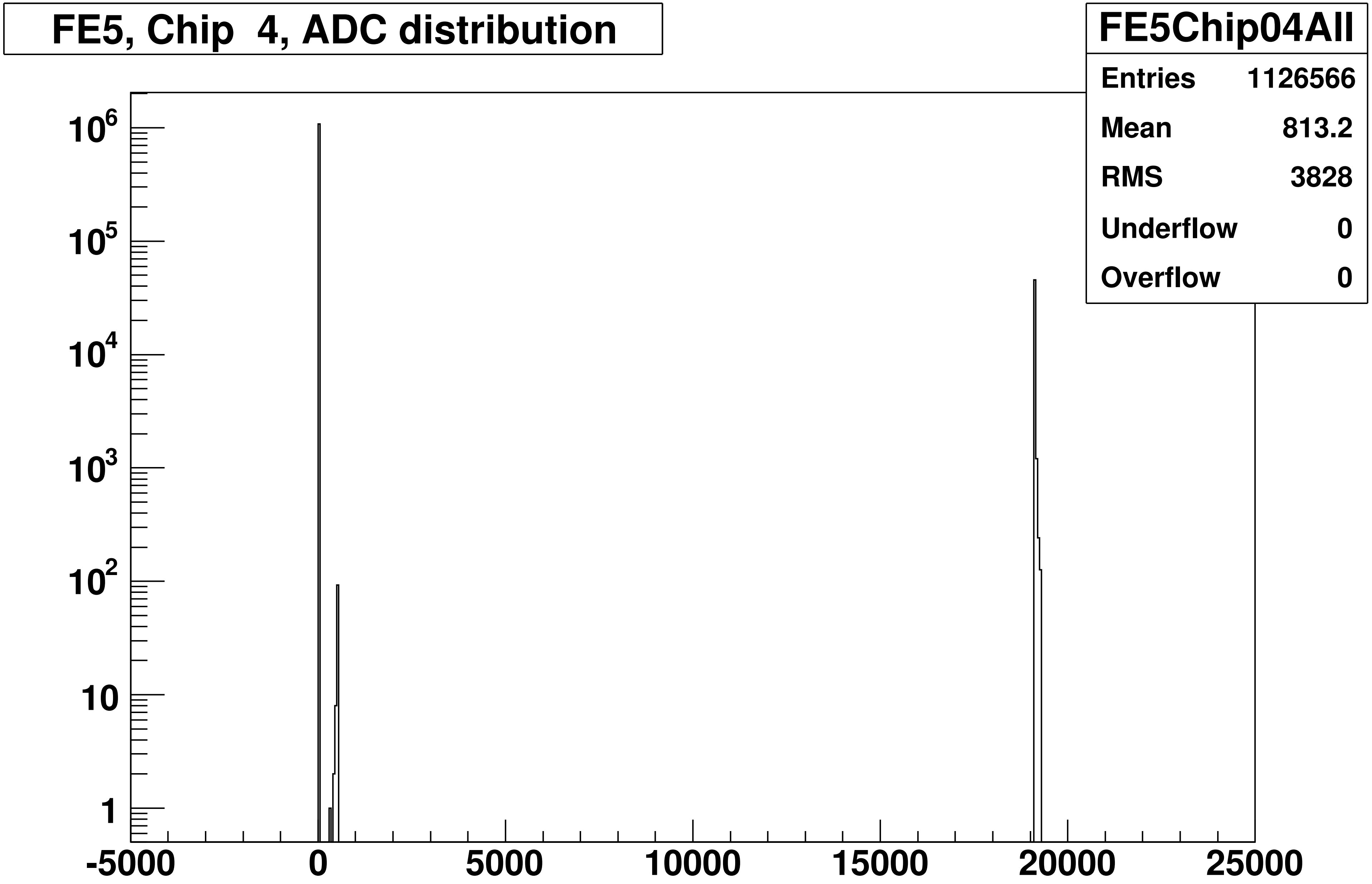
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 1126566
Mean 10.3
RMS 1.292
Underflow 0
Overflow 4.693e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 1126566
Mean 1.917e+04
RMS 8.403
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 1126566
Mean 813.2
RMS 3828
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
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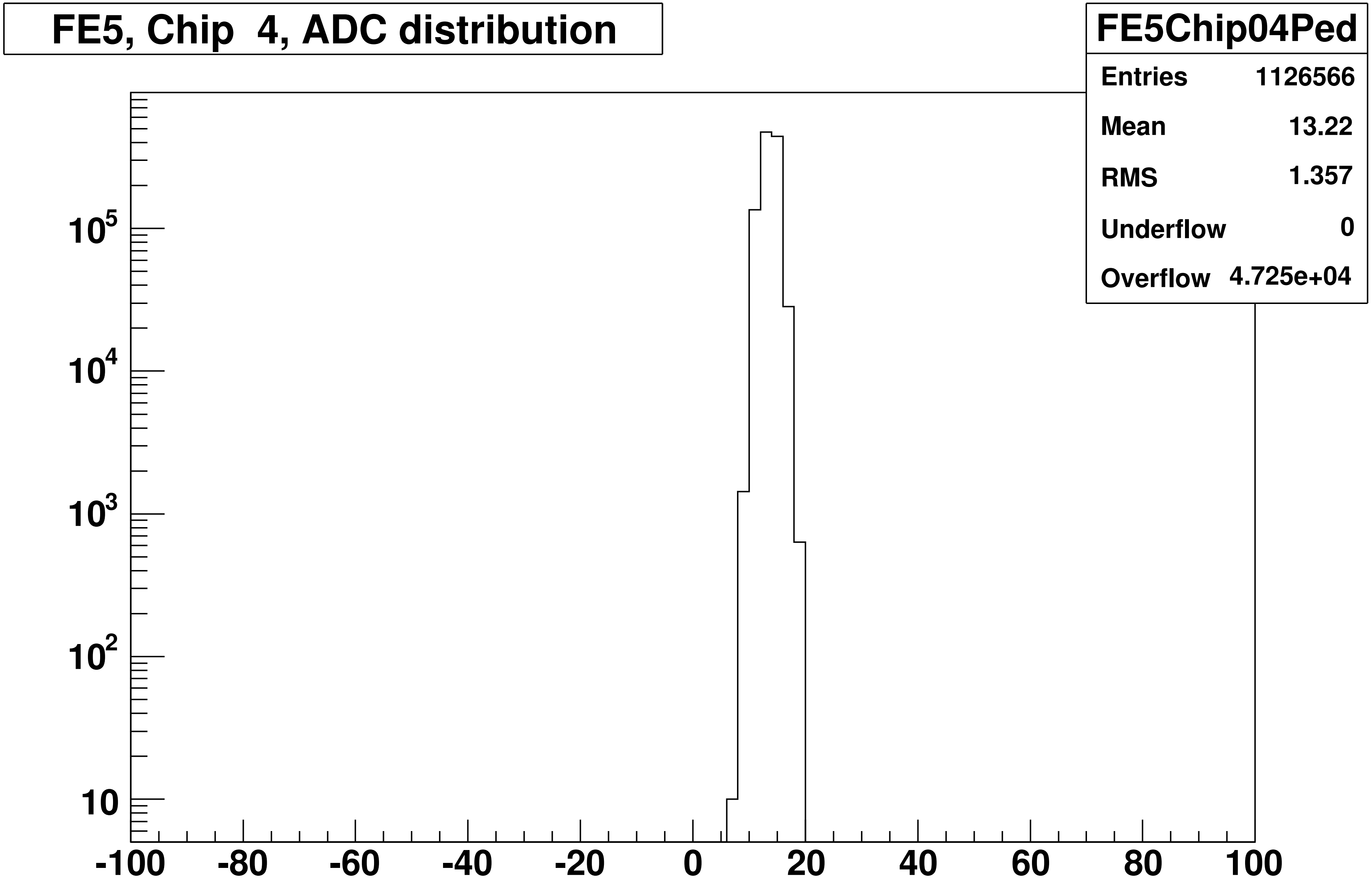
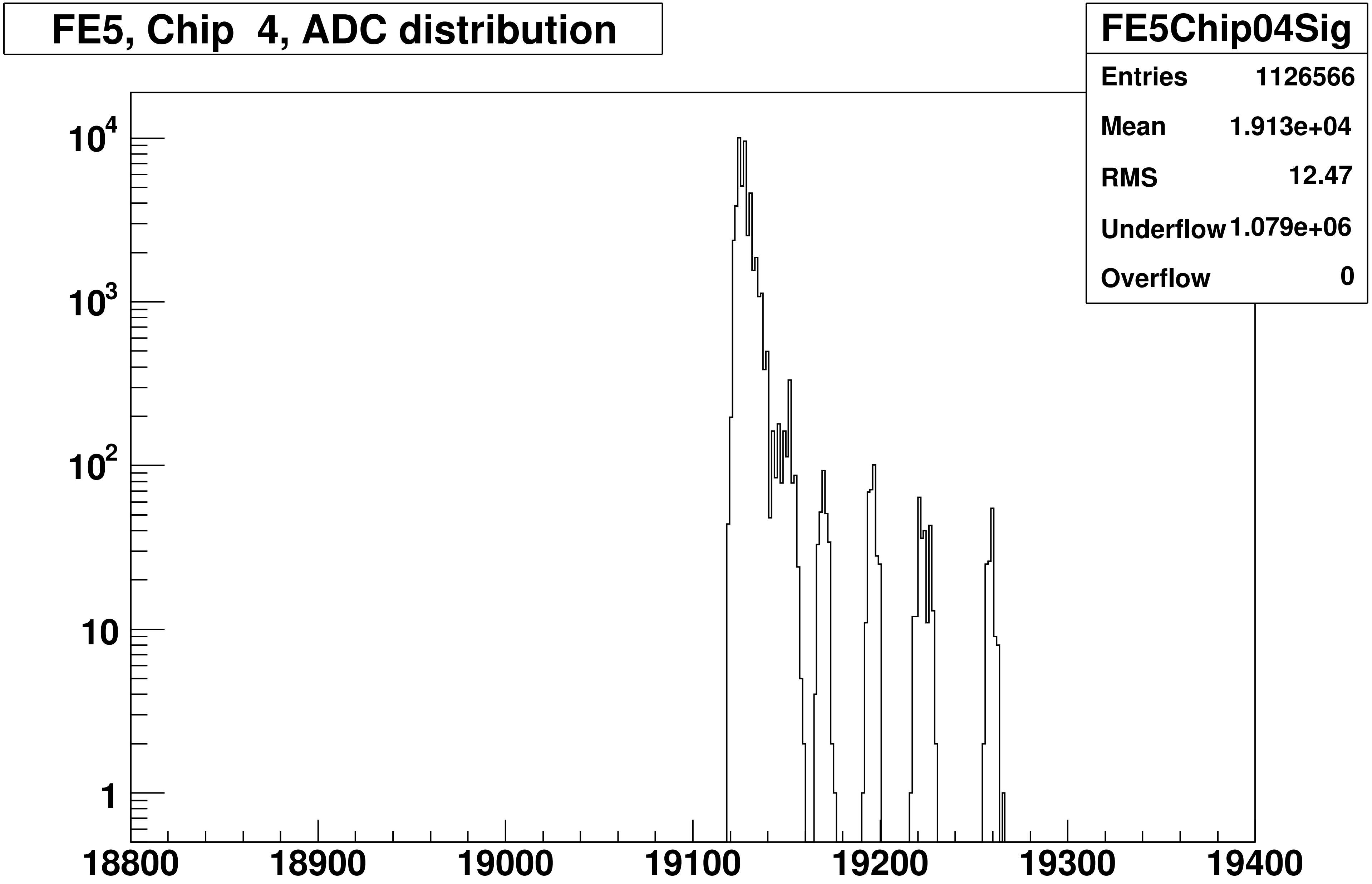
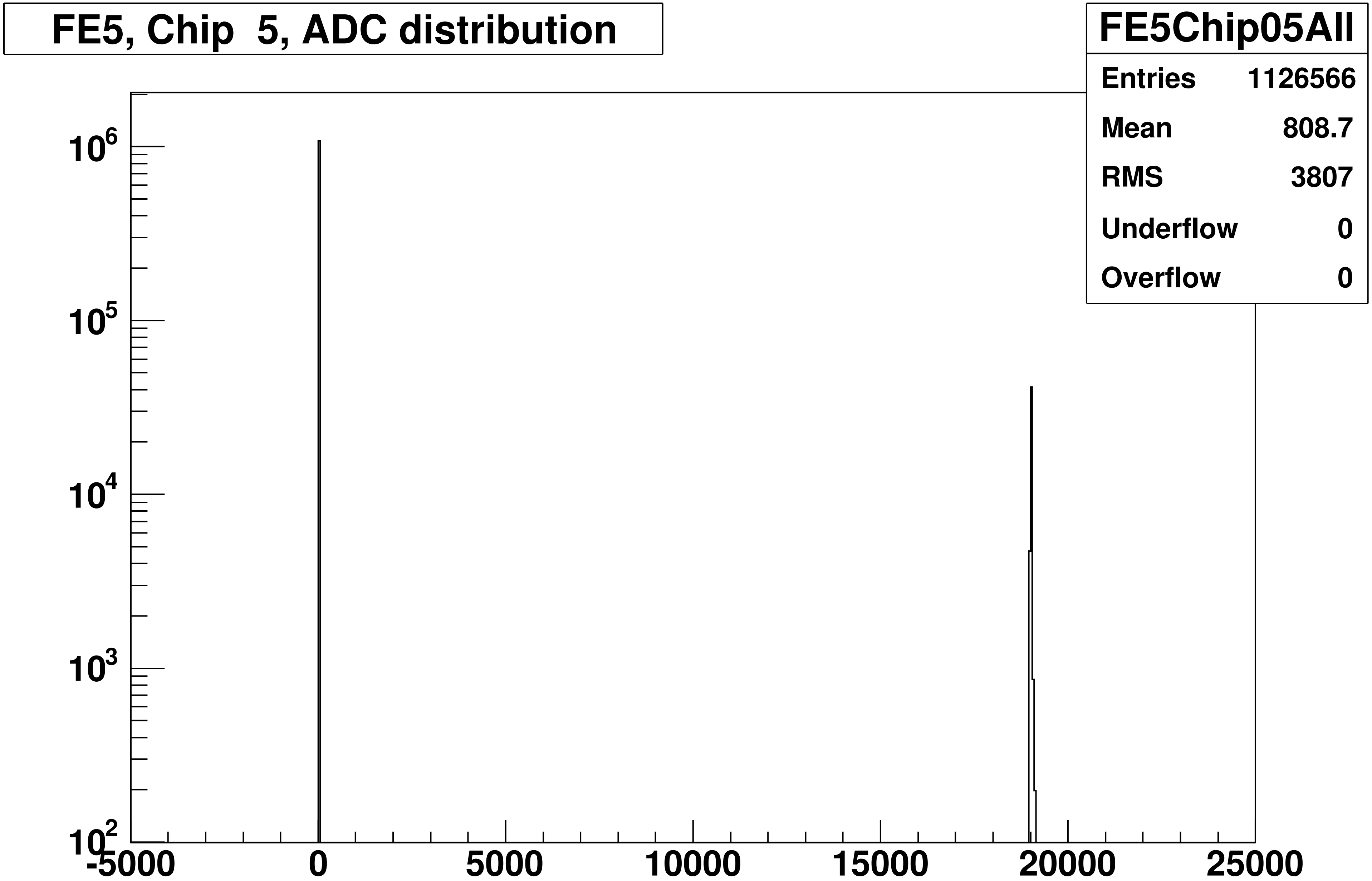
25000


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 1126566
Mean 13.22
RMS 1.357
Underflow 0
Overflow 4.725e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 1126566
Mean 1.913e+04
RMS 12.47
Underflow 1.079e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 1126566
Mean 808.7
RMS 3807
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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20000
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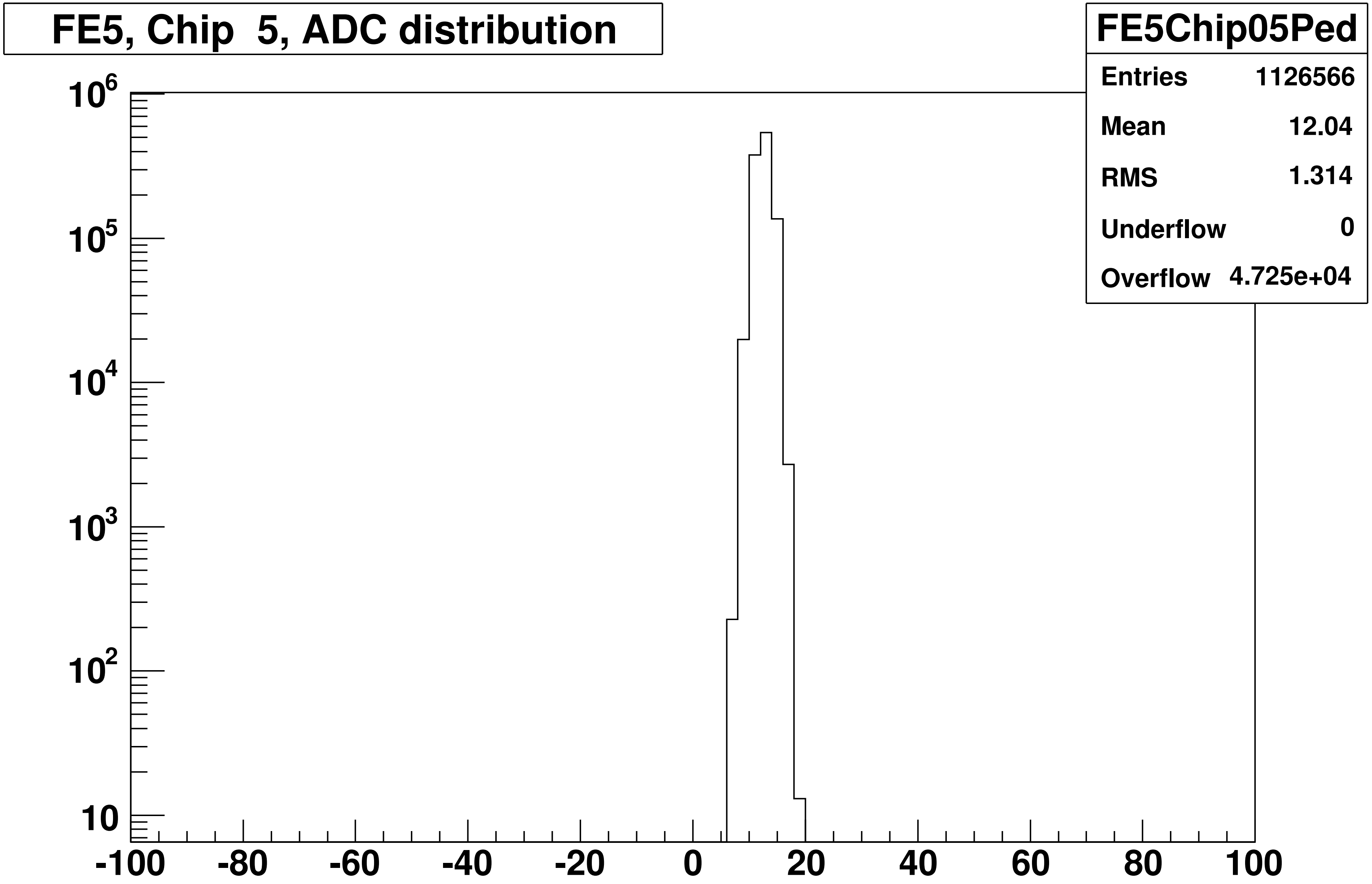
FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 1126566
Mean 12.04
RMS 1.314
Underflow 0
Overflow 4.725e+04
10^6
10^5
10^4
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10^2
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-80
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-40
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80
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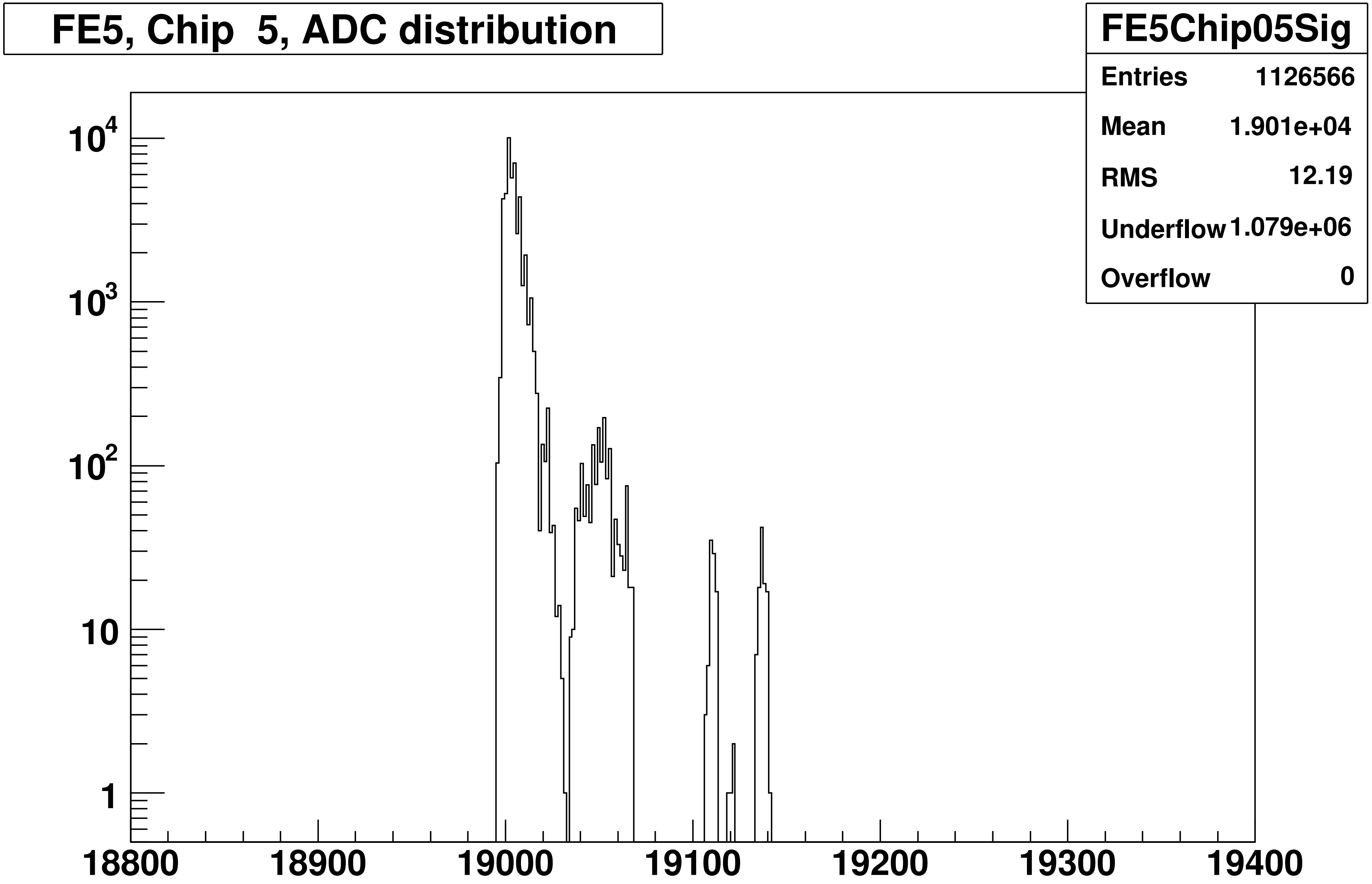
FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 1126566
Mean 1.901e+04
RMS 12.19
Underflow 1.079e+06
Overflow 0
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10^3
10^2
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1
18800
18900
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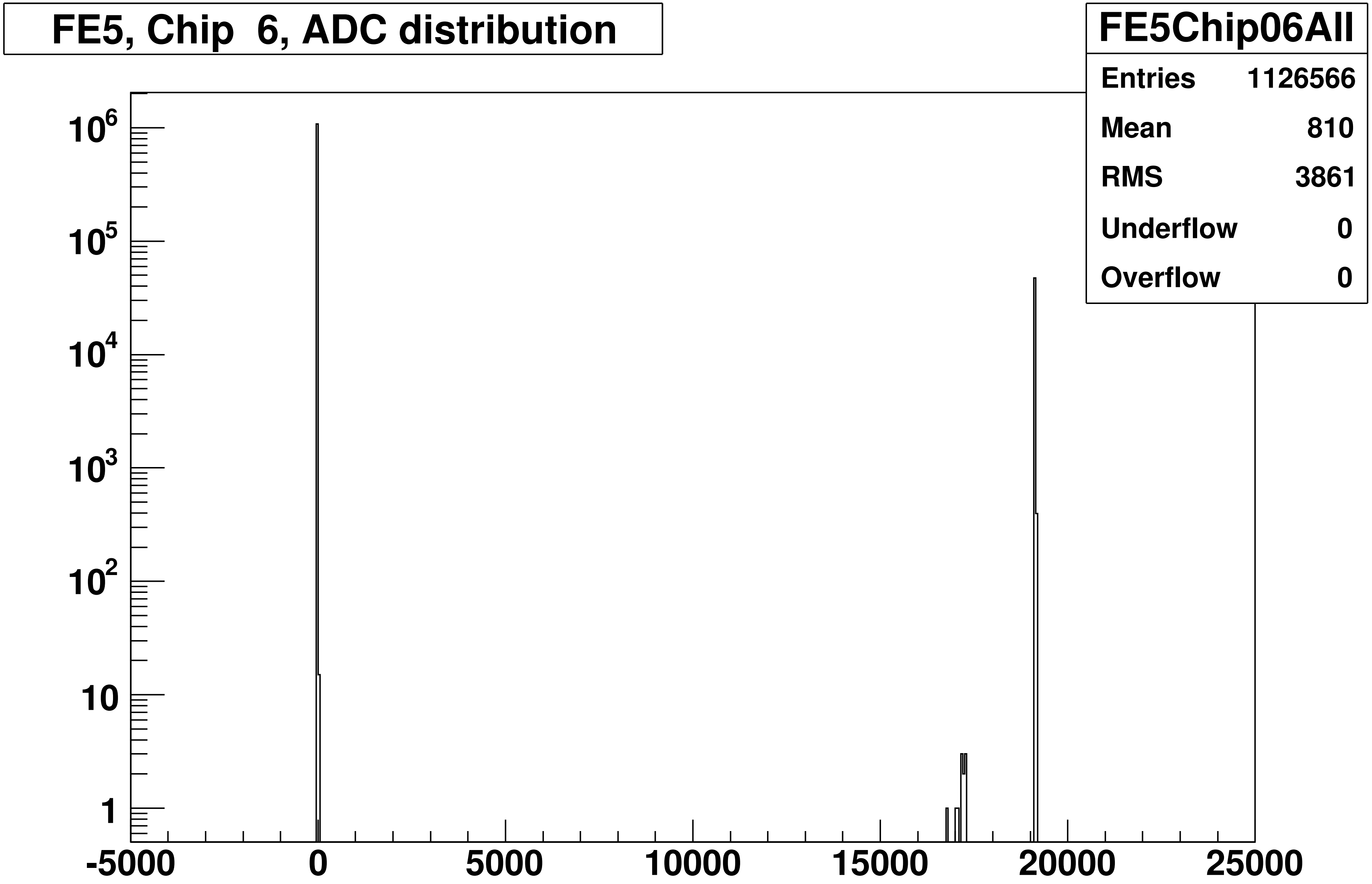
FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 1126566
Mean 810
RMS 3861
Underflow 0
Overflow 0
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10^4
10^3
10^2
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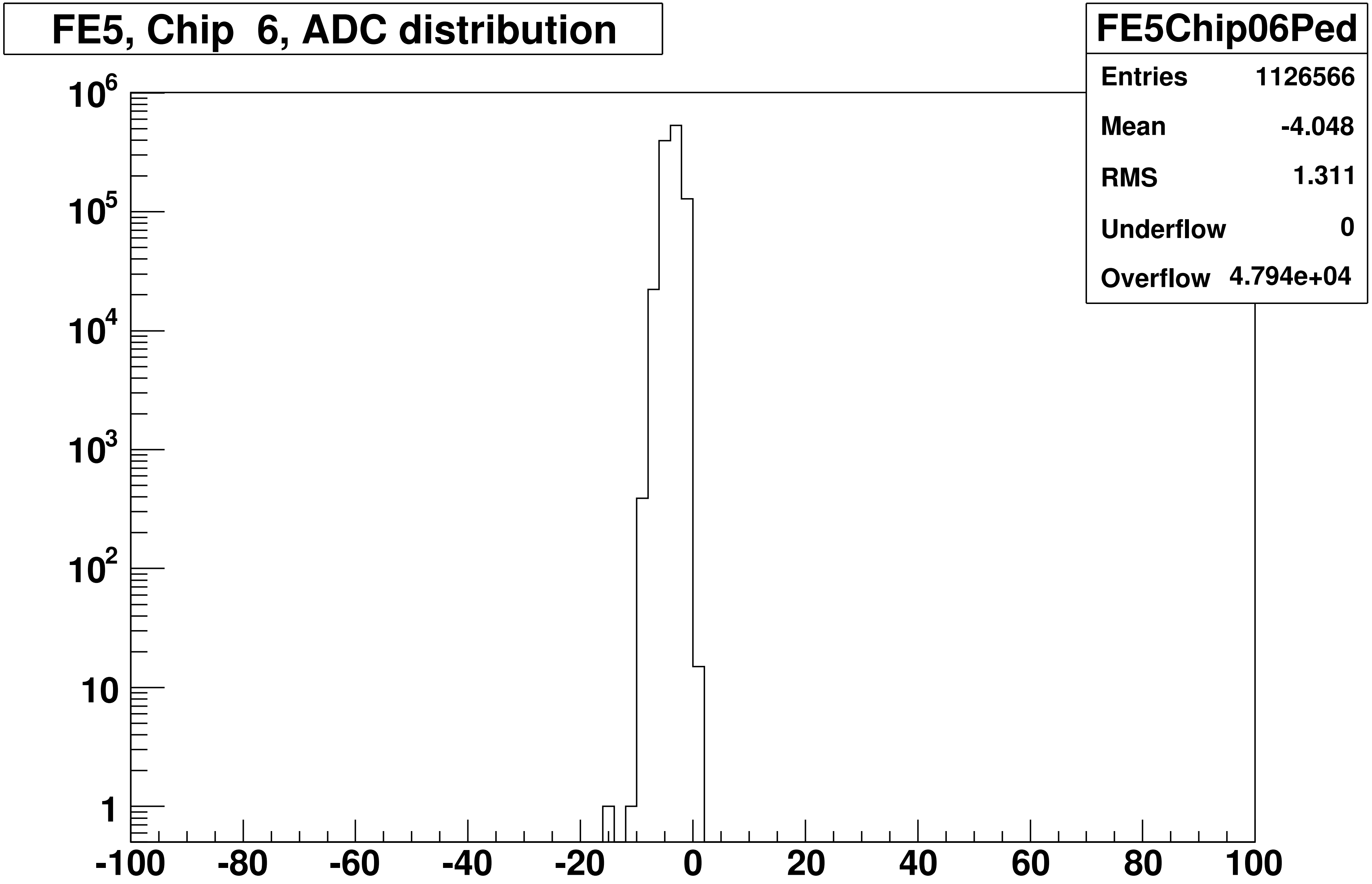

FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 1126566
Mean -4.048
RMS 1.311
Underflow 0
Overflow 4.794e+04
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10^5
10^4
10^3
10^2
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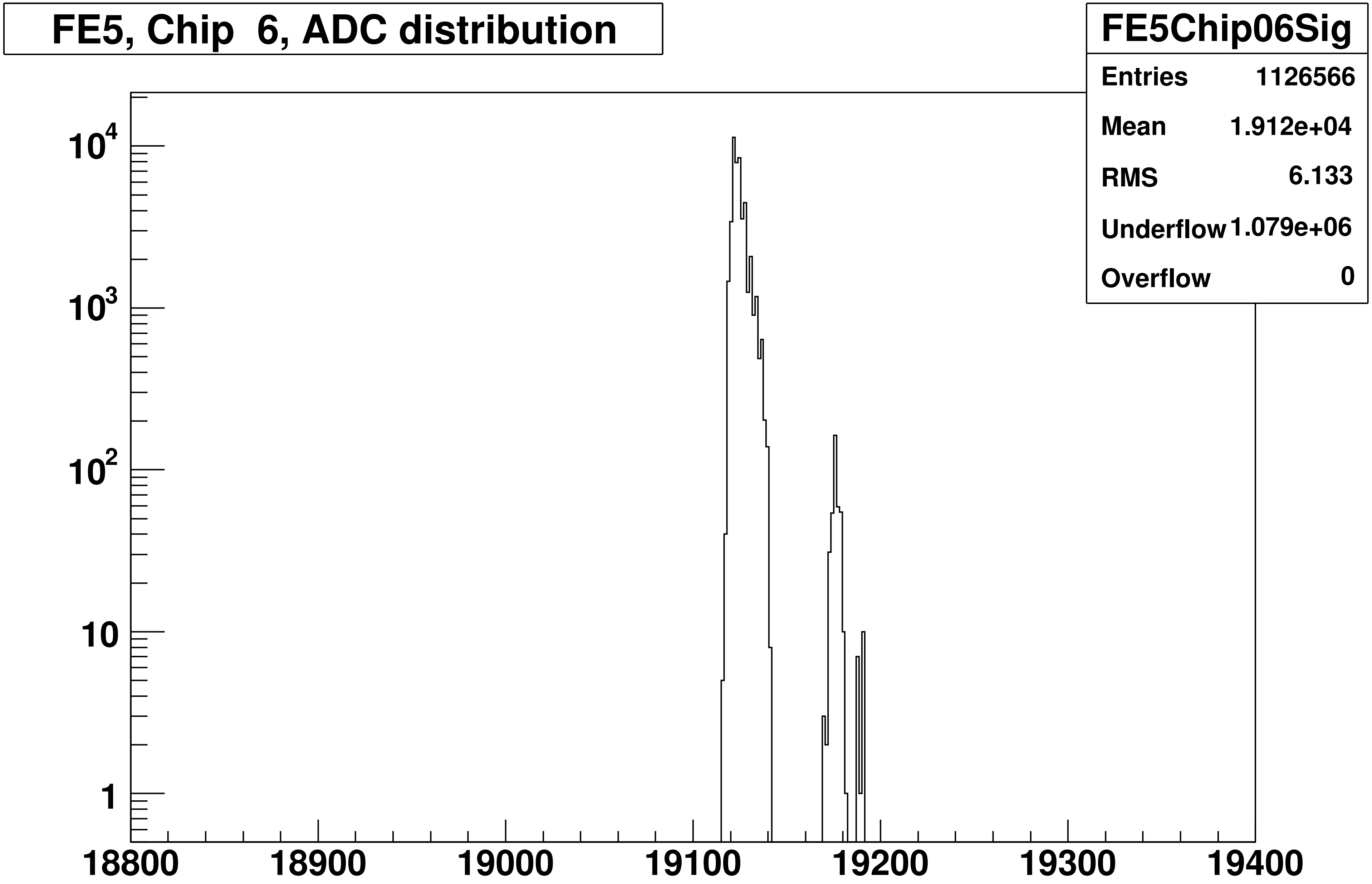

FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 1126566
Mean 1.912e+04
RMS 6.133
Underflow 1.079e+06
Overflow 0
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10^3
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18900
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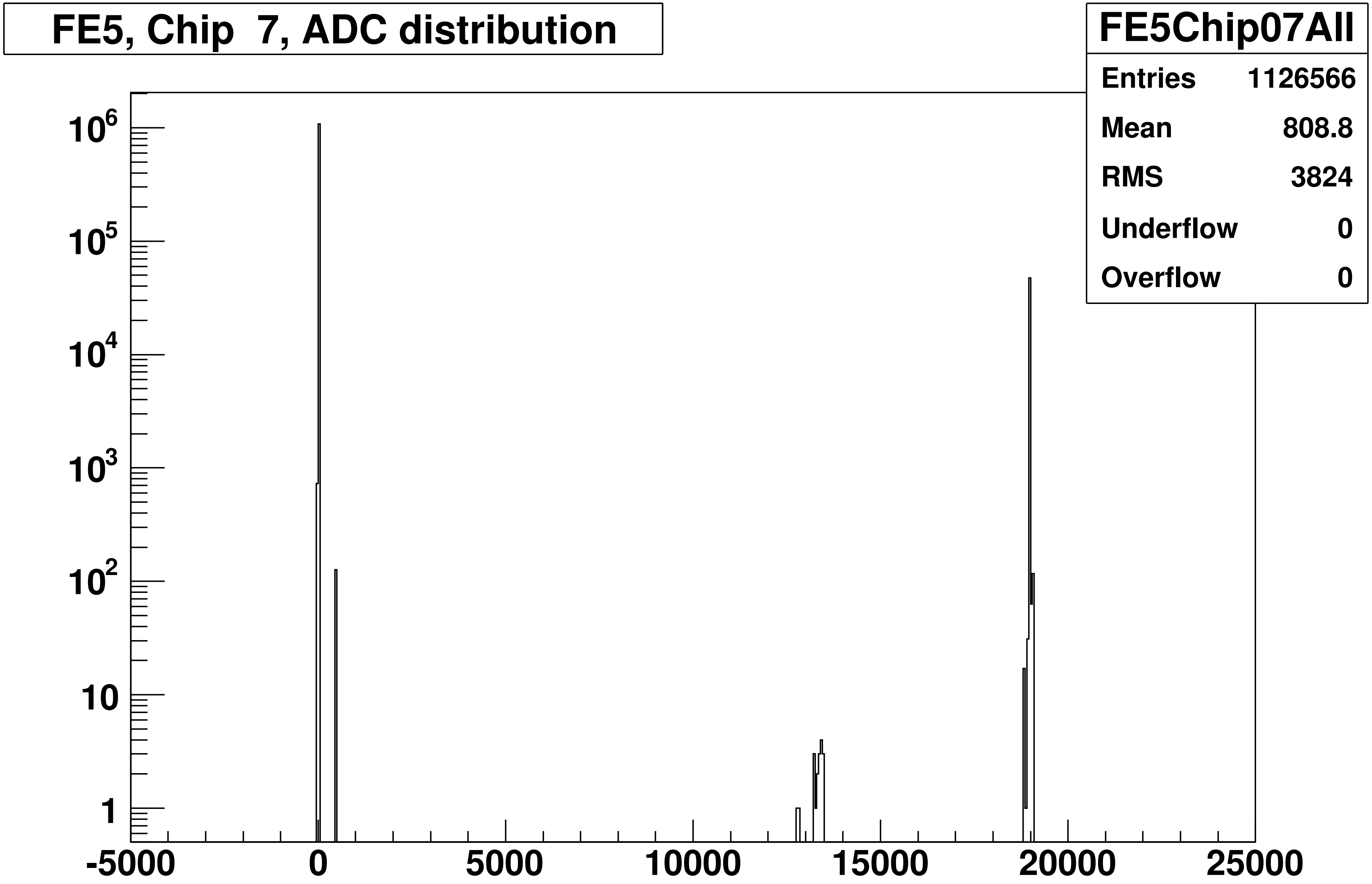
19200
19300
19400


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 1126566
Mean 808.8
RMS 3824
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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20000
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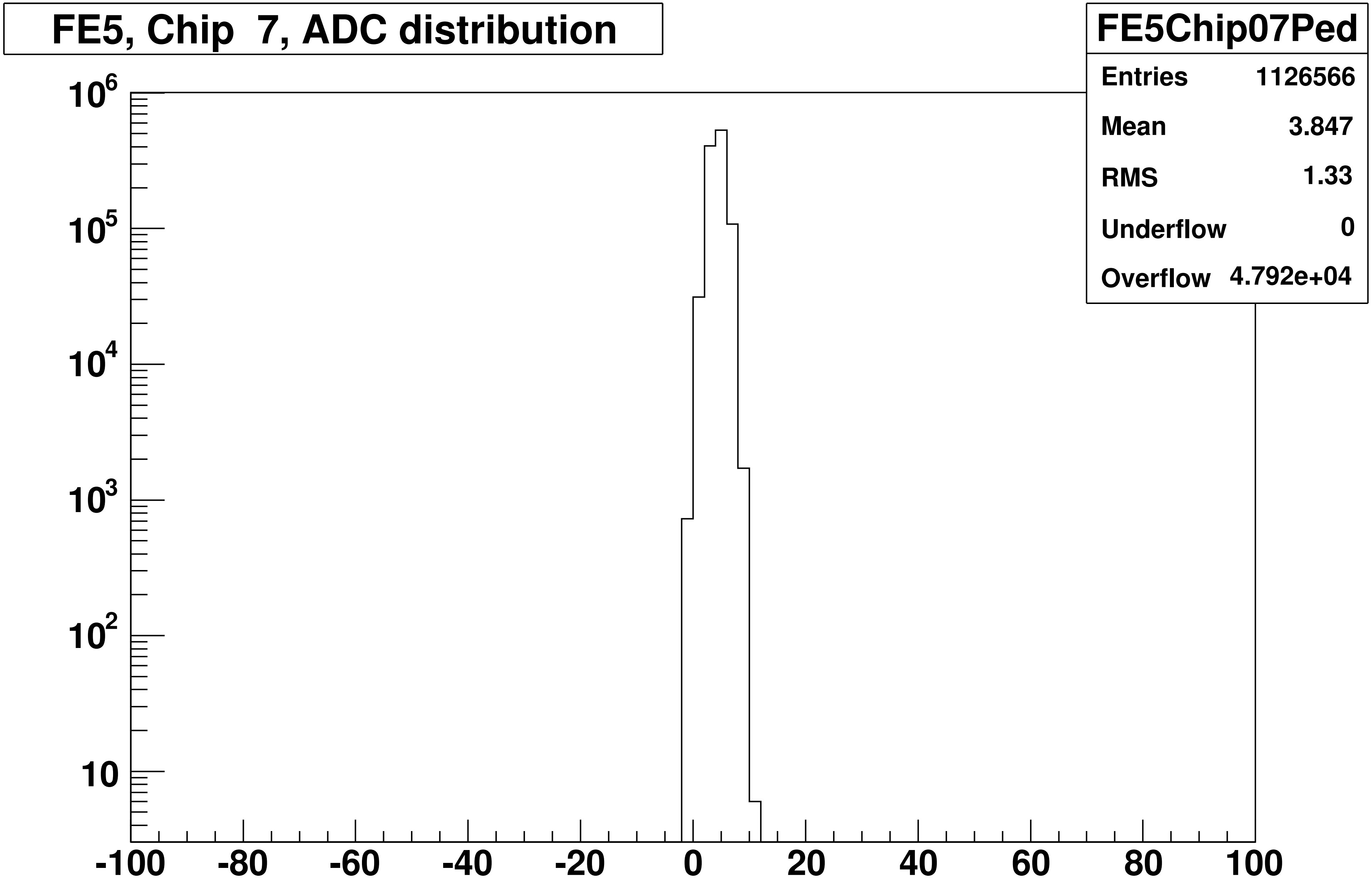
FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 1126566
Mean 3.847
RMS 1.33
Underflow 0
Overflow 4.792e+04
10^6
10^5
10^4
10^3
10^2
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80
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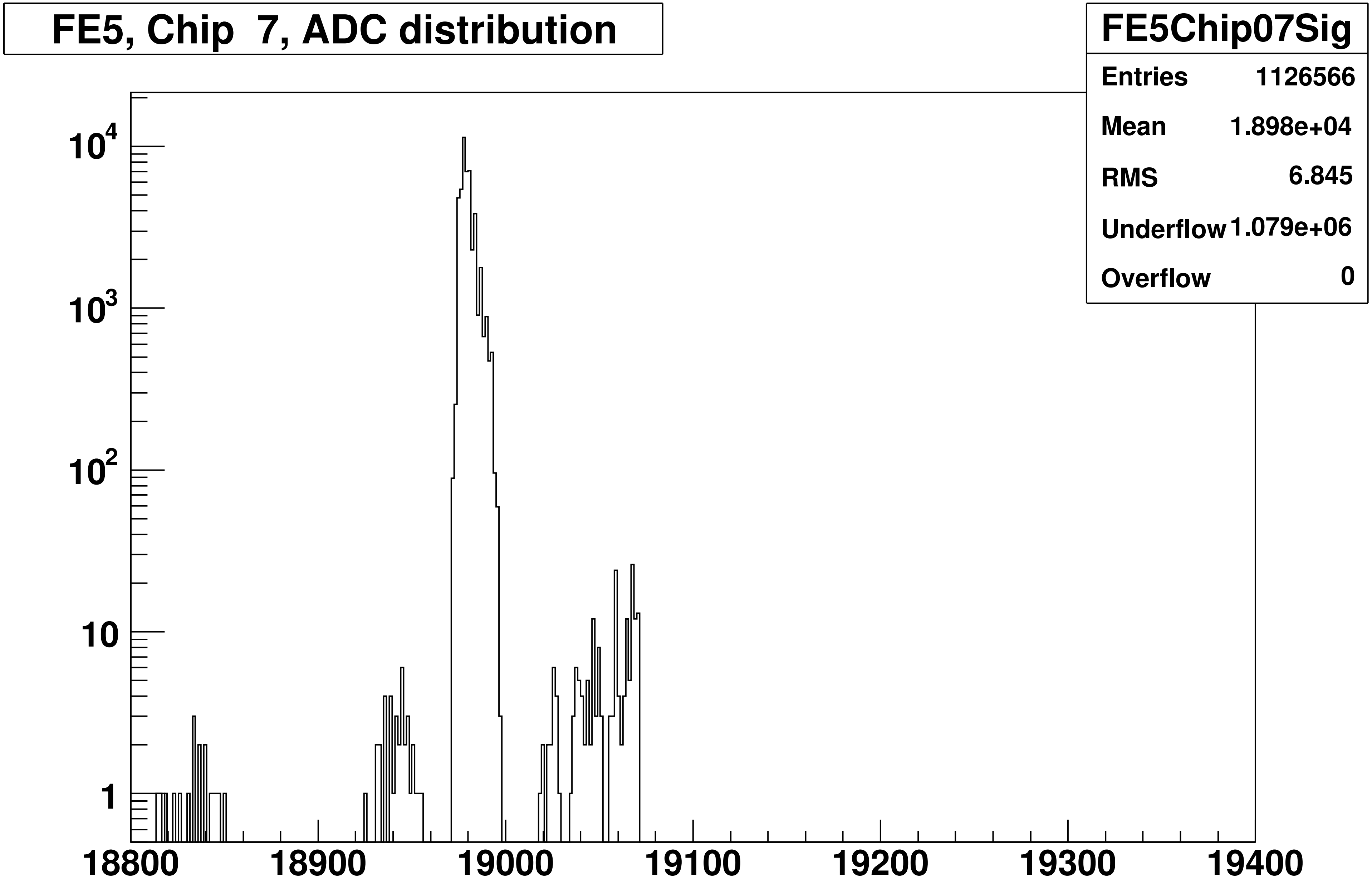
FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 1126566
Mean 1.898e+04
RMS 6.845
Underflow 1.079e+06
Overflow 0
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10^3
10^2
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18900
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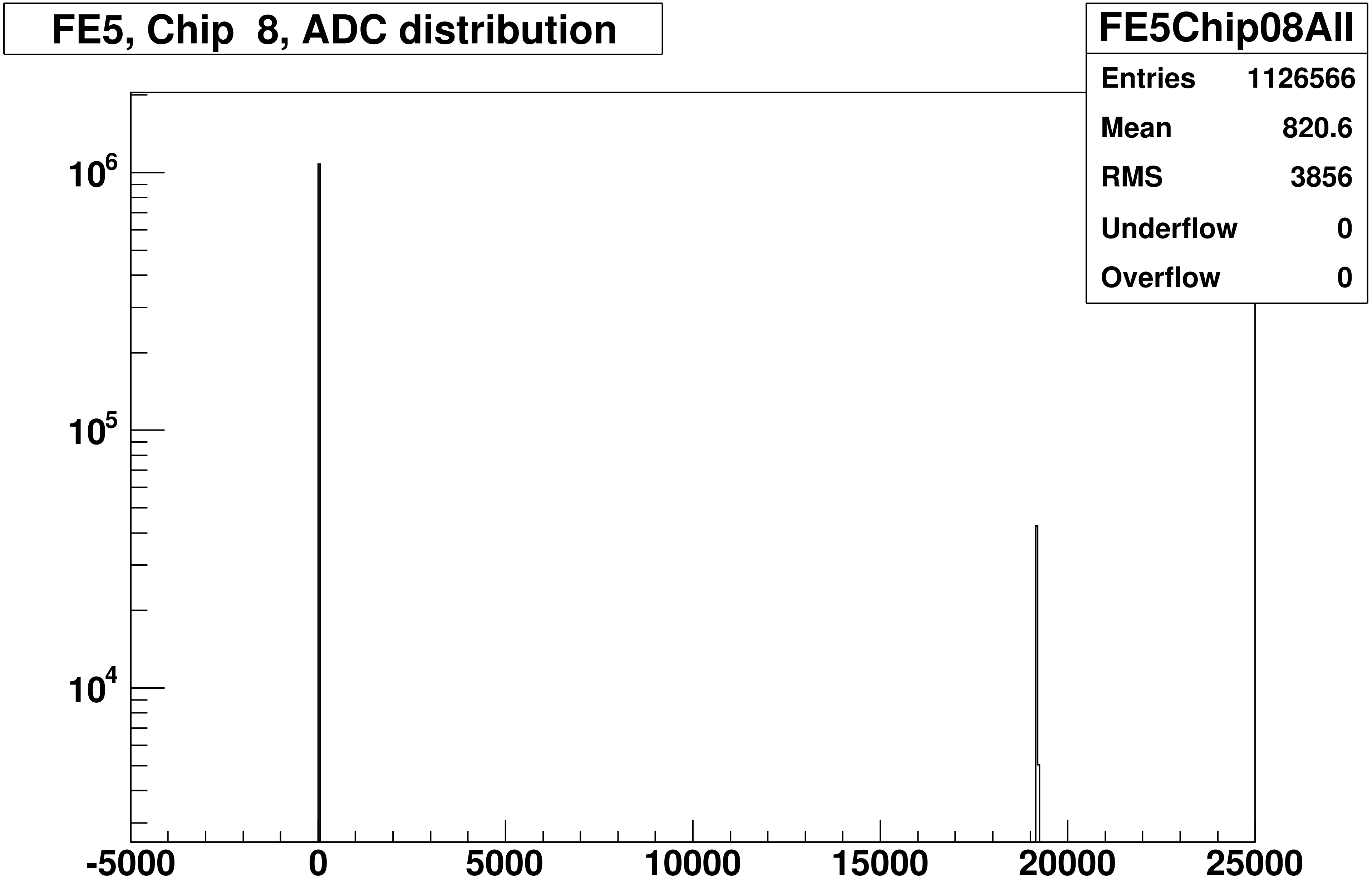
FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 1126566
Mean 820.6
RMS 3856
Underflow 0
Overflow 0
10^6
10^5
10^4
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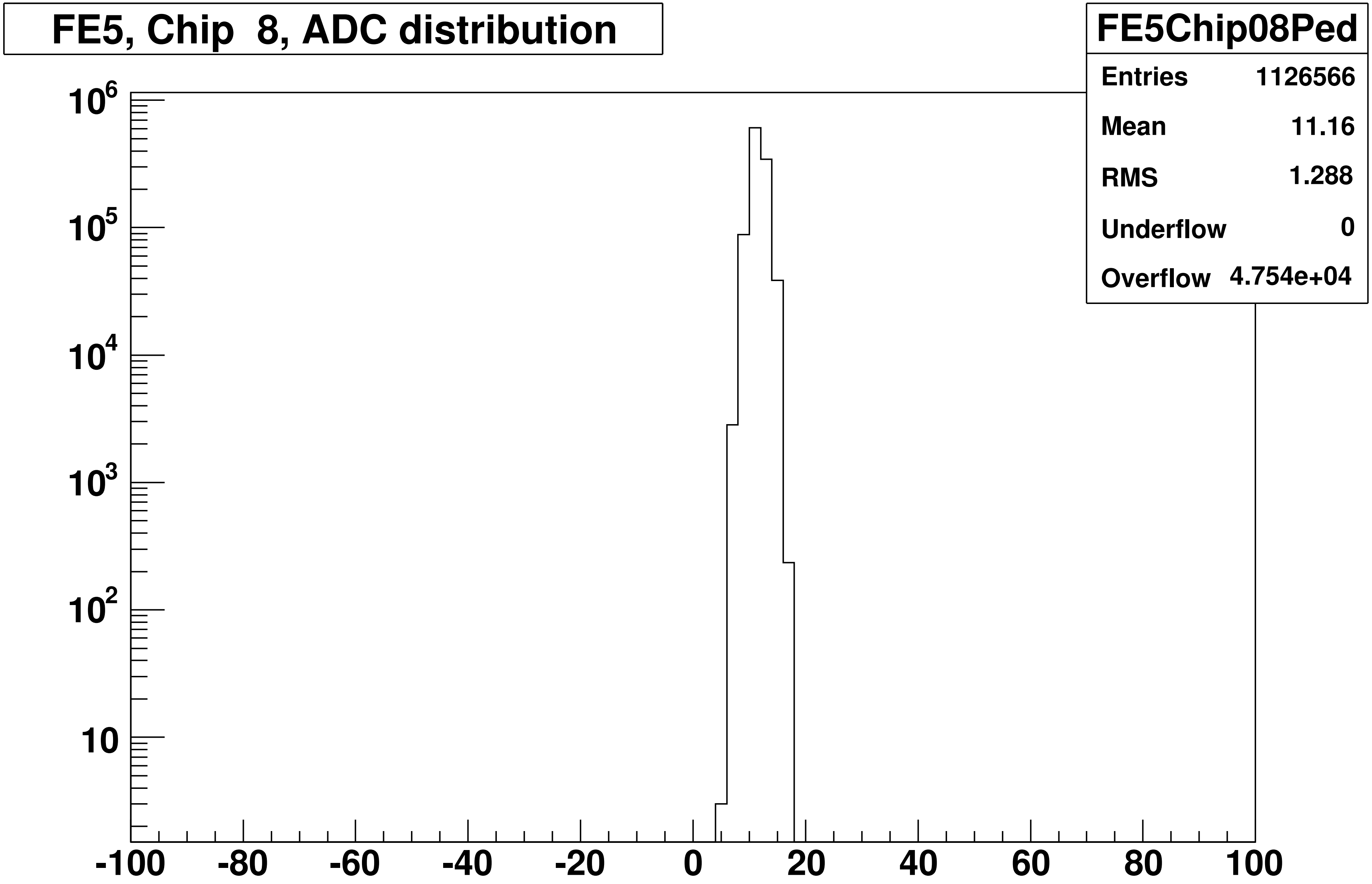
10000
15000
20000
25000


FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 1126566
Mean 11.16
RMS 1.288
Underflow 0
Overflow 4.754e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
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-40
-20
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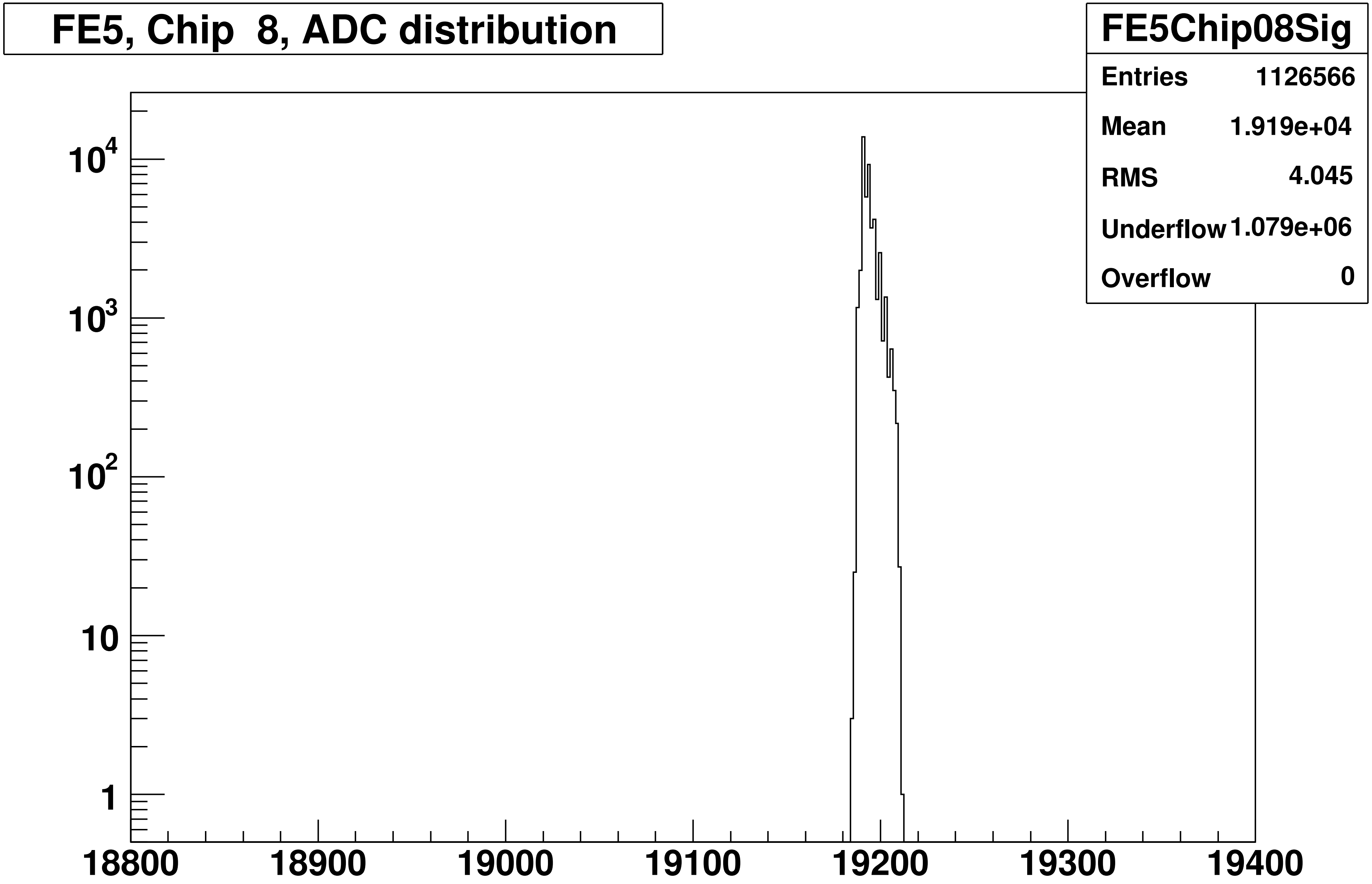
40
60
80
100



FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 1126566
Mean 1.919e+04
RMS 4.045
Underflow 1.079e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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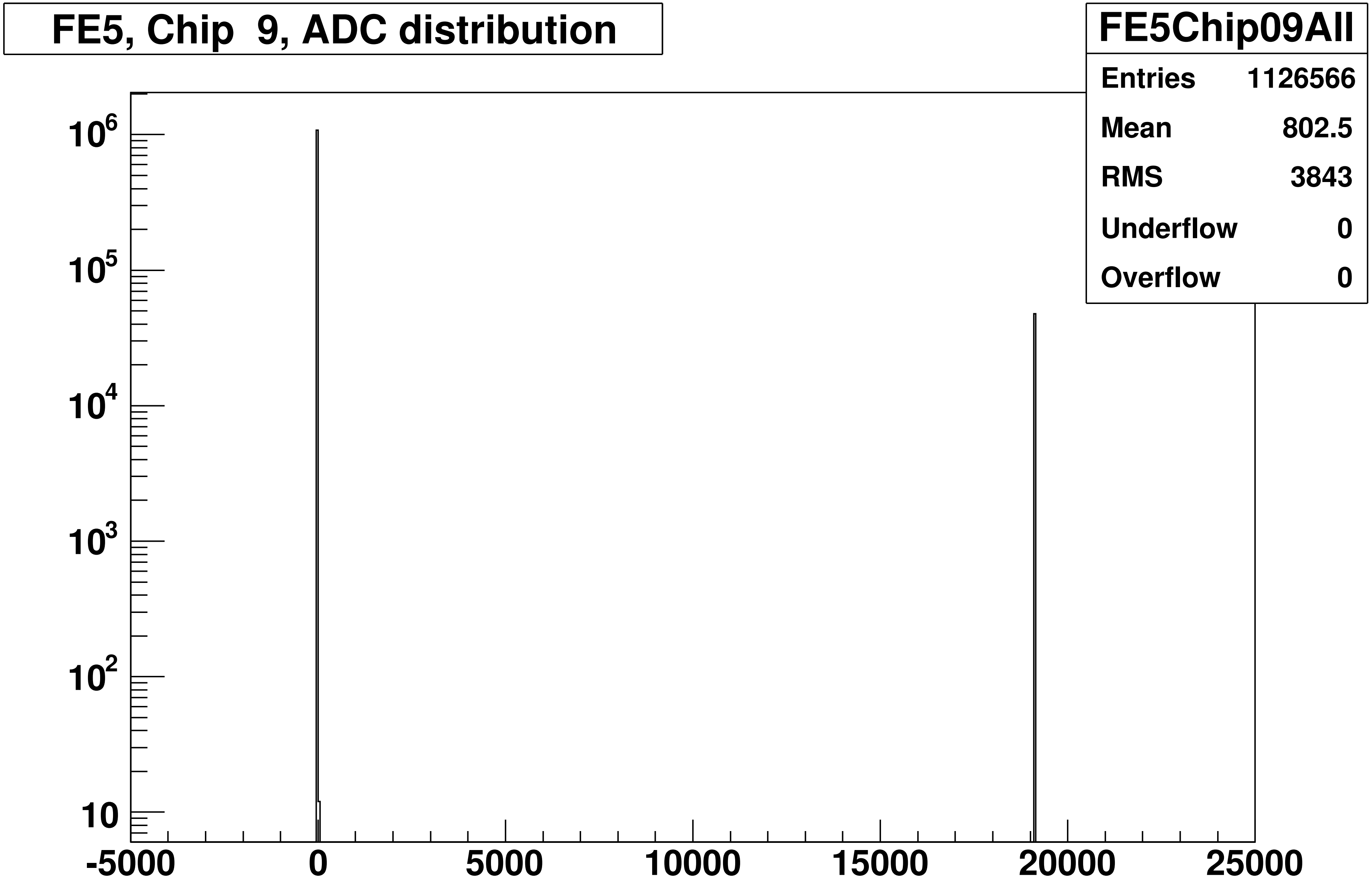
19100
19200
19300
19400


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 1126566
Mean 802.5
RMS 3843
Underflow 0
Overflow 0
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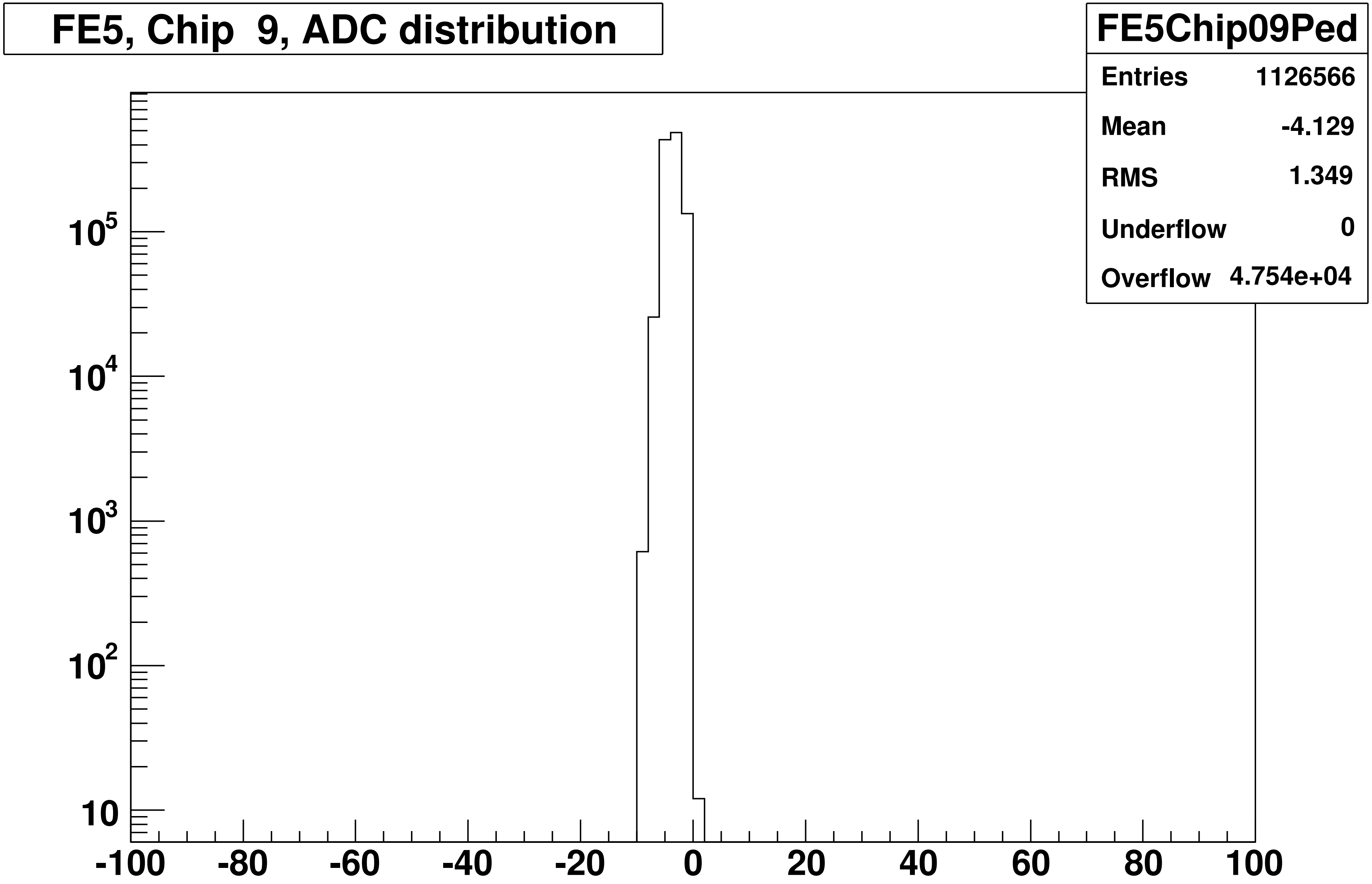
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 1126566
Mean -4.129
RMS 1.349
Underflow 0
Overflow 4.754e+04
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10^4
10^3
10^2
10
-100
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-60
-40
-20
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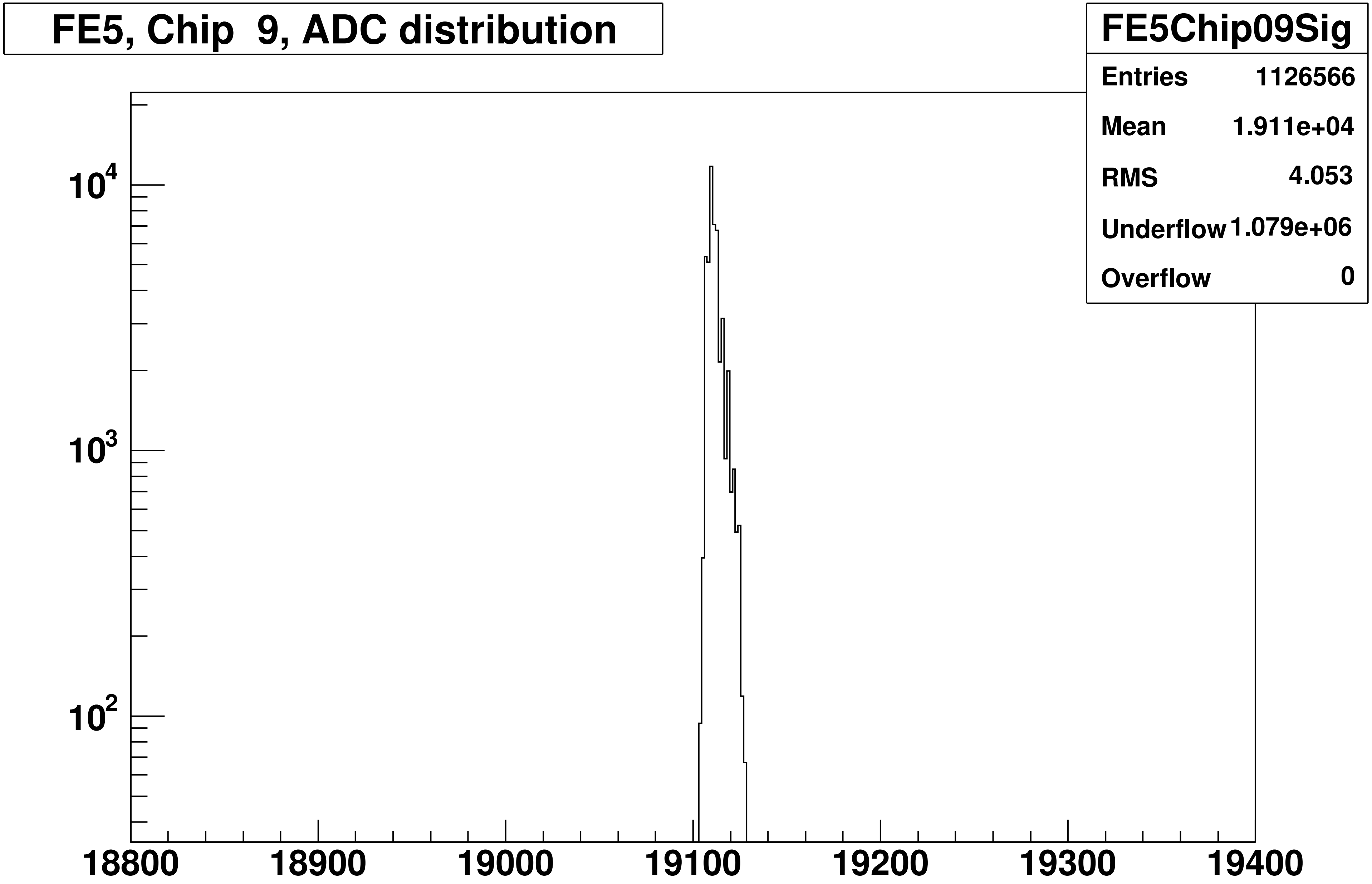
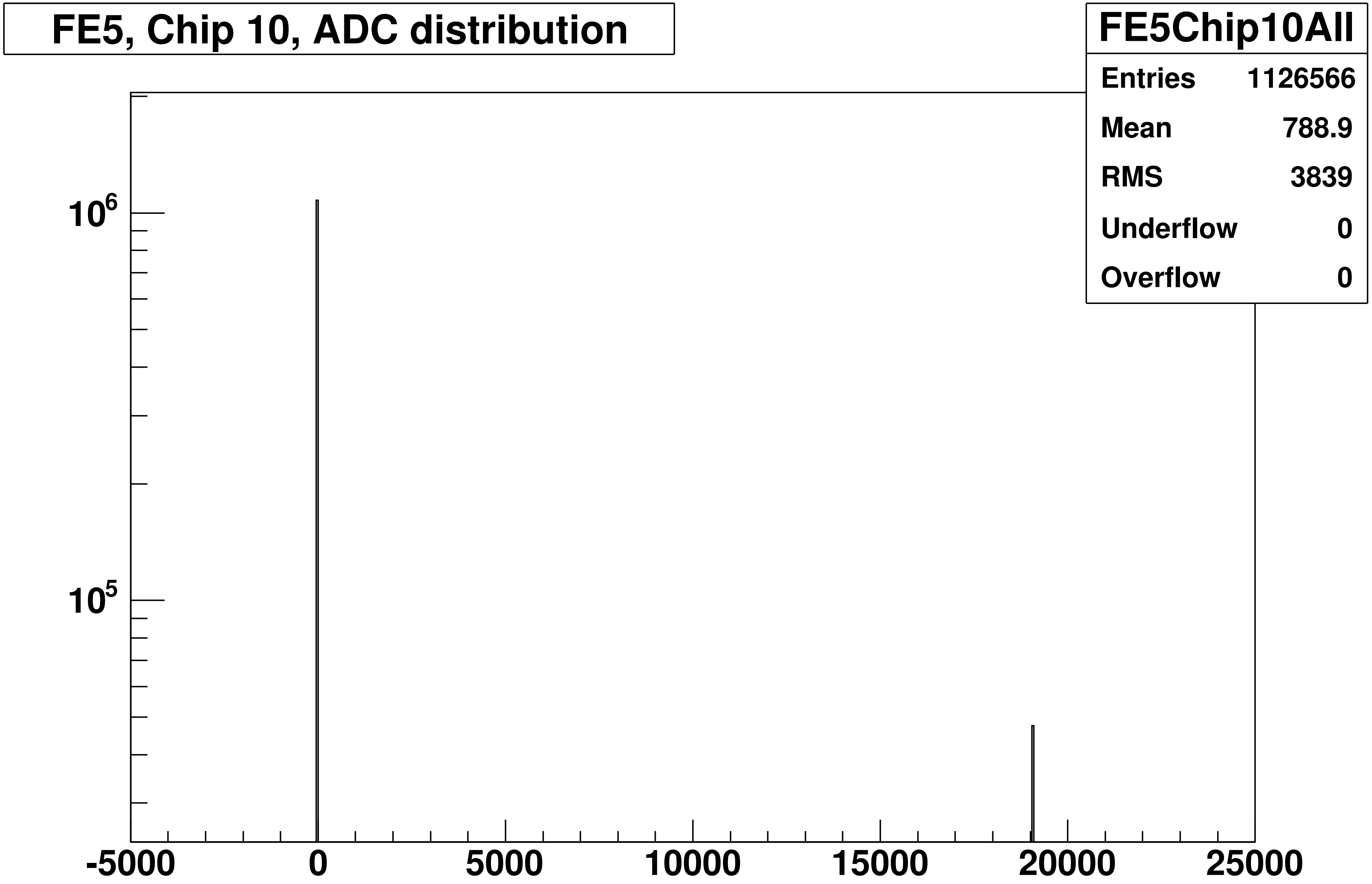
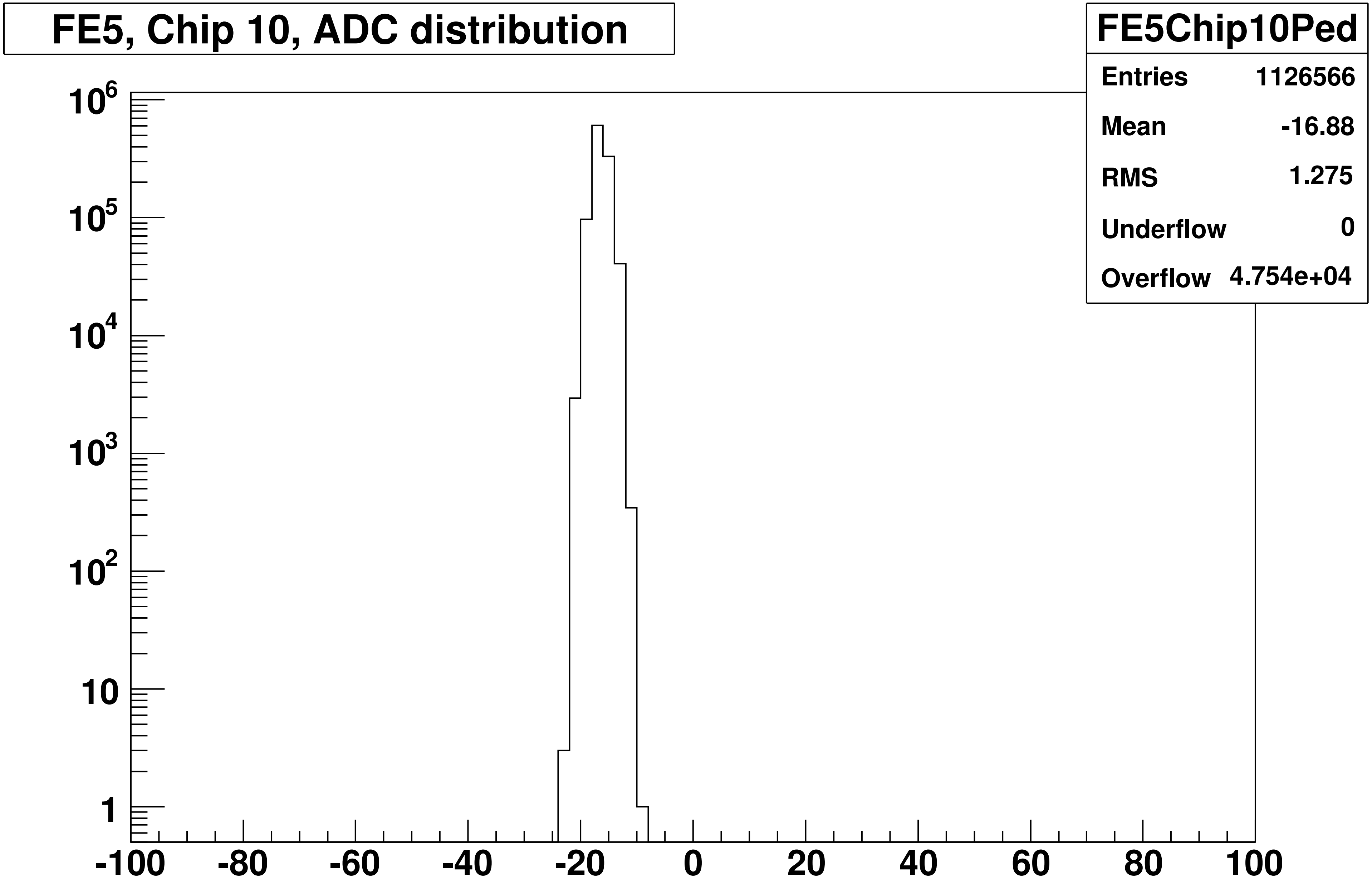
40
60
80
100


FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 1126566
Mean 1.911e+04
RMS 4.053
Underflow 1.079e+06
Overflow 0
10^4
10^3
10^2
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 1126566
Mean 788.9
RMS 3839
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 1126566
Mean -16.88
RMS 1.275
Underflow 0
Overflow 4.754e+04
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10^5
10^4
10^3
10^2
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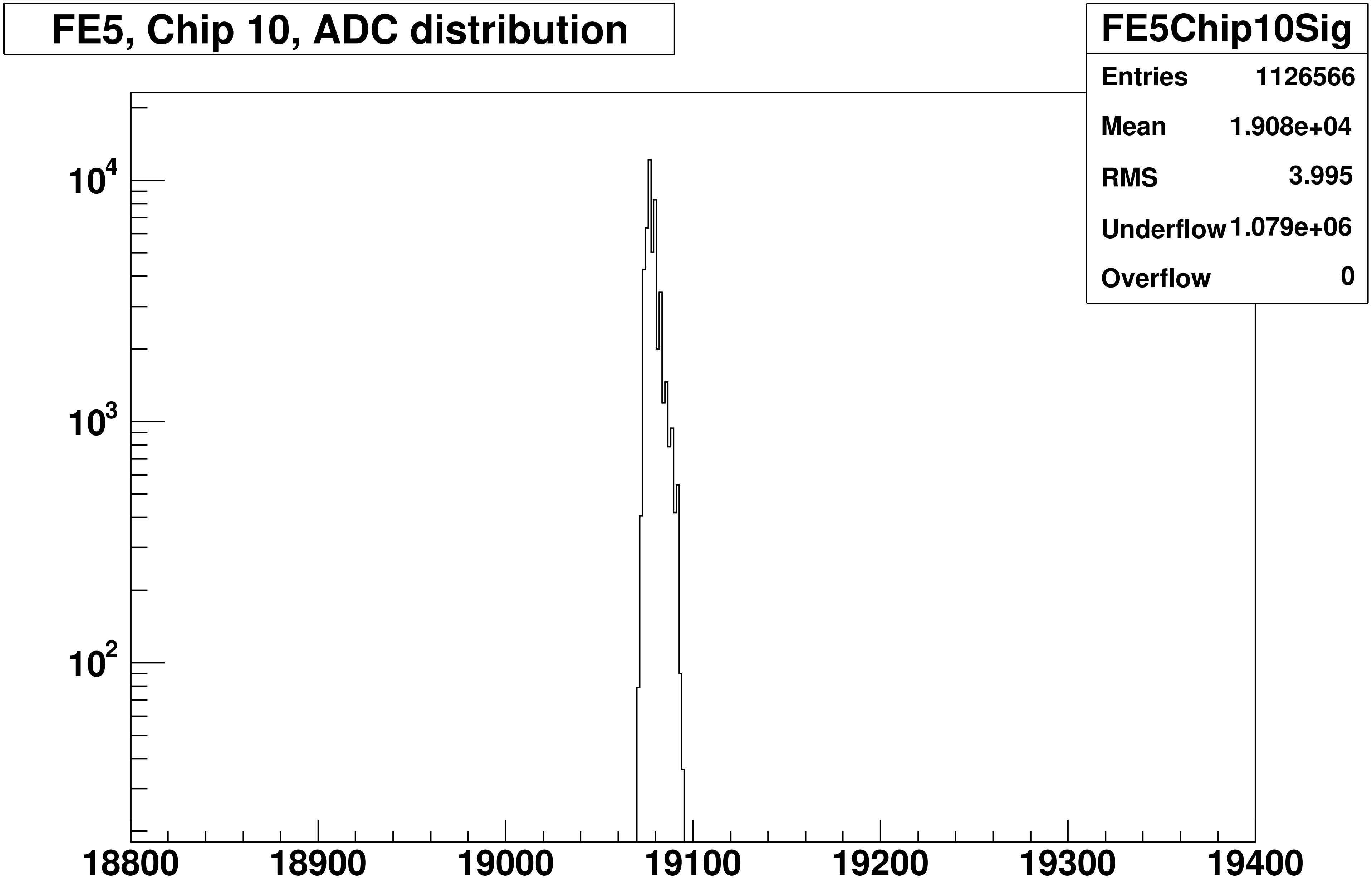
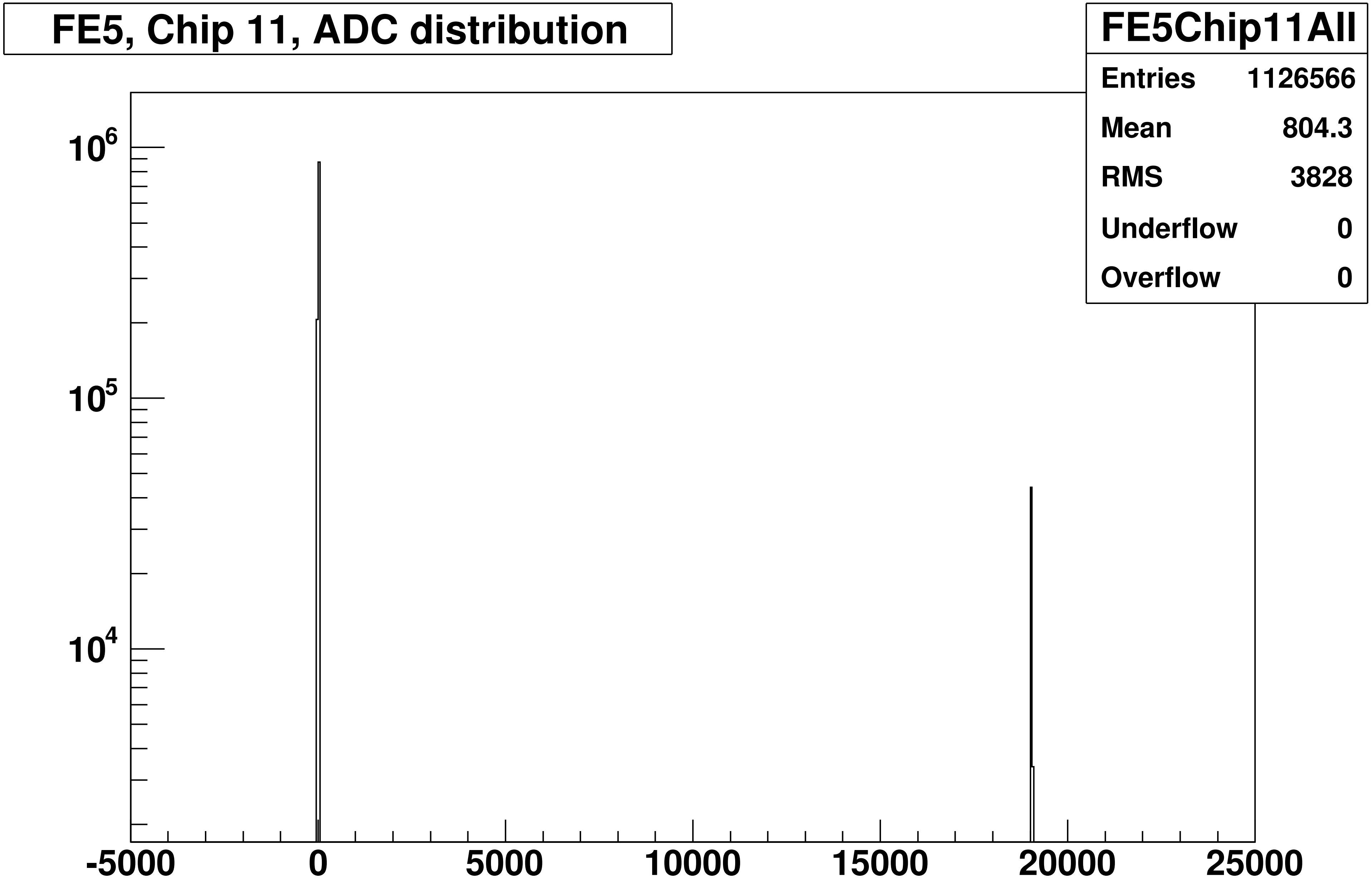
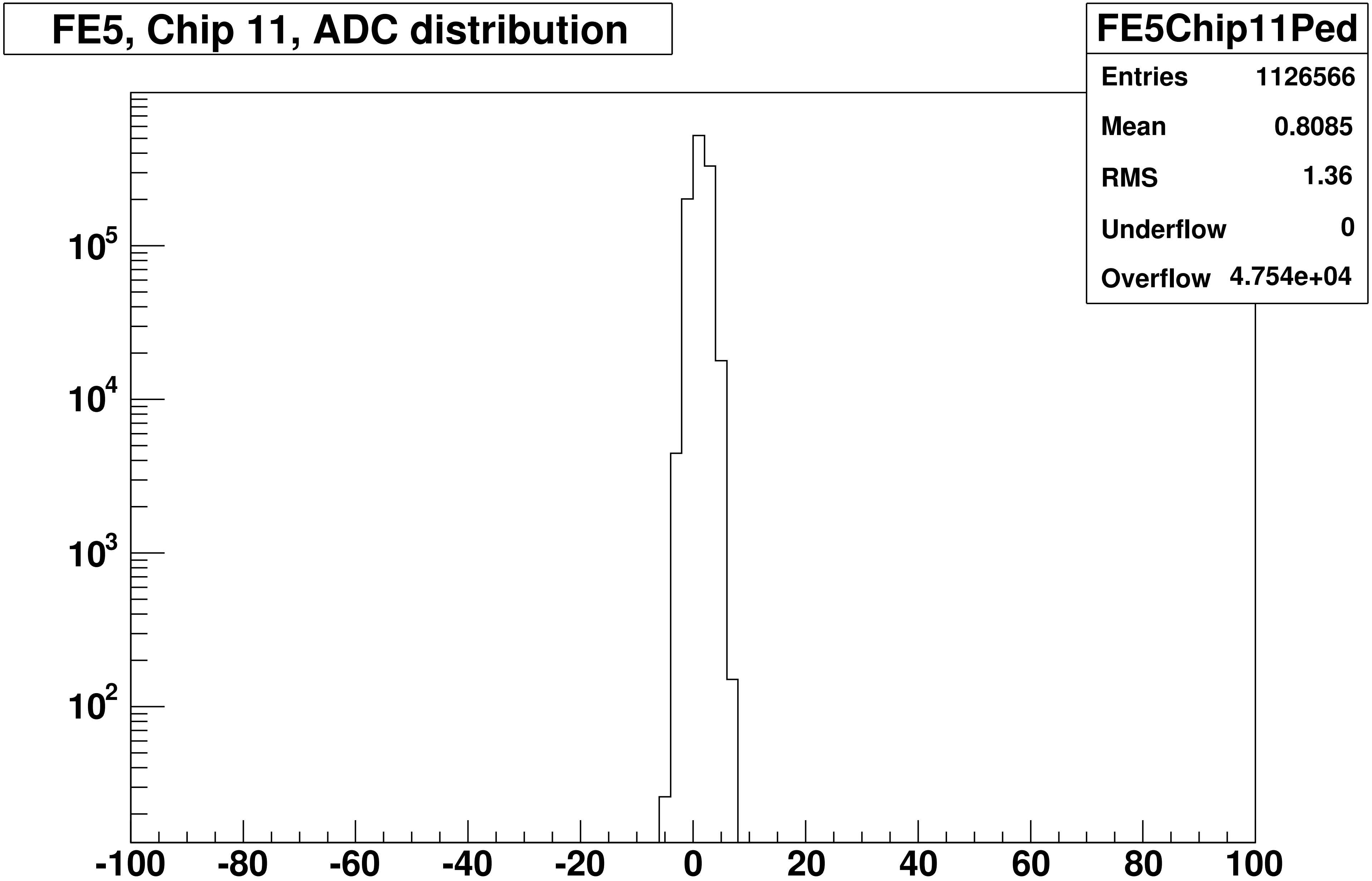

FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 1126566
Mean 1.908e+04
RMS 3.995
Underflow 1.079e+06
Overflow 0
10^4
10^3
10^2
18800
18900
19000
19100
19200
19300
19400


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 1126566
Mean 804.3
RMS 3828
Underflow 0
Overflow 0
10^6
10^5
10^4
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 1126566
Mean 0.8085
RMS 1.36
Underflow 0
Overflow 4.754e+04
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10^4
10^3
10^2
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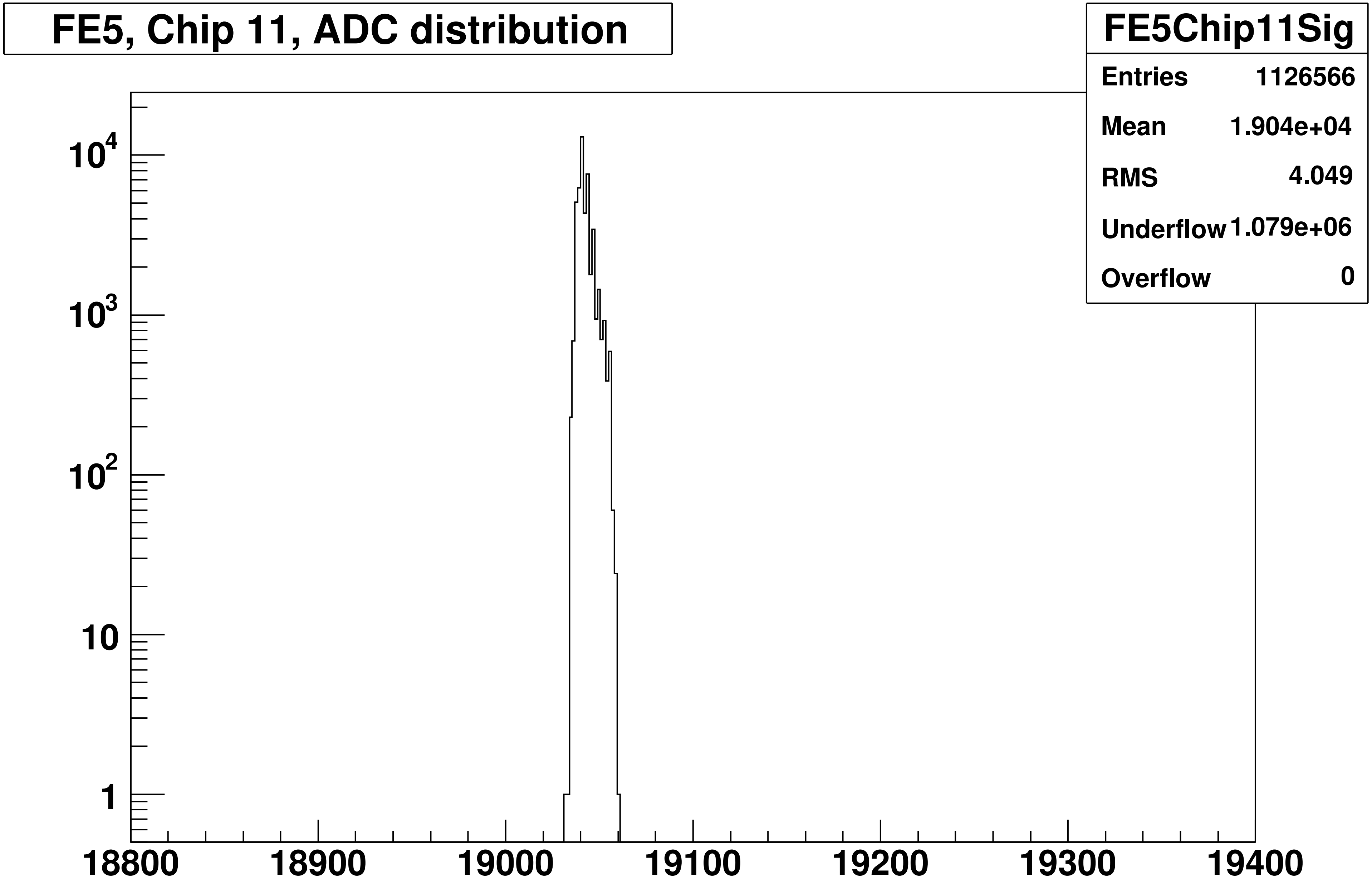
-40
-20
0
20
40
60
80
100


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 1126566
Mean 1.904e+04
RMS 4.049
Underflow 1.079e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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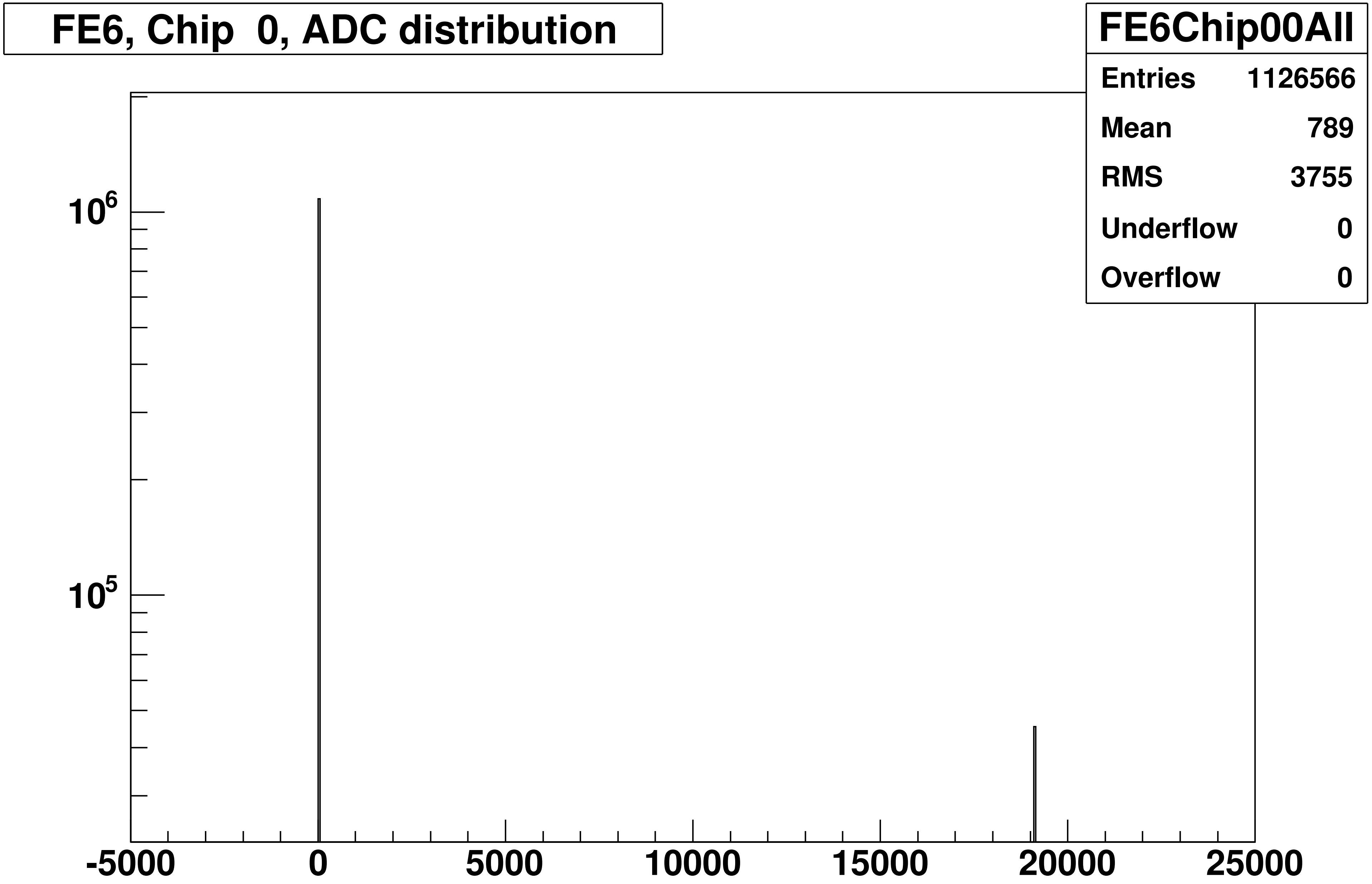
19400


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 1126566
Mean 789
RMS 3755
Underflow 0
Overflow 0
10^6
10^5
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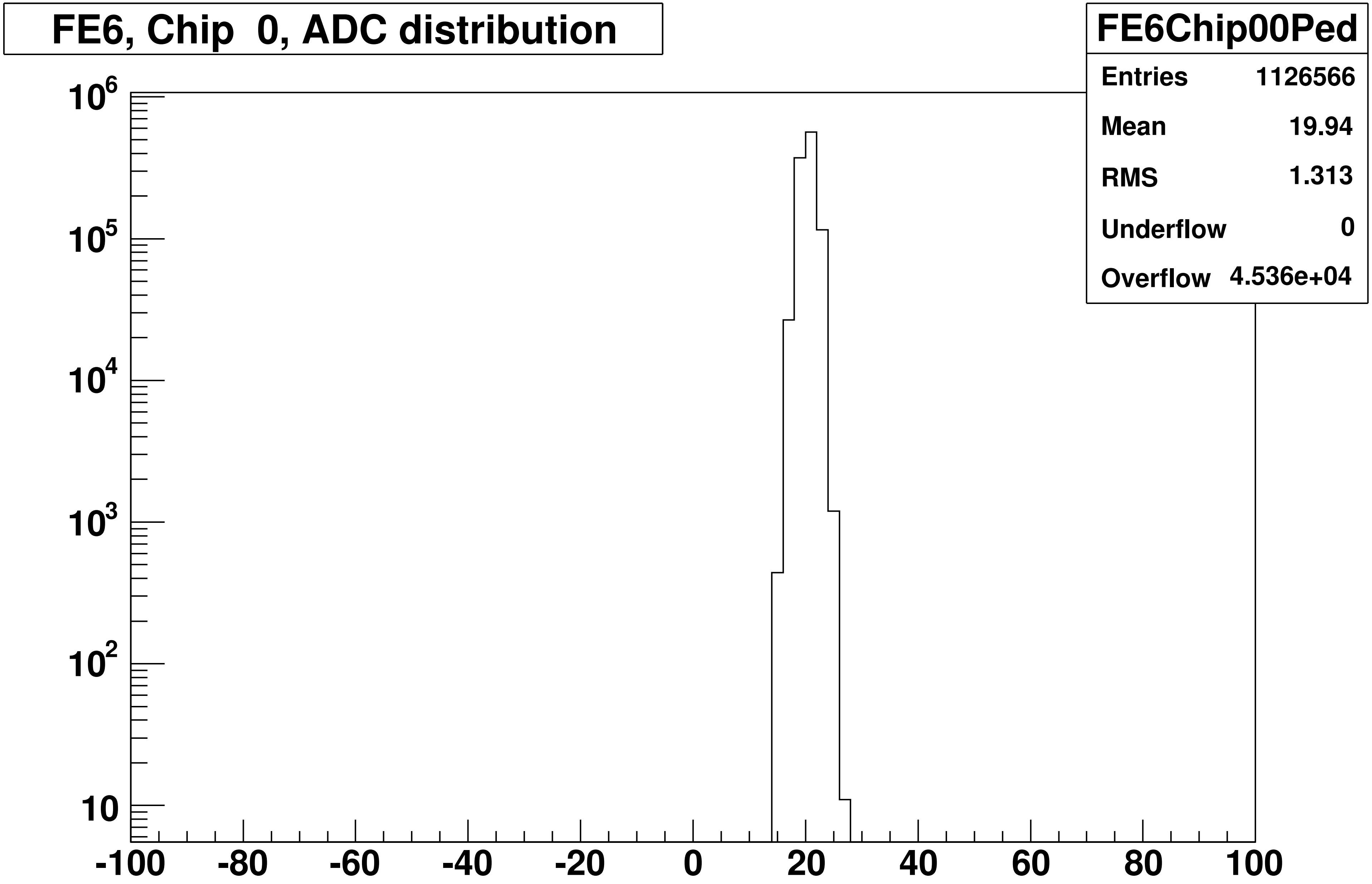
0
5000
10000
15000
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 1126566
Mean 19.94
RMS 1.313
Underflow 0
Overflow 4.536e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
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-20
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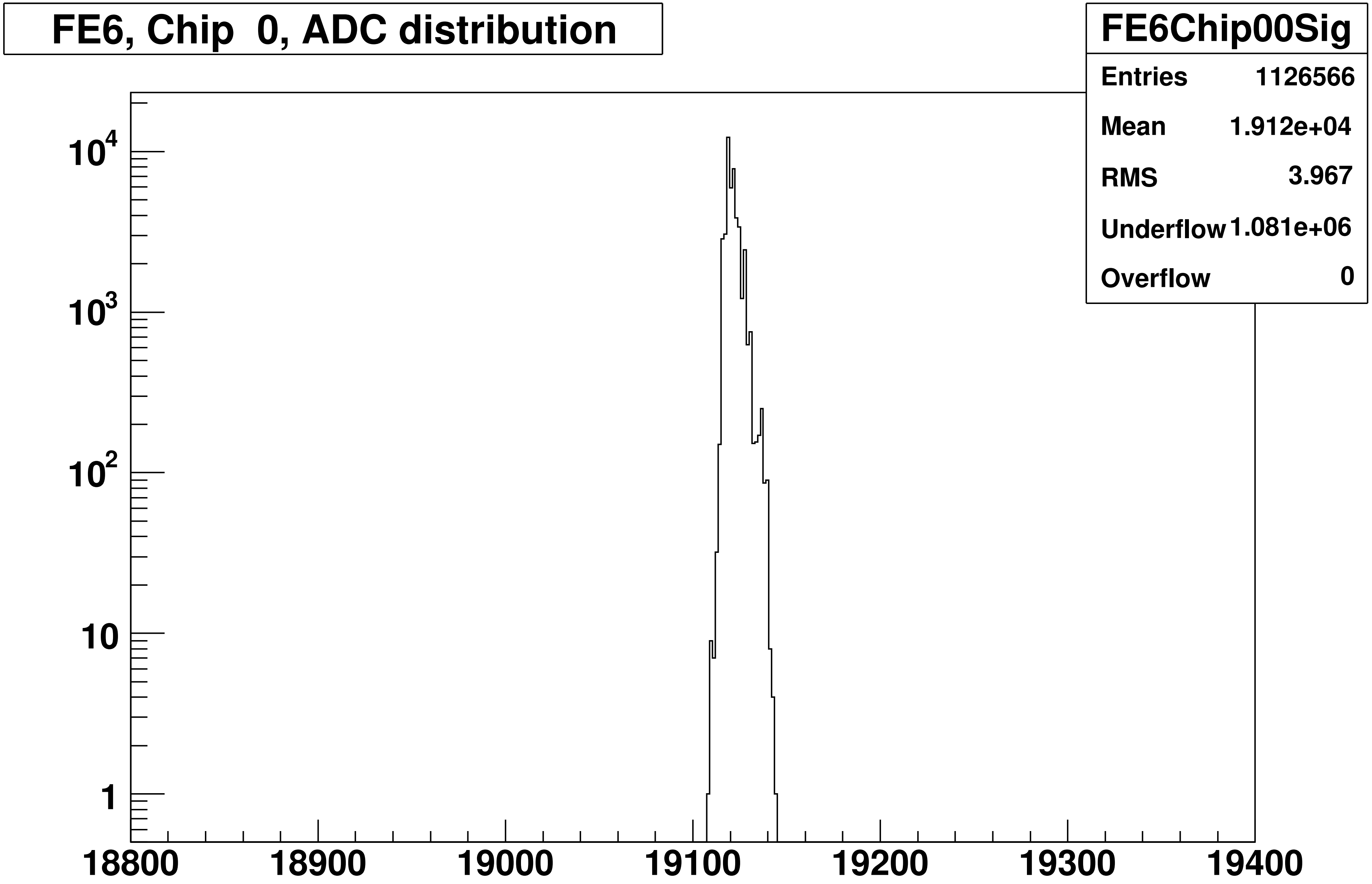
80
100


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 1126566
Mean 1.912e+04
RMS 3.967
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
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19000
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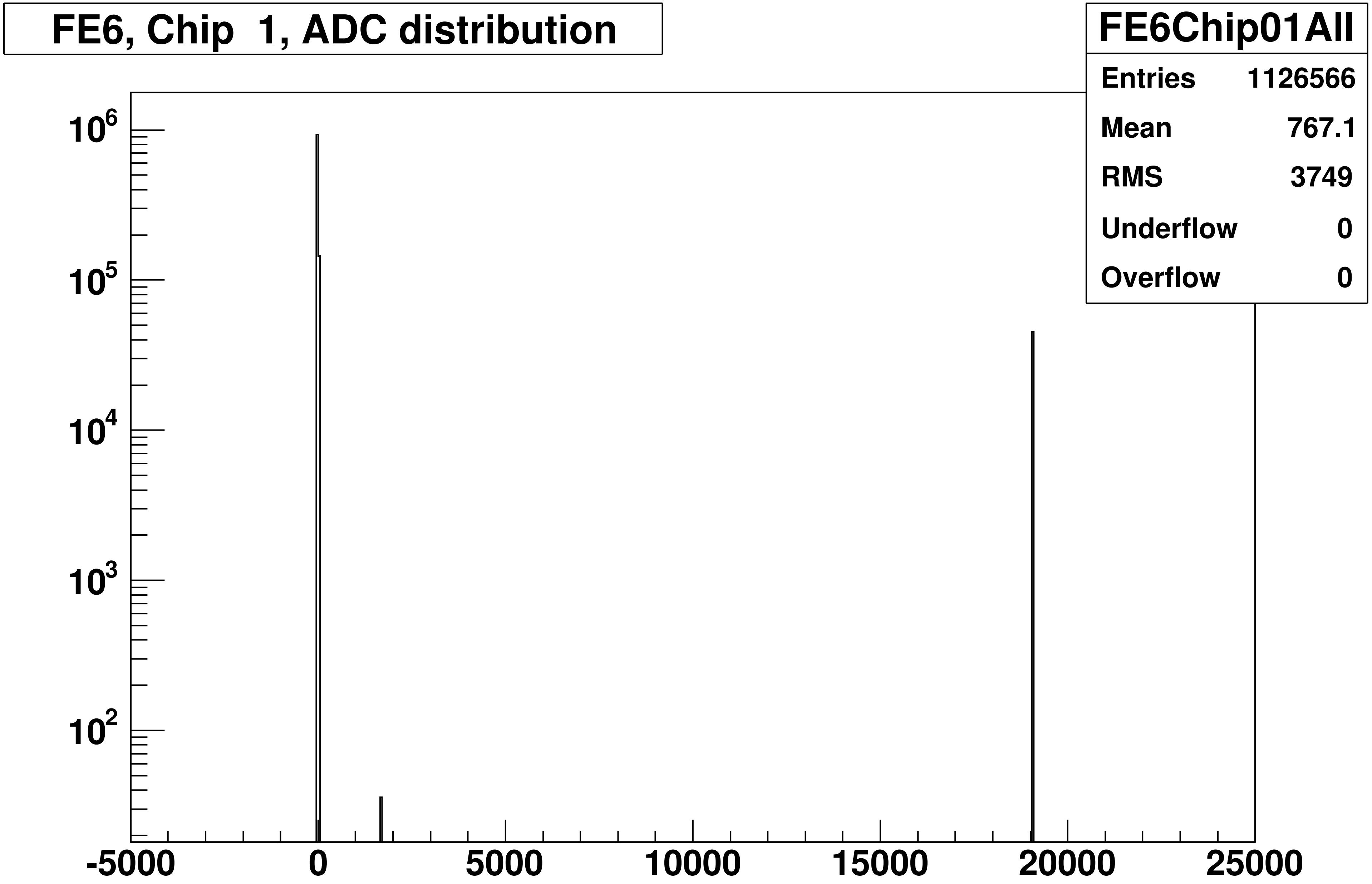
19400


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 1126566
Mean 767.1
RMS 3749
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
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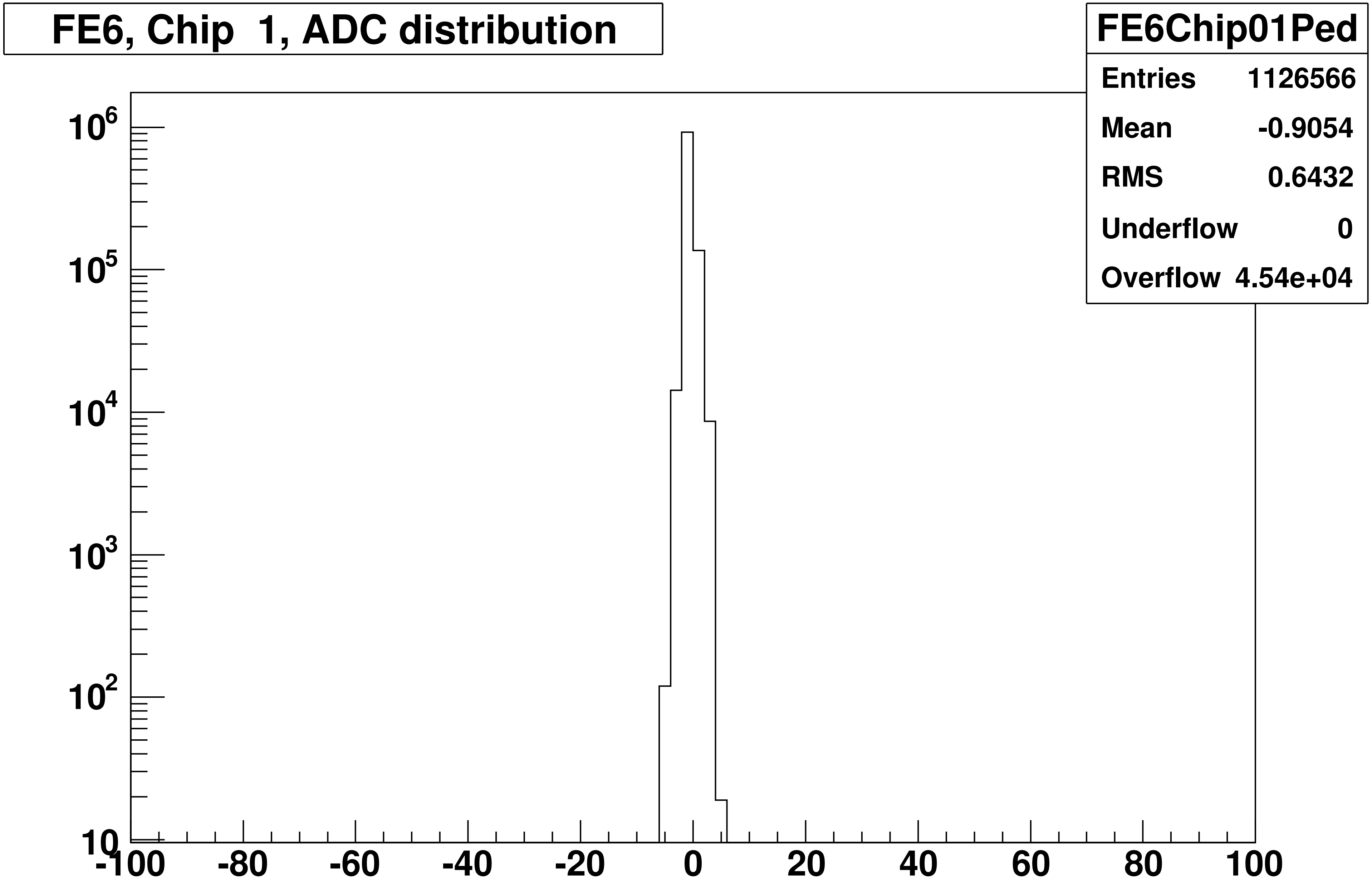
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 1126566
Mean -0.9054
RMS 0.6432
Underflow 0
Overflow 4.54e+04
10
10^2
10^3
10^4
10^5
10^6
-100
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-40
-20
0
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60
80
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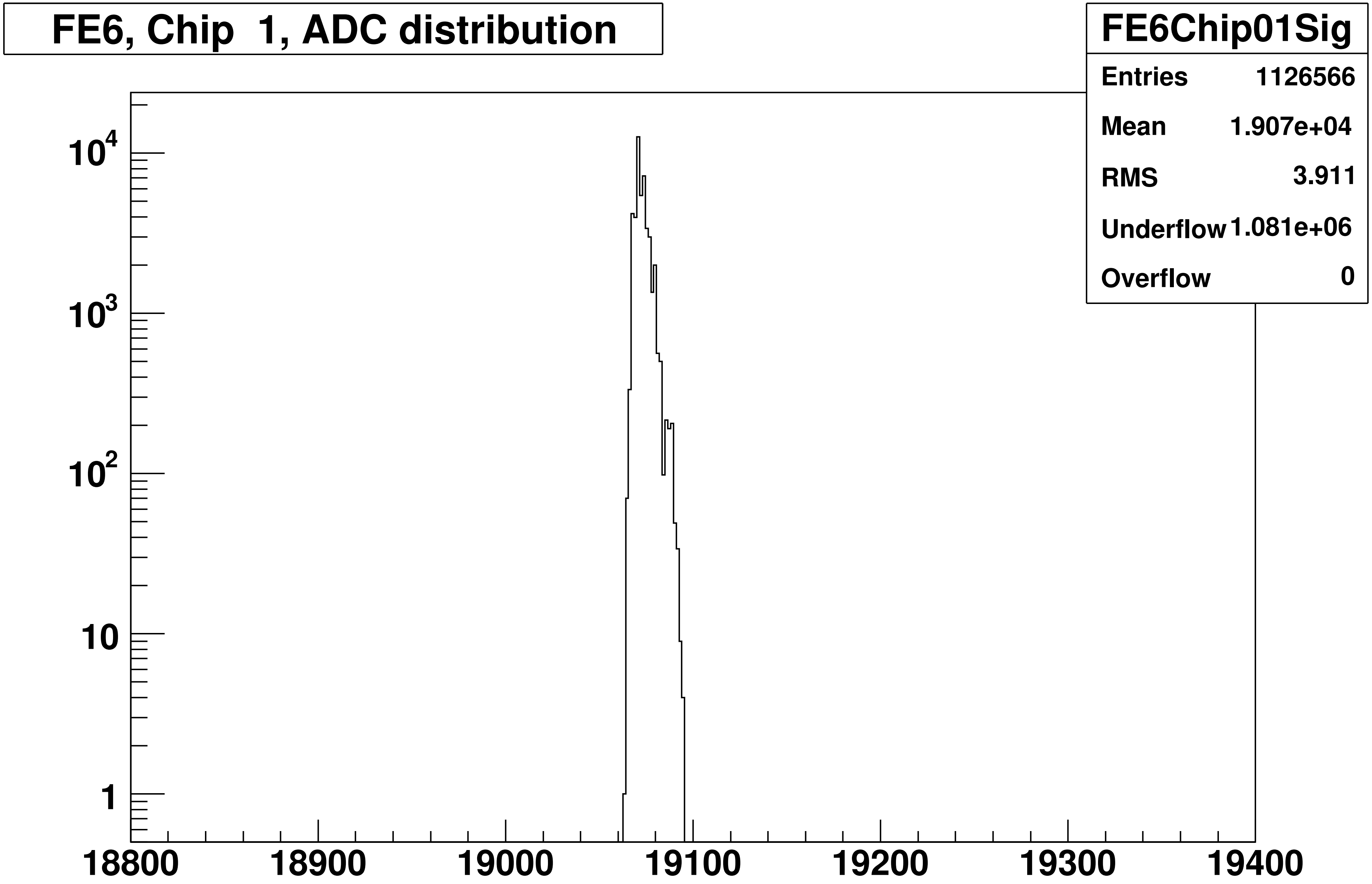
FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 1126566
Mean 1.907e+04
RMS 3.911
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
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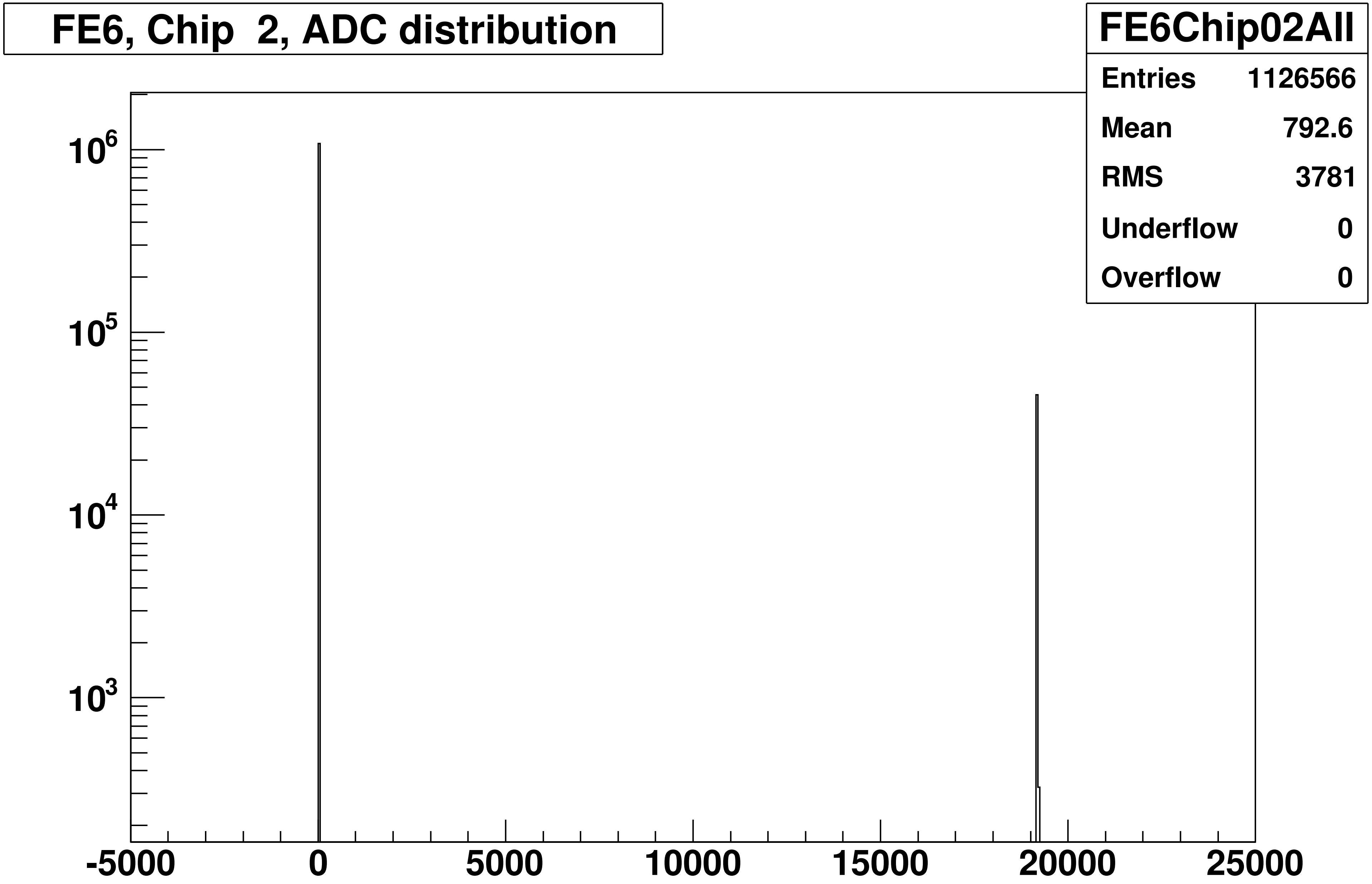
19400


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 1126566
Mean 792.6
RMS 3781
Underflow 0
Overflow 0
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10^5
10^4
10^3
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0
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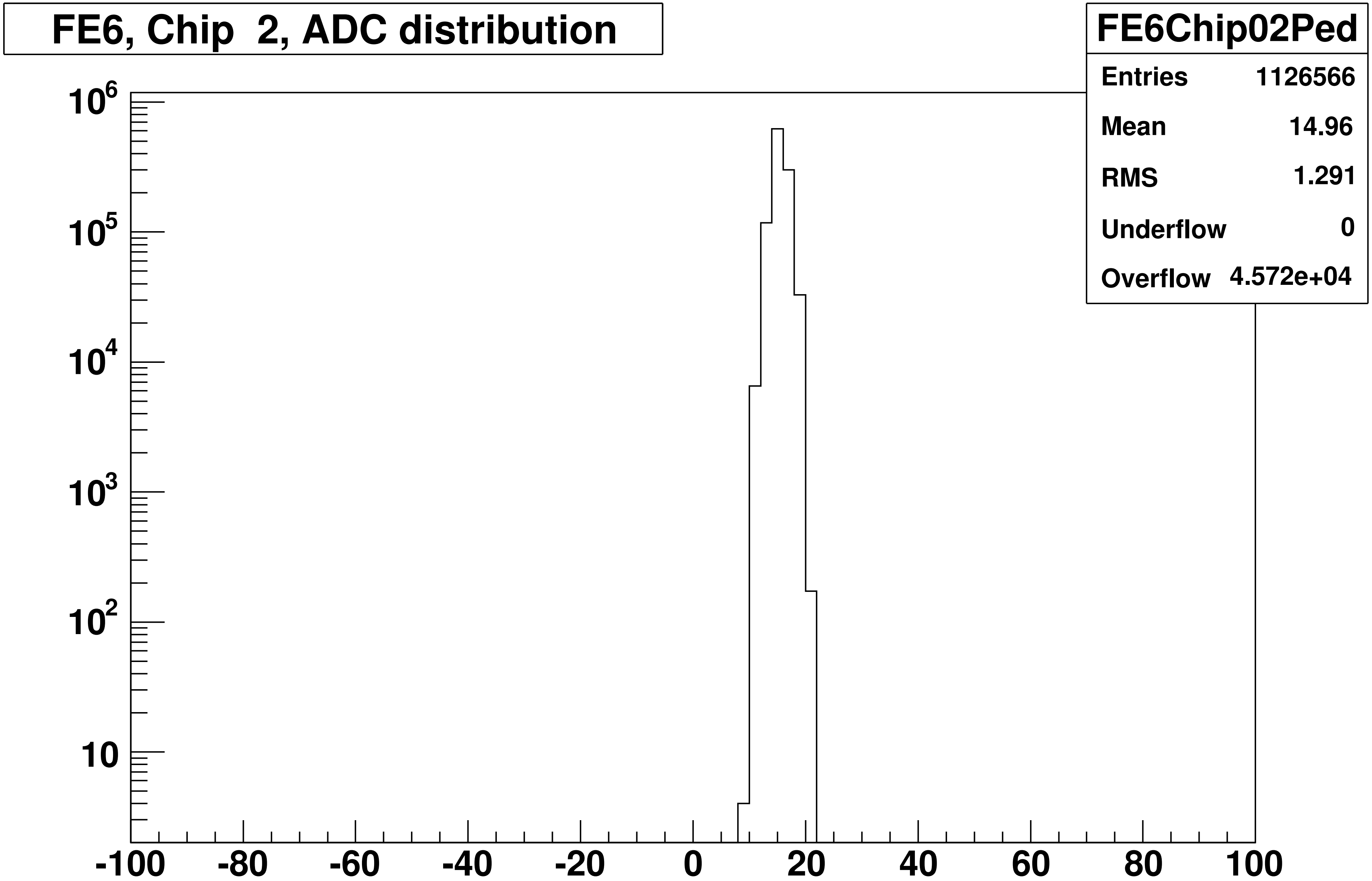
10000
15000
20000
25000



FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 1126566
Mean 14.96
RMS 1.291
Underflow 0
Overflow 4.572e+04
10
10^2
10^3
10^4
10^5
10^6
-100
-80
-60
-40
-20
0
20
40
60
80
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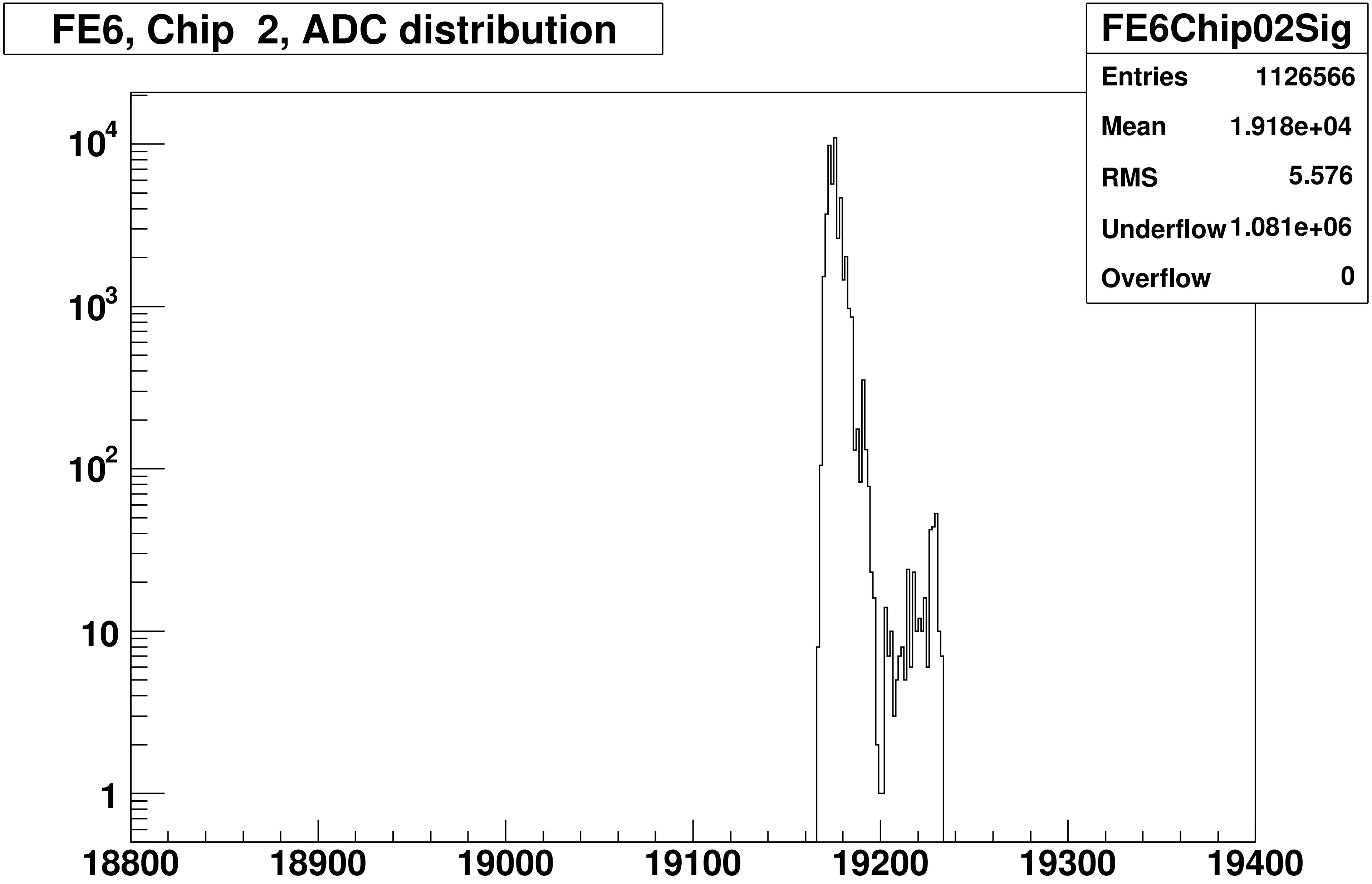
FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 1126566
Mean 1.918e+04
RMS 5.576
Underflow 1.081e+06
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
19100
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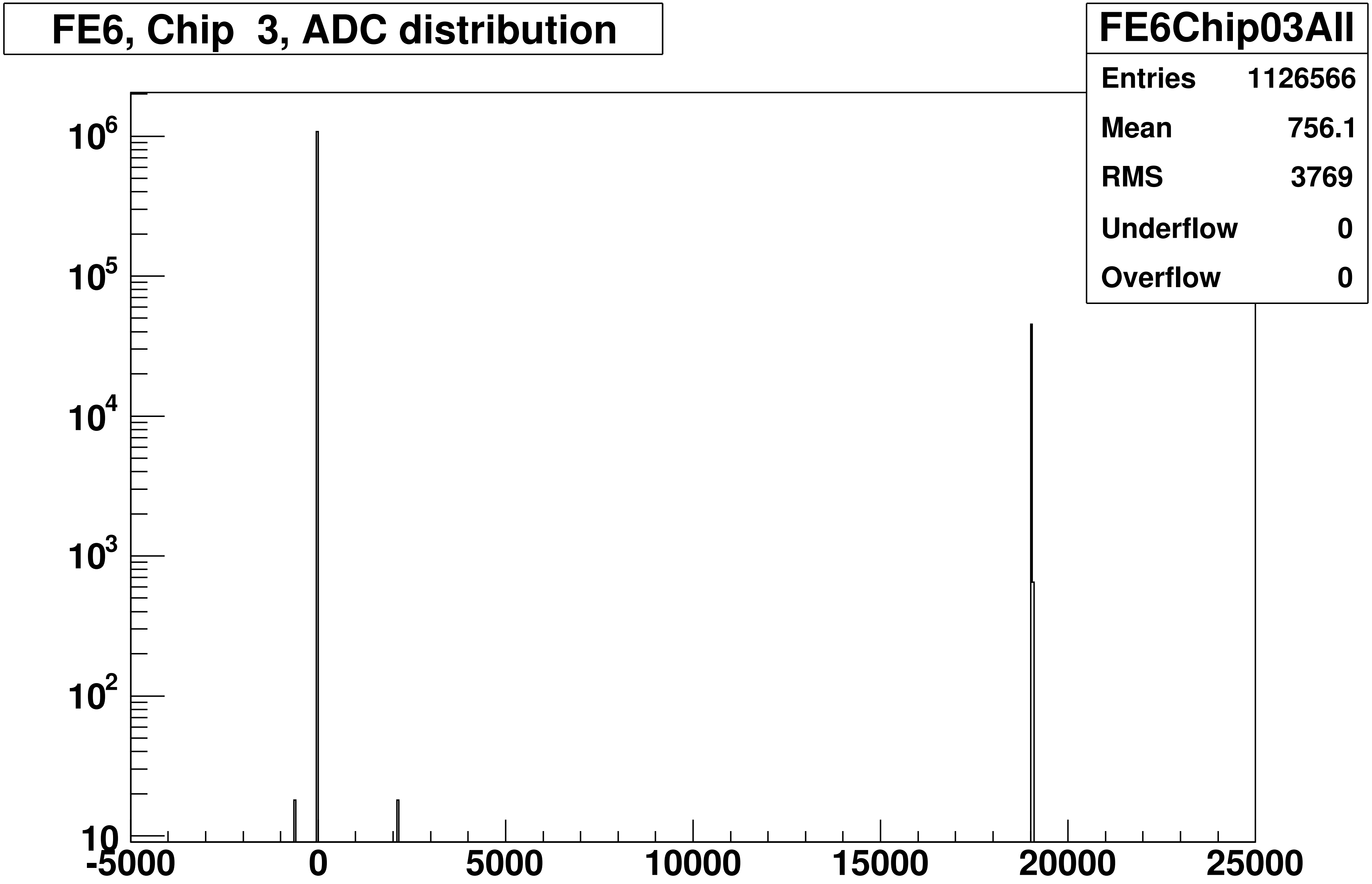
19400



FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 1126566
Mean 756.1
RMS 3769
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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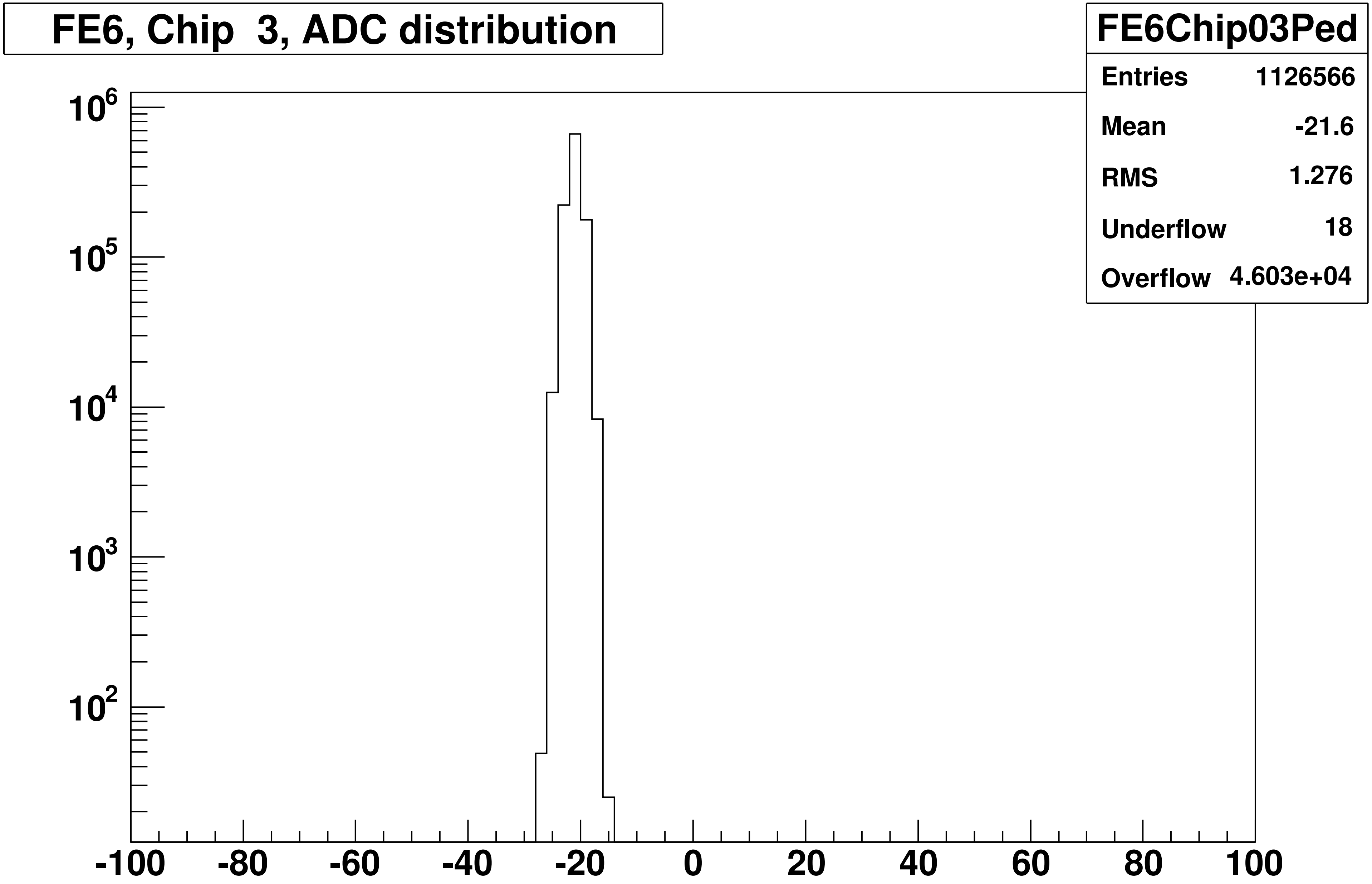
10000
15000
20000
25000


FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 1126566
Mean -21.6
RMS 1.276
Underflow 18
Overflow 4.603e+04
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10^5
10^4
10^3
10^2
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-20
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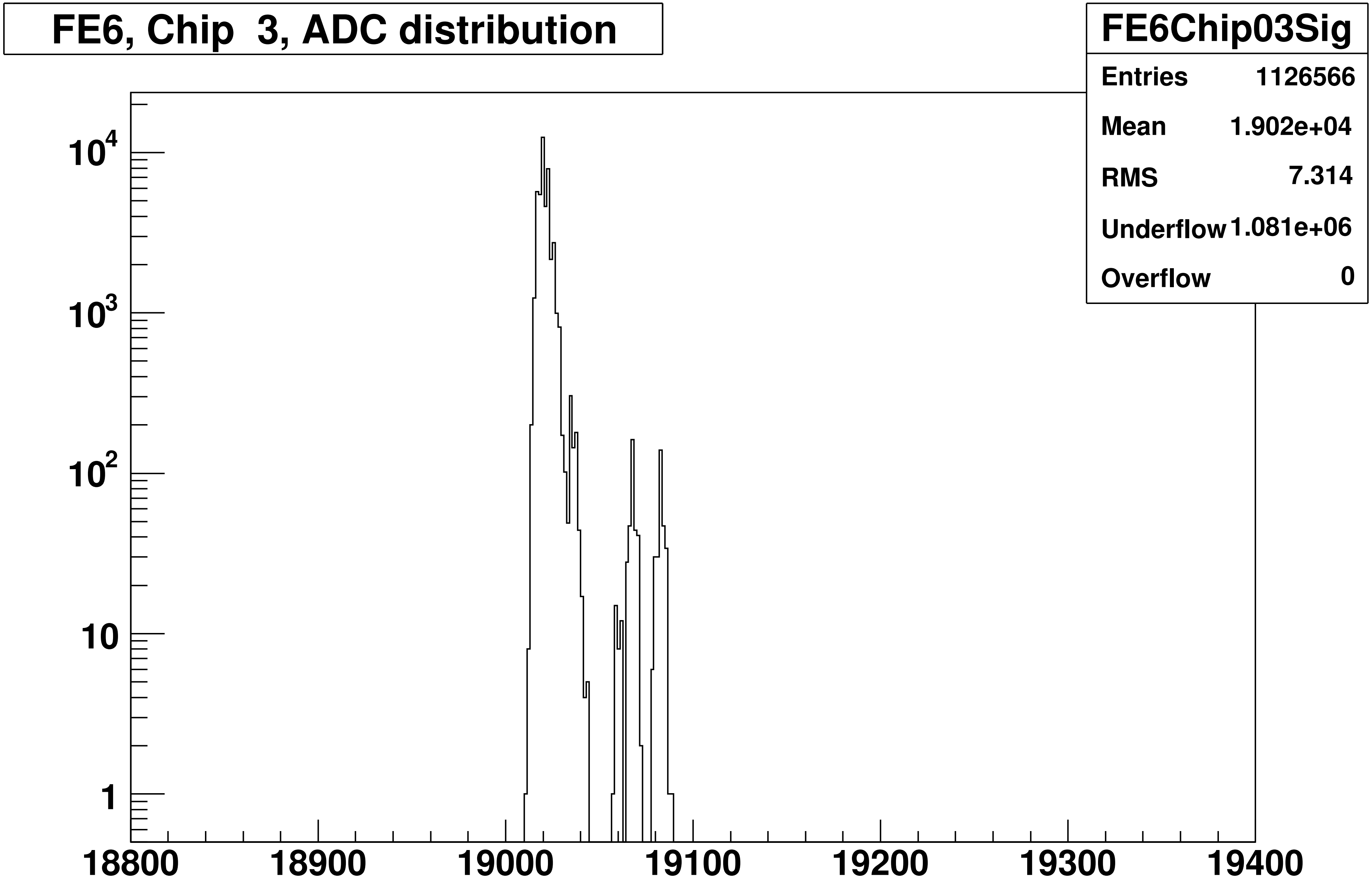
80
100


FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 1126566
Mean 1.902e+04
RMS 7.314
Underflow 1.081e+06
Overflow 0
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10^3
10^2
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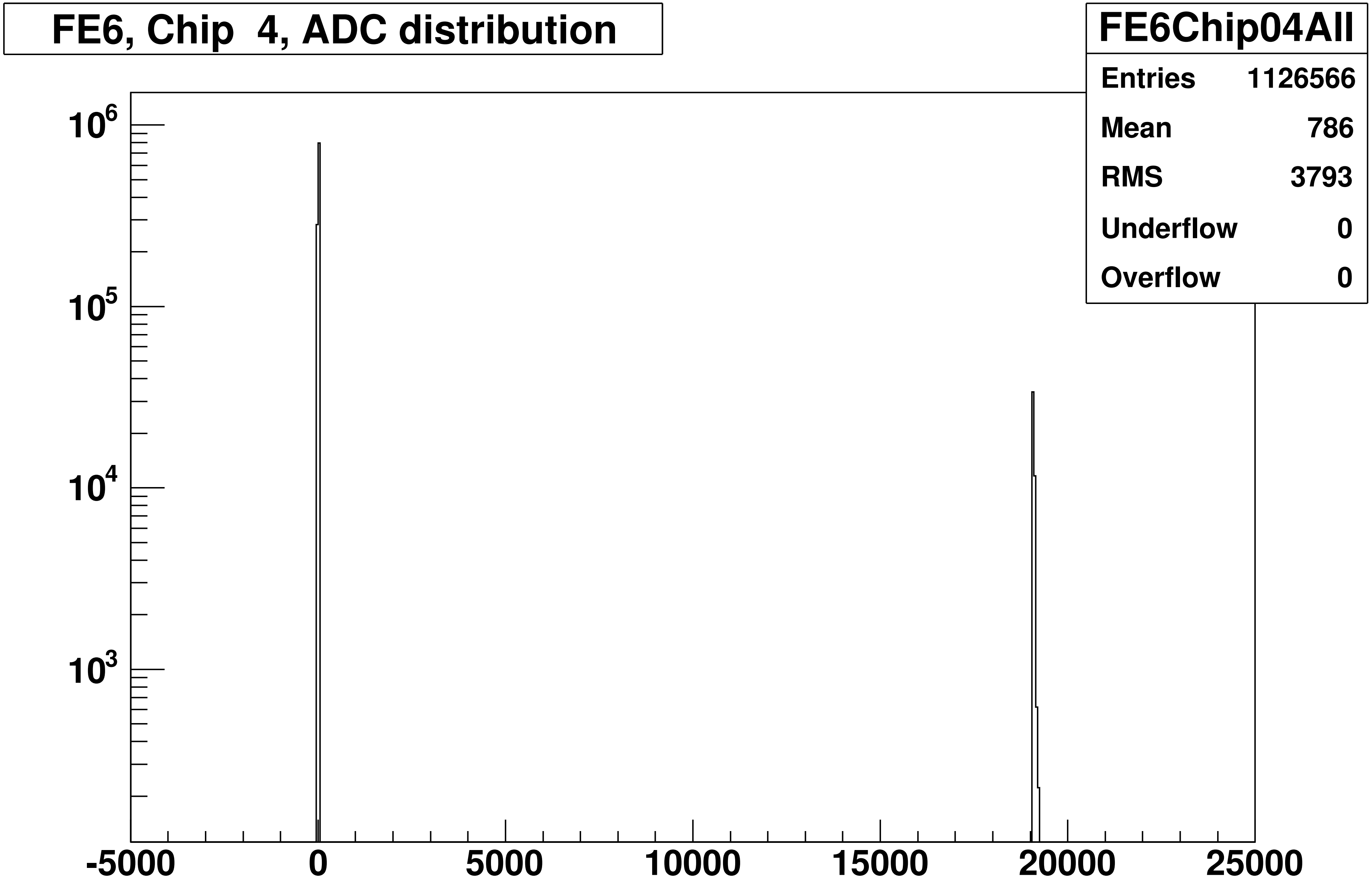
FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 1126566
Mean 786
RMS 3793
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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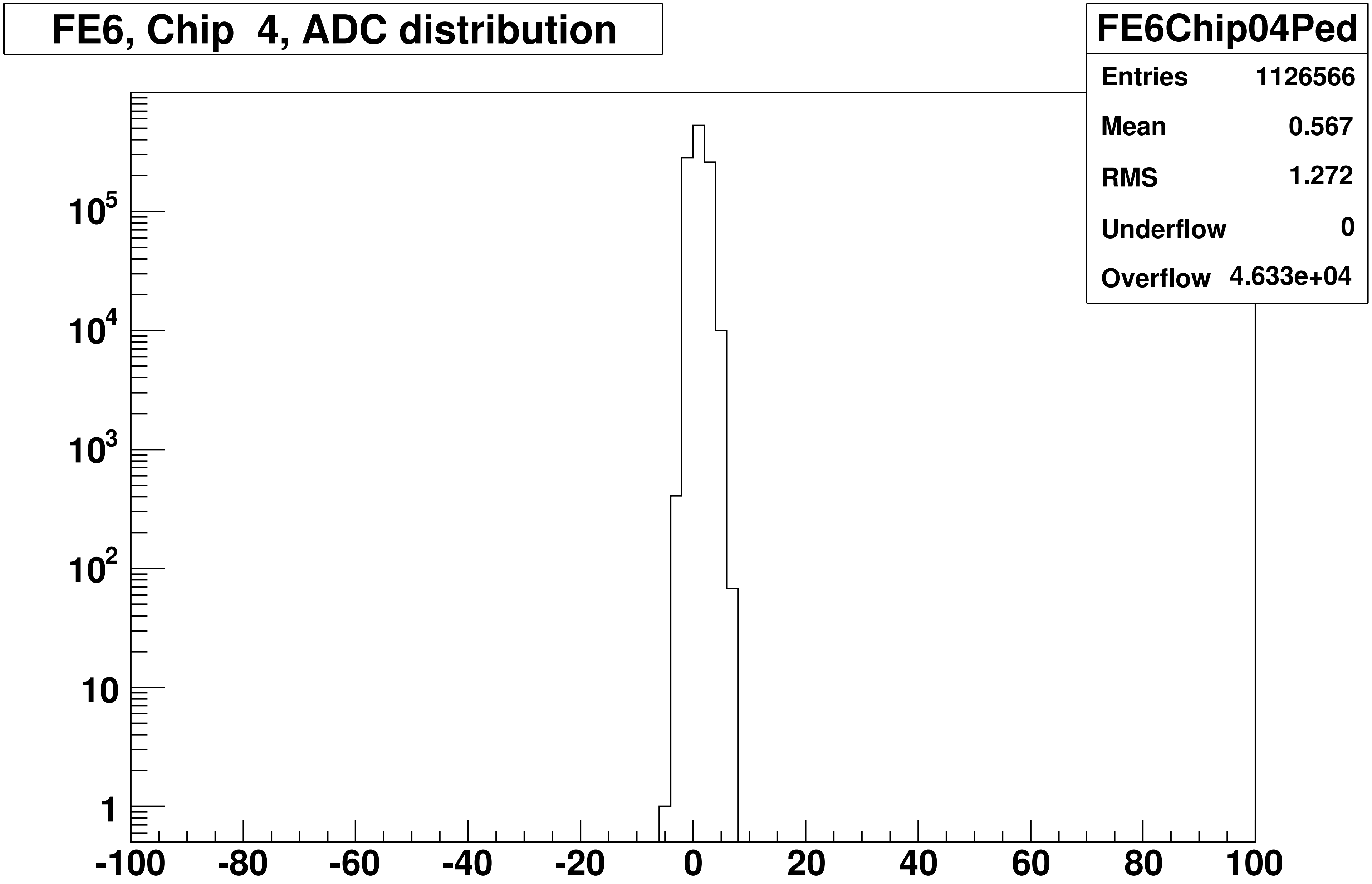
0
5000
10000
15000
20000
25000


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 1126566
Mean 0.567
RMS 1.272
Underflow 0
Overflow 4.633e+04
10^5
10^4
10^3
10^2
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1
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-20
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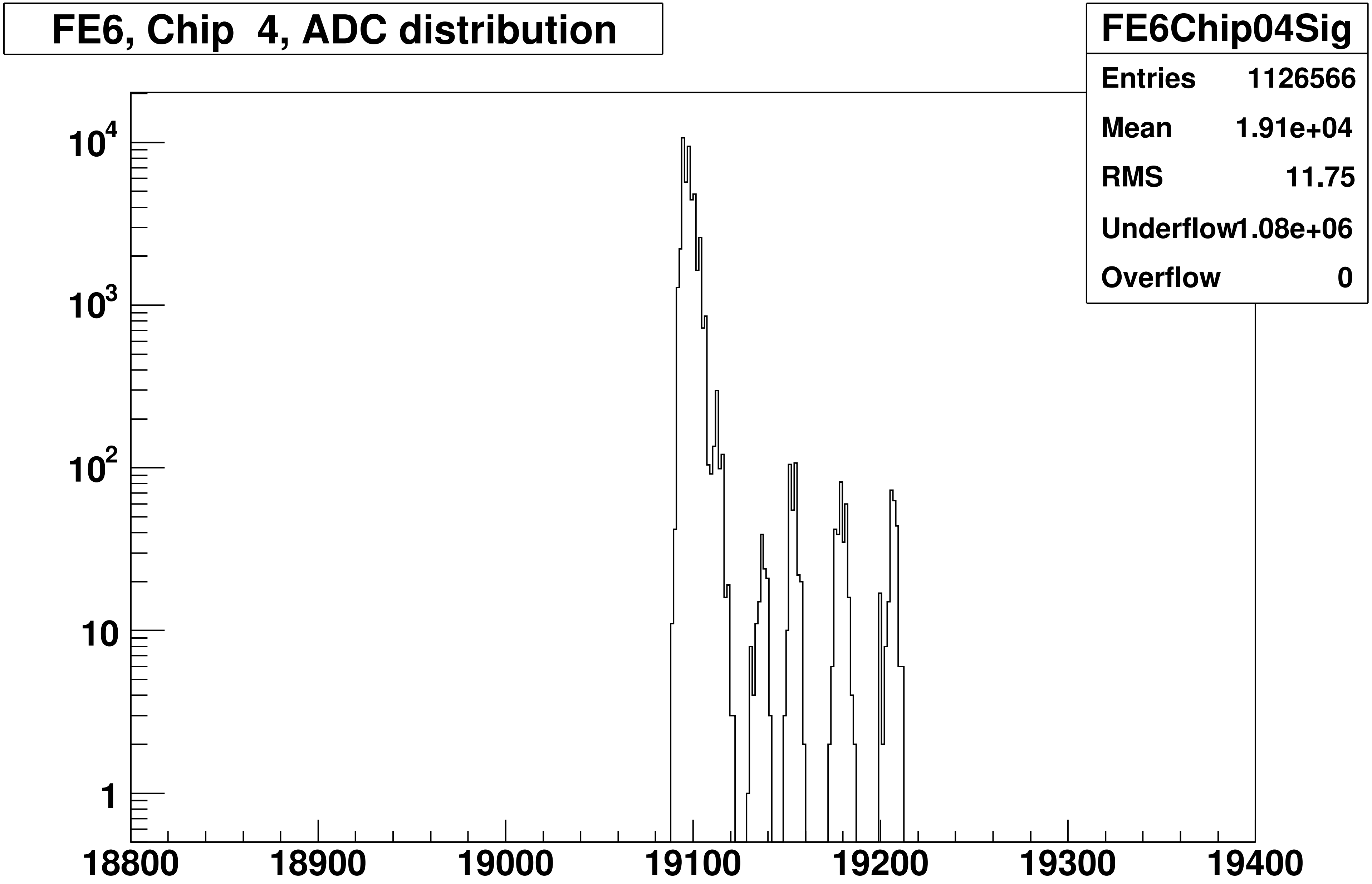

FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 1126566
Mean 1.91e+04
RMS 11.75
Underflow 1.08e+06
Overflow 0
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10^3
10^2
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1
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18900
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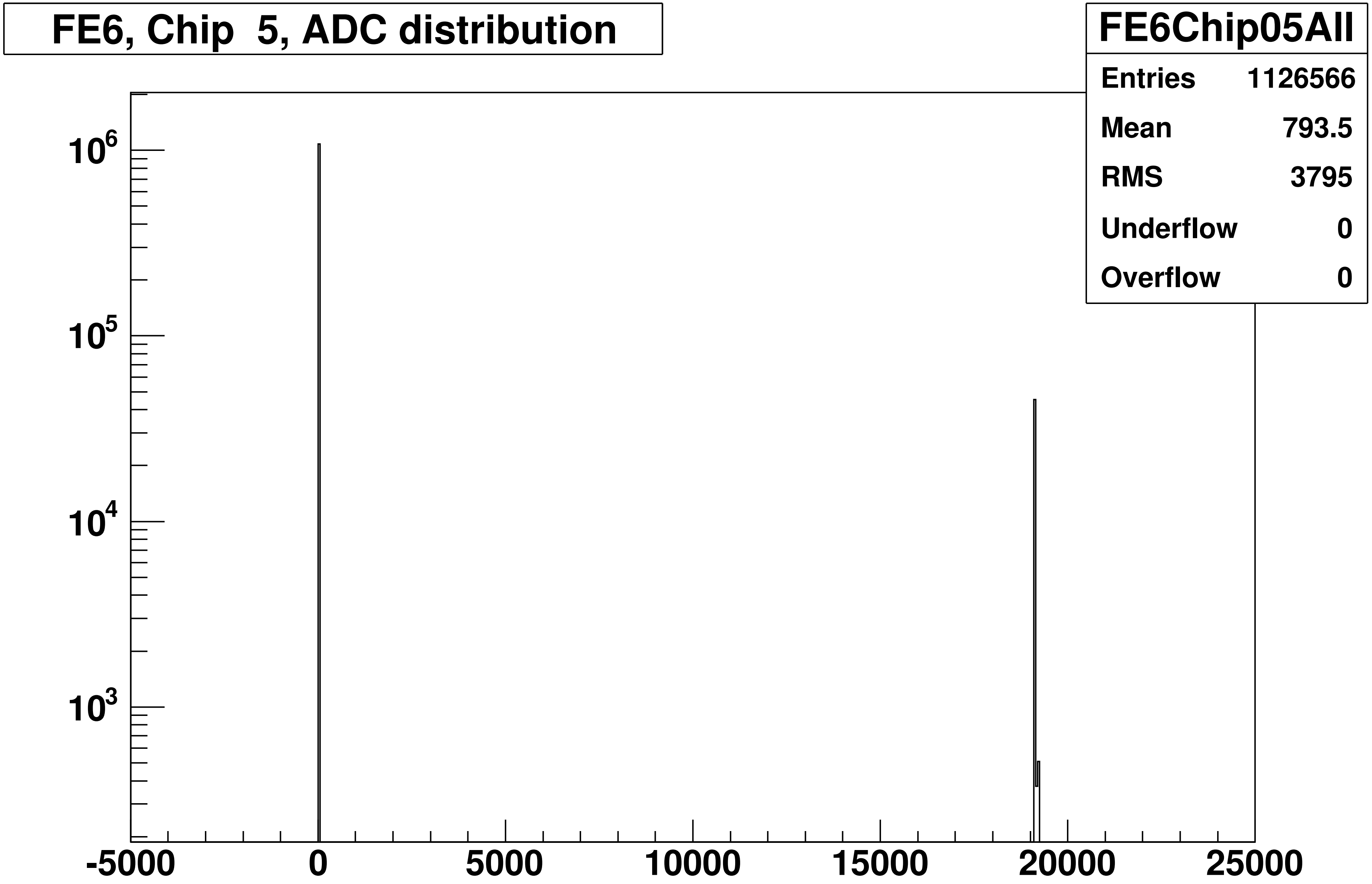
FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 1126566
Mean 793.5
RMS 3795
Underflow 0
Overflow 0
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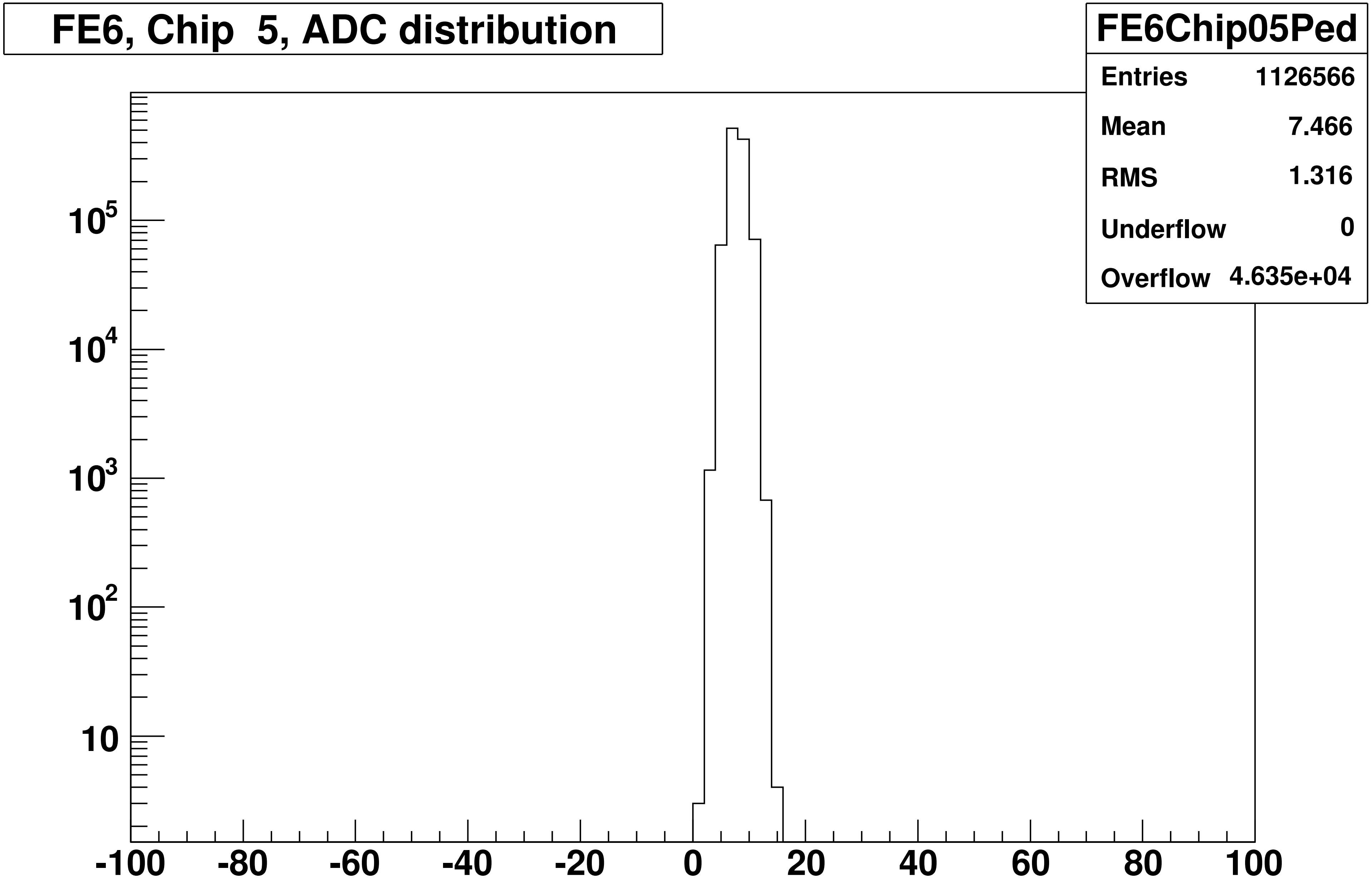
20000
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 1126566
Mean 7.466
RMS 1.316
Underflow 0
Overflow 4.635e+04
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10^3
10^2
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60
80
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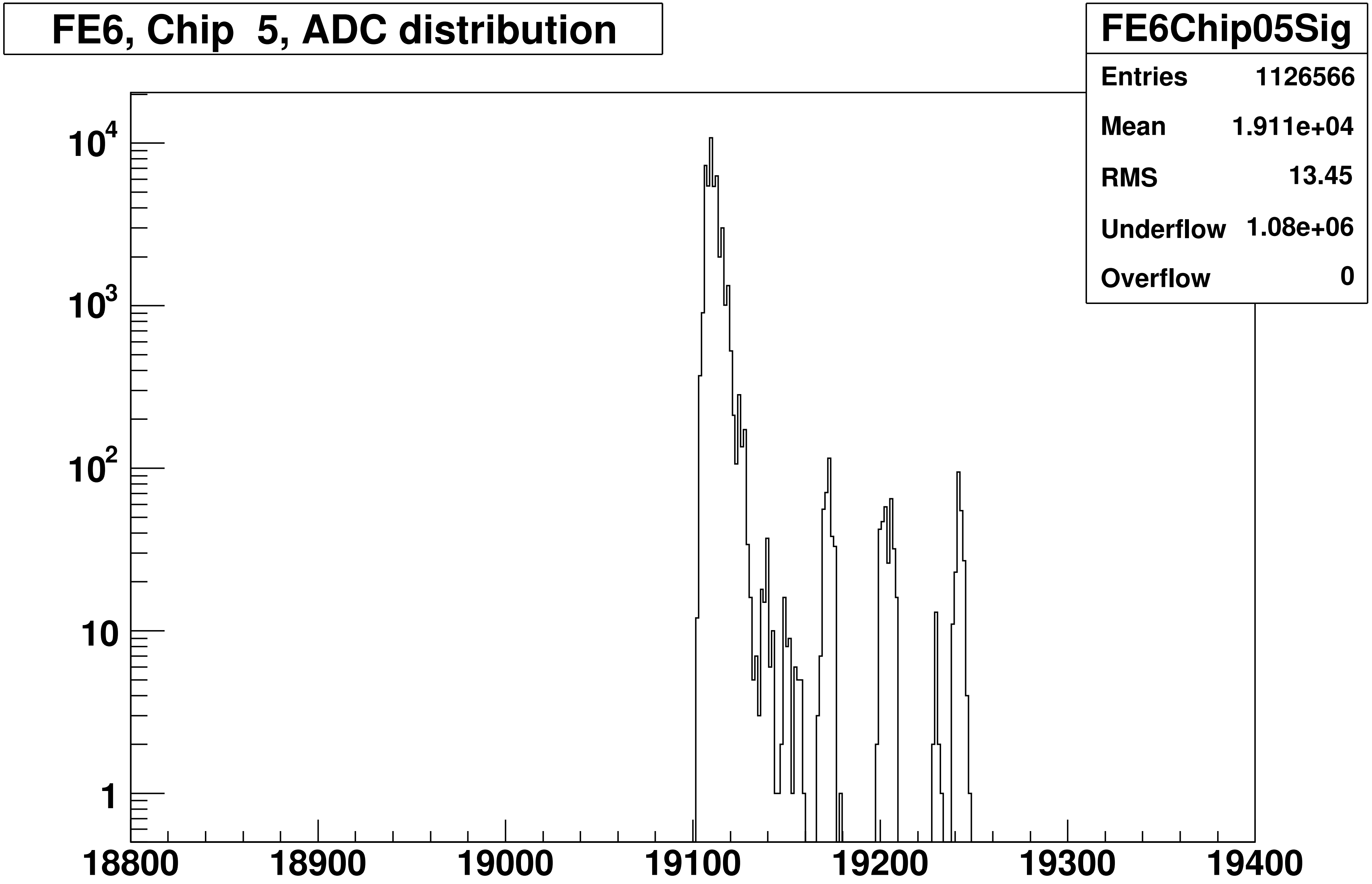
FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 1126566
Mean 1.911e+04
RMS 13.45
Underflow 1.08e+06
Overflow 0
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10^3
10^2
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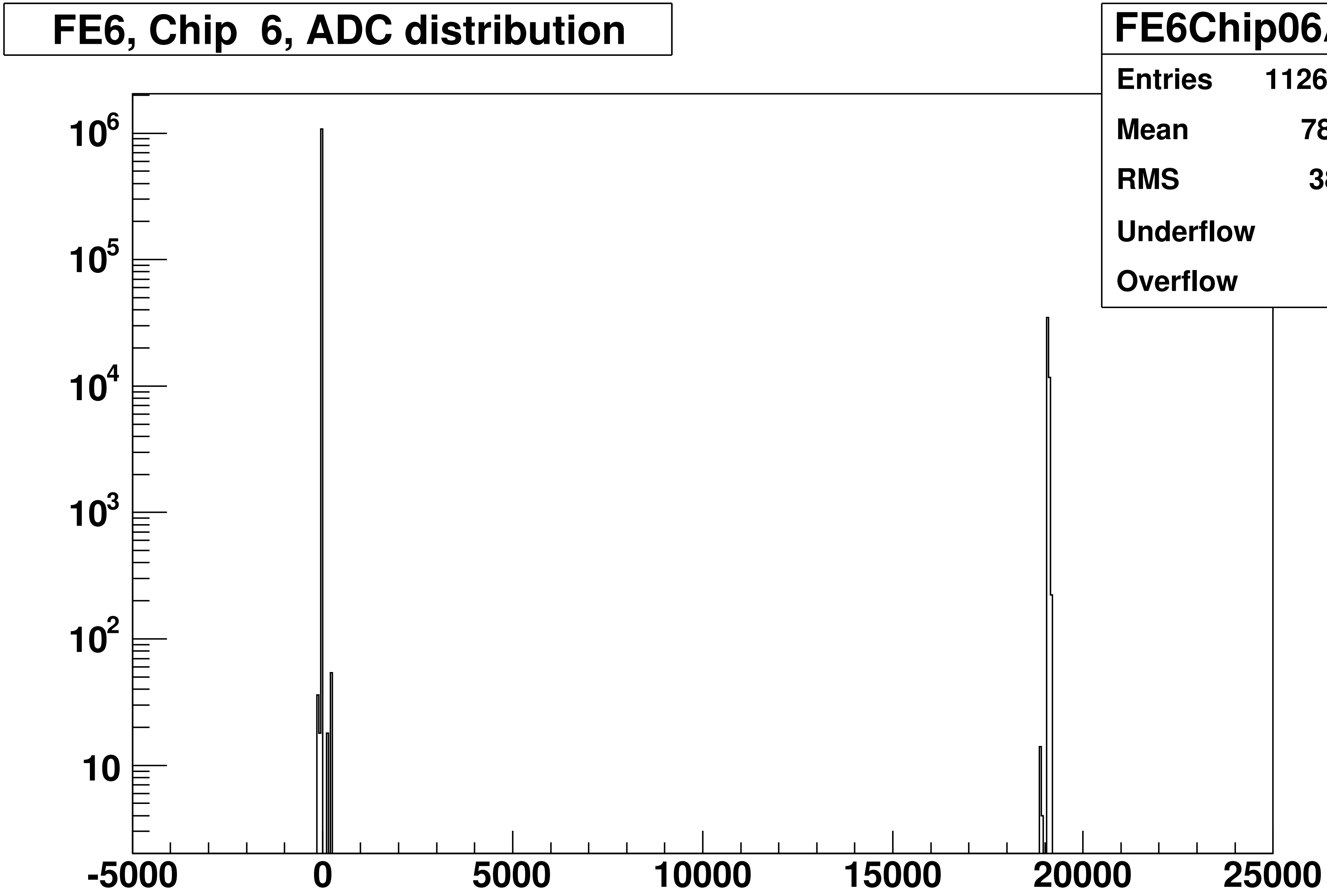
19400


FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 1126566
Mean 782.9
RMS 3808
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
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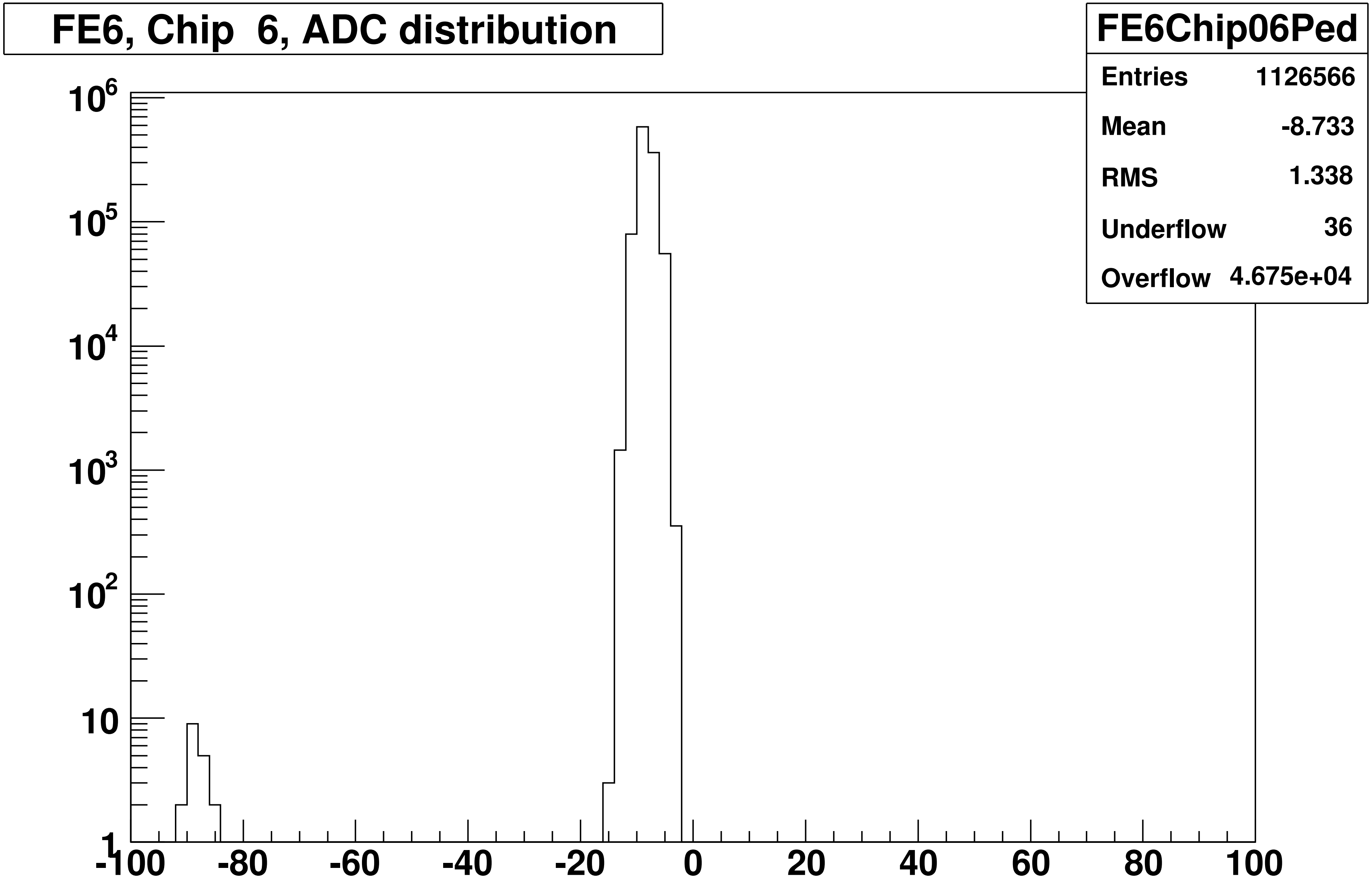
10000
15000
20000
25000



FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 1126566
Mean -8.733
RMS 1.338
Underflow 36
Overflow 4.675e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
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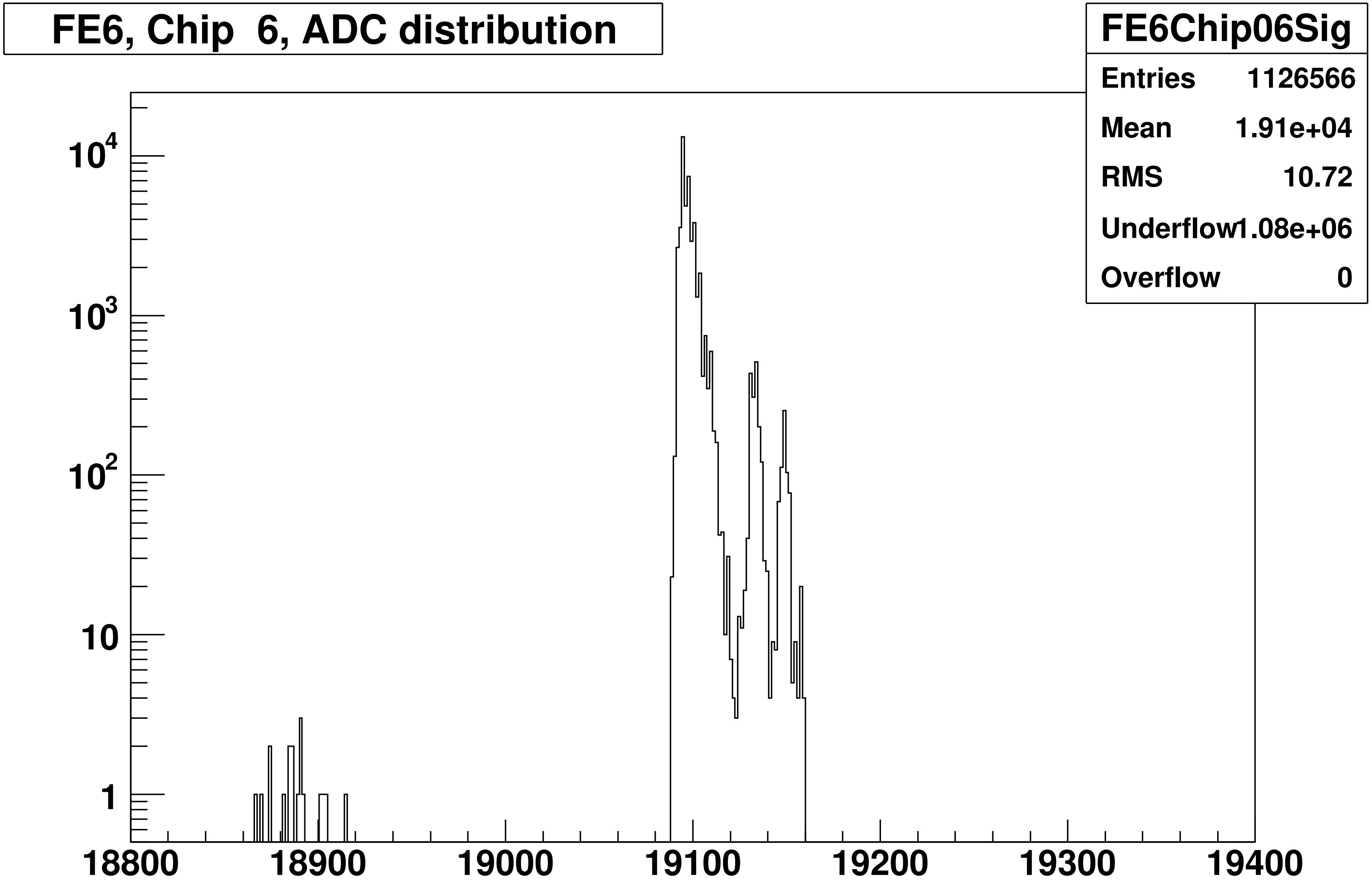
FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 1126566
Mean 1.91e+04
RMS 10.72
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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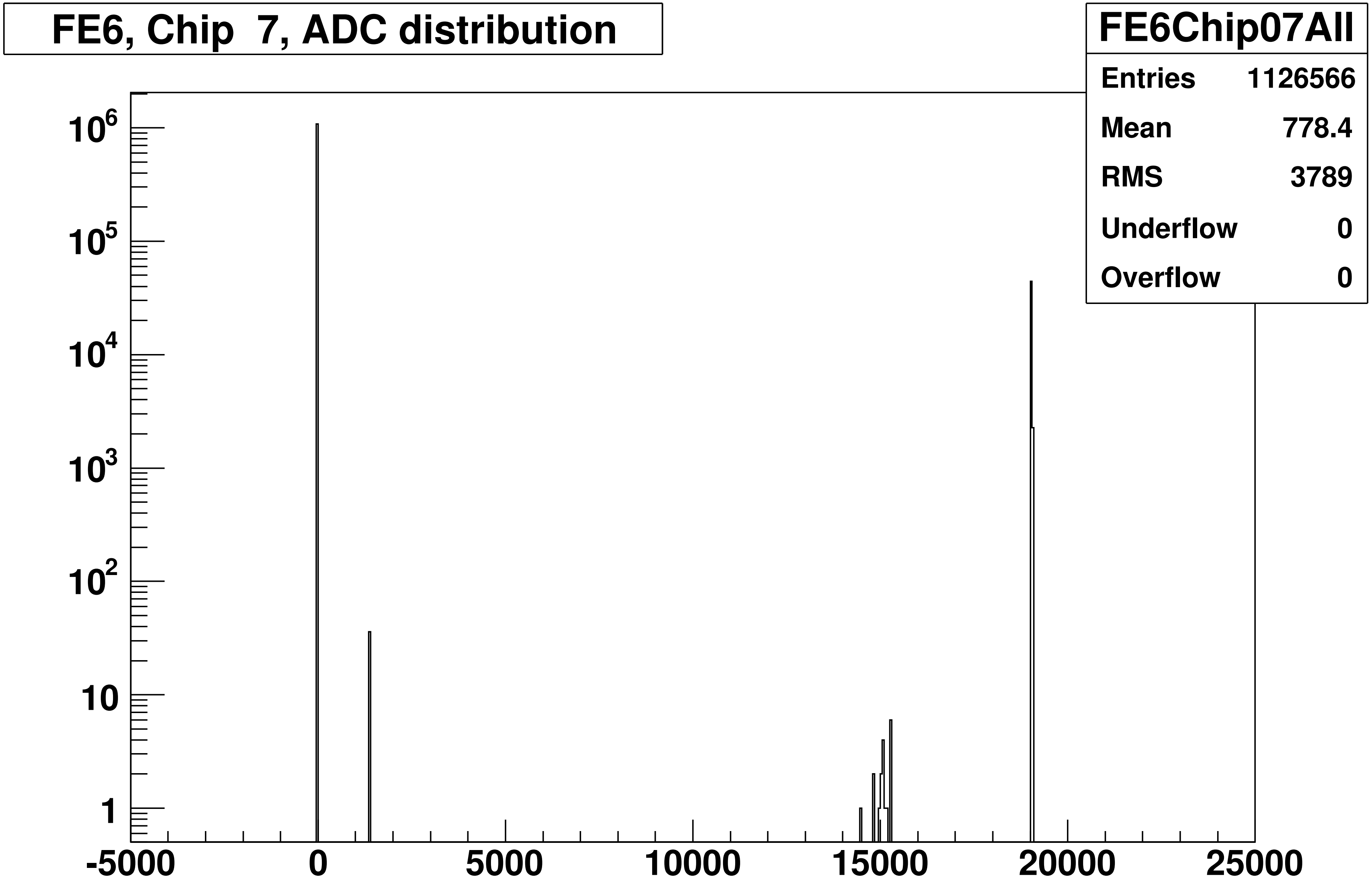
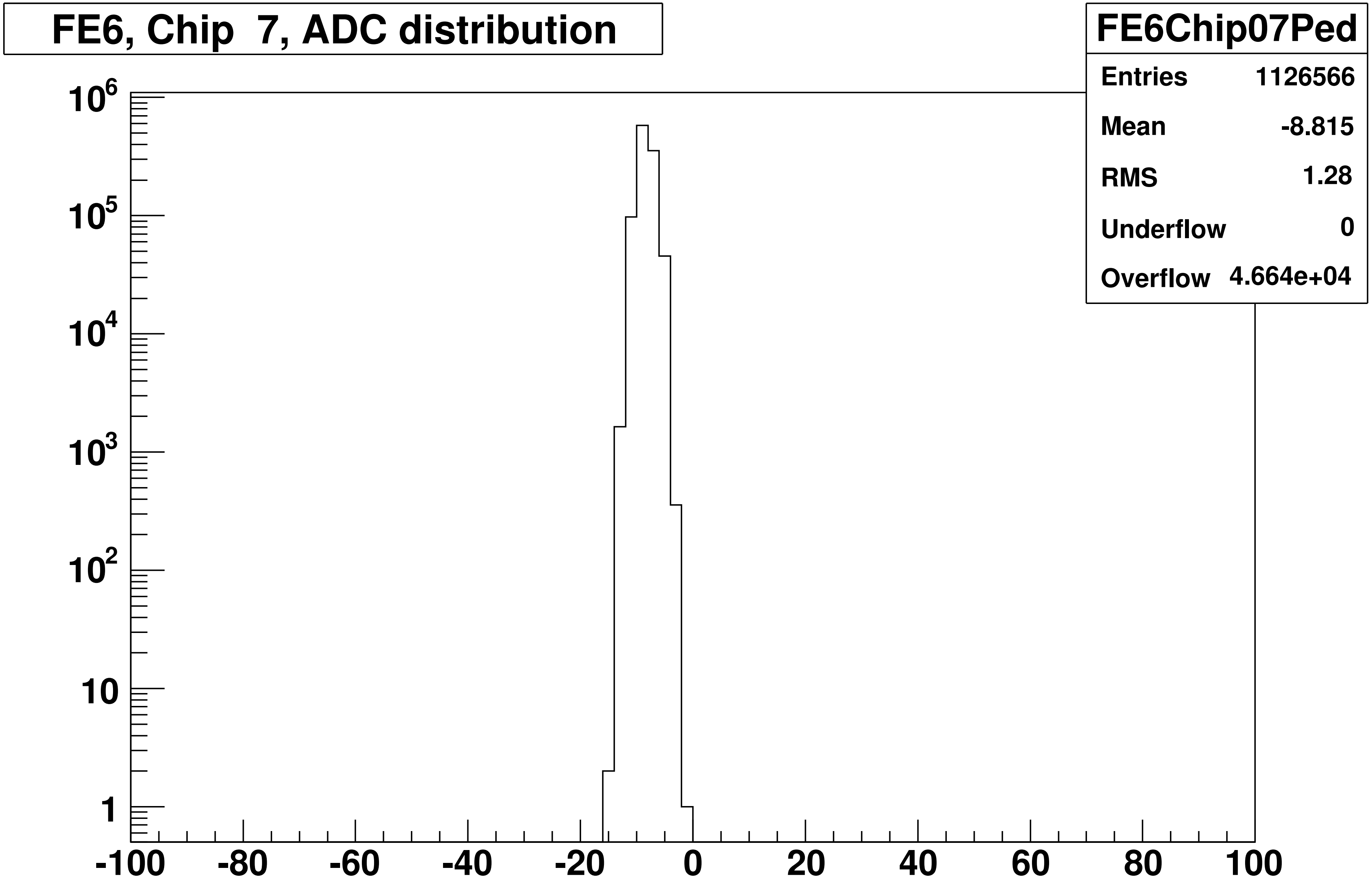
19100
19200
19300
19400


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 1126566
Mean 778.4
RMS 3789
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 1126566
Mean -8.815
RMS 1.28
Underflow 0
Overflow 4.664e+04
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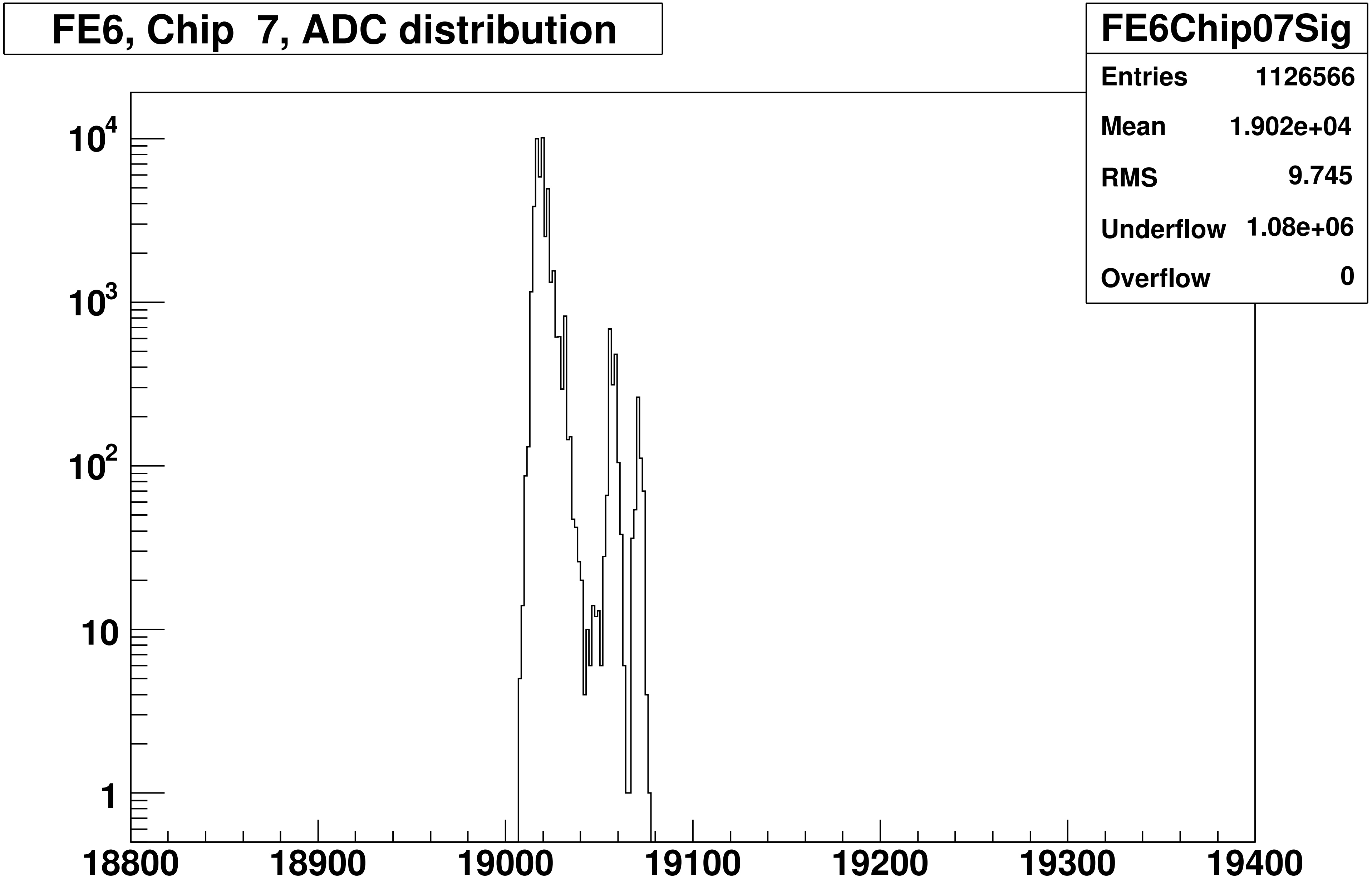
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 1126566
Mean 1.902e+04
RMS 9.745
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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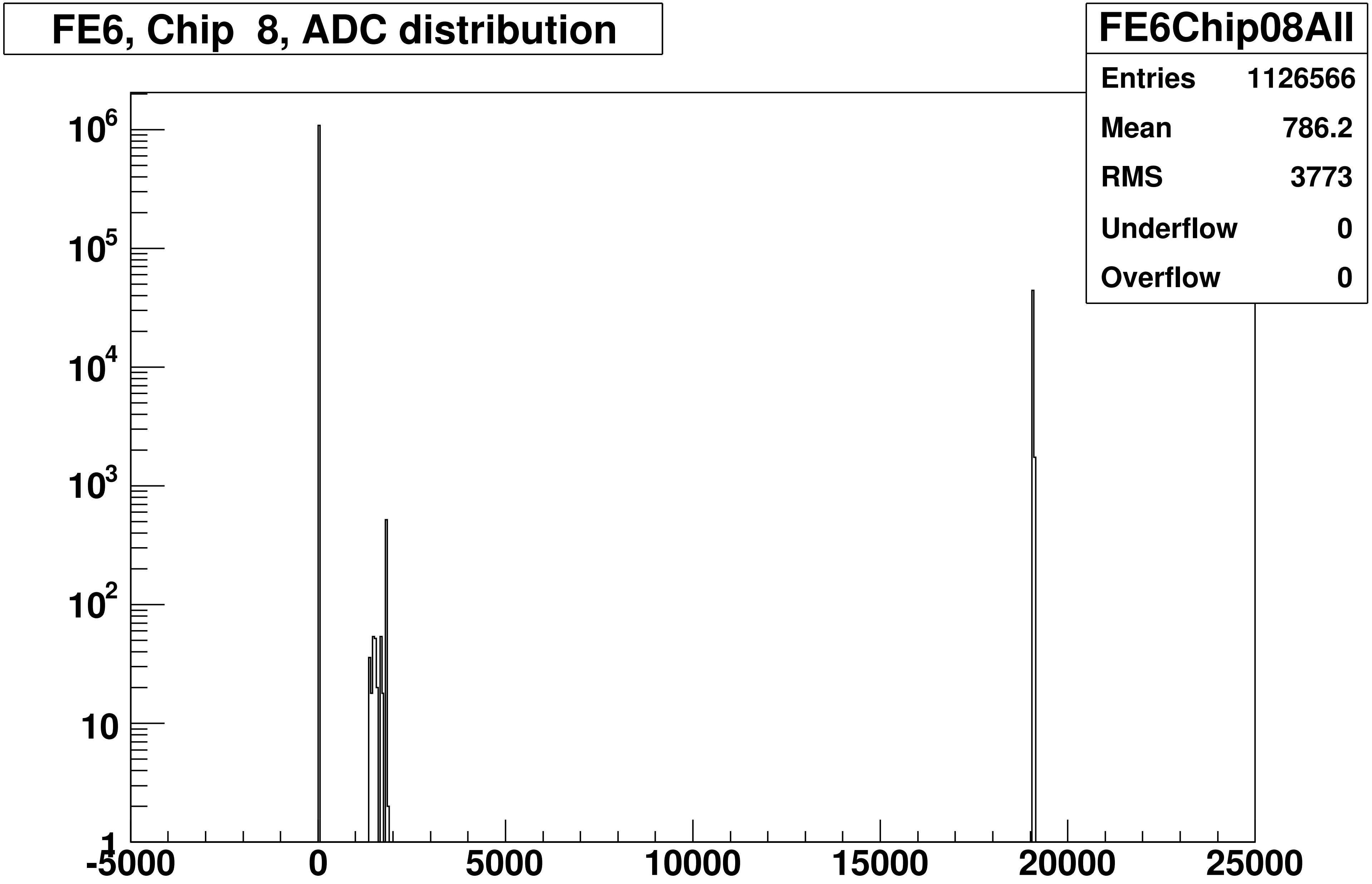
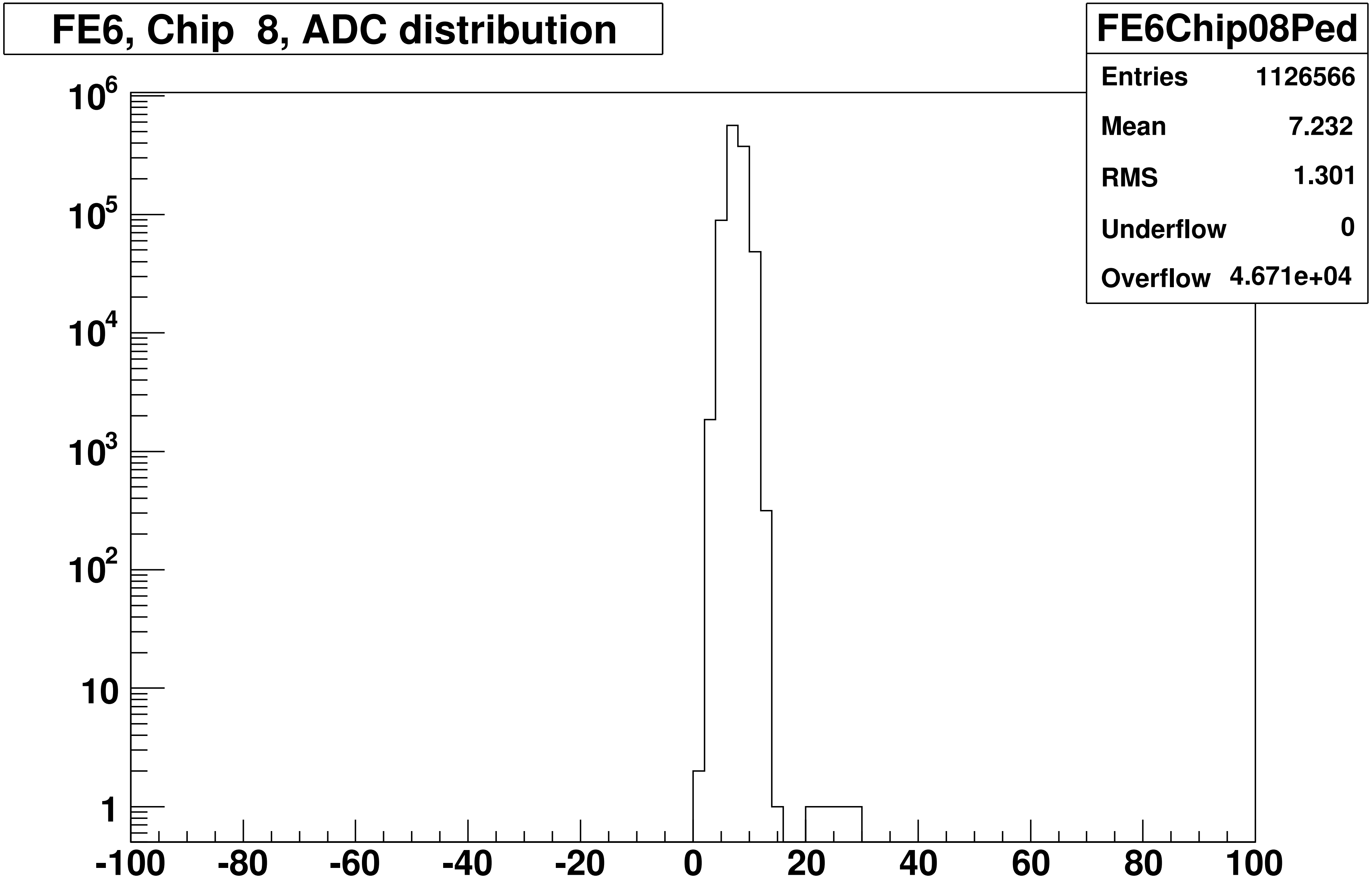
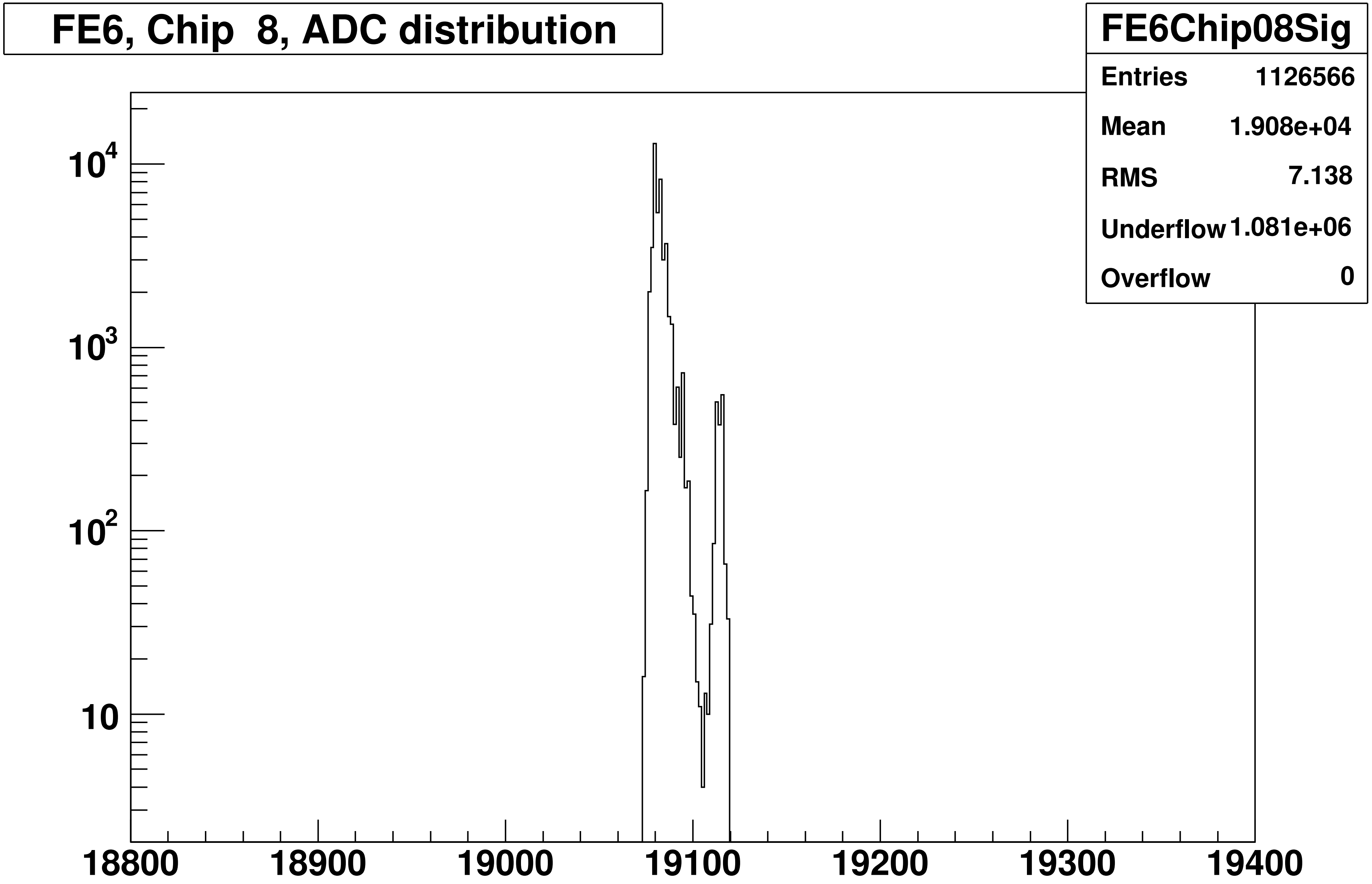
19100
19200
19300
19400


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 1126566
Mean 786.2
RMS 3773
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 1126566
Mean 7.232
RMS 1.301
Underflow 0
Overflow 4.671e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 1126566
Mean 1.908e+04
RMS 7.138
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
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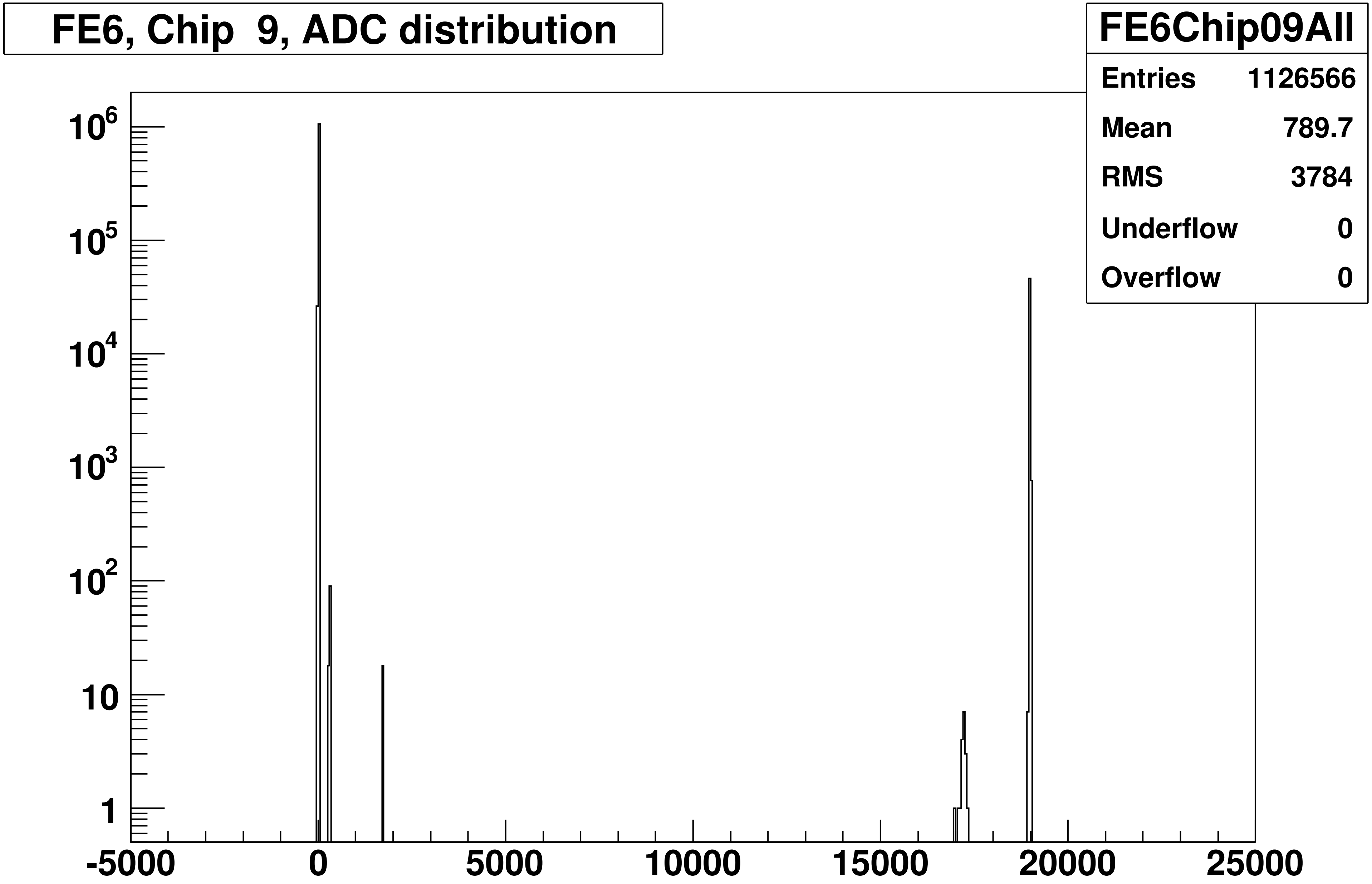
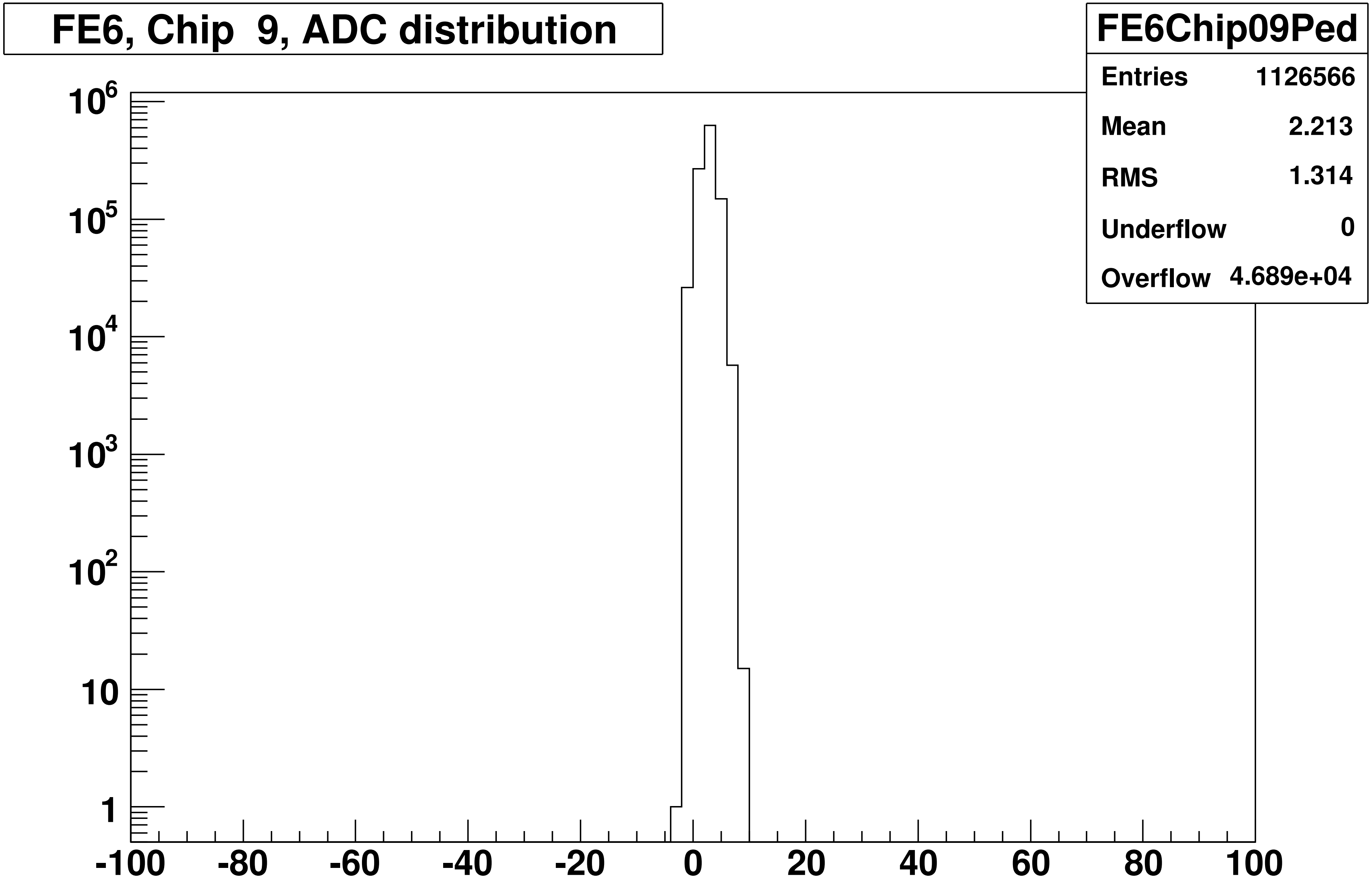
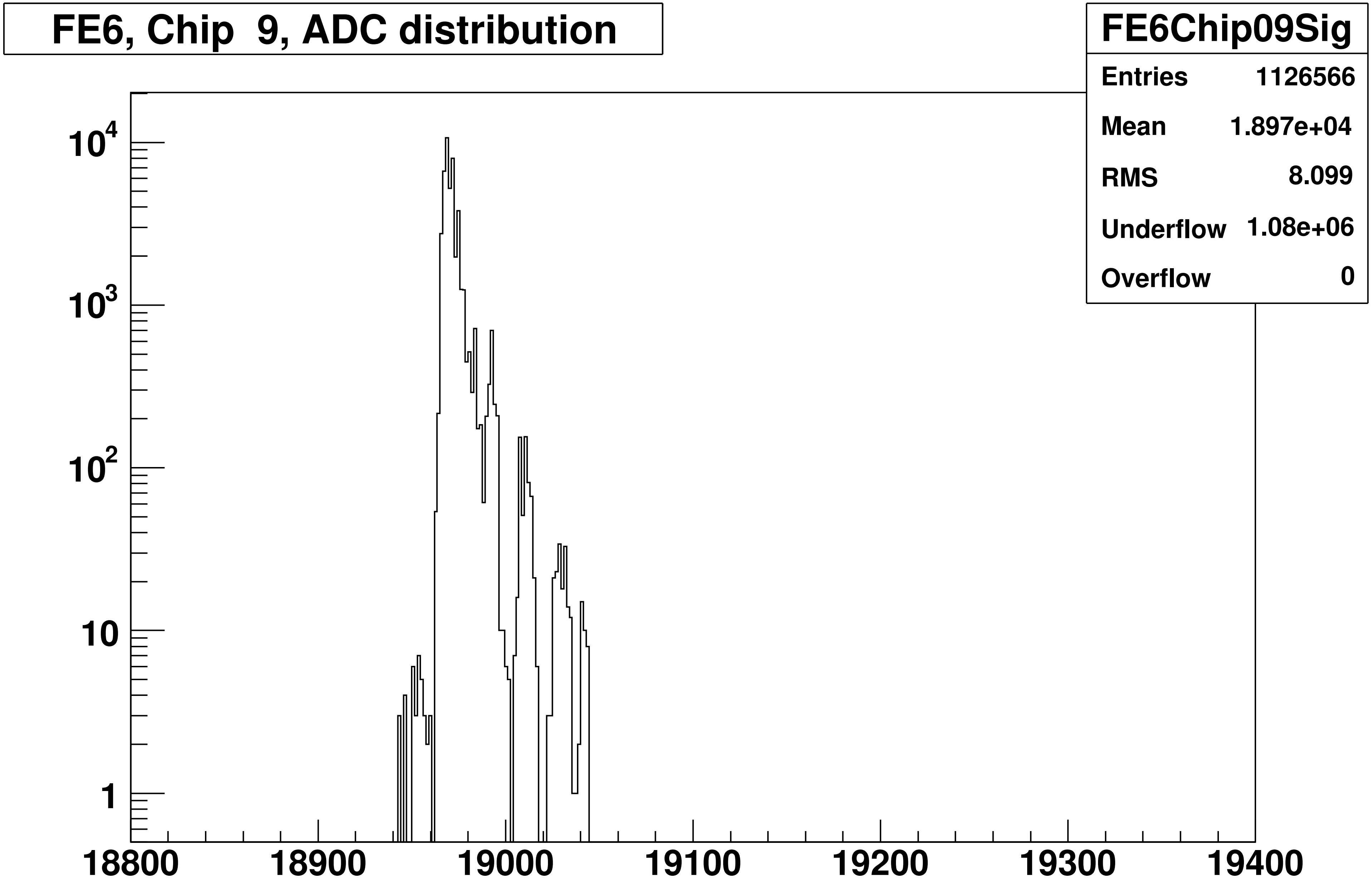
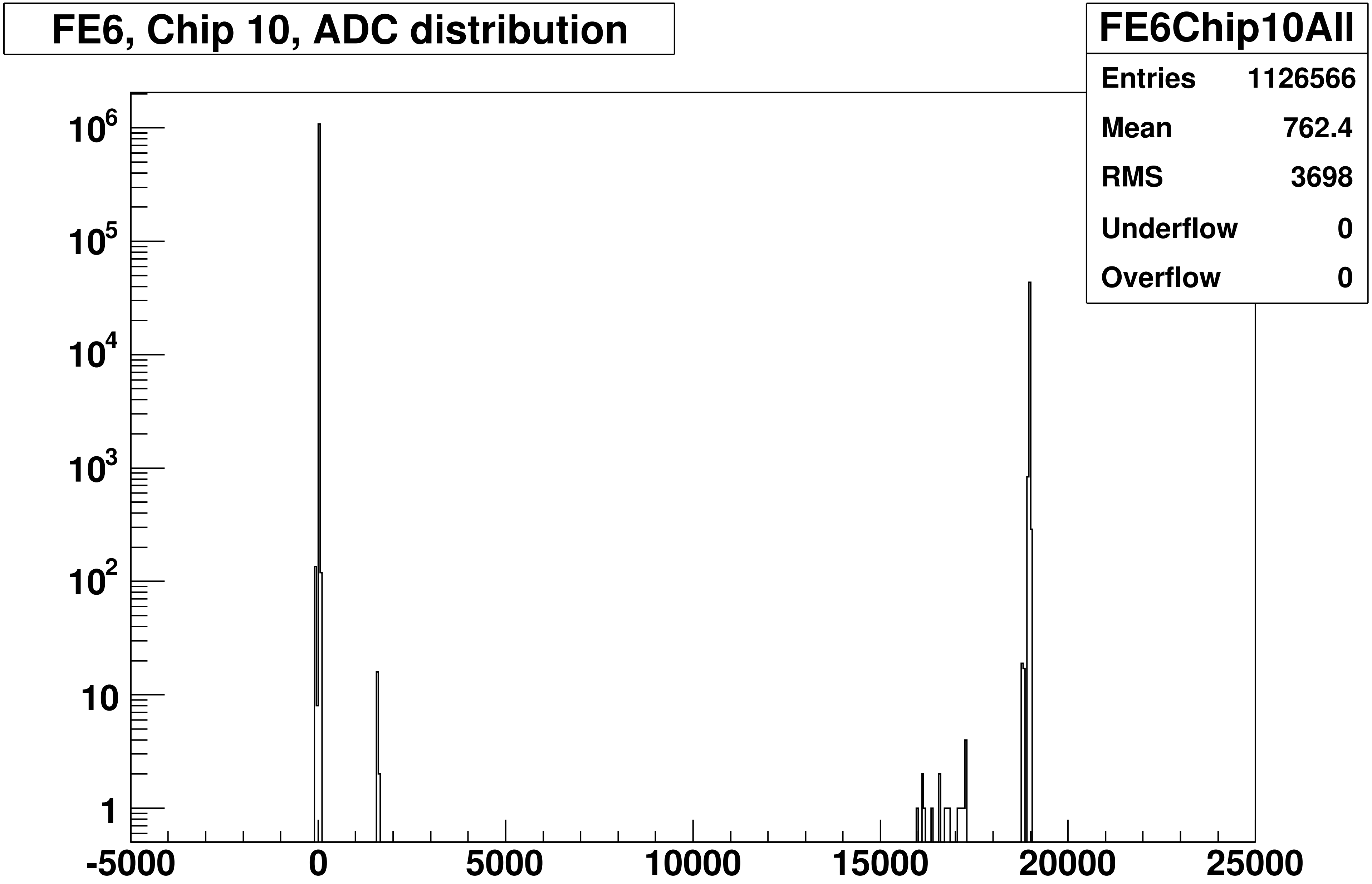


FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 1126566
Mean 789.7
RMS 3784
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

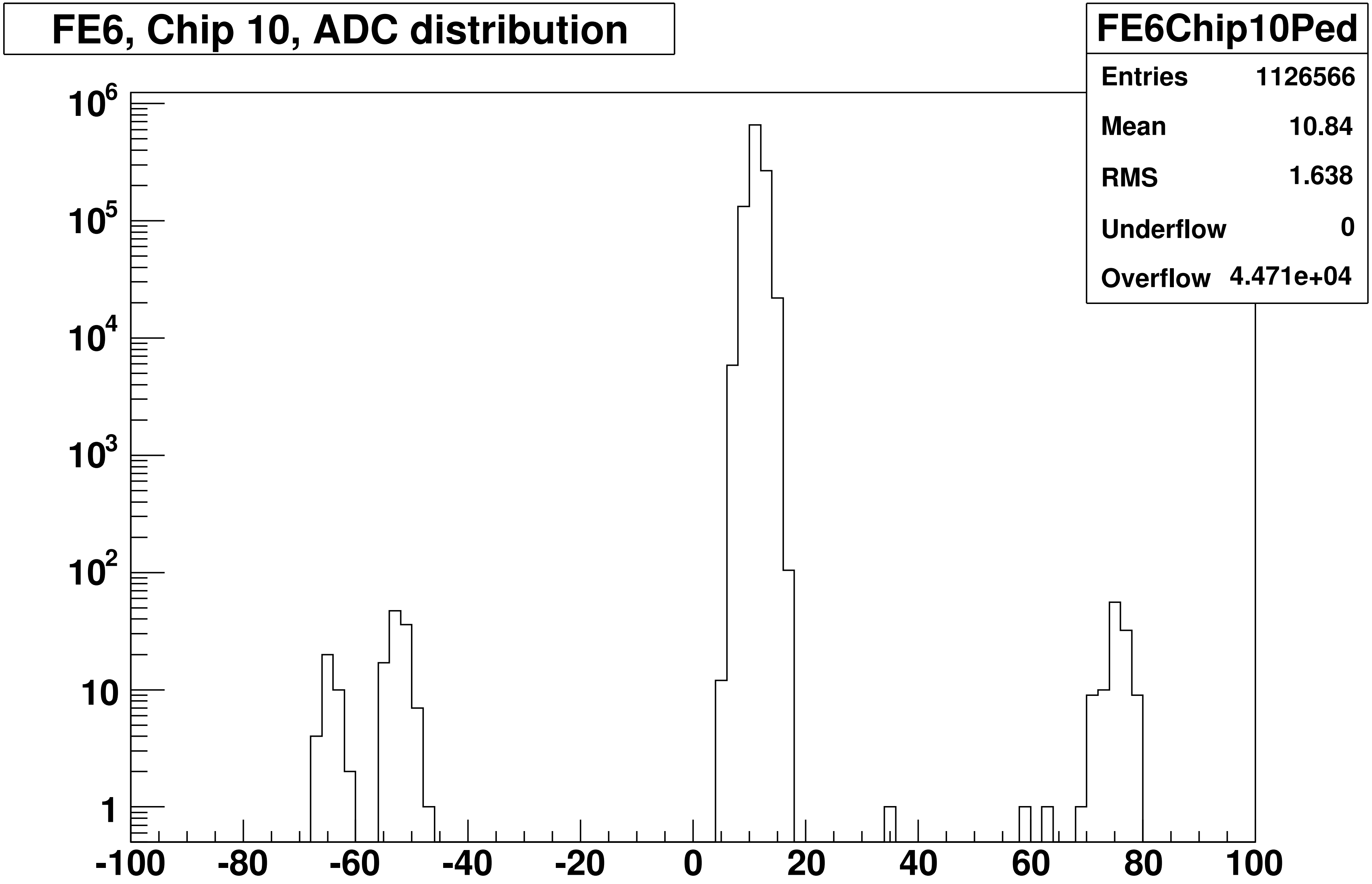

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 1126566
Mean 2.213
RMS 1.314
Underflow 0
Overflow 4.689e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

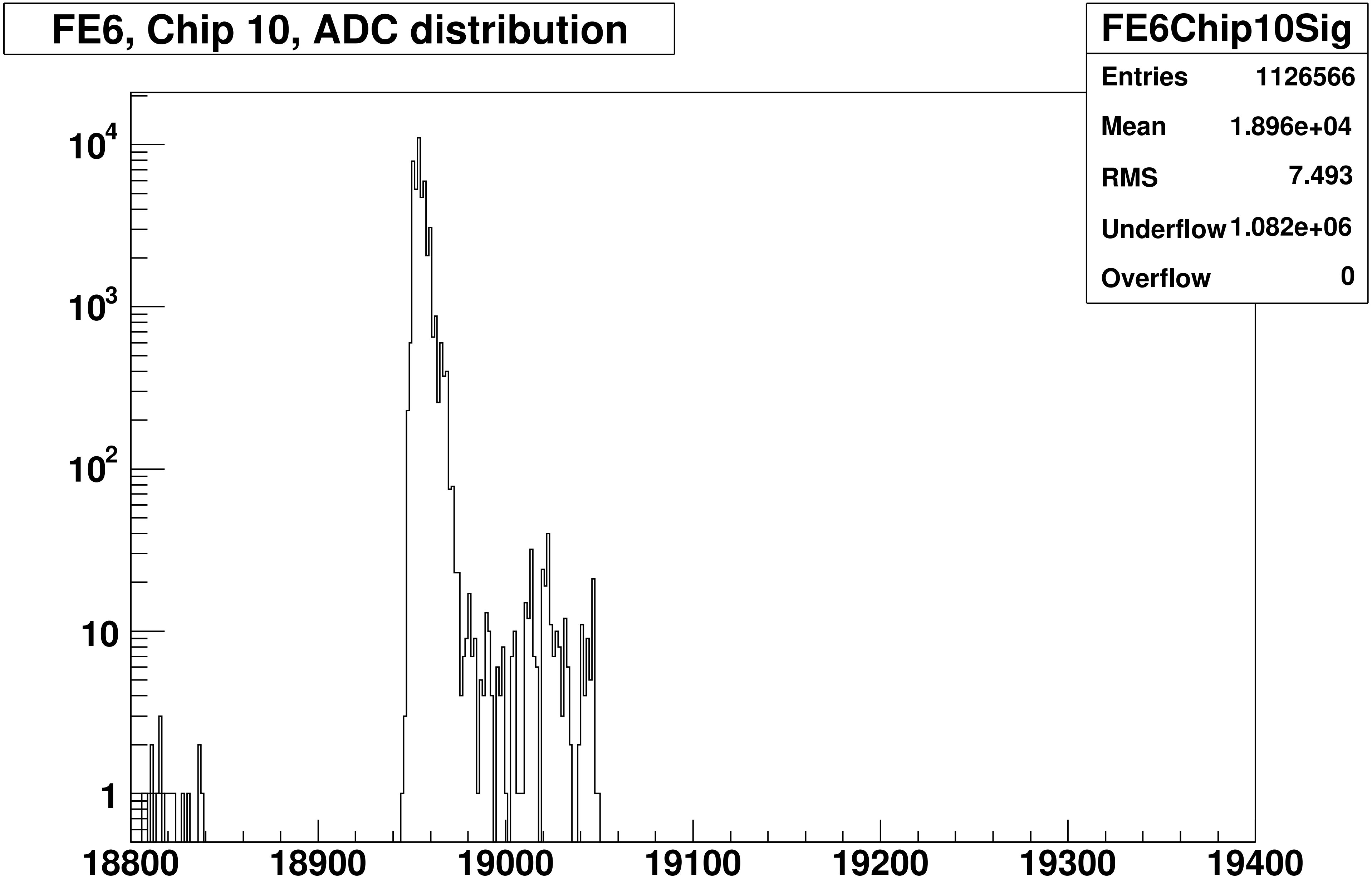


FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 1126566
Mean 1.897e+04
RMS 8.099
Underflow 1.08e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 1126566
Mean 762.4
RMS 3698
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 1126566
Mean 10.84
RMS 1.638
Underflow 0
Overflow 4.471e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 1126566
Mean 1.896e+04
RMS 7.493
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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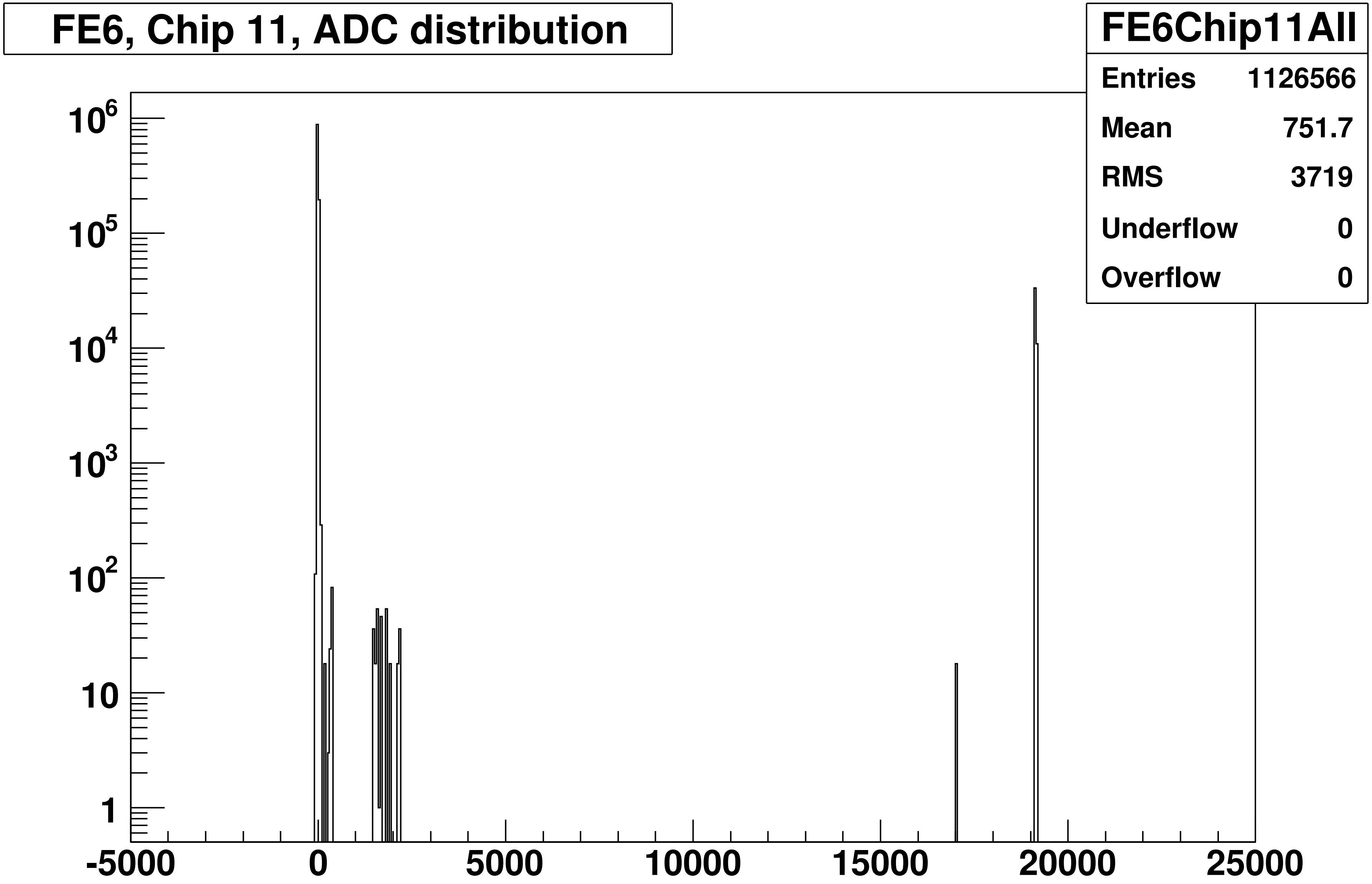
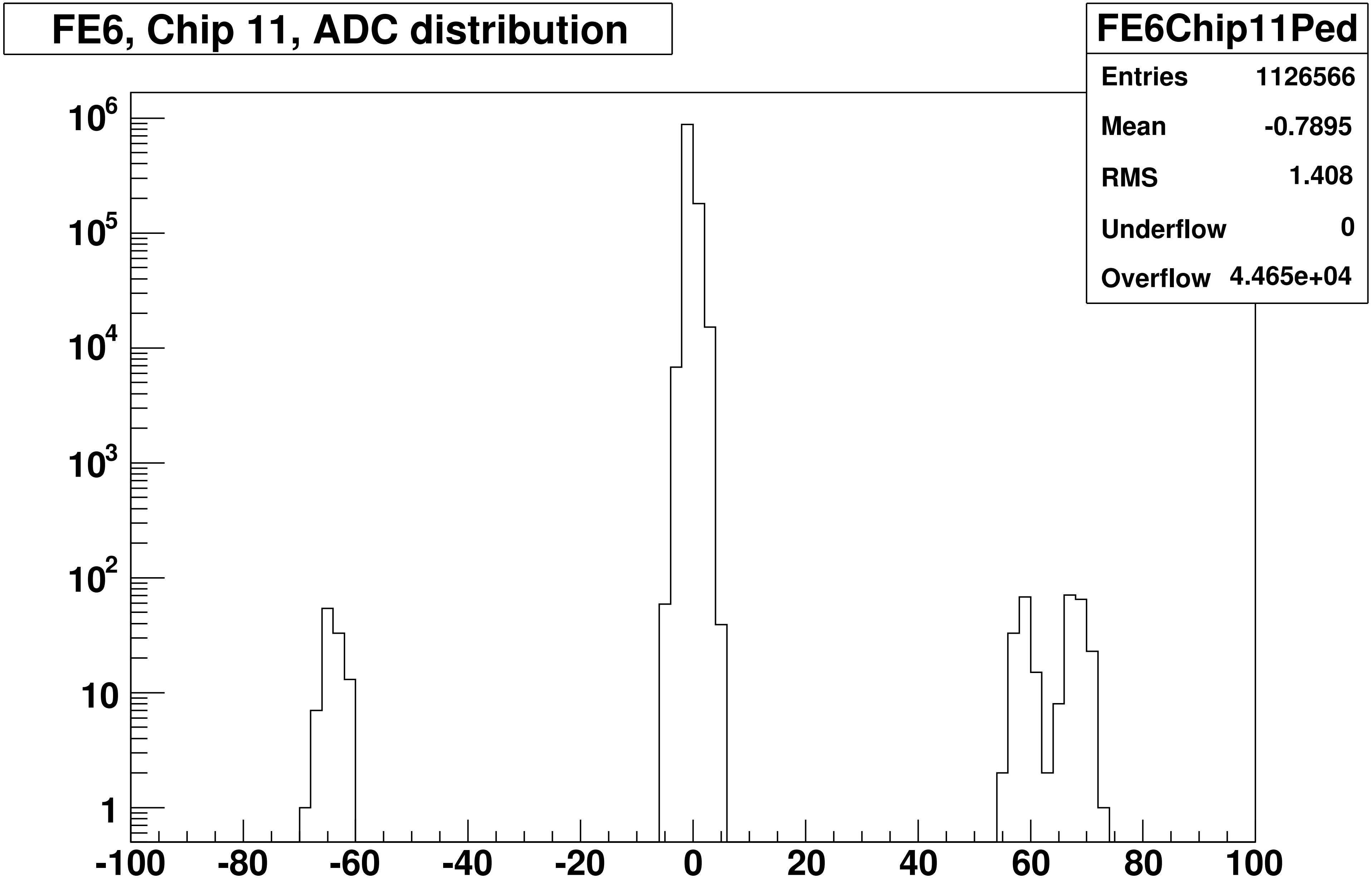
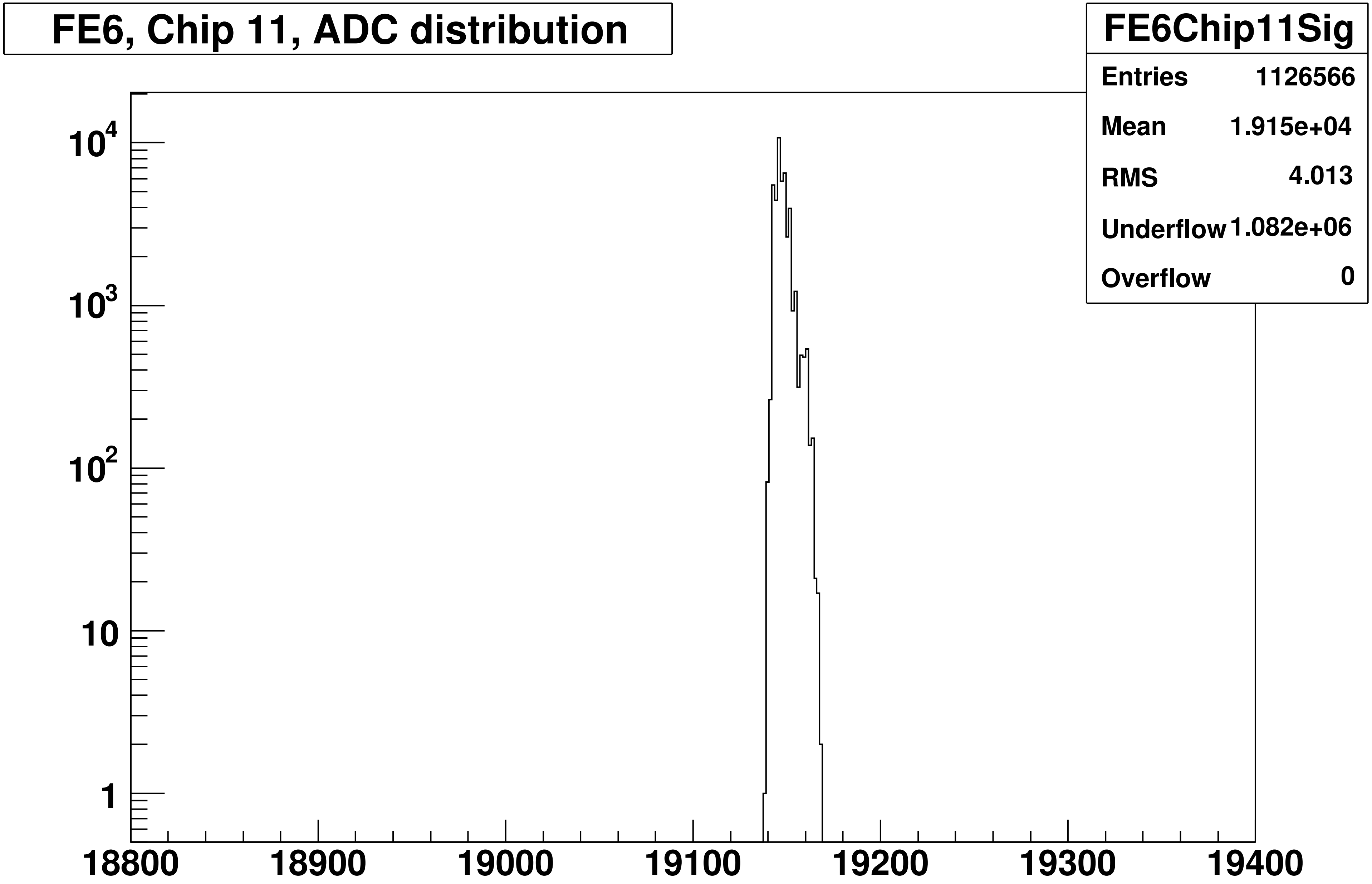
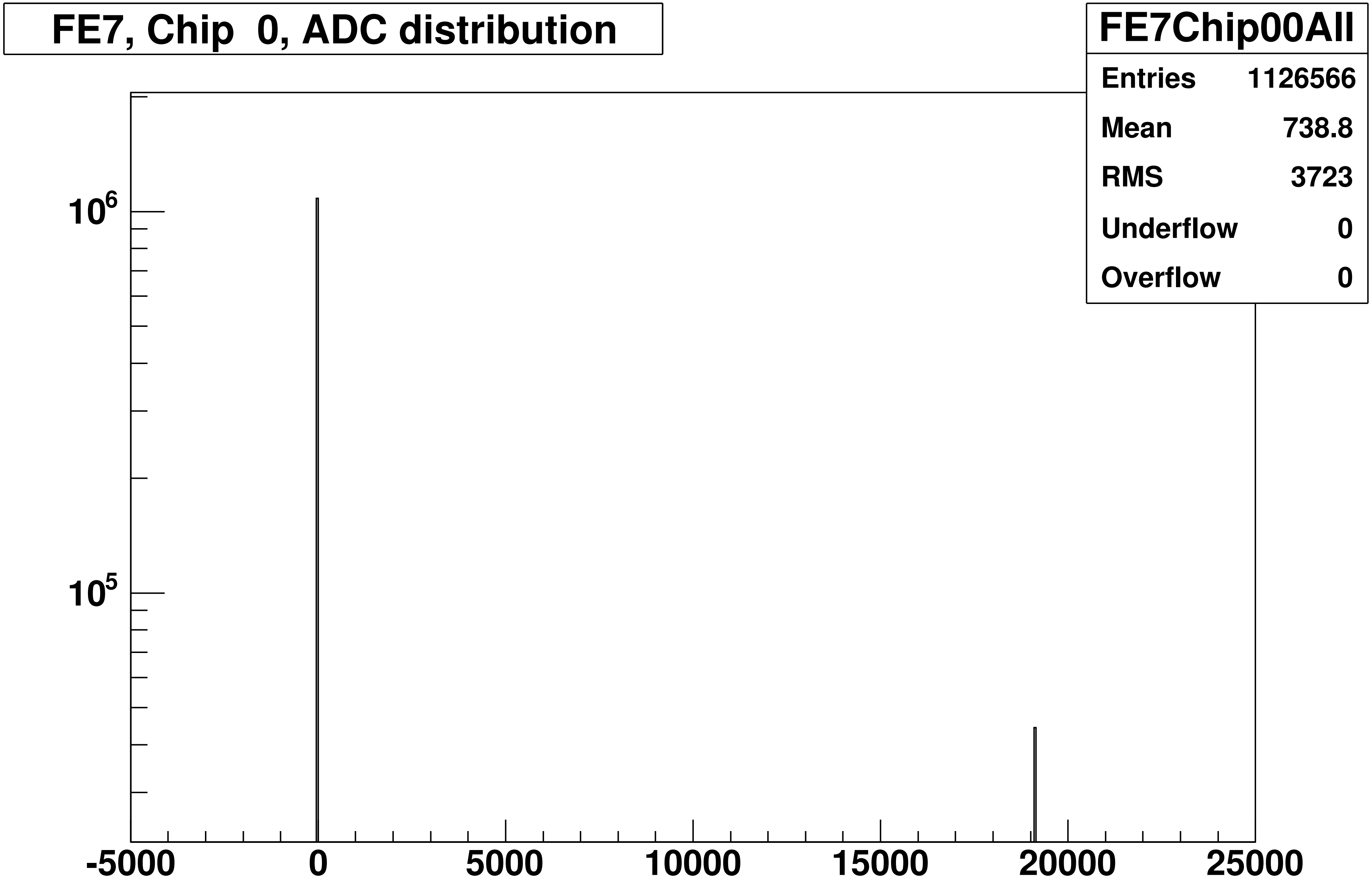
19100
19200
19300
19400

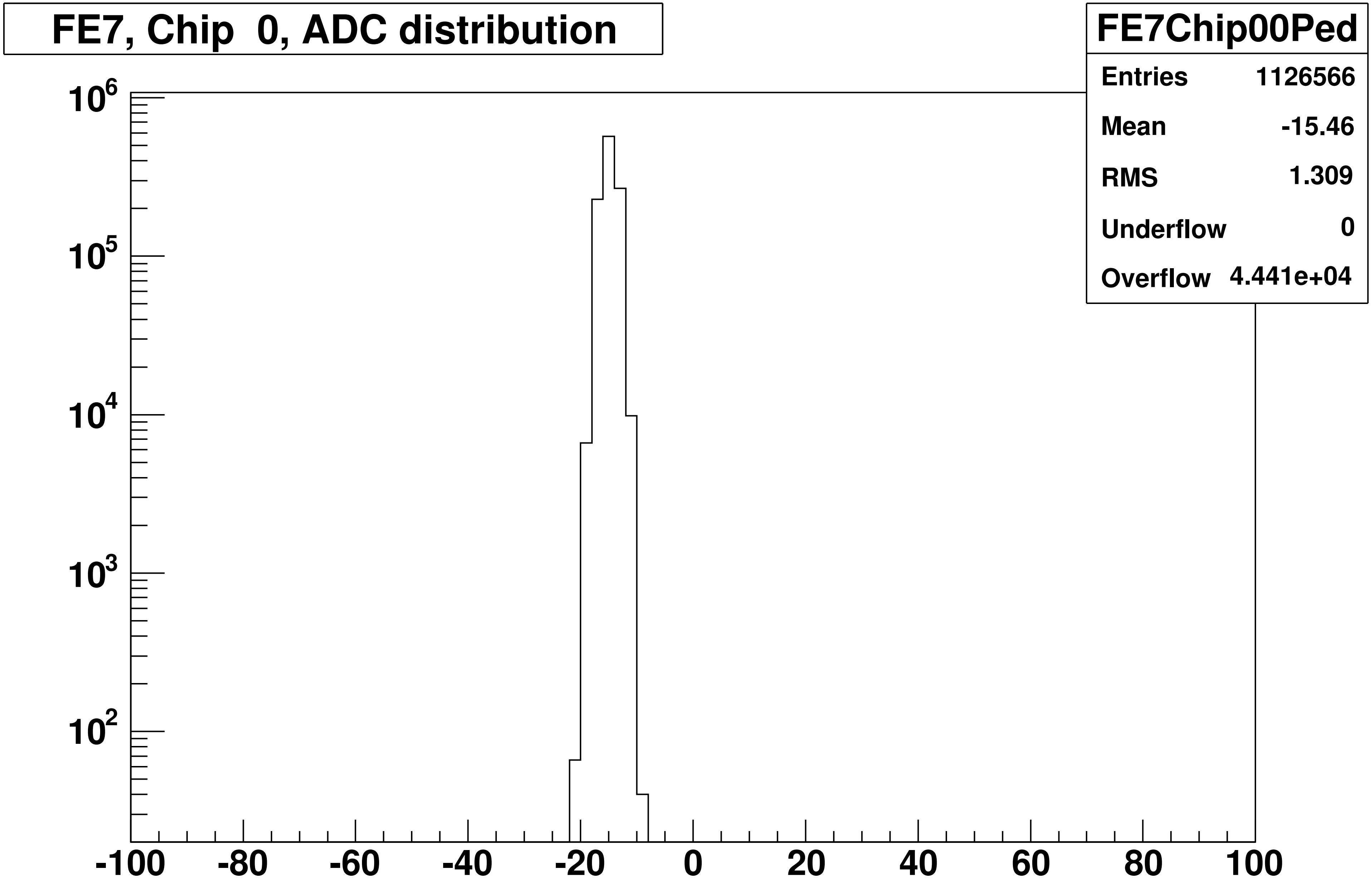

FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 1126566
Mean 751.7
RMS 3719
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 1126566
Mean -0.7895
RMS 1.408
Underflow 0
Overflow 4.465e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

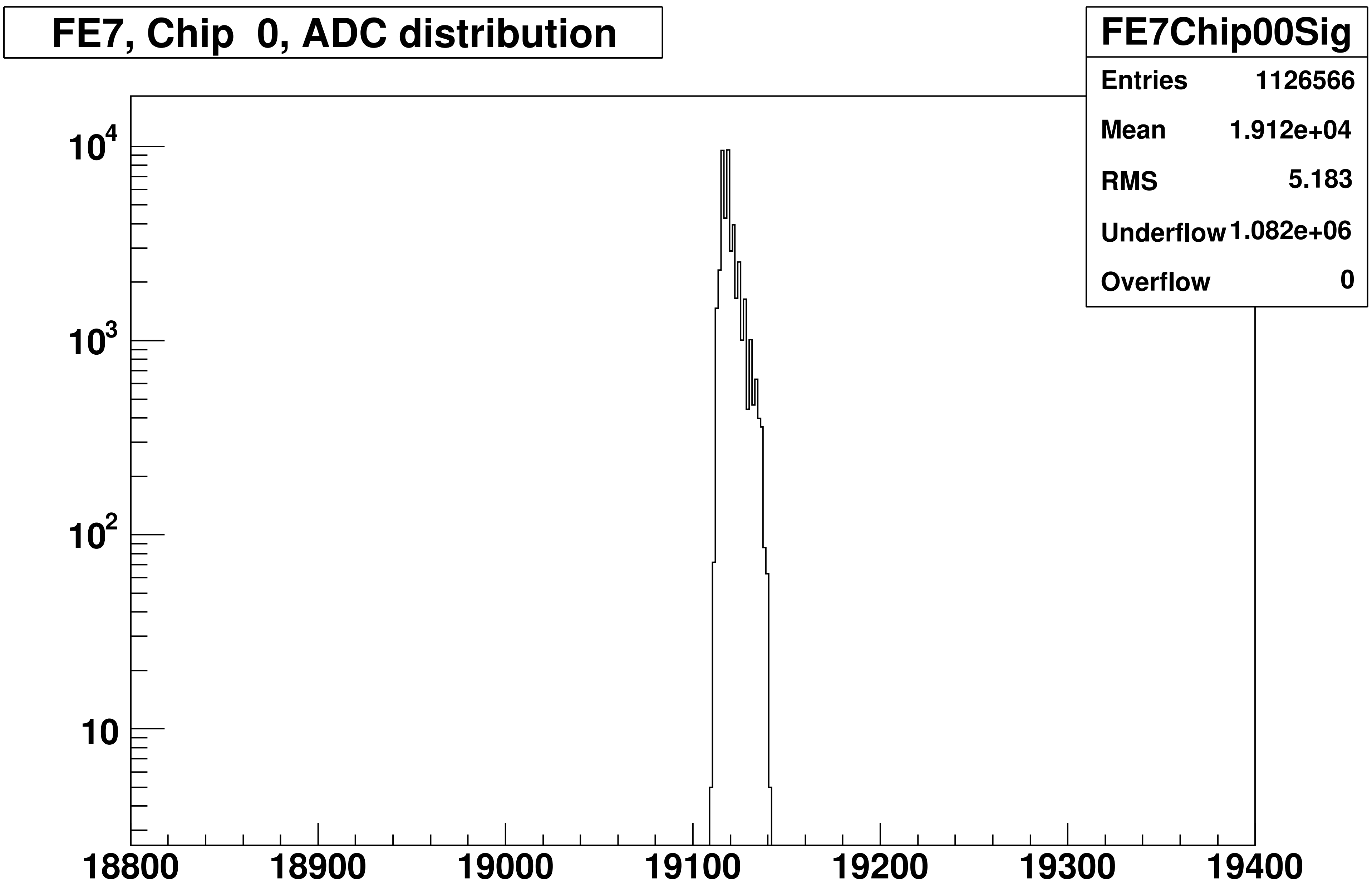


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 1126566
Mean 1.915e+04
RMS 4.013
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

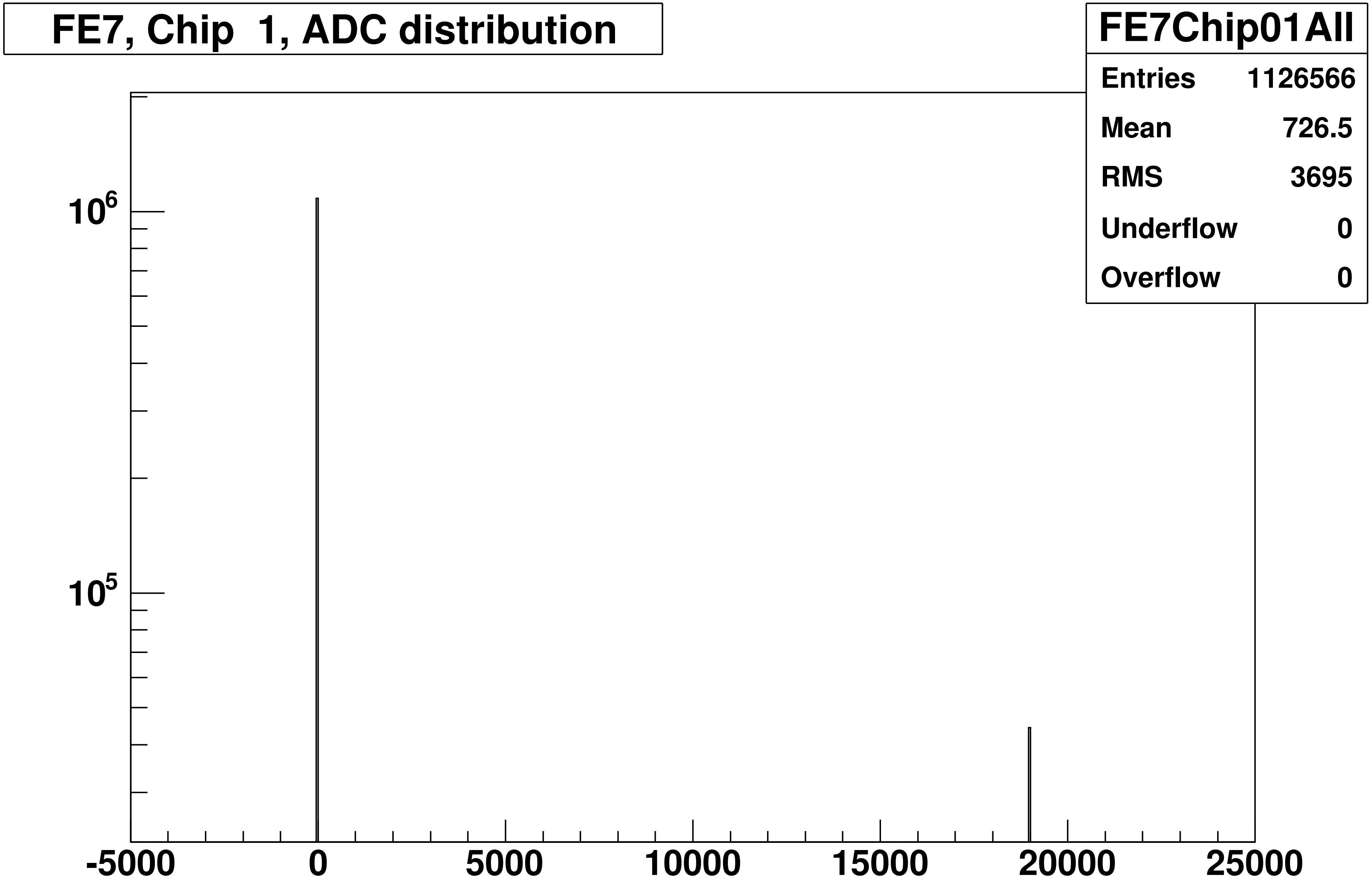

FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 1126566
Mean 738.8
RMS 3723
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000

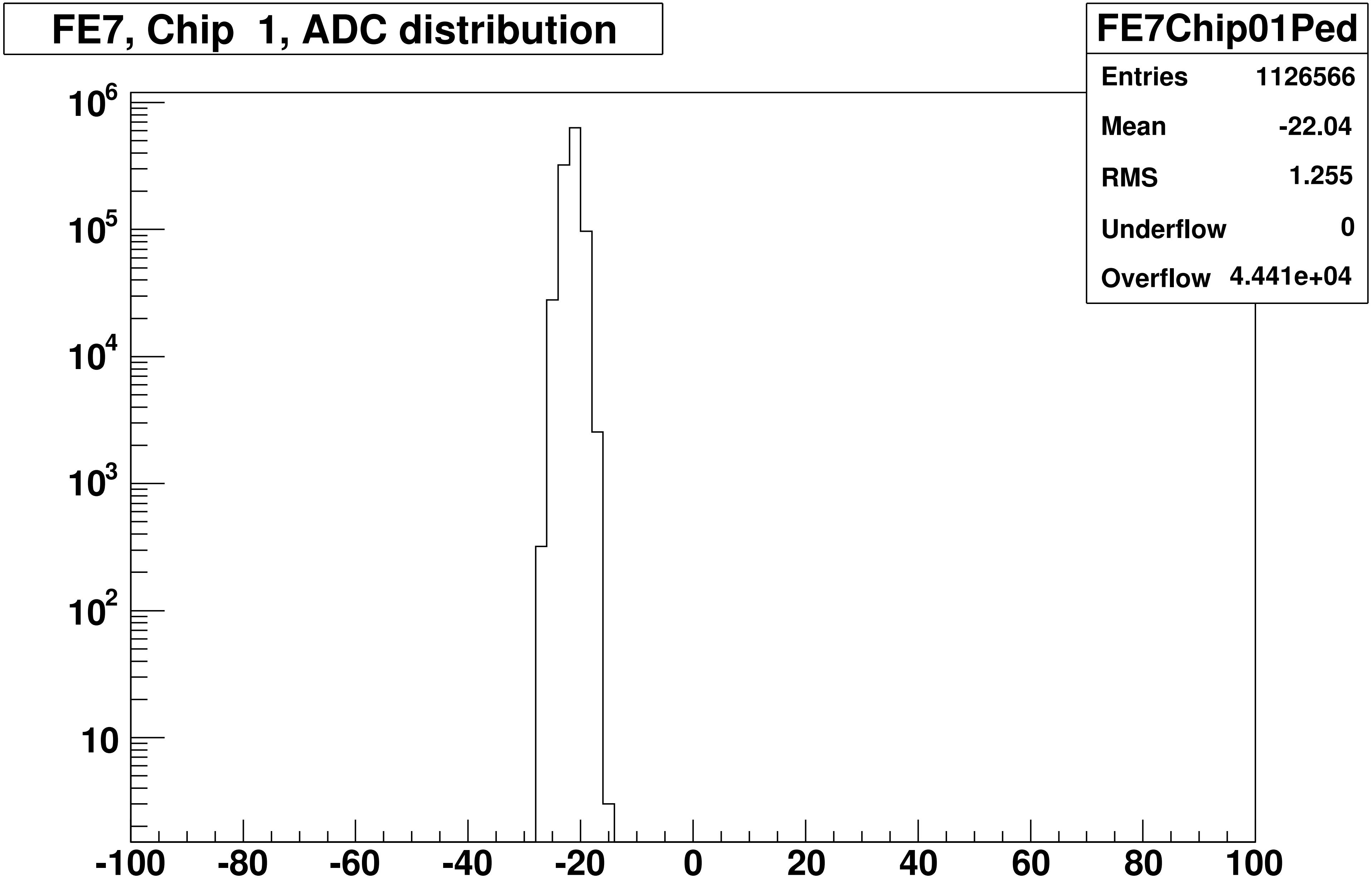


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 1126566
Mean -15.46
RMS 1.309
Underflow 0
Overflow 4.441e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

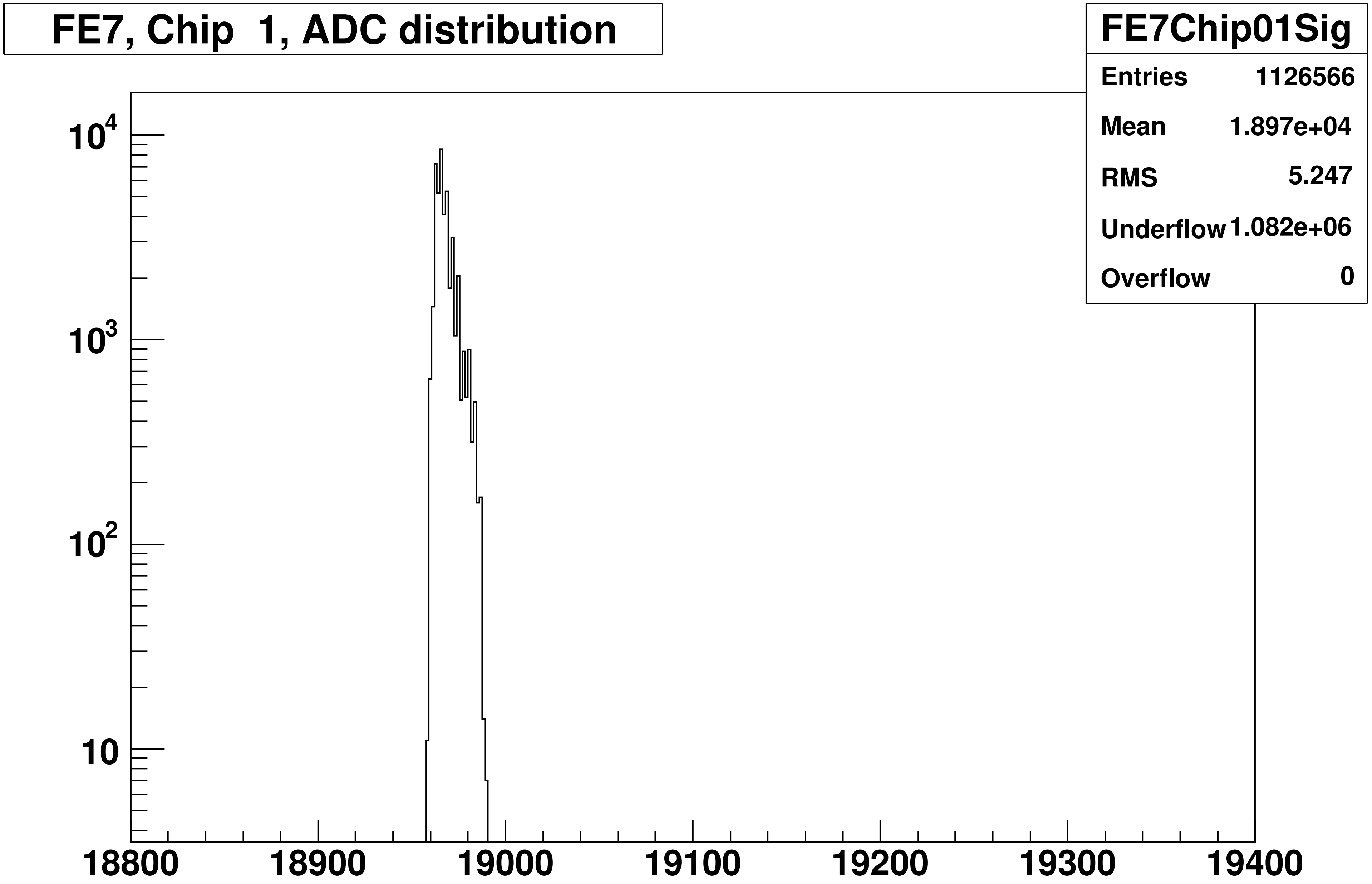


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 1126566
Mean 1.912e+04
RMS 5.183
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

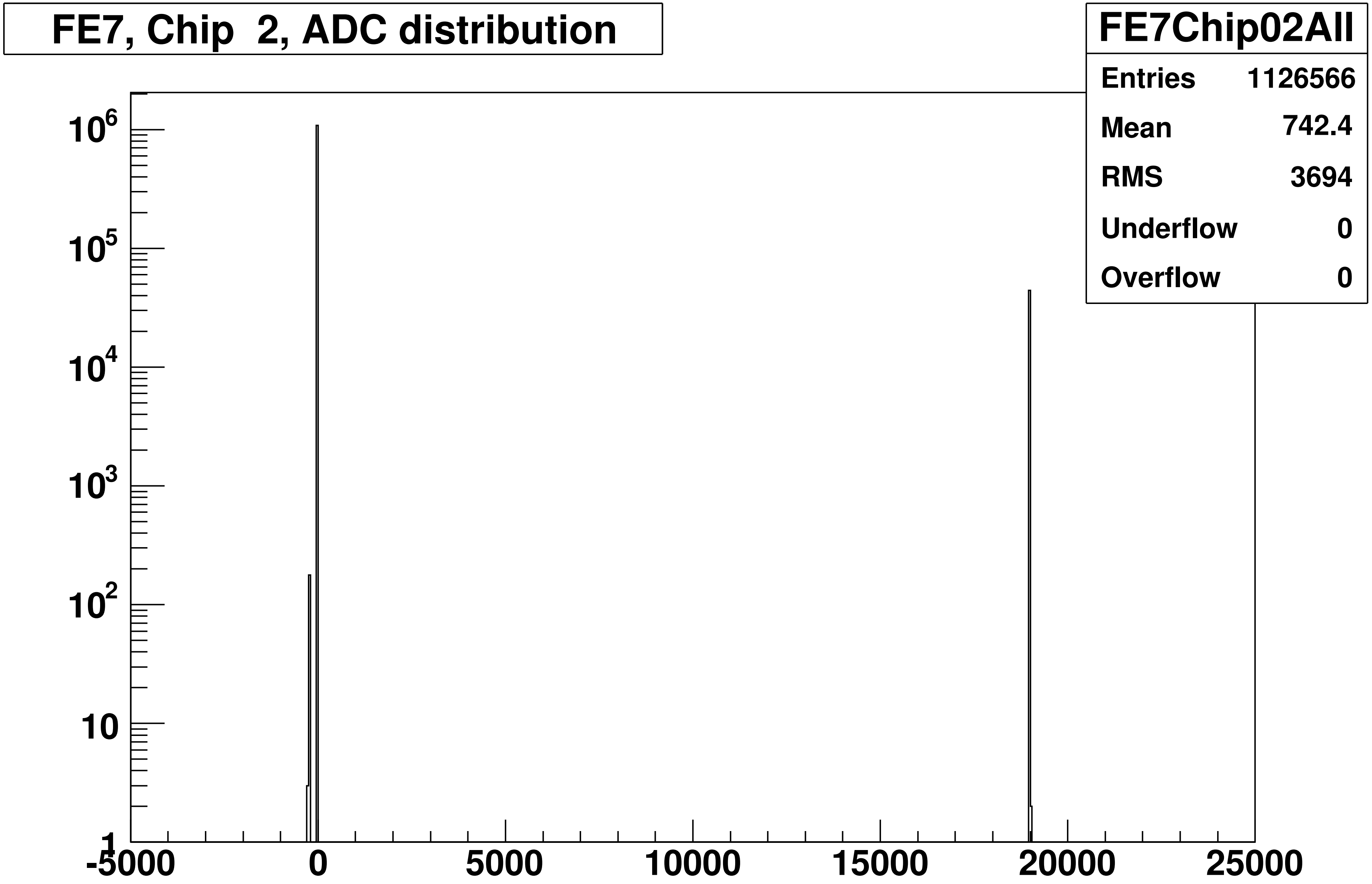

FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 1126566
Mean 726.5
RMS 3695
Underflow 0
Overflow 0
10^6
10^5
-5000
0
5000
10000
15000
20000
25000



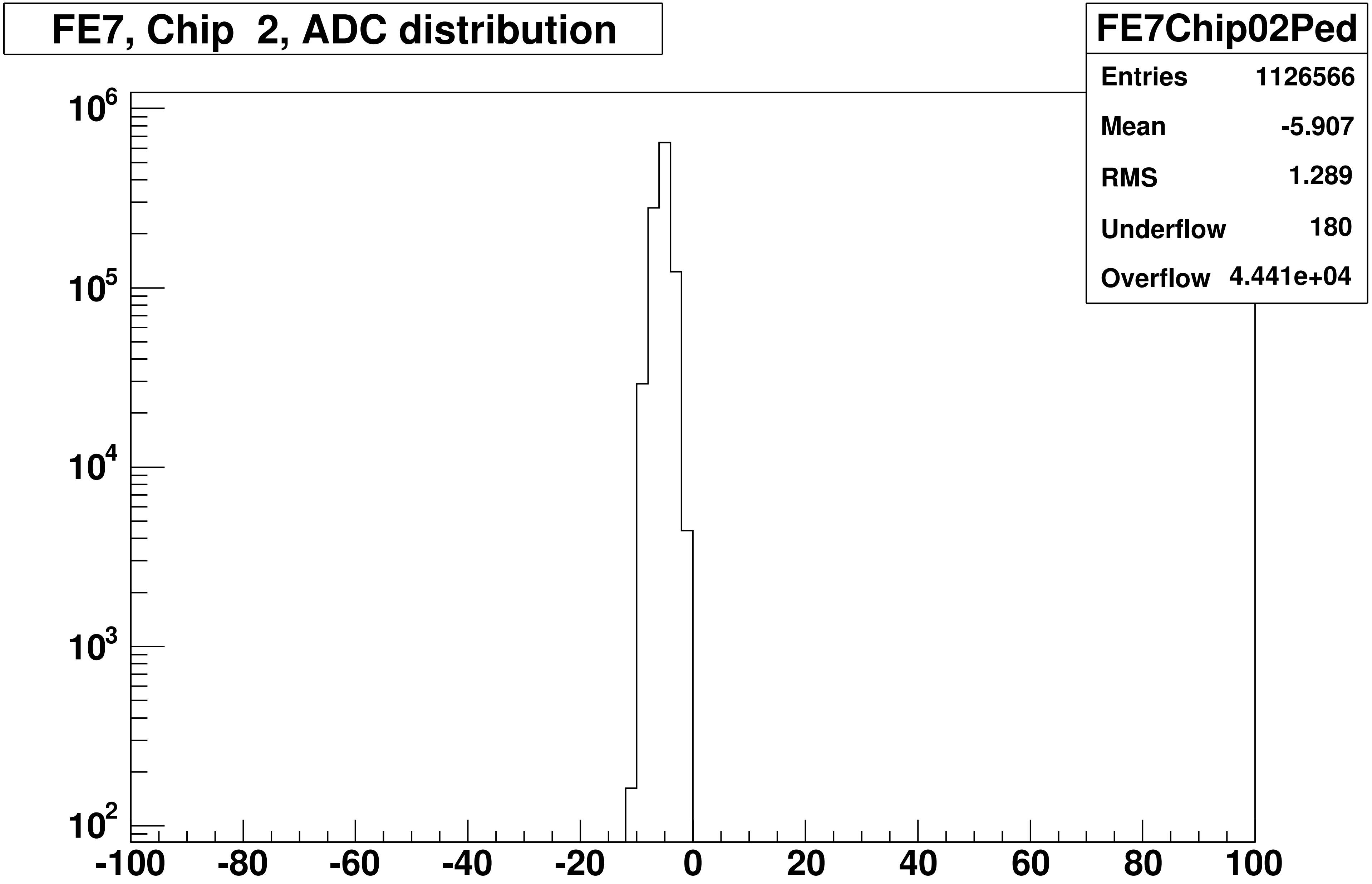
FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 1126566
Mean -22.04
RMS 1.255
Underflow 0
Overflow 4.441e+04
10^6
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 1126566
Mean 1.897e+04
RMS 5.247
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400



FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 1126566
Mean 742.4
RMS 3694
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 1126566
Mean -5.907
RMS 1.289
Underflow 180
Overflow 4.441e+04
10^6
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
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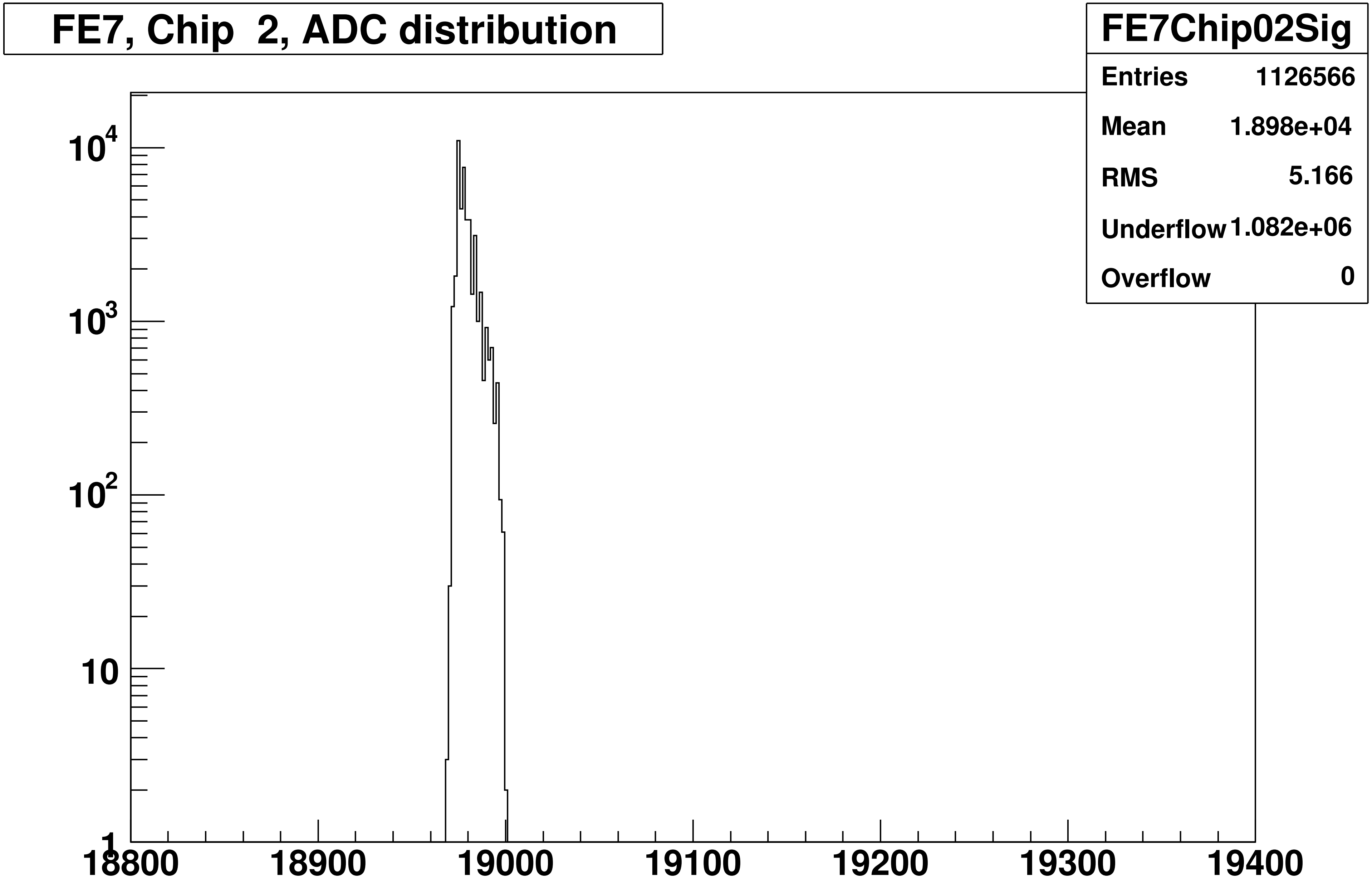
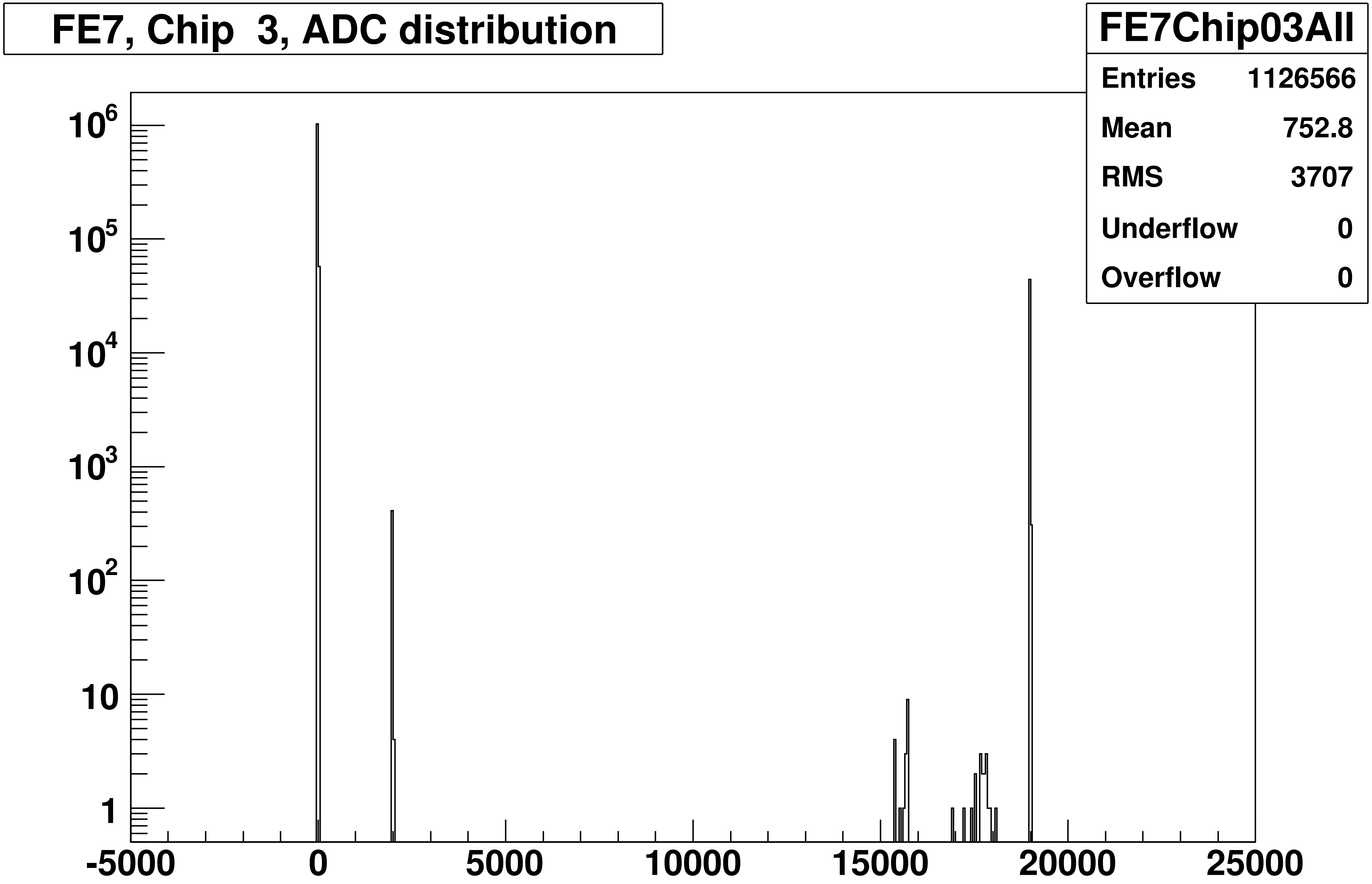
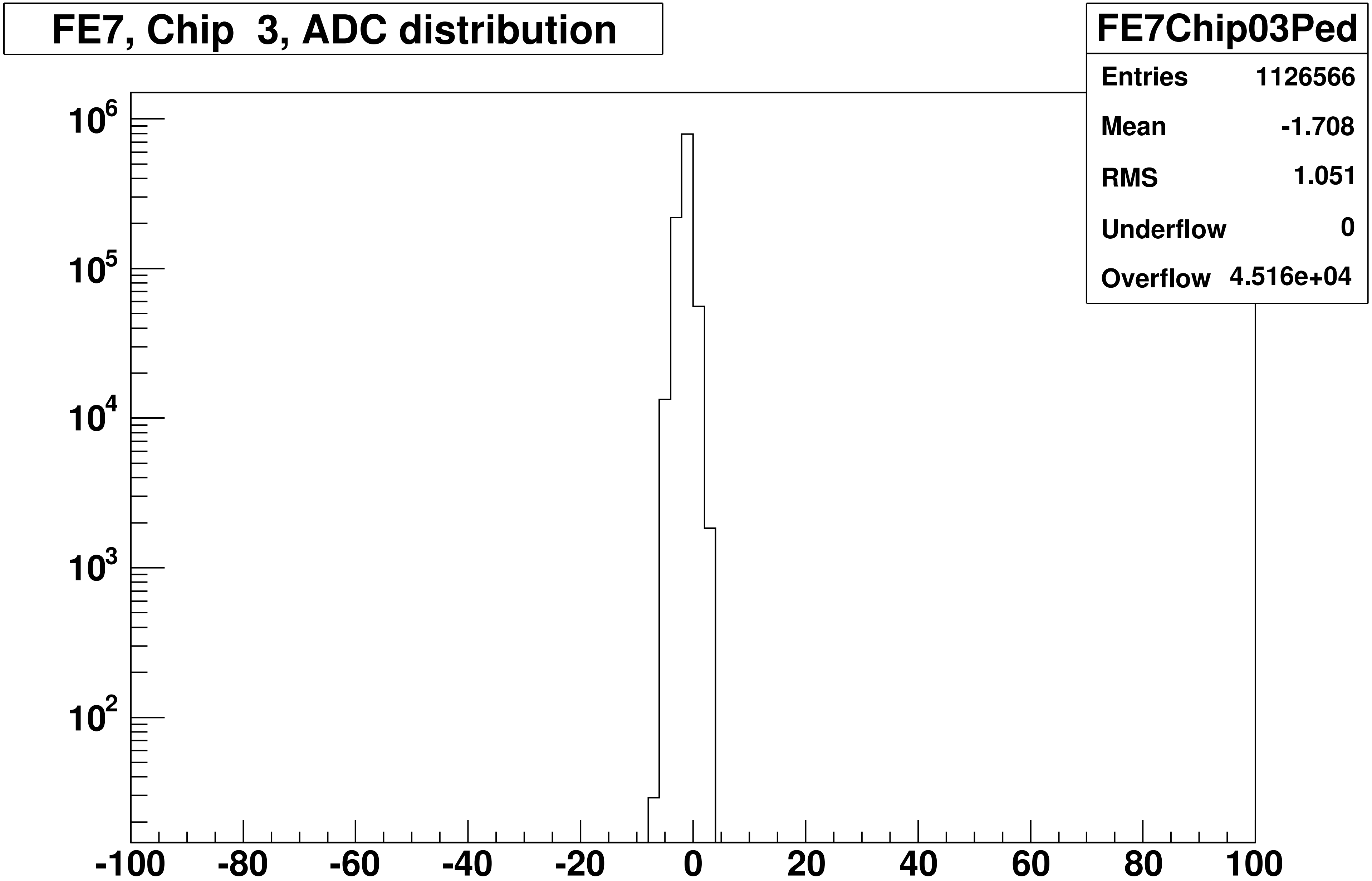
60
80
100

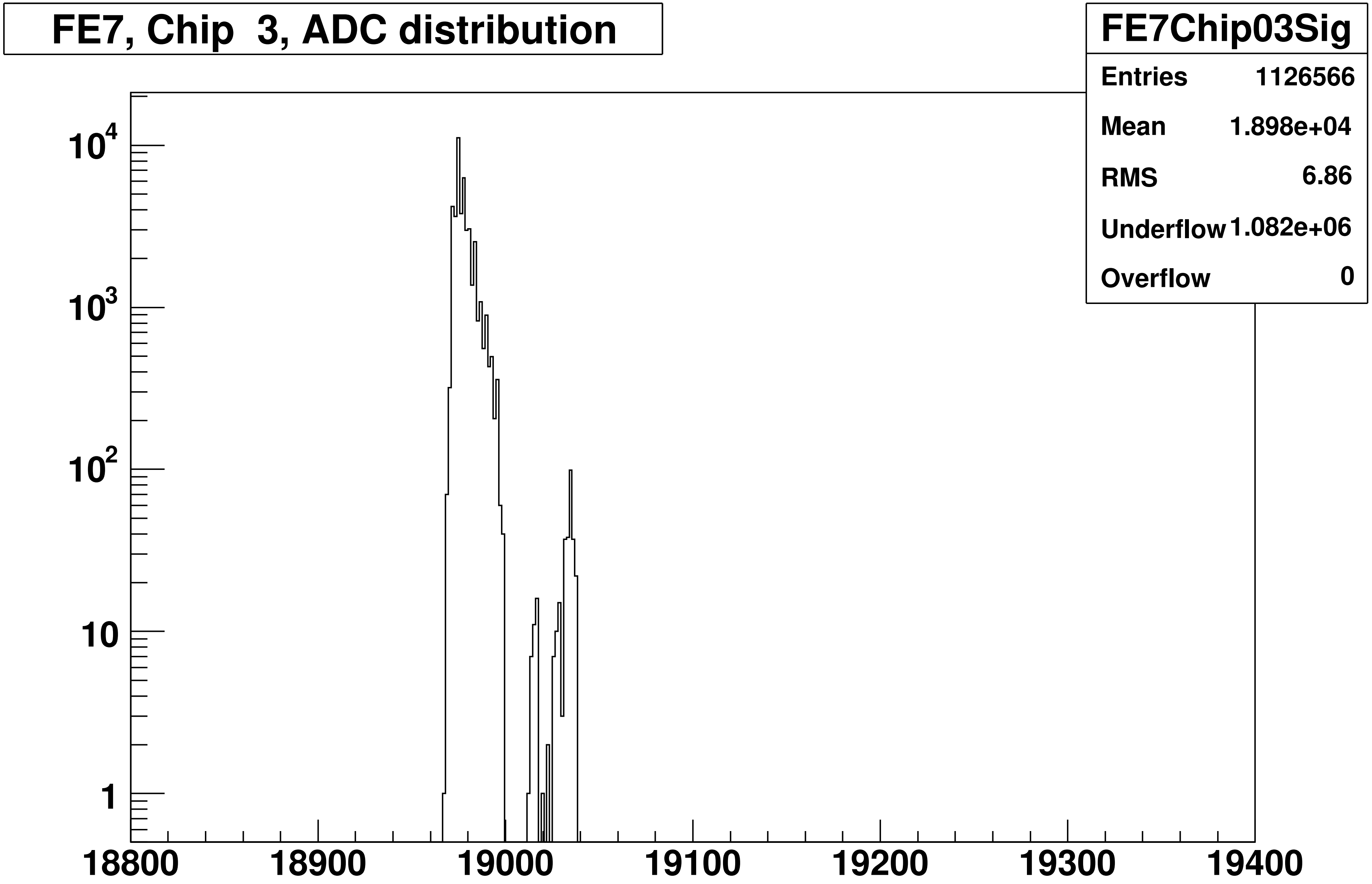


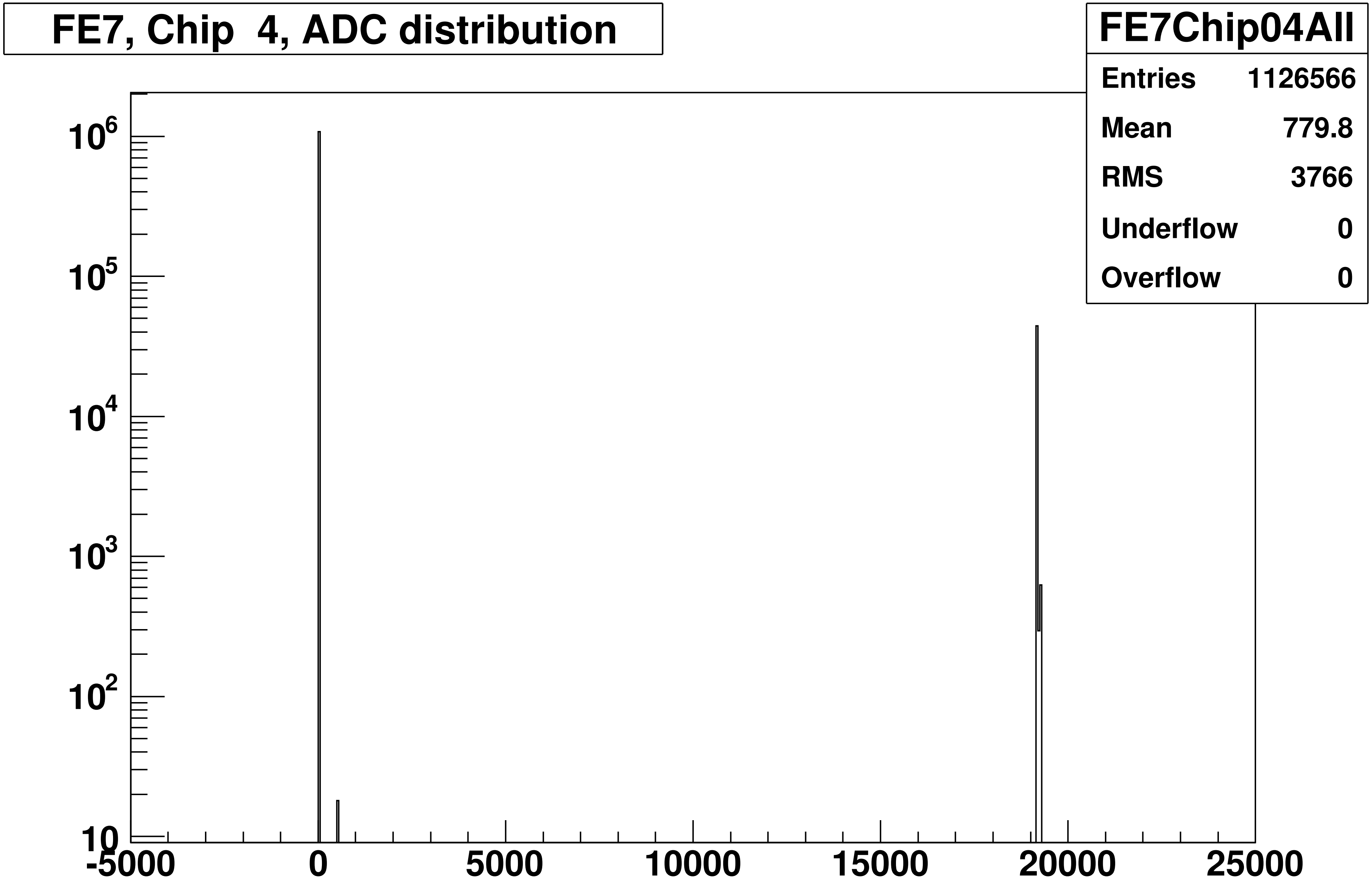
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 1126566
Mean 1.898e+04
RMS 5.166
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

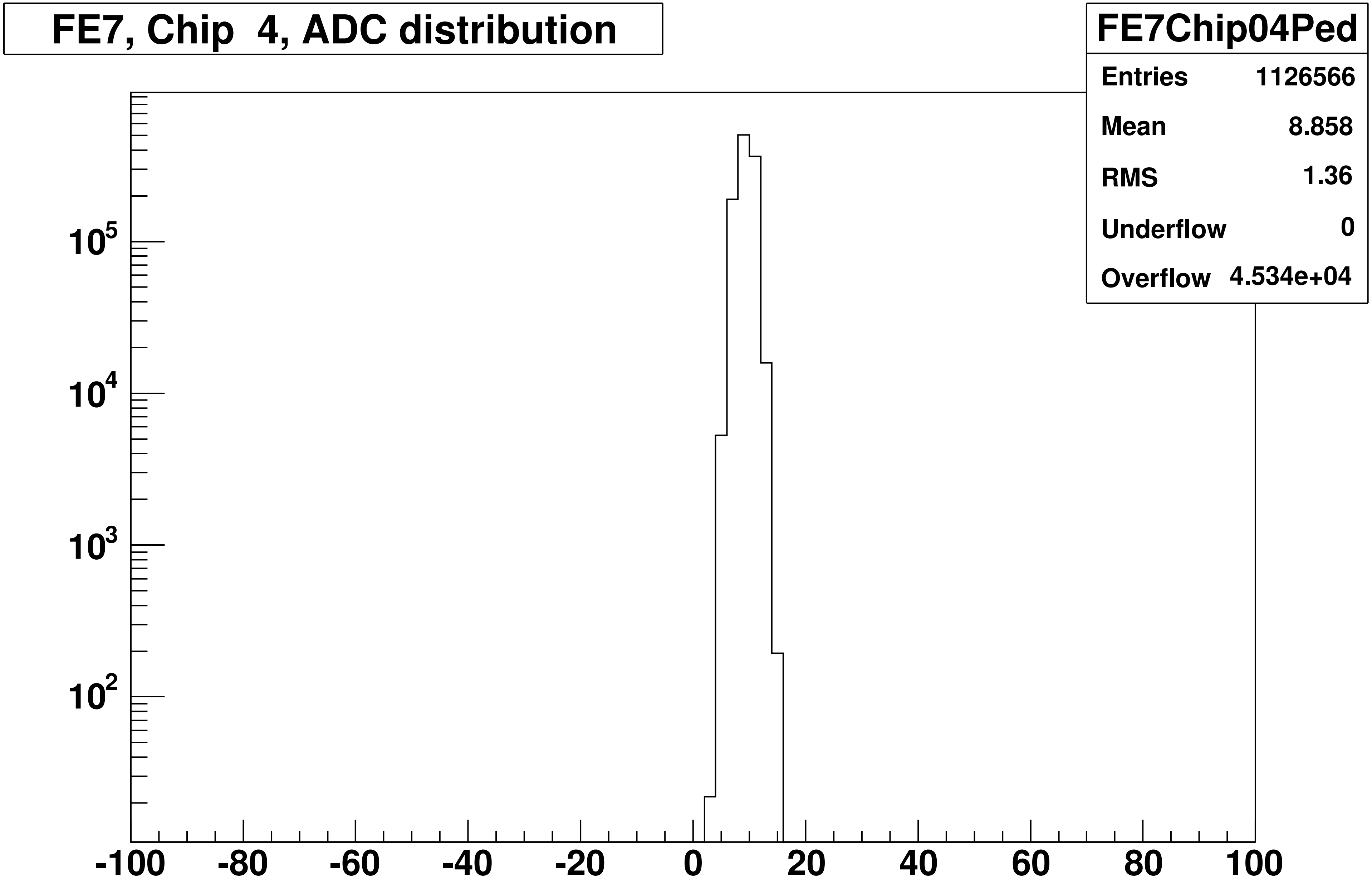


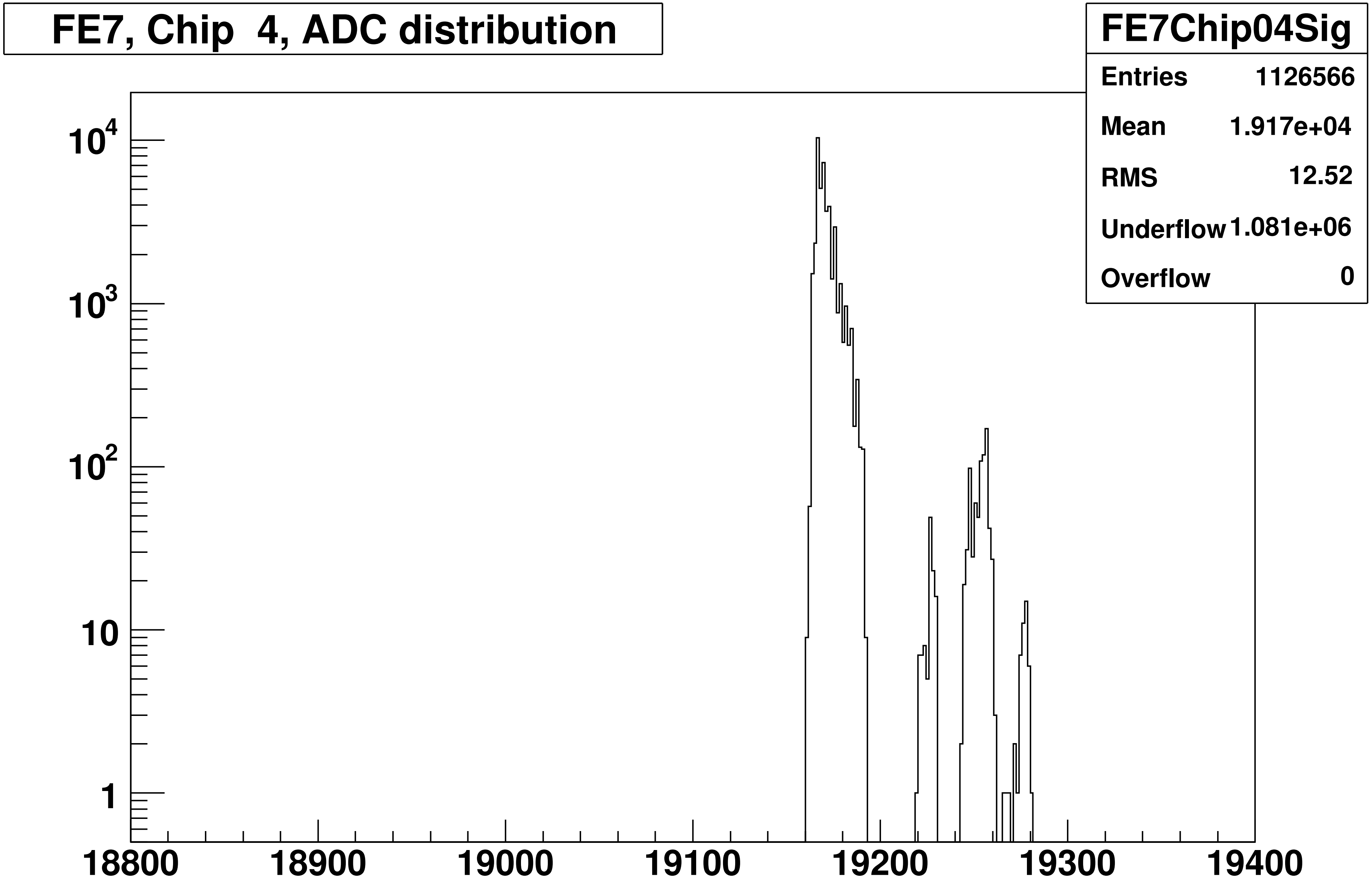
FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 1126566
Mean 752.8
RMS 3707
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 1126566
Mean -1.708
RMS 1.051
Underflow 0
Overflow 4.516e+04
10^6
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

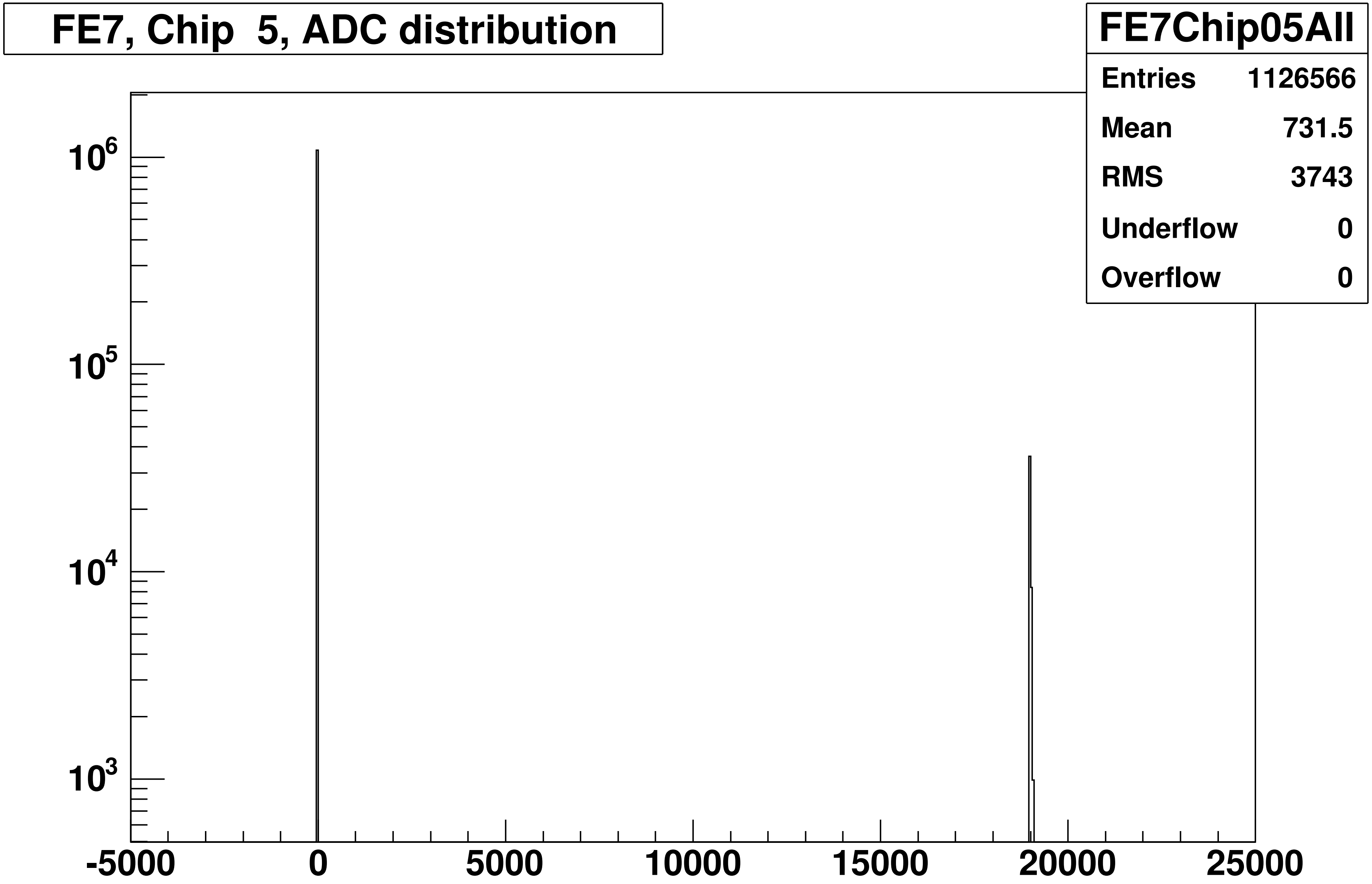

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 1126566
Mean 1.898e+04
RMS 6.86
Underflow 1.082e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 1126566
Mean 779.8
RMS 3766
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

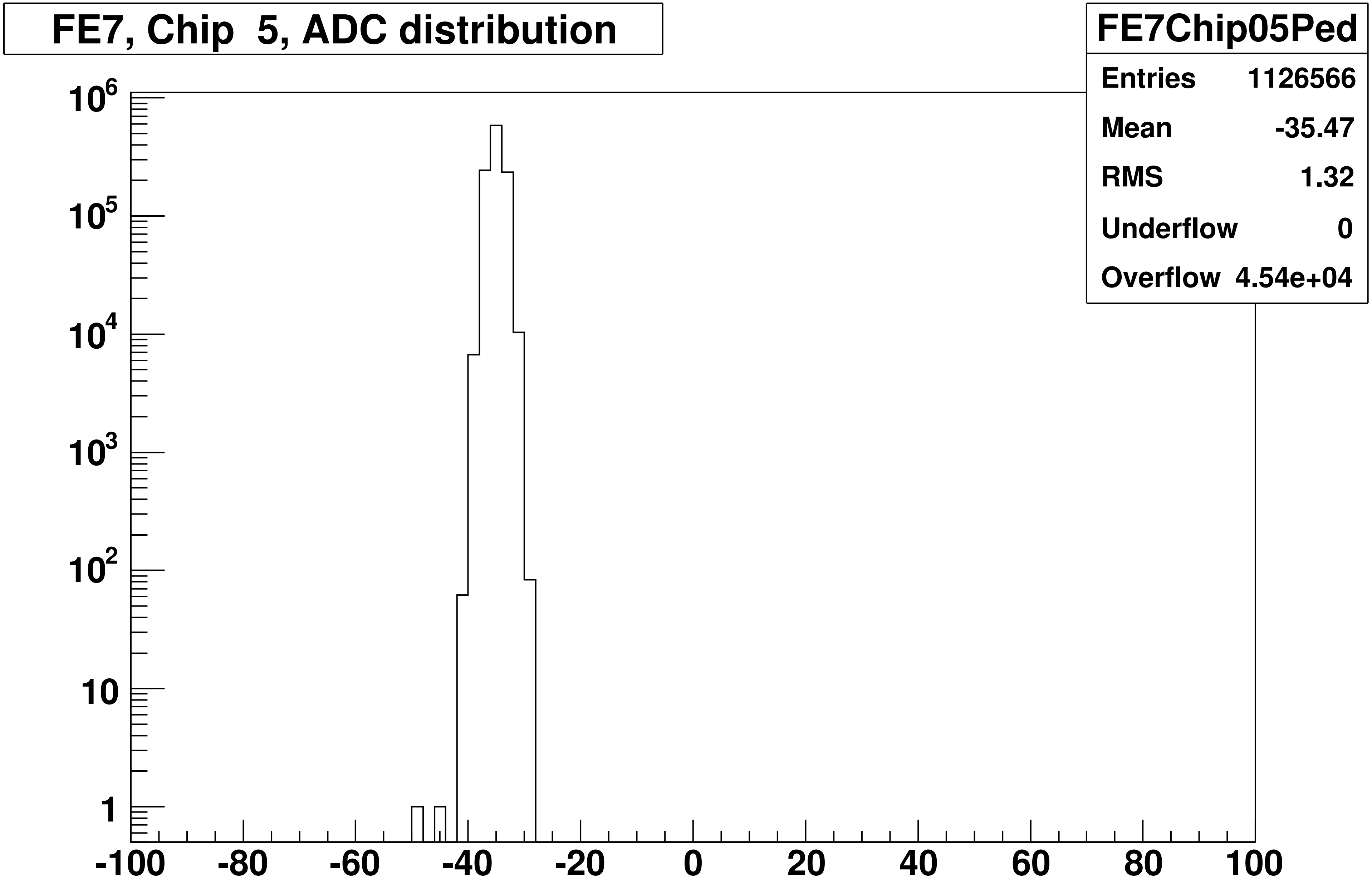

FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 1126566
Mean 8.858
RMS 1.36
Underflow 0
Overflow 4.534e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

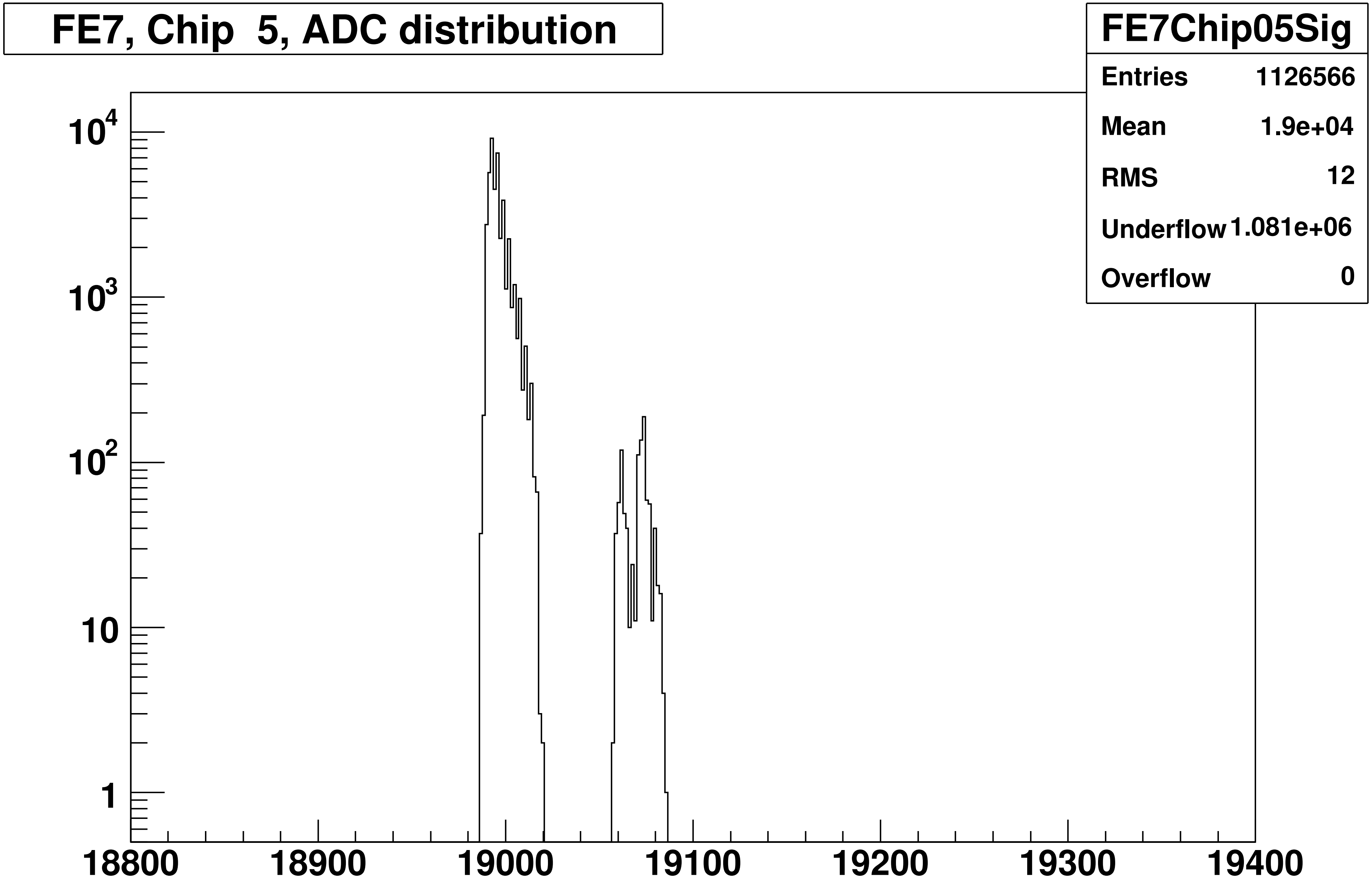


FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 1126566
Mean 1.917e+04
RMS 12.52
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

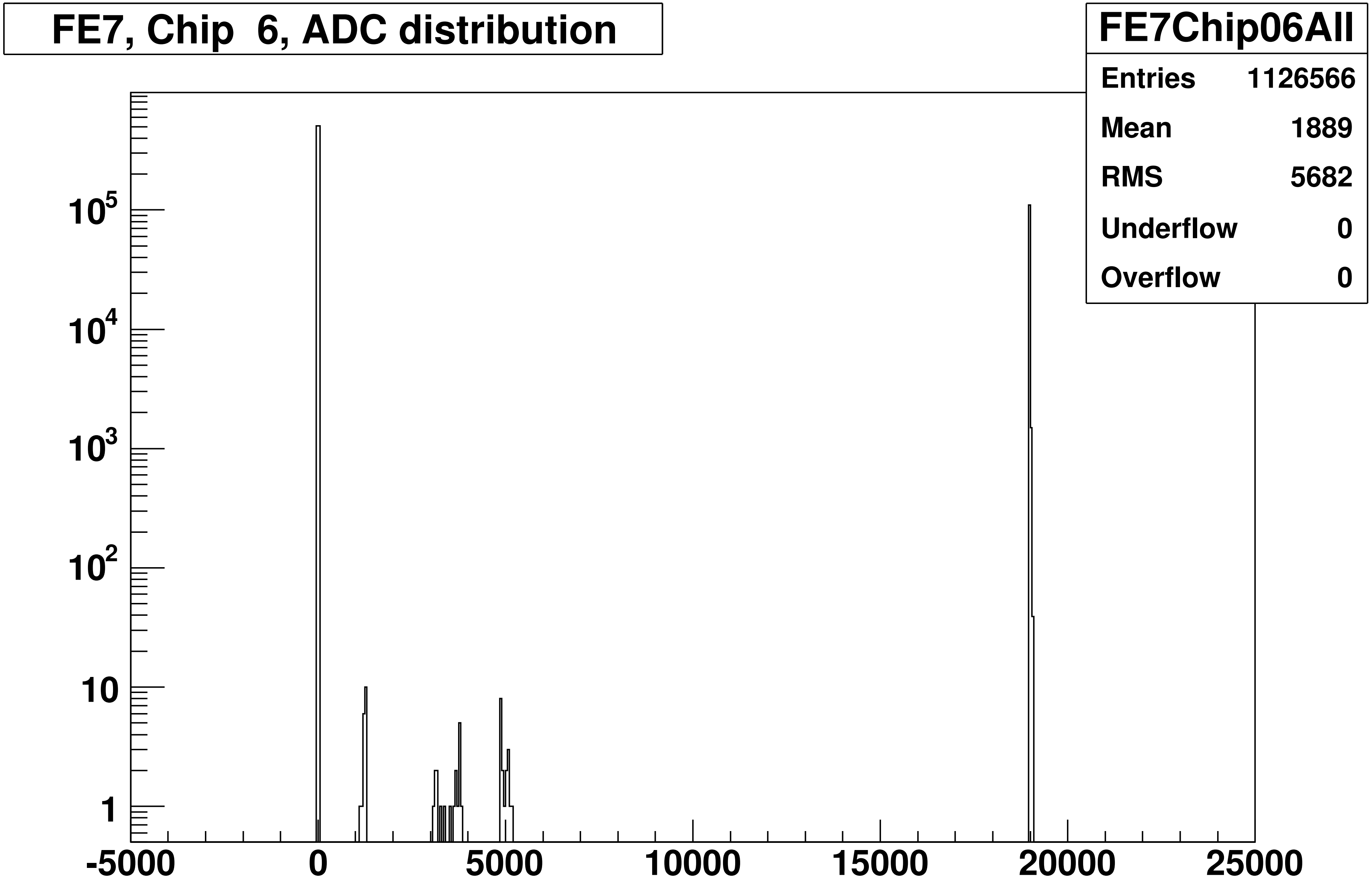


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 1126566
Mean 731.5
RMS 3743
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000

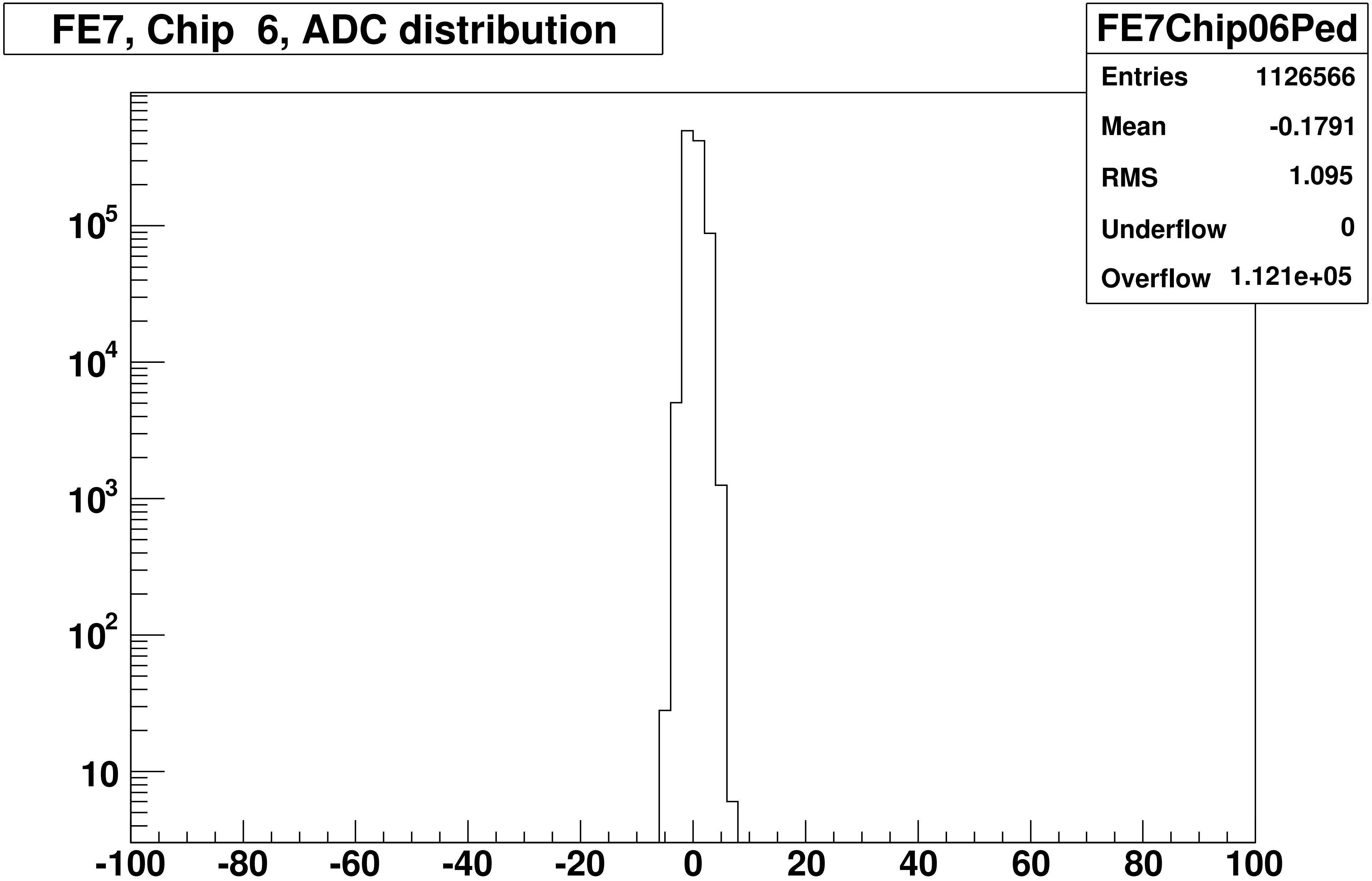

FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 1126566
Mean -35.47
RMS 1.32
Underflow 0
Overflow 4.54e+04
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 1126566
Mean 1.9e+04
RMS 12
Underflow 1.081e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 1126566
Mean 1889
RMS 5682
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 1126566
Mean -0.1791
RMS 1.095
Underflow 0
Overflow 1.121e+05
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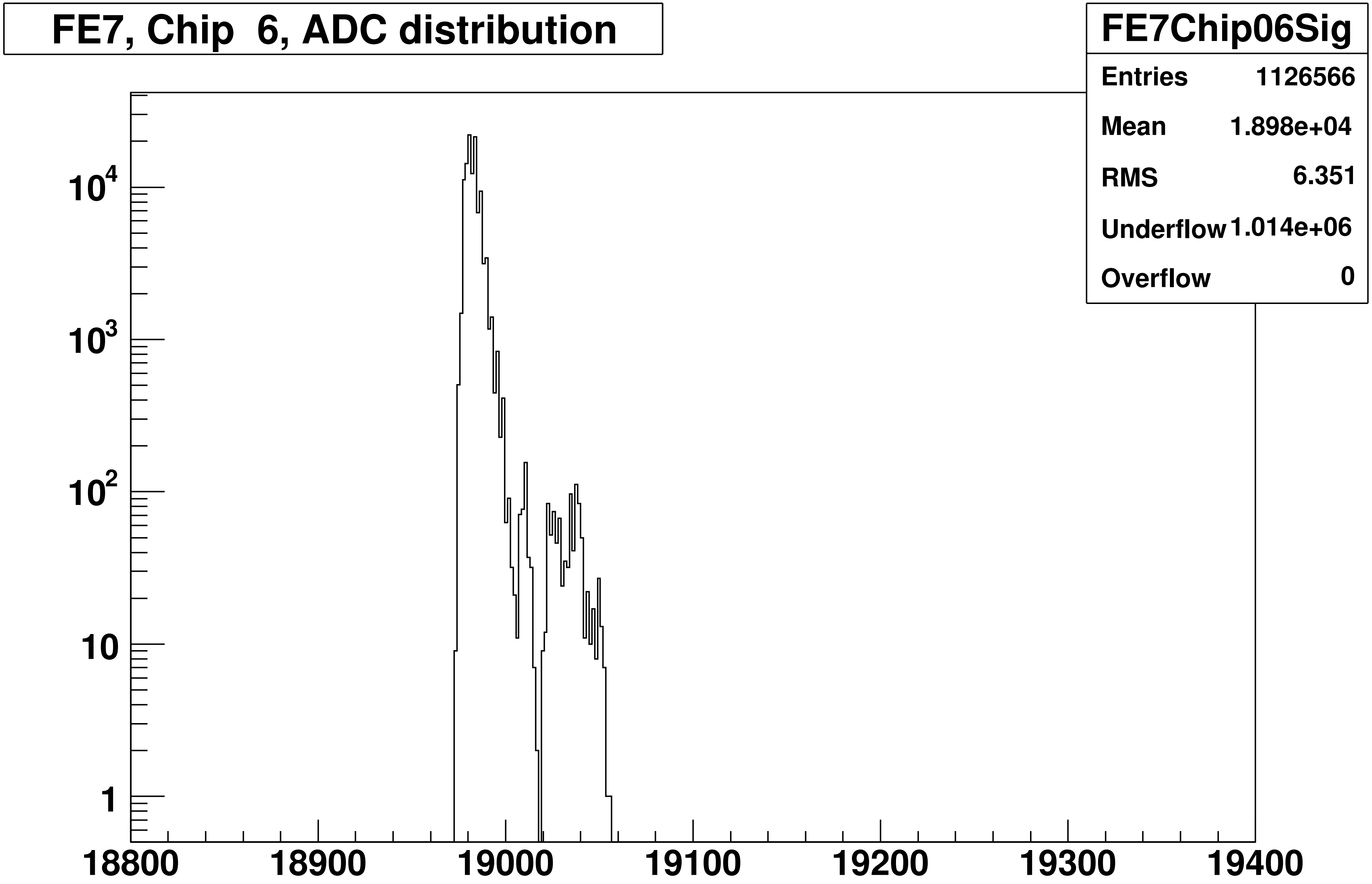
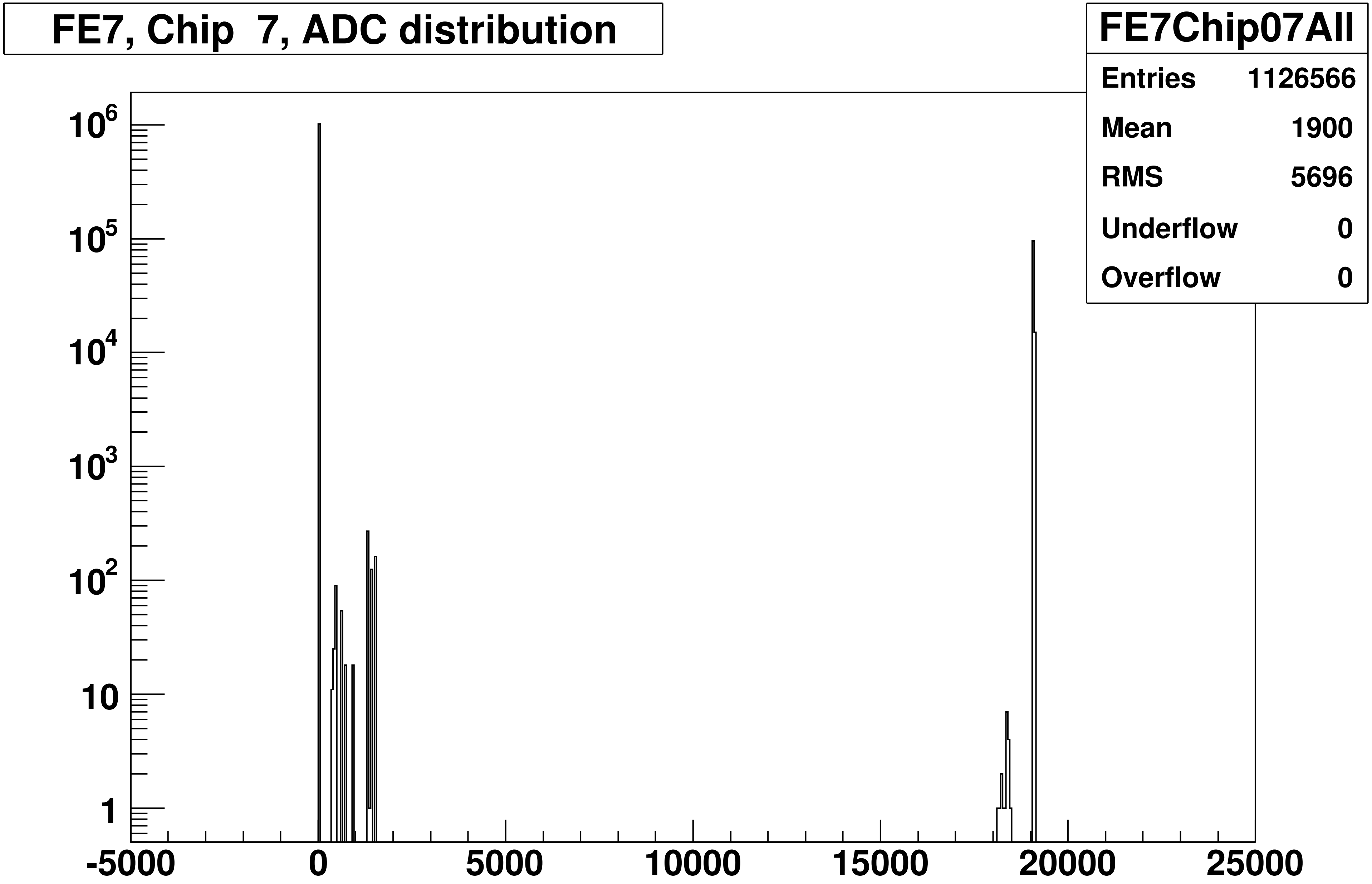
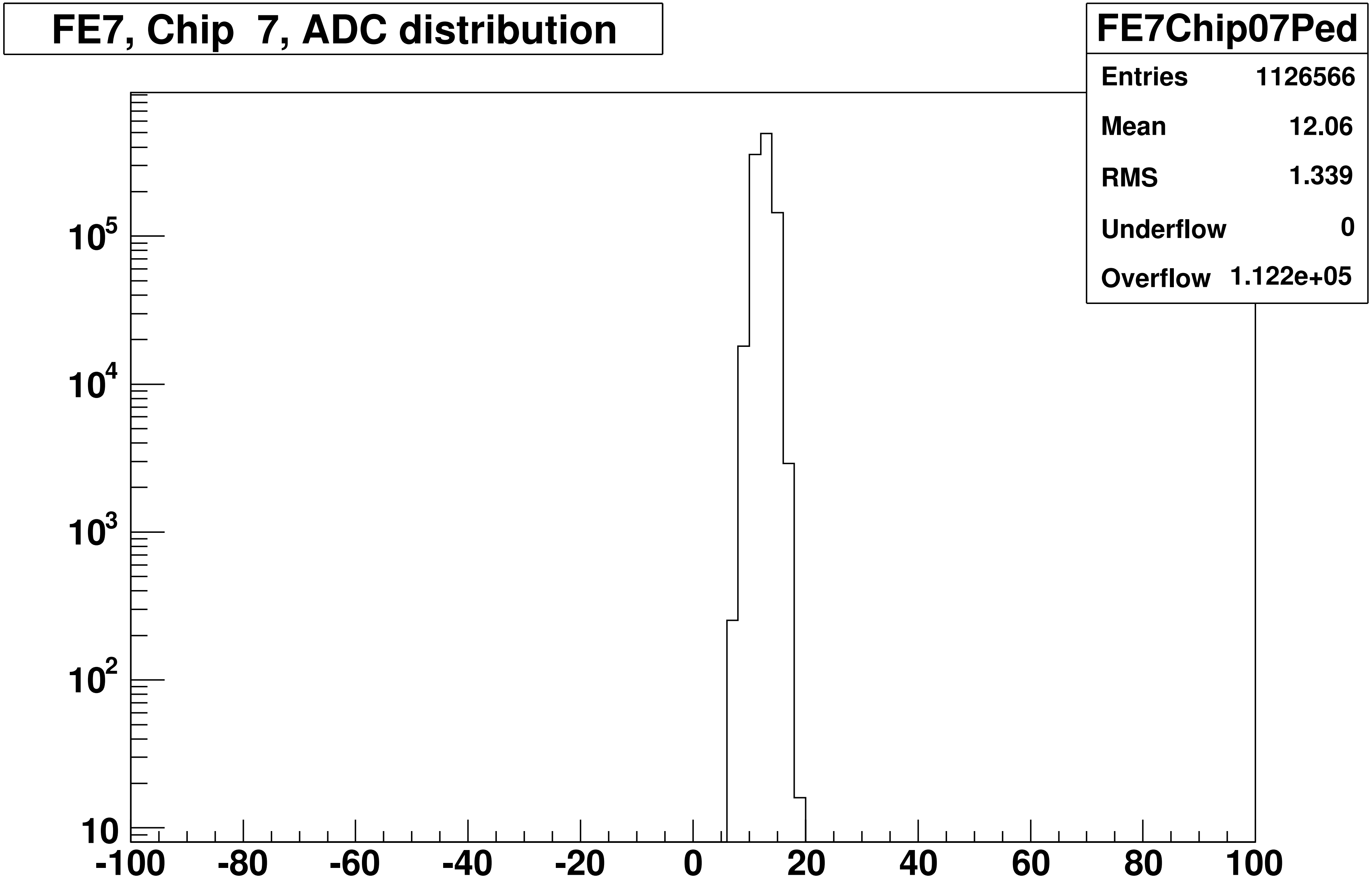
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100

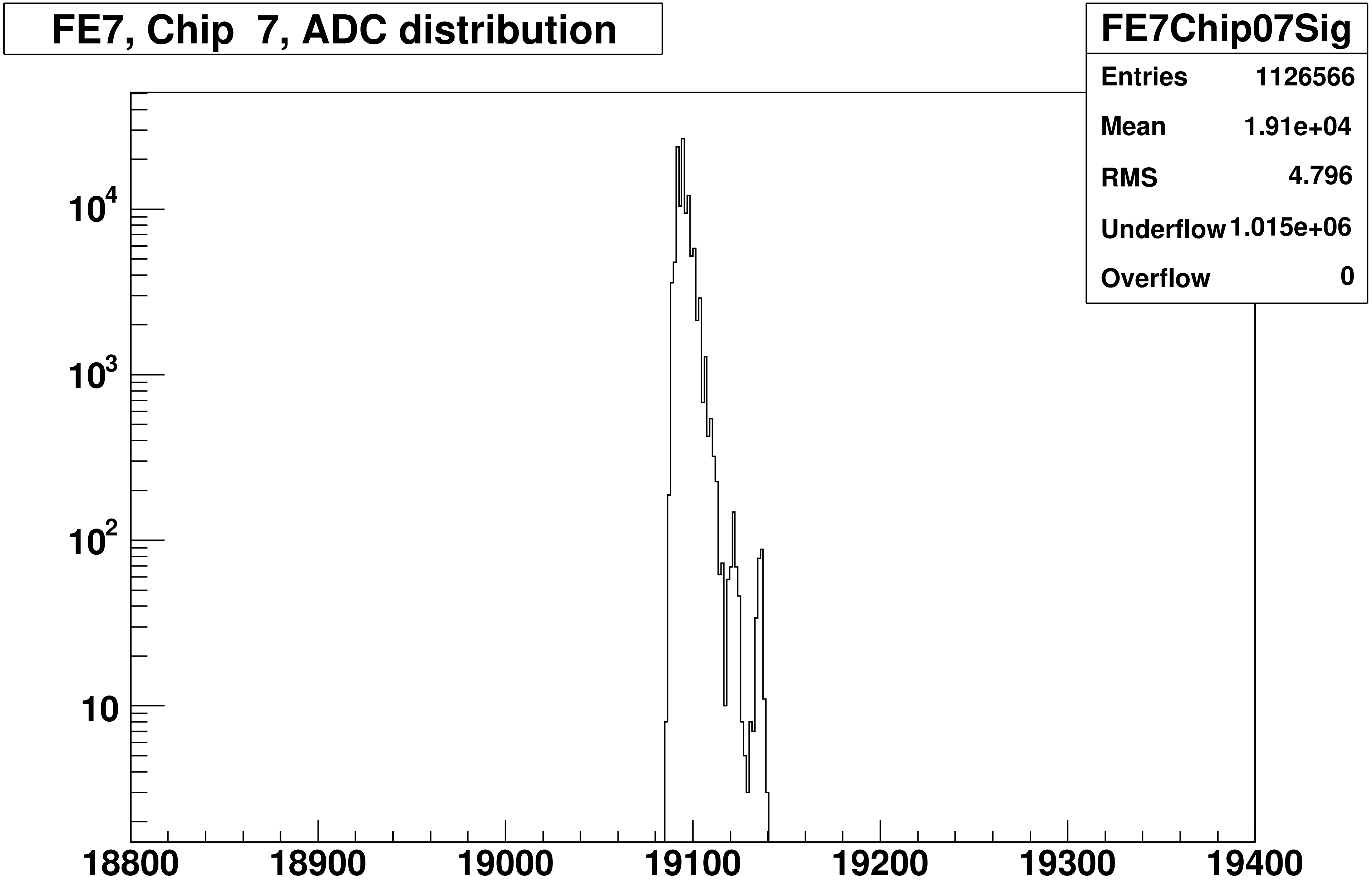

FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 1126566
Mean 1.898e+04
RMS 6.351
Underflow 1.014e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



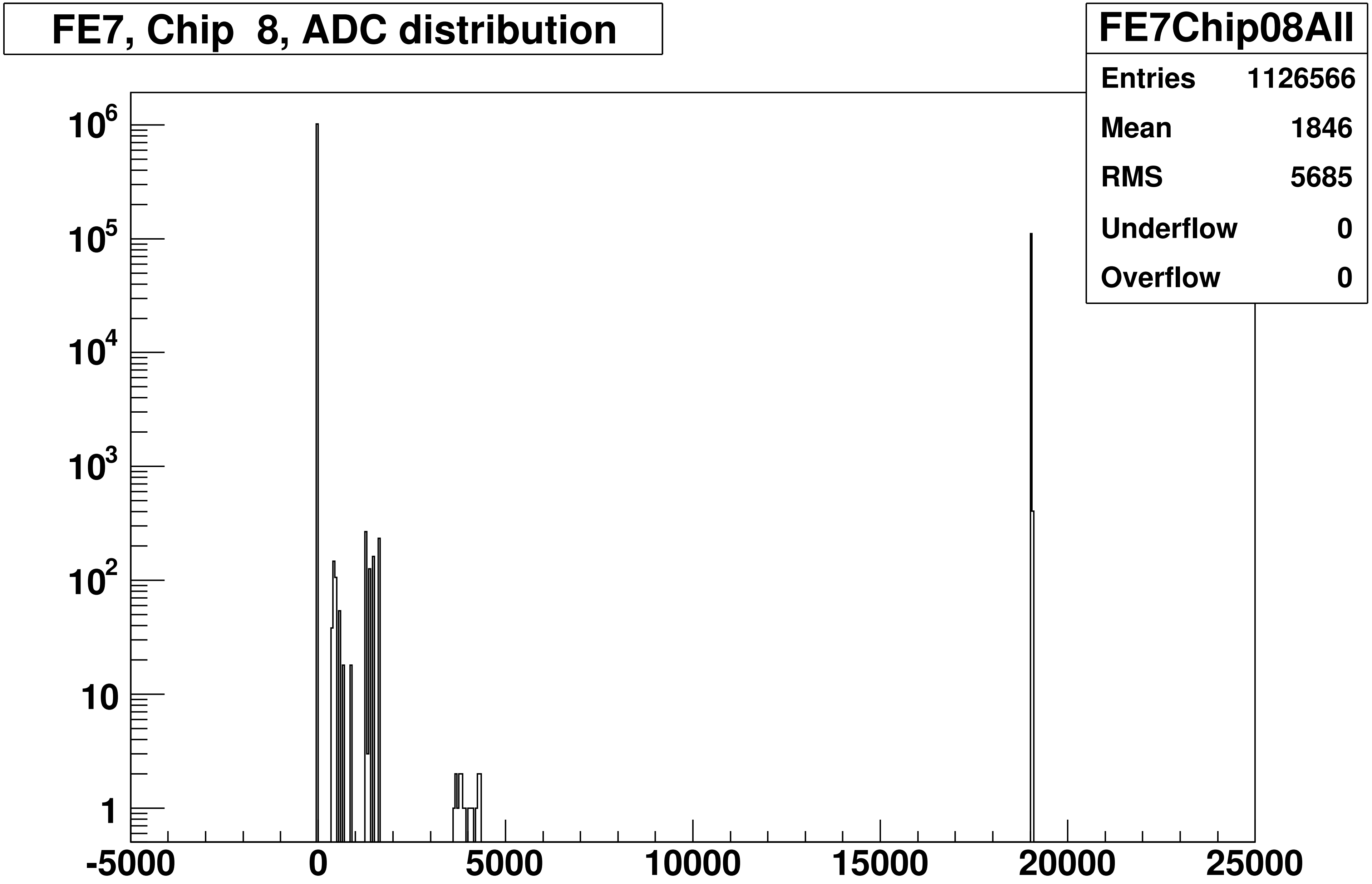
FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 1126566
Mean 1900
RMS 5696
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

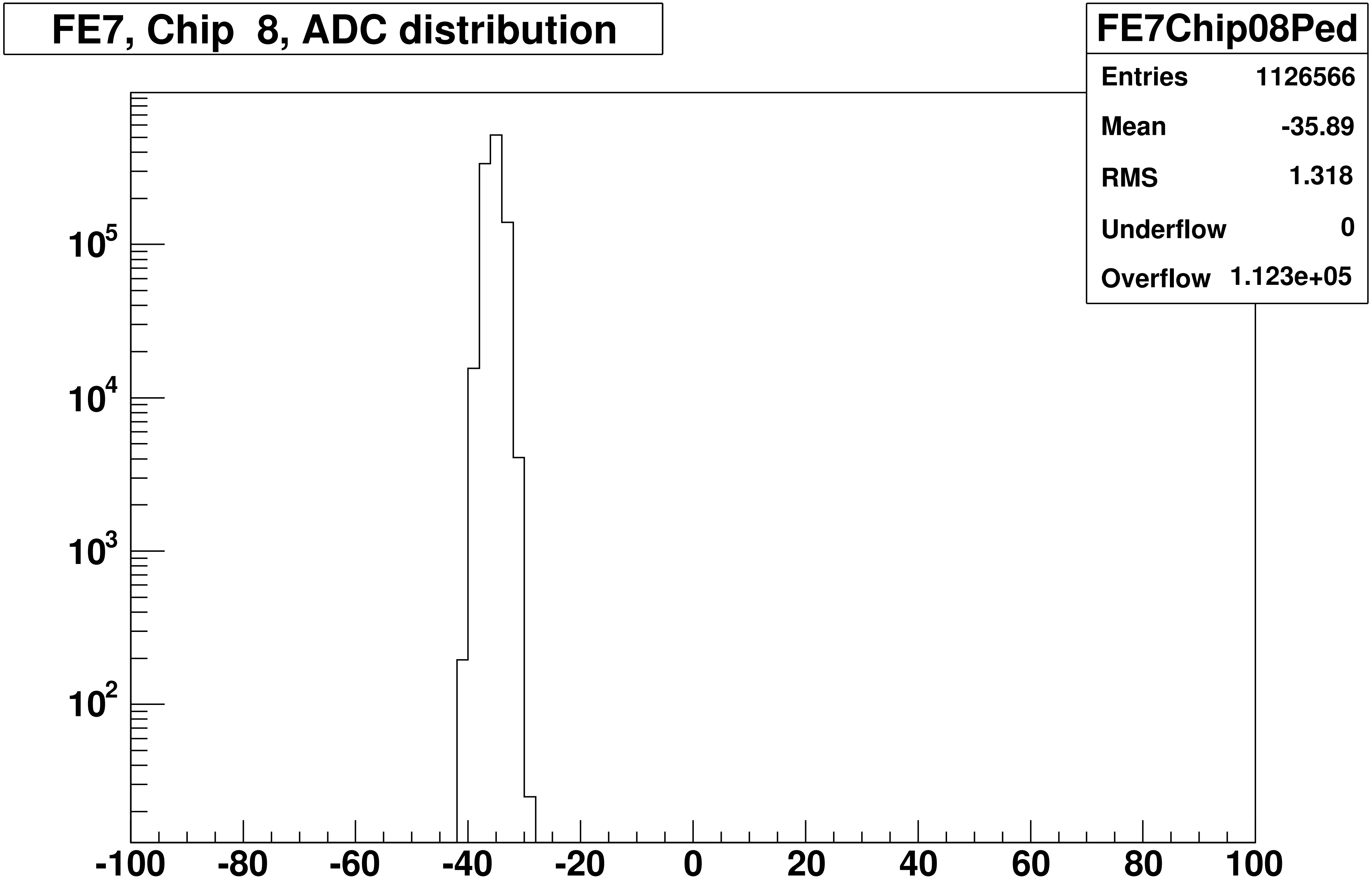


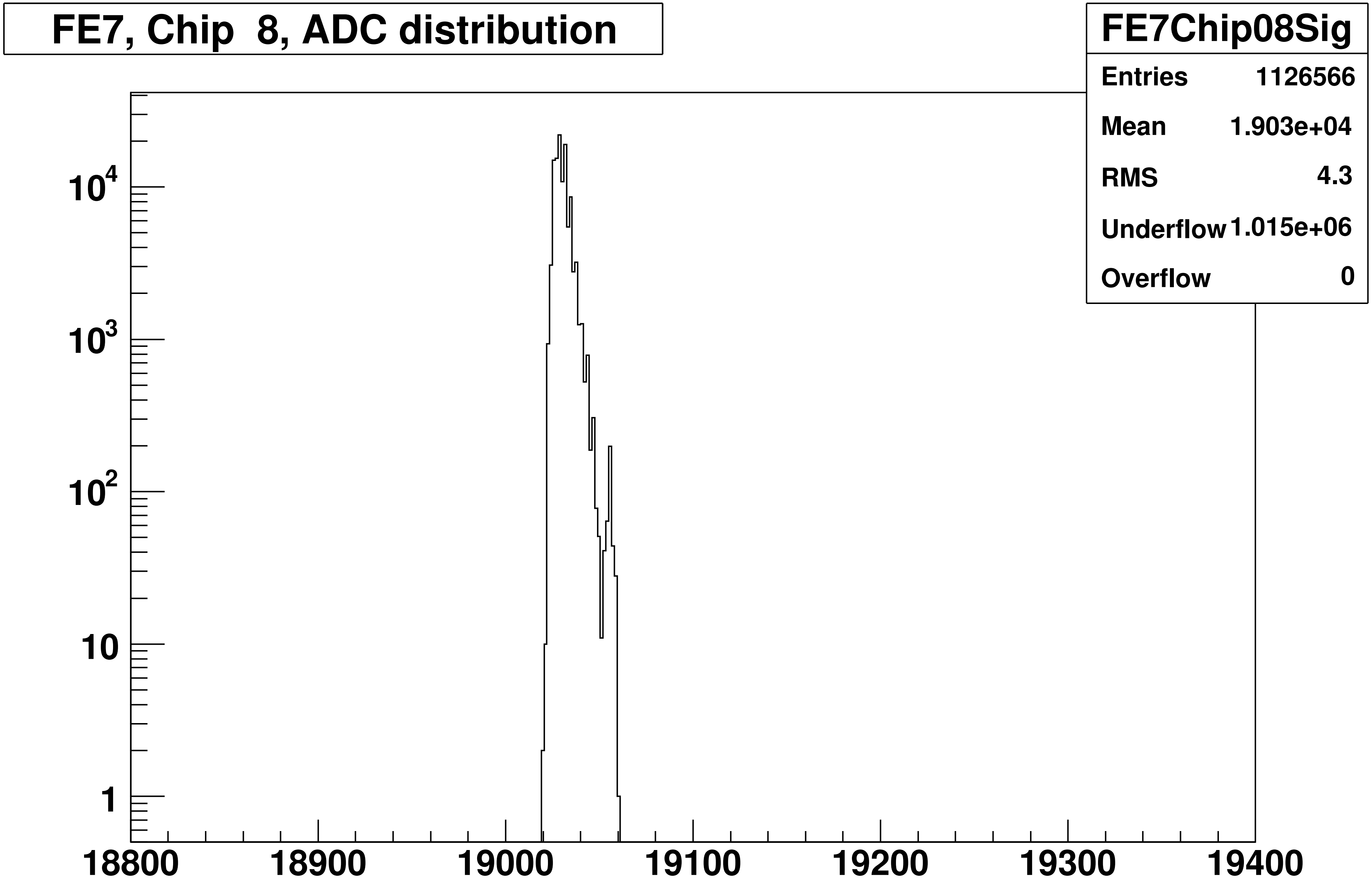
FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 1126566
Mean 12.06
RMS 1.339
Underflow 0
Overflow 1.122e+05
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100



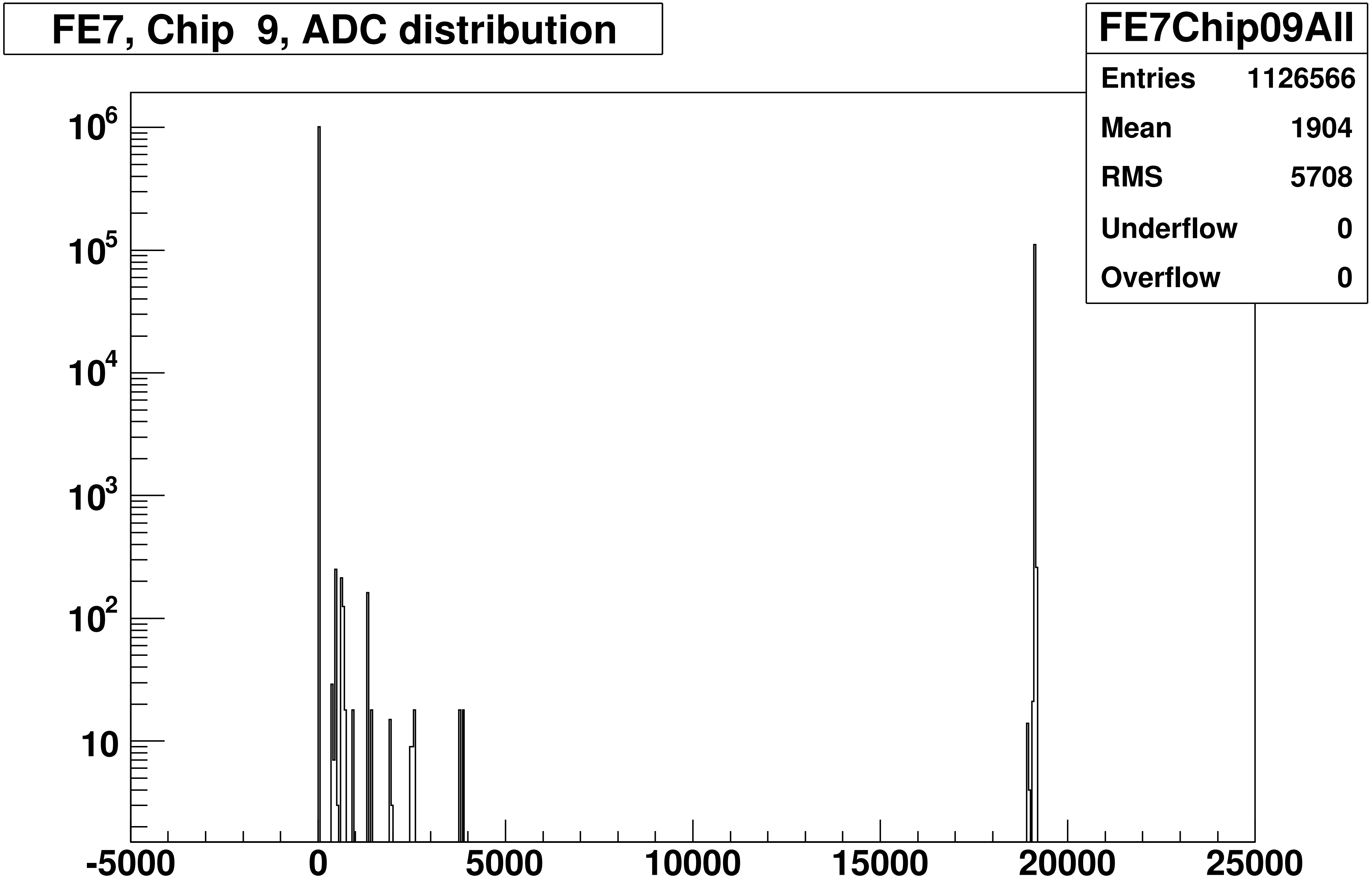
FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 1126566
Mean 1.91e+04
RMS 4.796
Underflow 1.015e+06
Overflow 0
10^4
10^3
10^2
10
18800
18900
19000
19100
19200
19300
19400

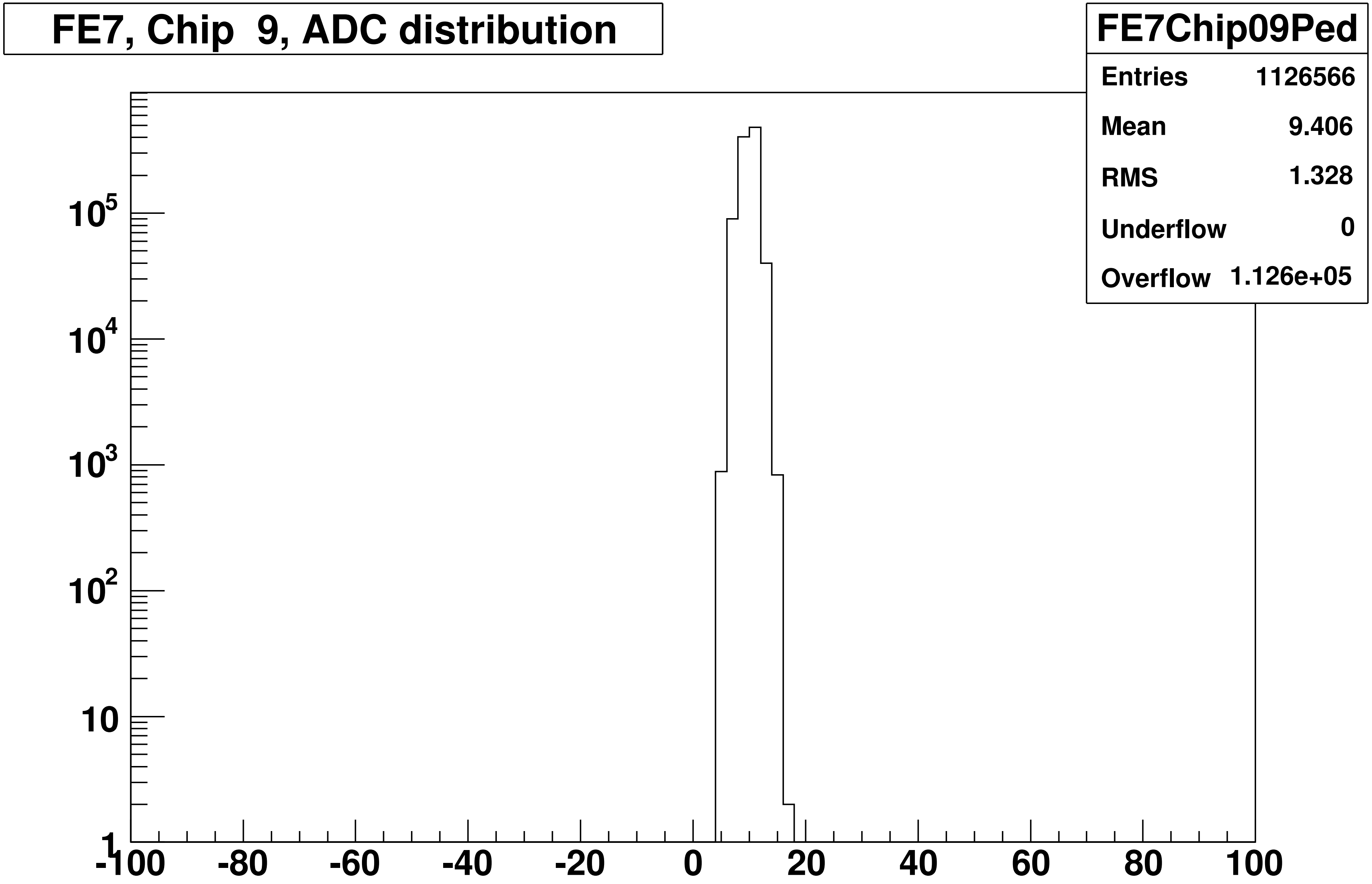

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 1126566
Mean 1846
RMS 5685
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 1126566
Mean -35.89
RMS 1.318
Underflow 0
Overflow 1.123e+05
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100

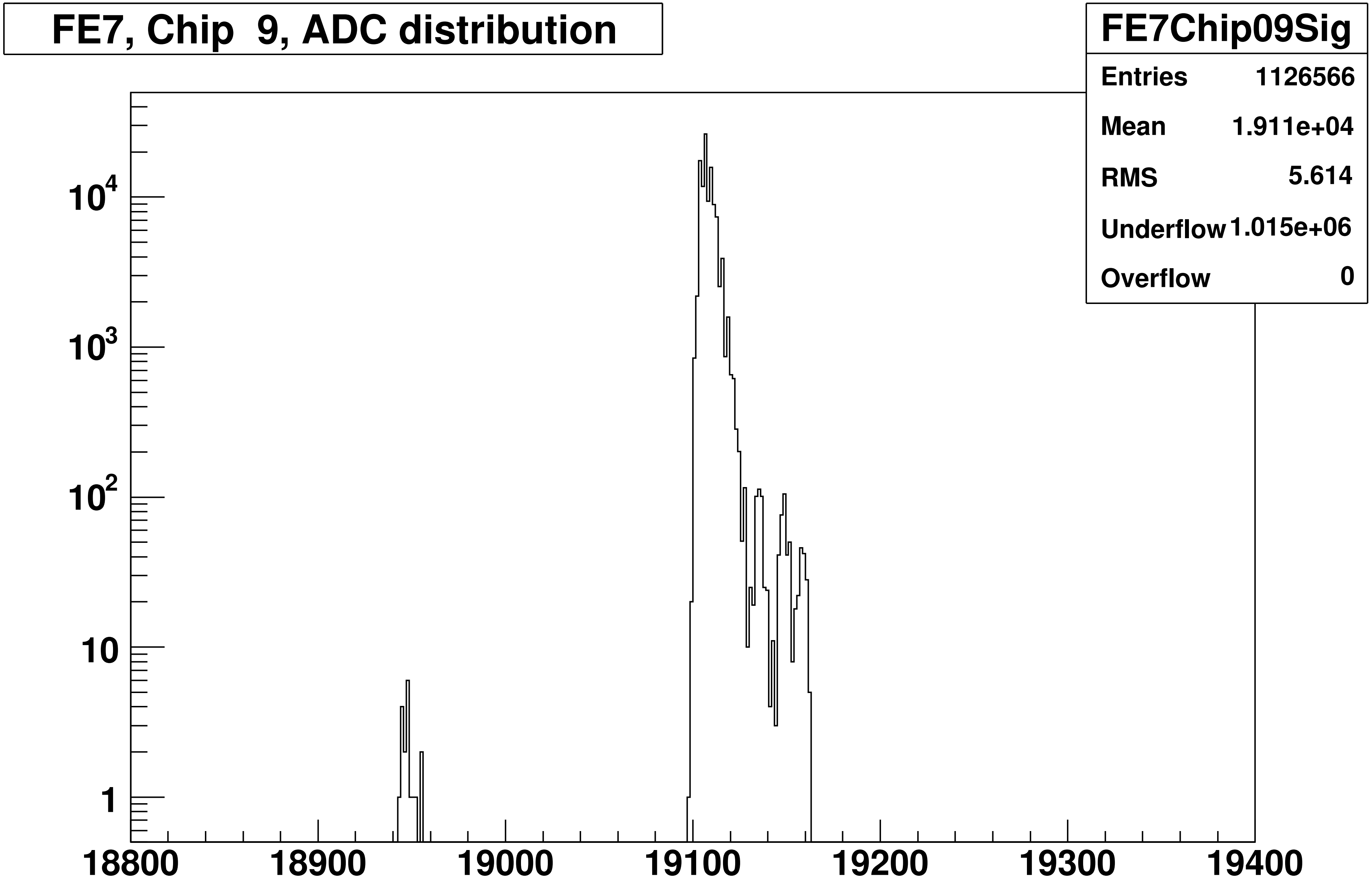


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 1126566
Mean 1.903e+04
RMS 4.3
Underflow 1.015e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

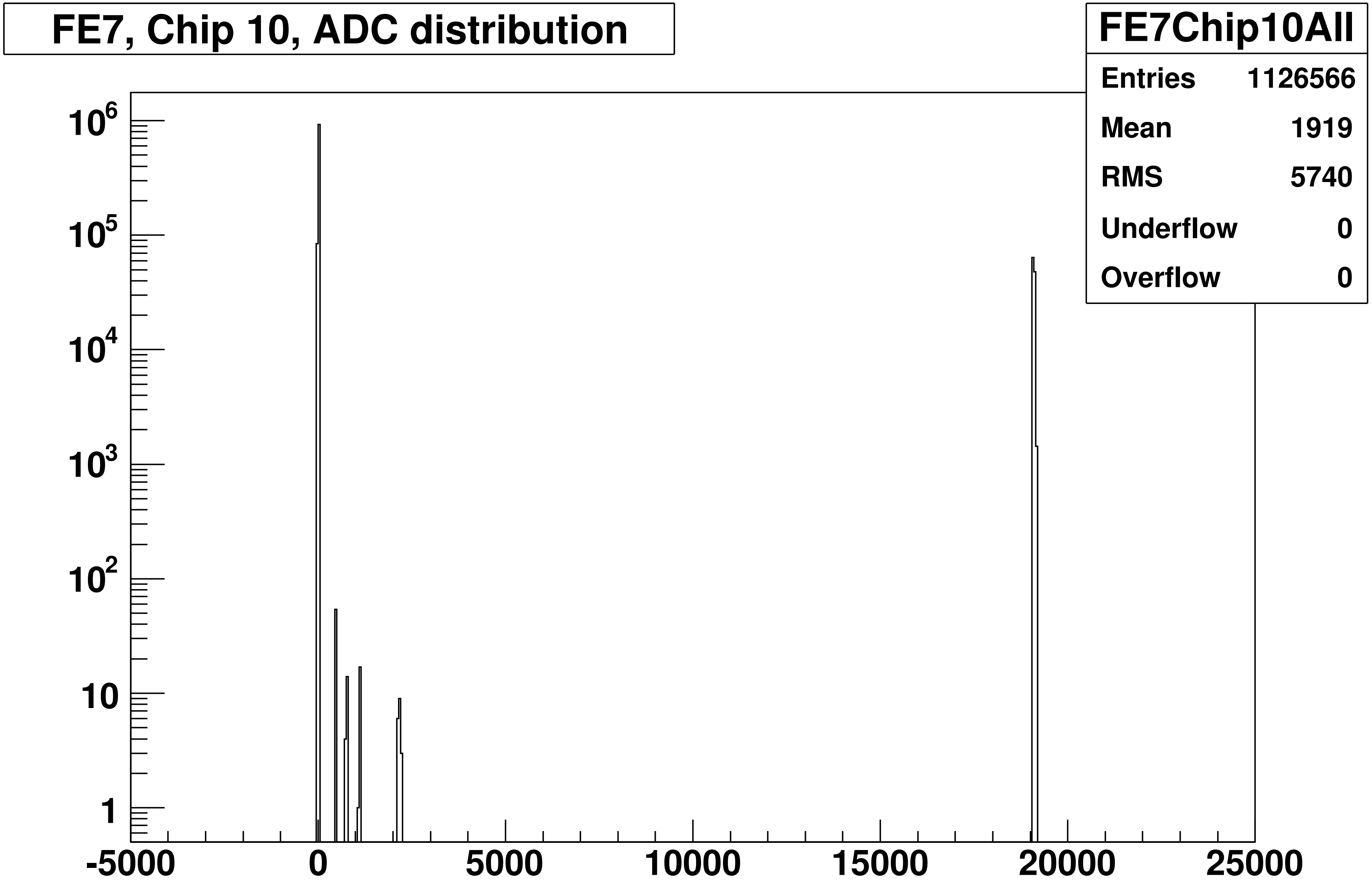

FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 1126566
Mean 1904
RMS 5708
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



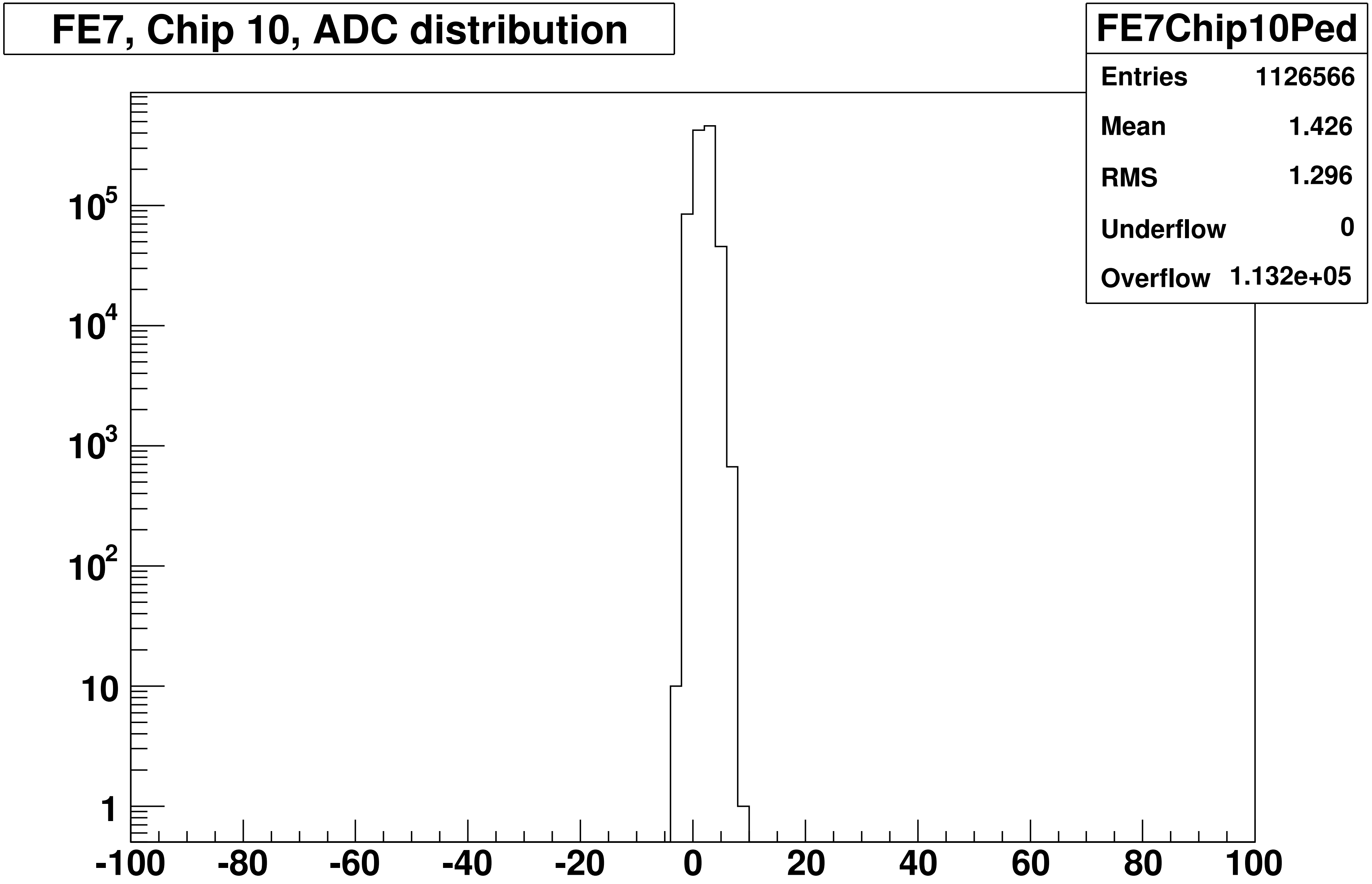
FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 1126566
Mean 9.406
RMS 1.328
Underflow 0
Overflow 1.126e+05
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

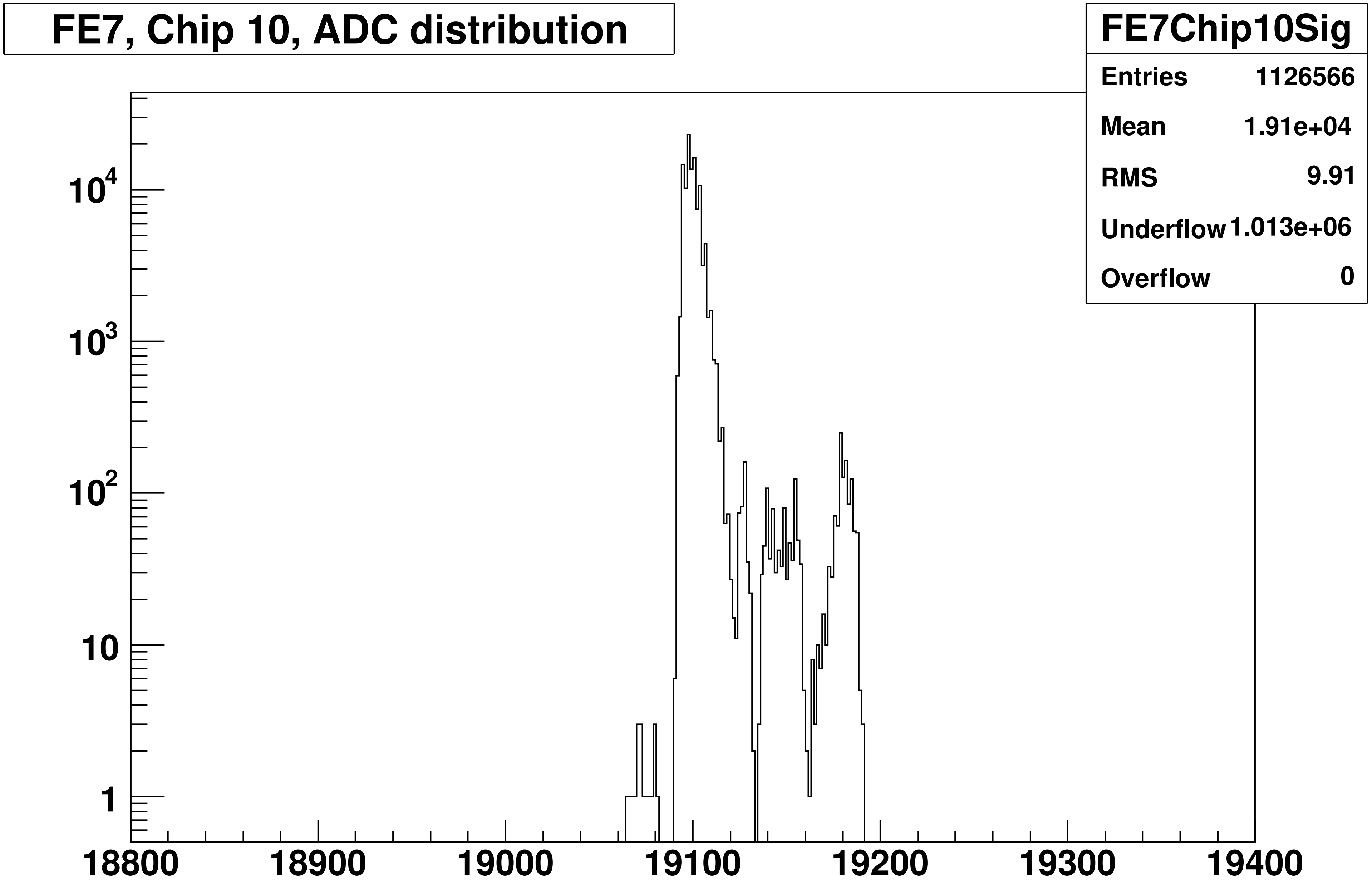


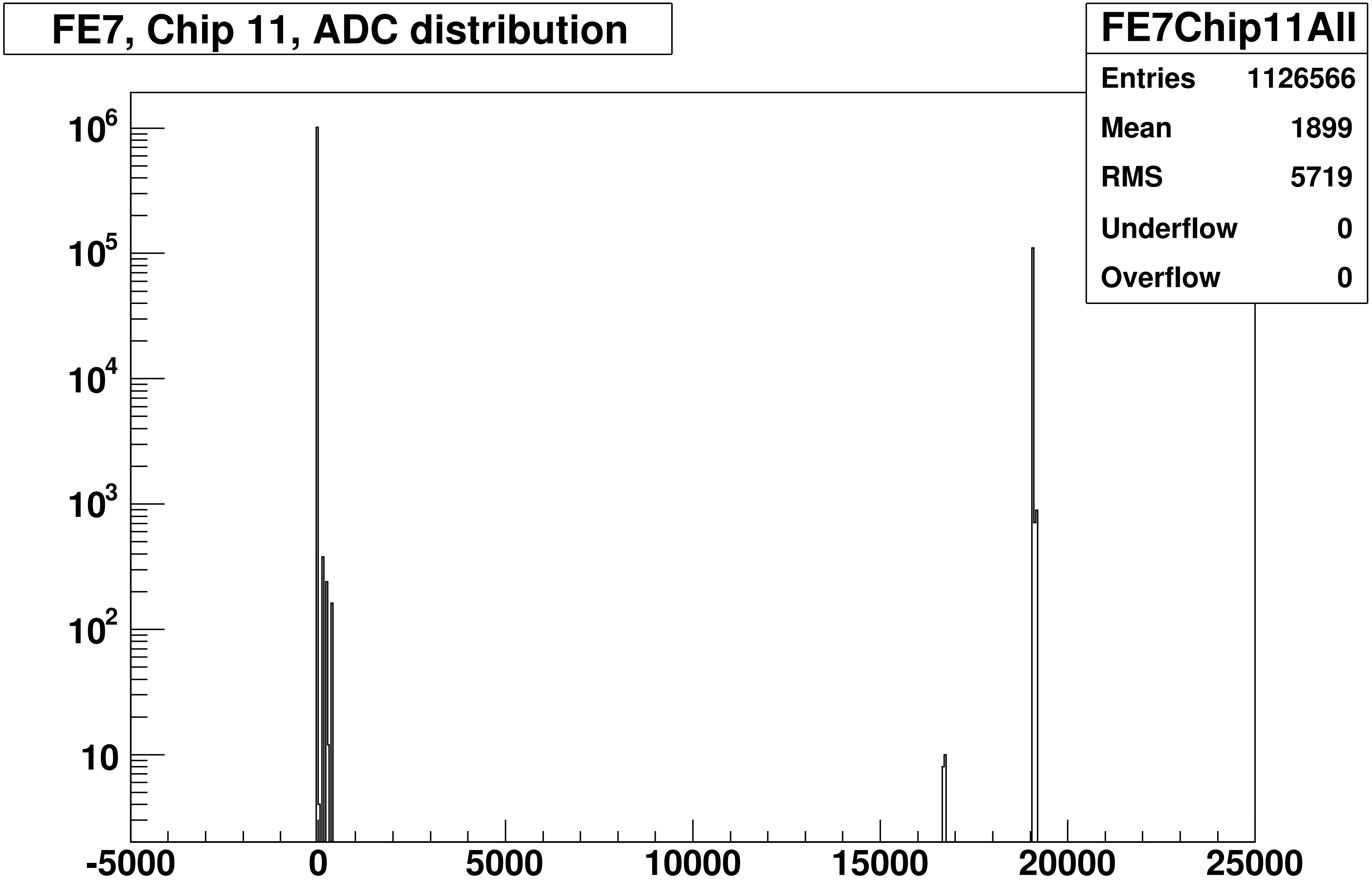
FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 1126566
Mean 1.911e+04
RMS 5.614
Underflow 1.015e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



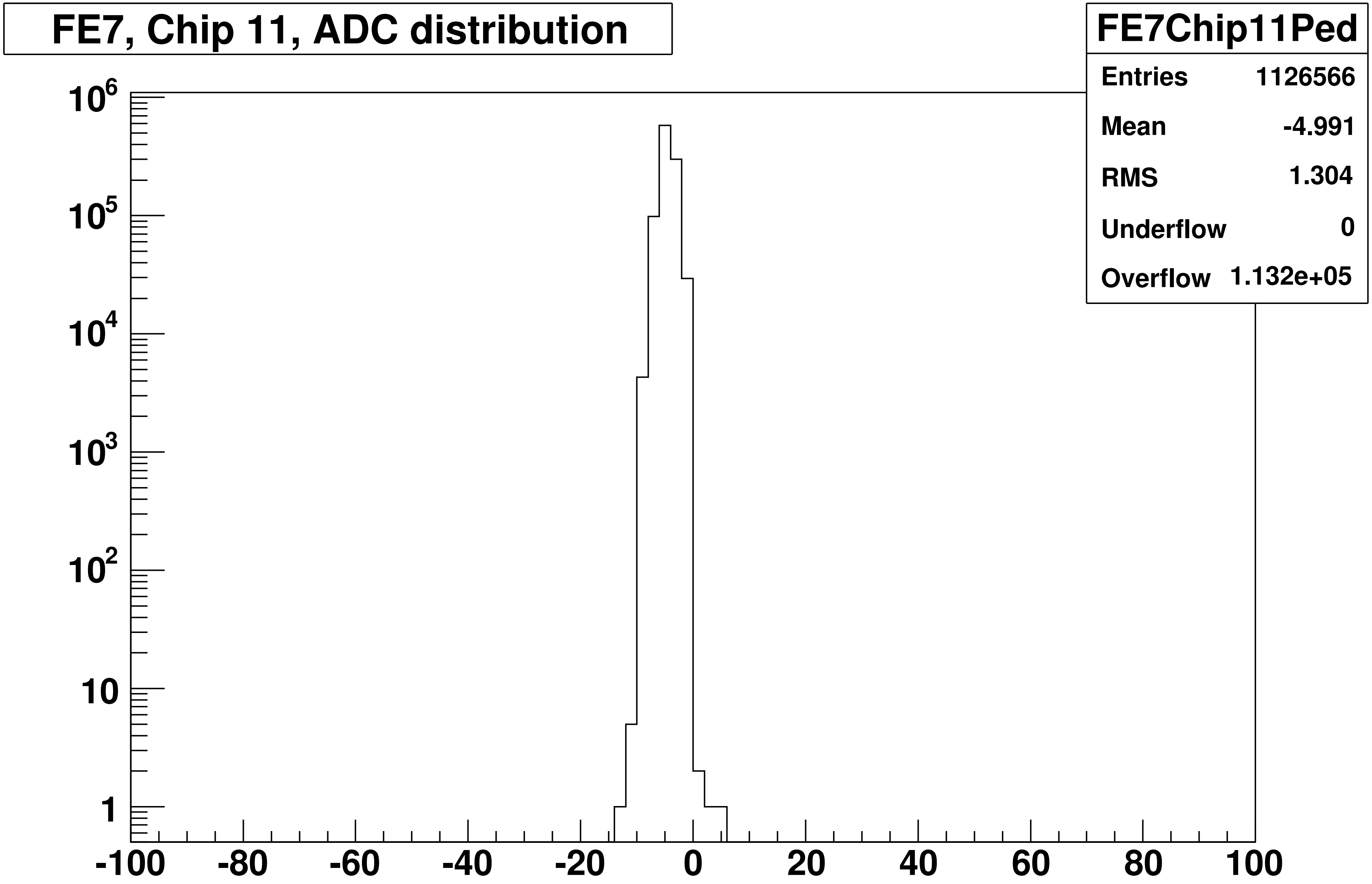
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 1126566
Mean 1919
RMS 5740
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

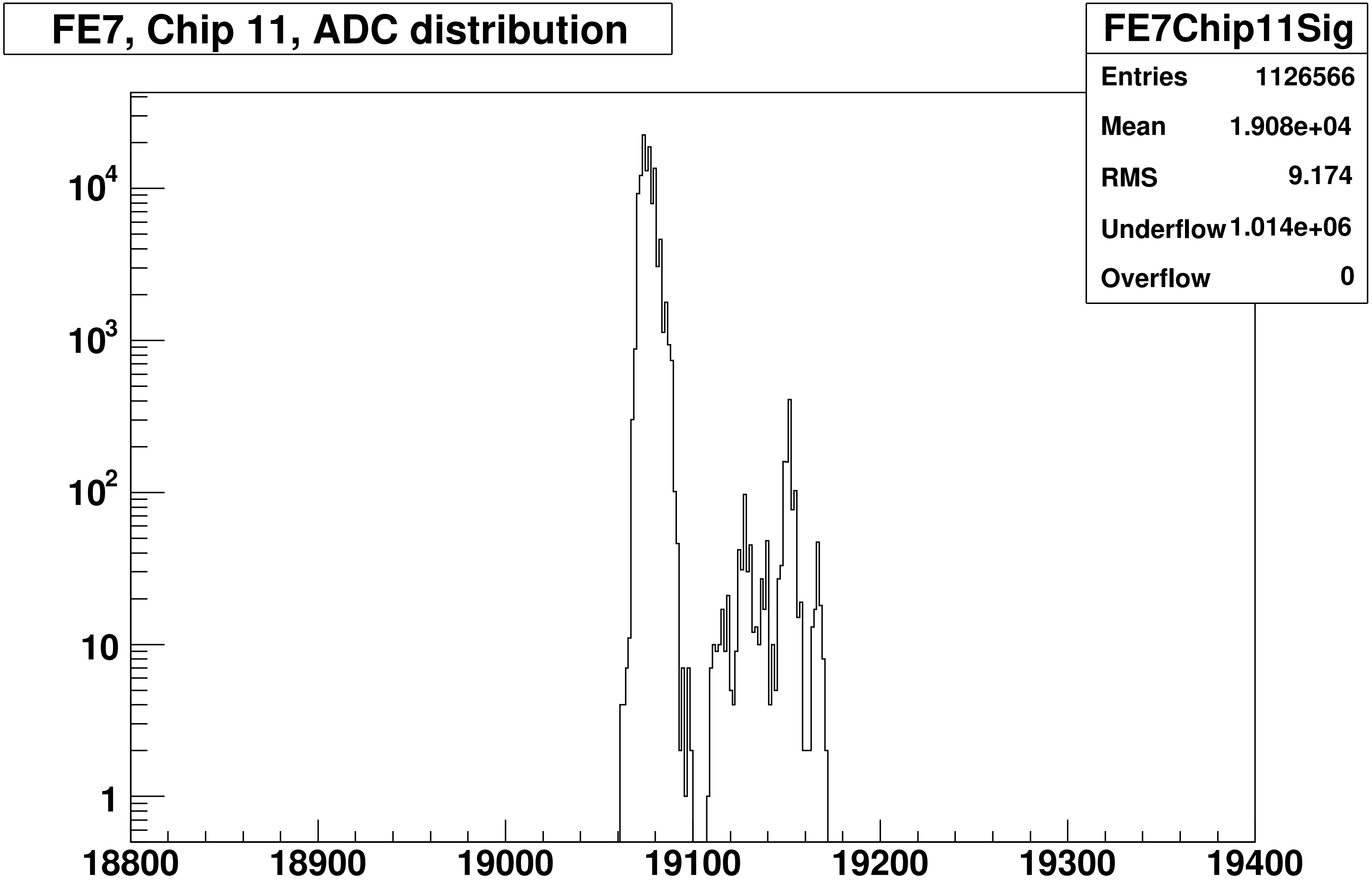

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 1126566
Mean 1.426
RMS 1.296
Underflow 0
Overflow 1.132e+05
10^5
10^4
10^3
10^2
10
1
-100 -80 -60 -40 -20 0 20 40 60 80 100

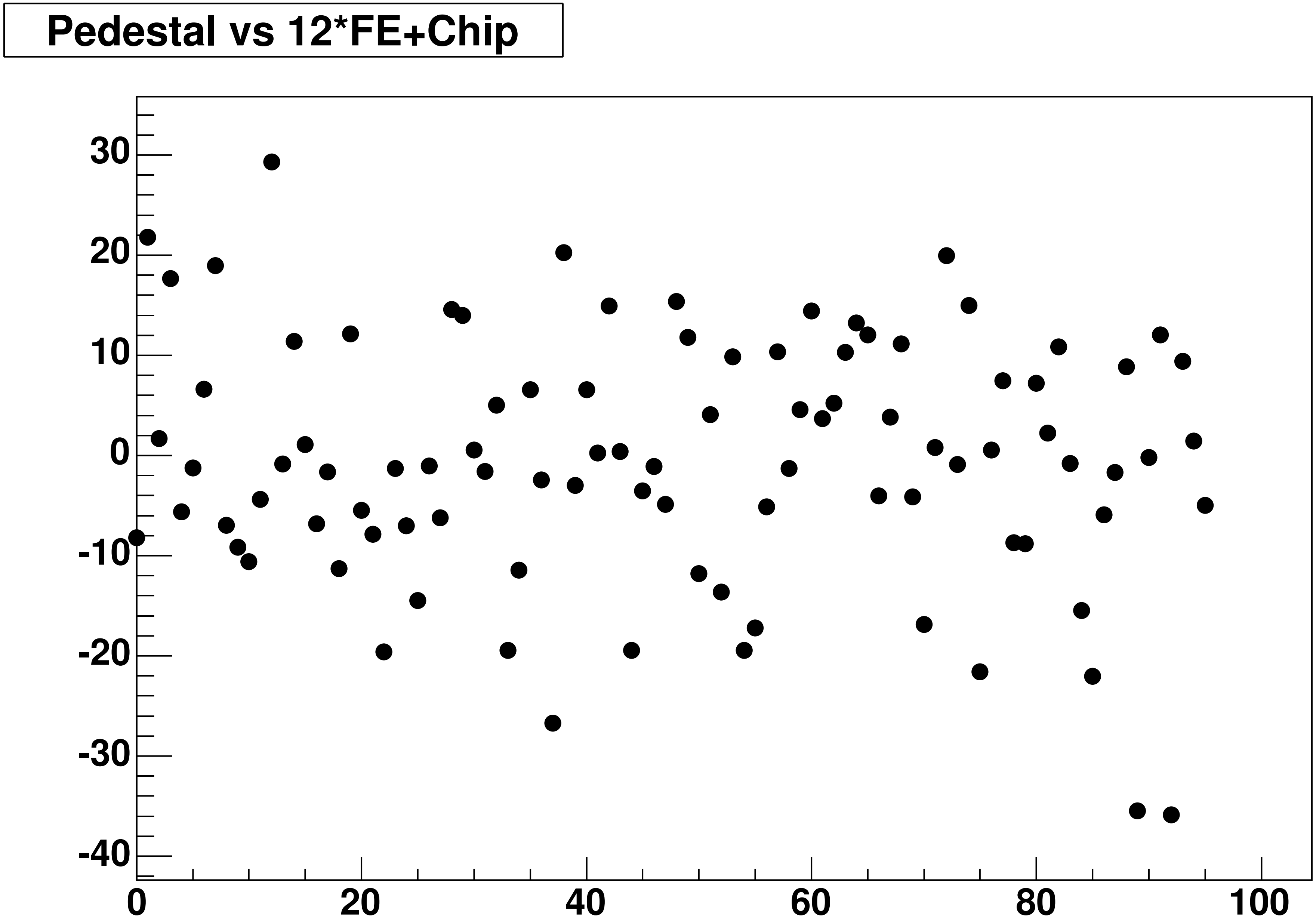

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 1126566
Mean 1.91e+04
RMS 9.91
Underflow 1.013e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



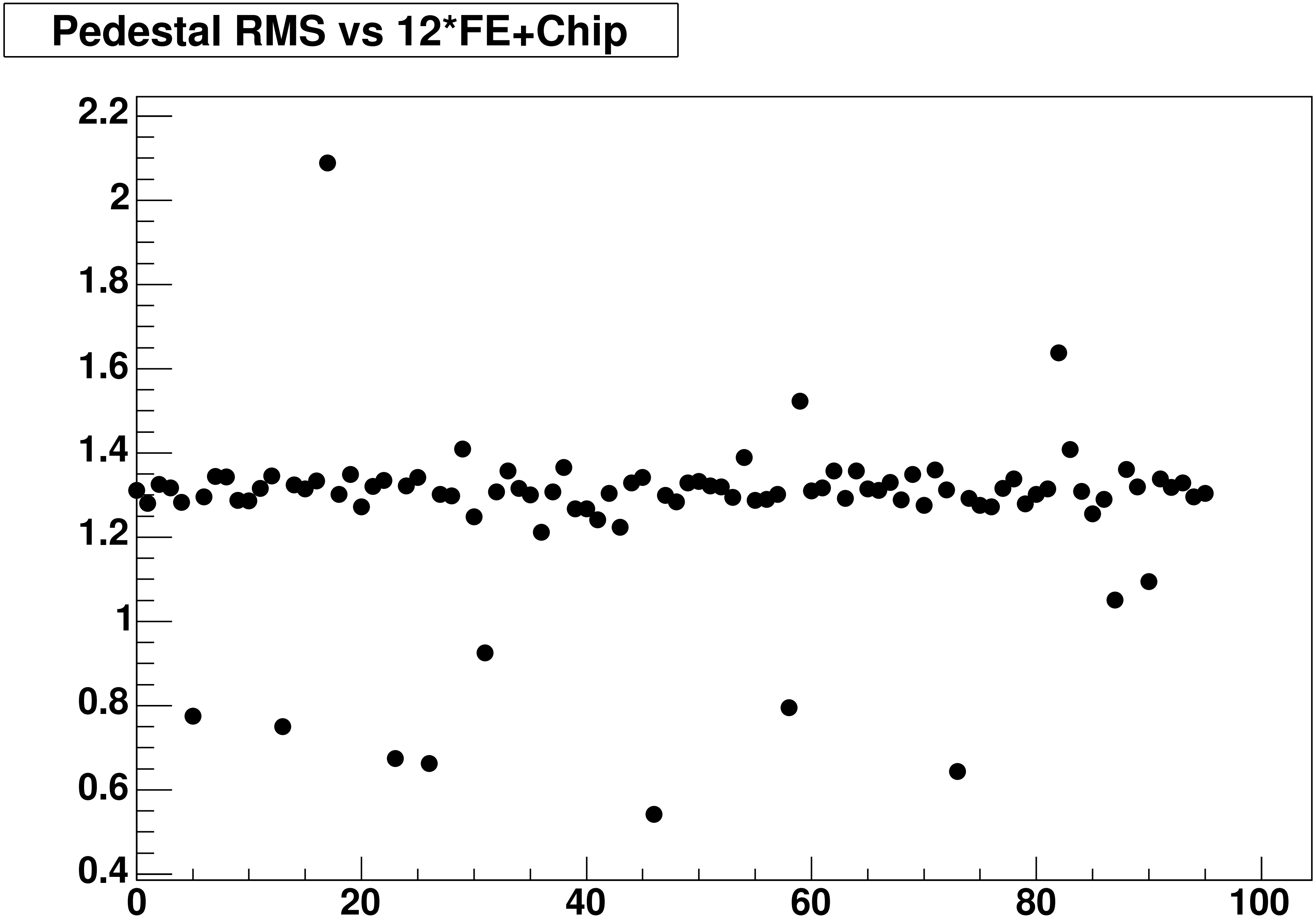
FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 1126566
Mean 1899
RMS 5719
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

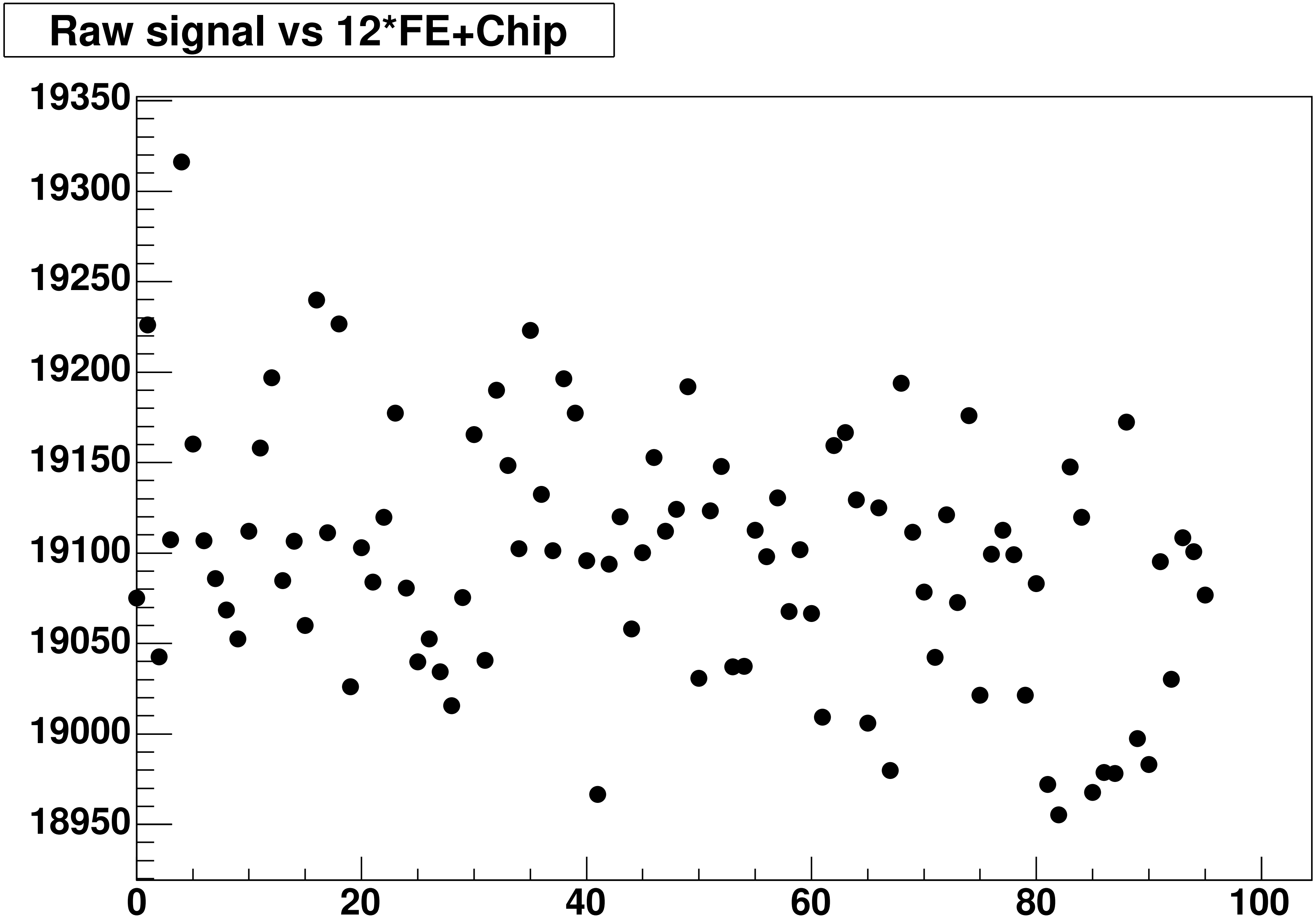

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 1126566
Mean -4.991
RMS 1.304
Underflow 0
Overflow 1.132e+05
10^6
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 1126566
Mean 1.908e+04
RMS 9.174
Underflow 1.014e+06
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

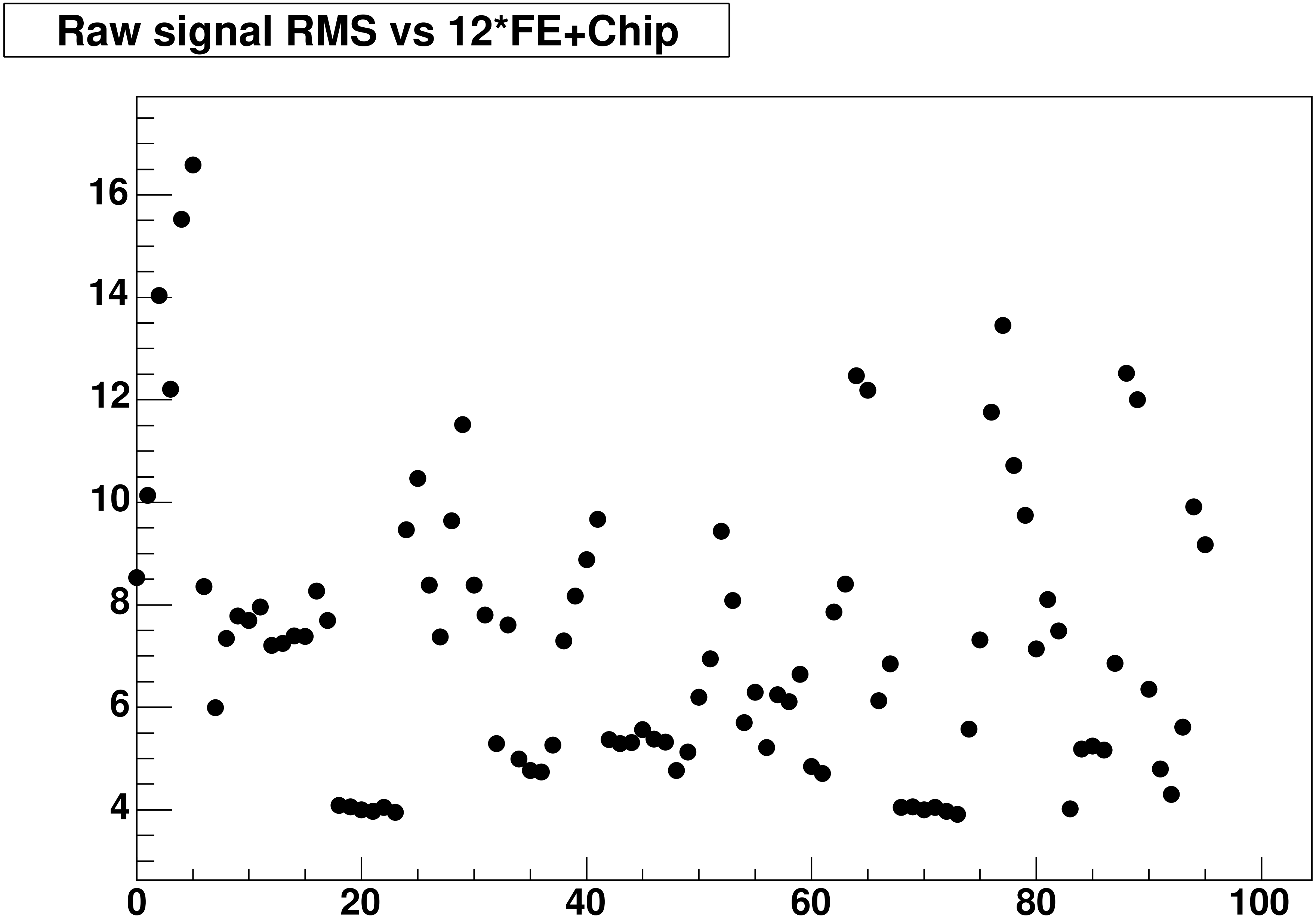


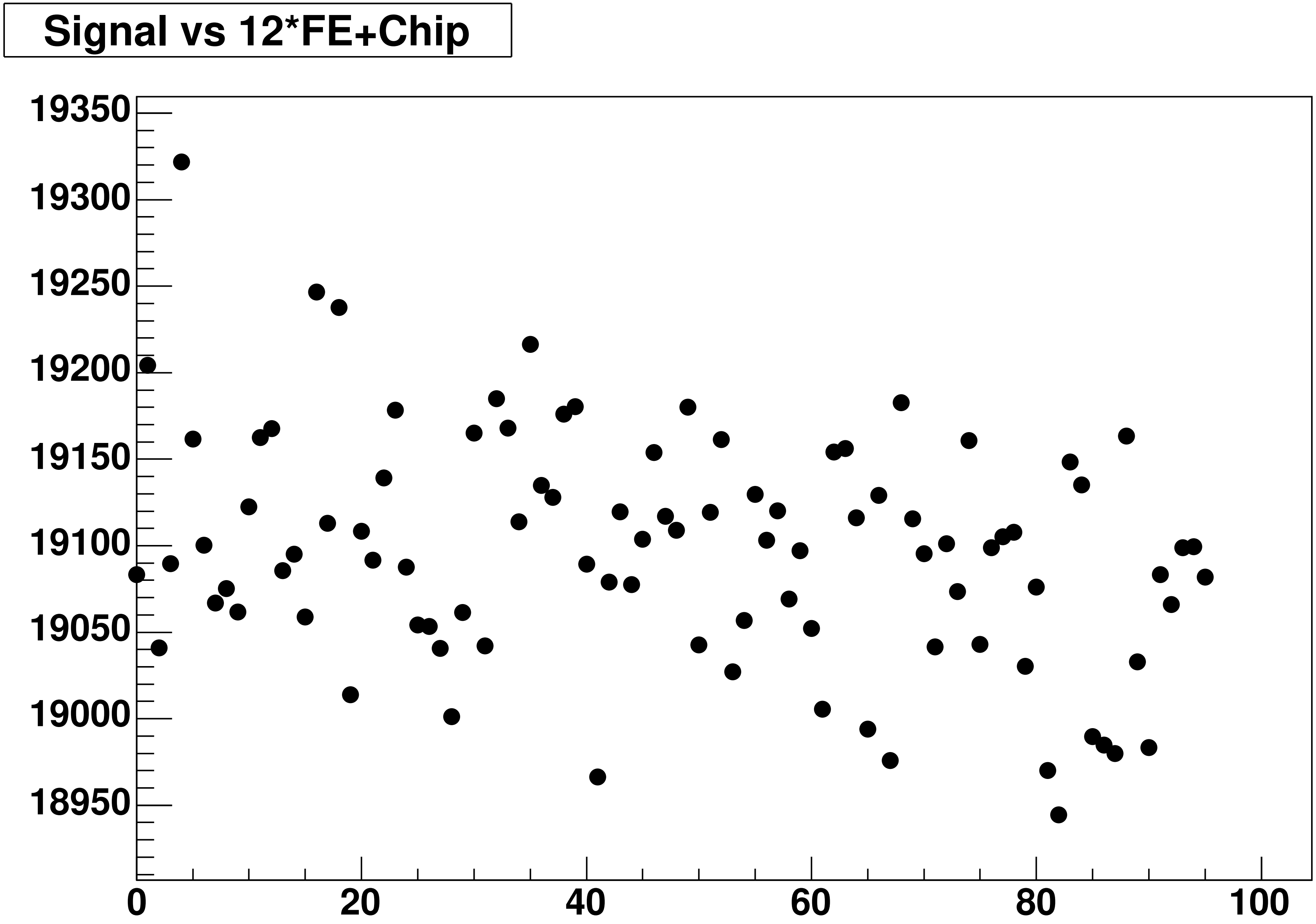
Pedestal vs 12*FE+Chip
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100

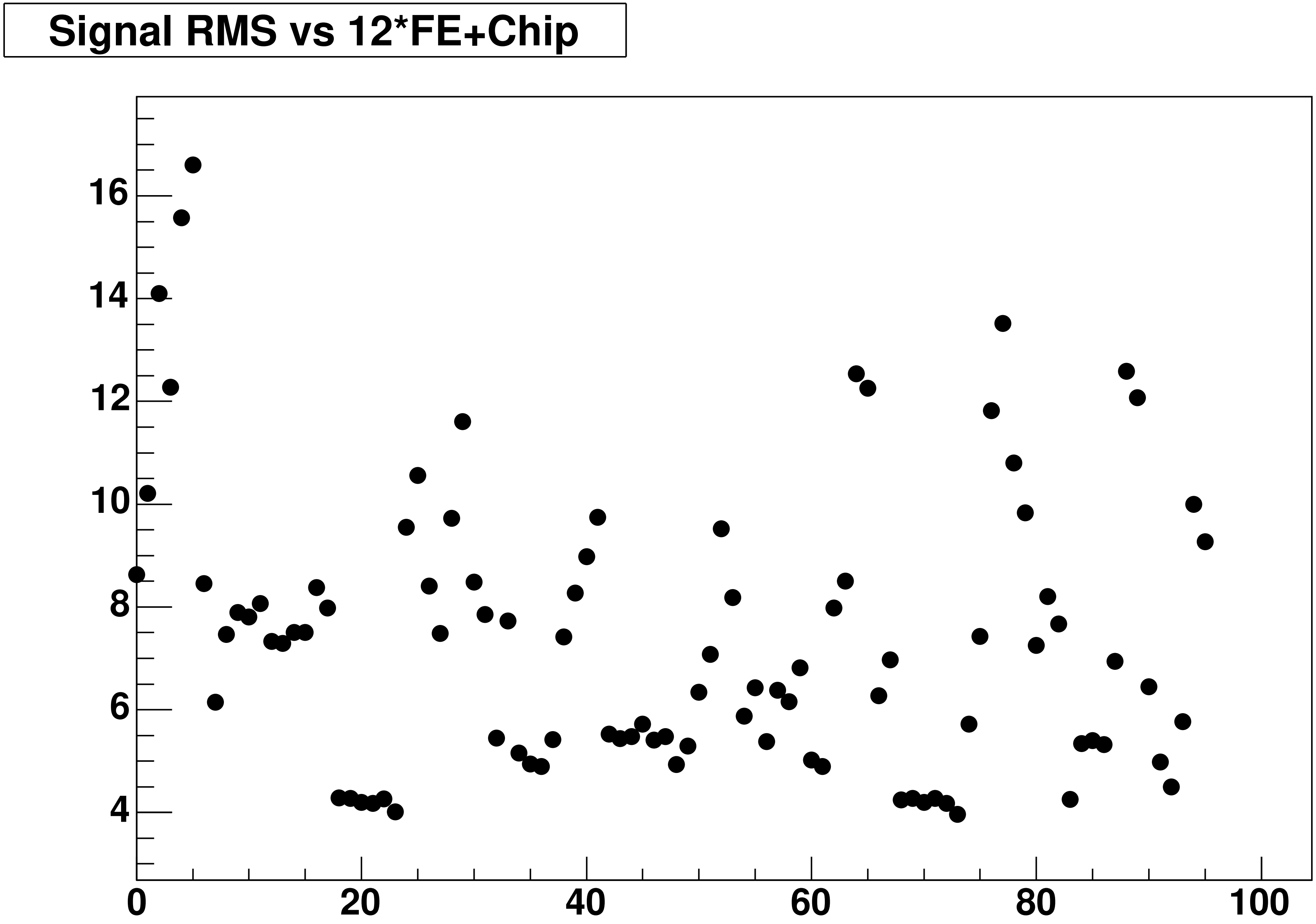

Pedestal RMS vs 12*FE+Chip
2.2
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0
20
40
60
80
100



Raw signal vs 12*FE+Chip
19350
19300
19250
19200
19150
19100
19050
19000
18950
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
16
14
12
10
8
6
4
0
20
40
60
80
100


Signal vs 12*FE+Chip
19350
19300
19250
19200
19150
19100
19050
19000
18950
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
16
14
12
10
8
6
4
0
20
40
60
80
100