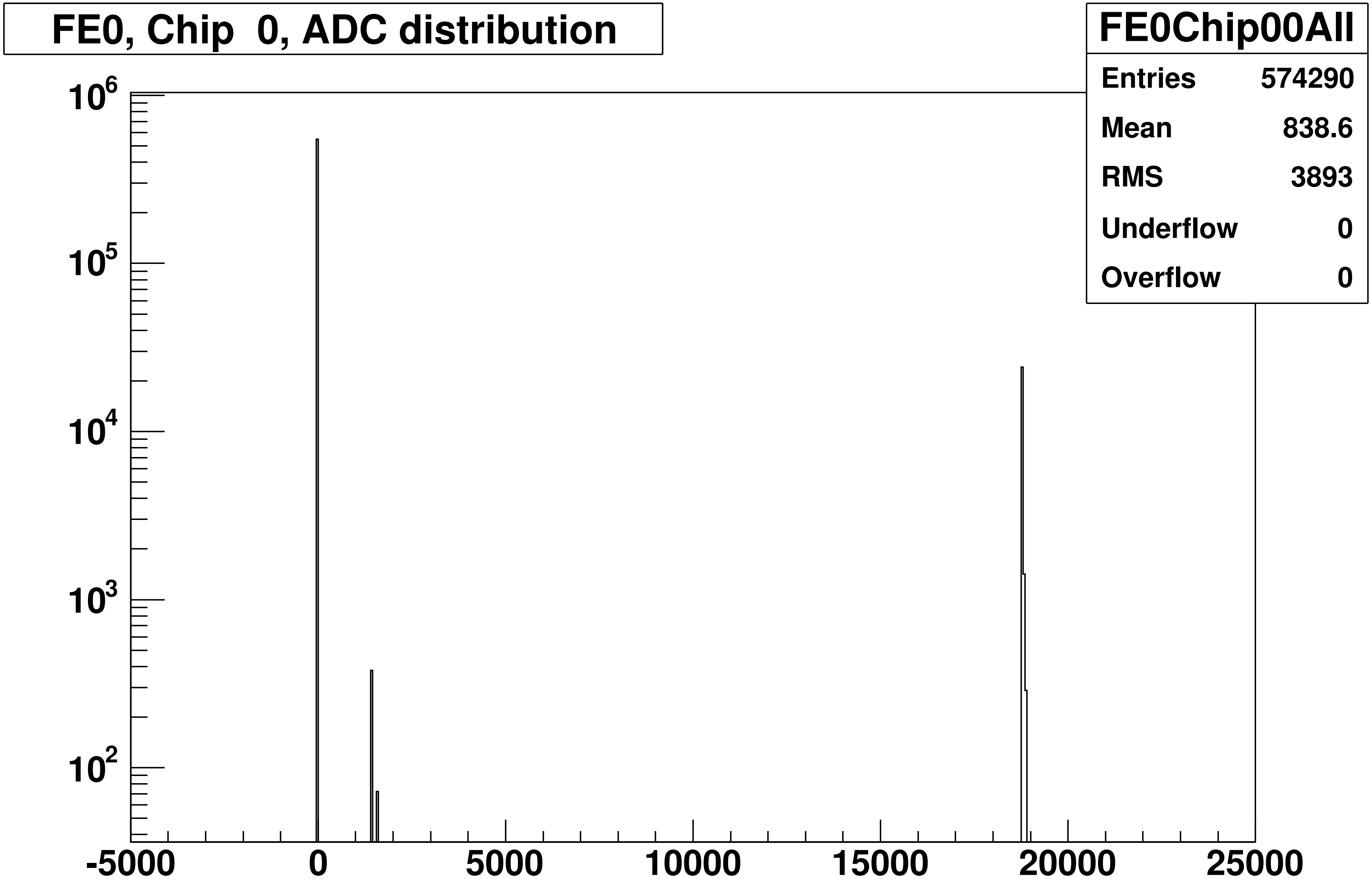

FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 574290
Mean 838.6
RMS 3893
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
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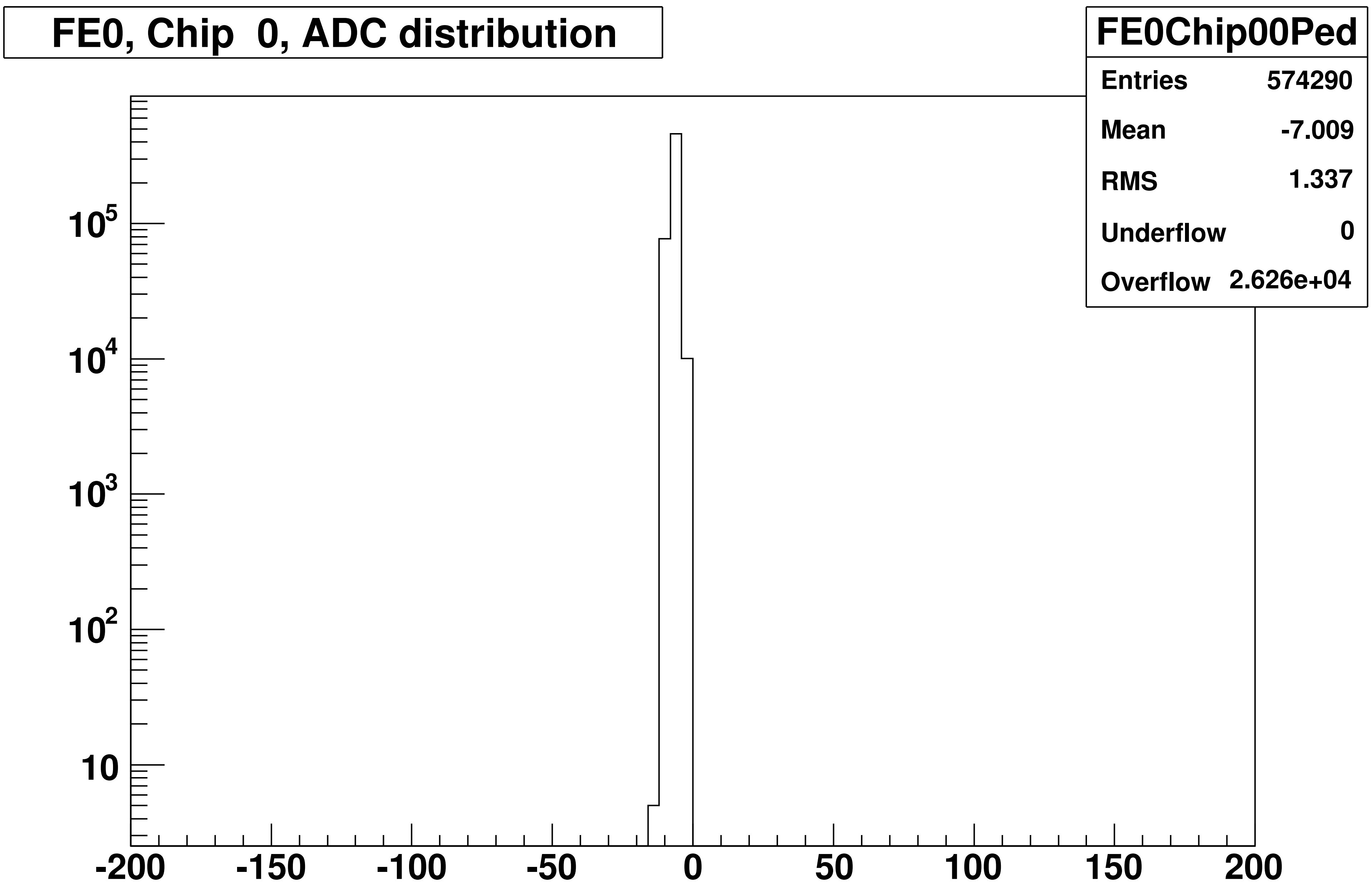
10000
15000
20000
25000


FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 574290
Mean -7.009
RMS 1.337
Underflow 0
Overflow 2.626e+04
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10^3
10^4
10^5
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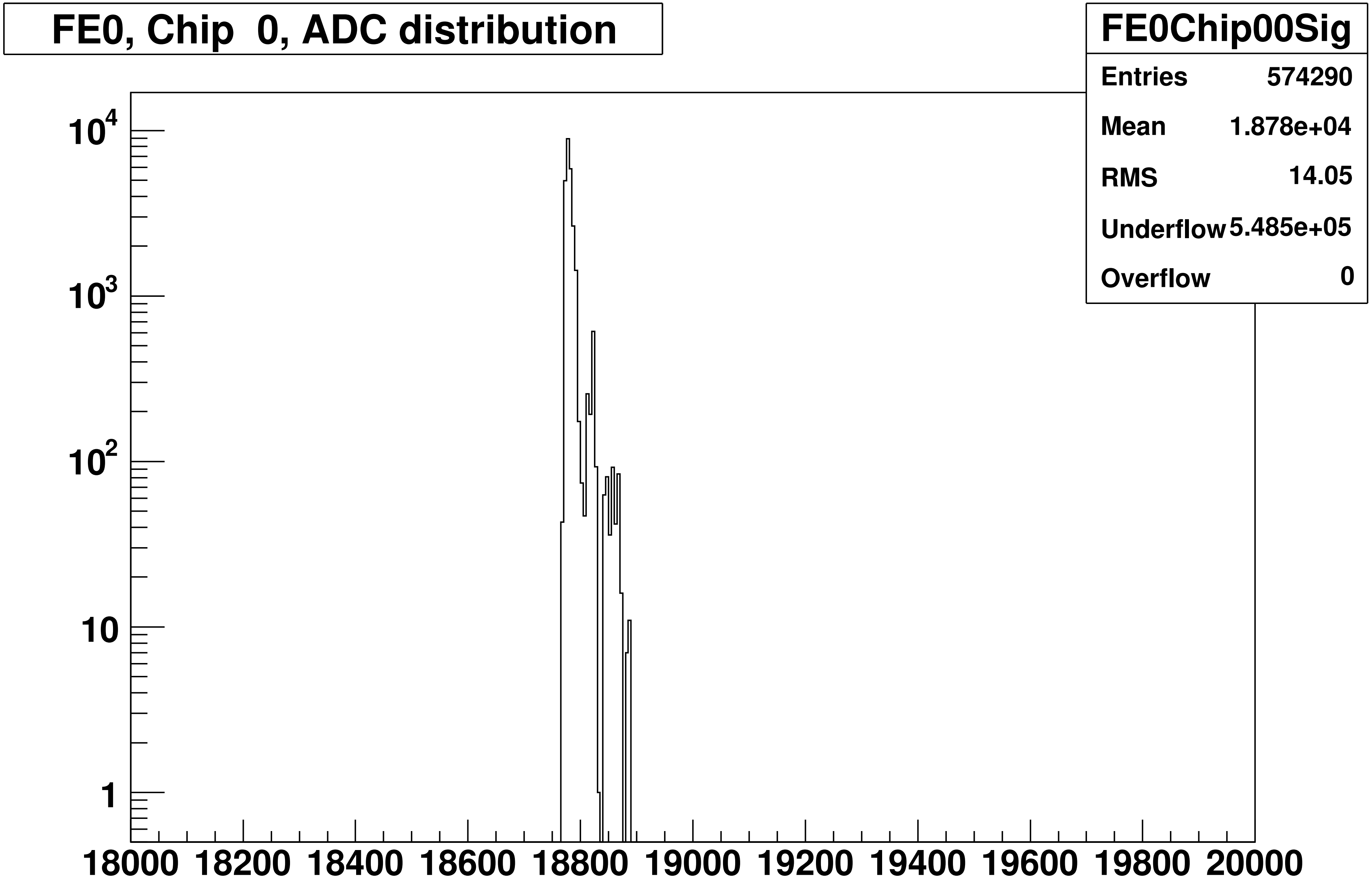
150
200



FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 574290
Mean 1.878e+04
RMS 14.05
Underflow 5.485e+05
Overflow 0
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10^3
10^2
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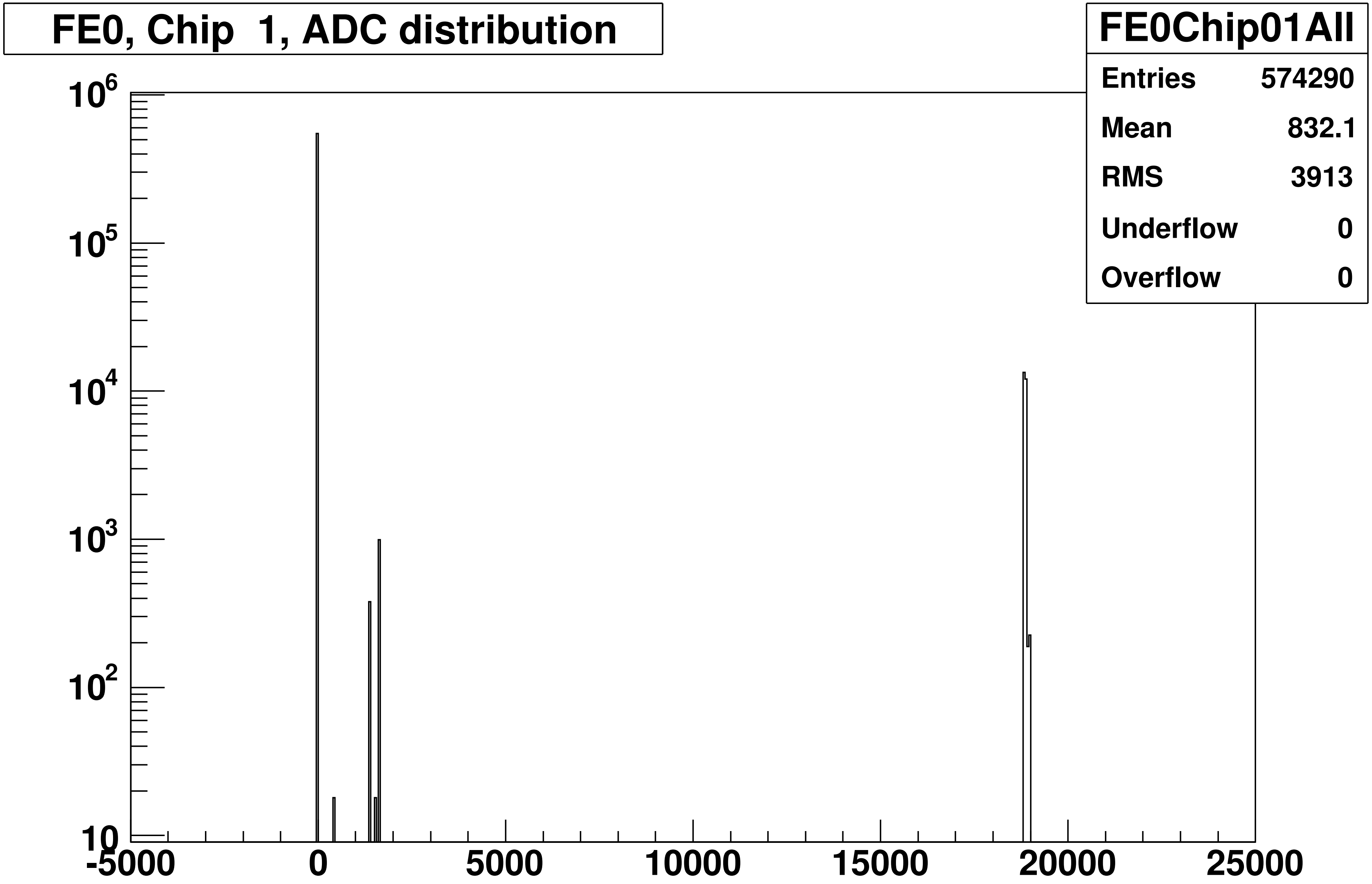
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 574290
Mean 832.1
RMS 3913
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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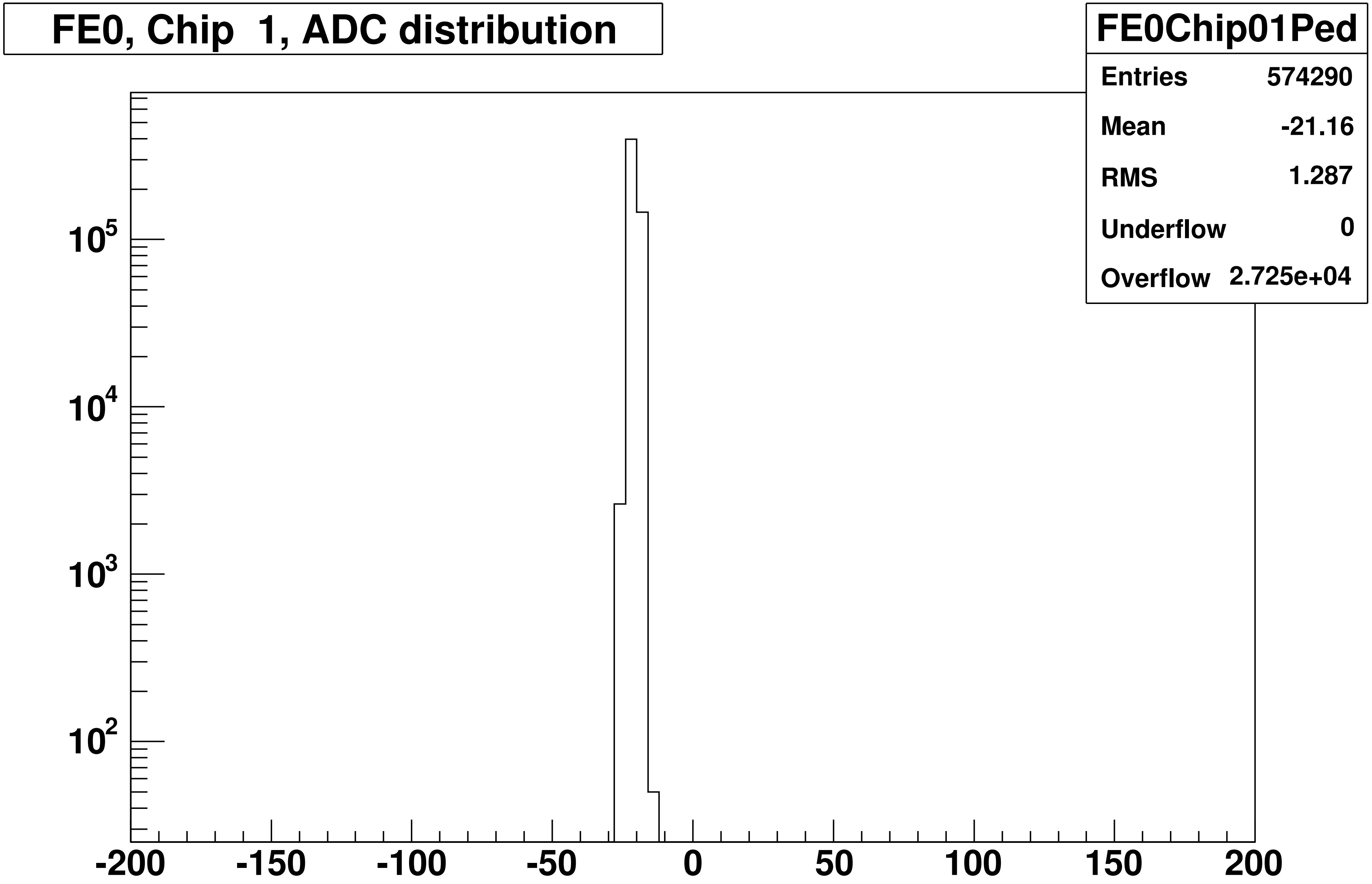
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 574290
Mean -21.16
RMS 1.287
Underflow 0
Overflow 2.725e+04
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10^3
10^2
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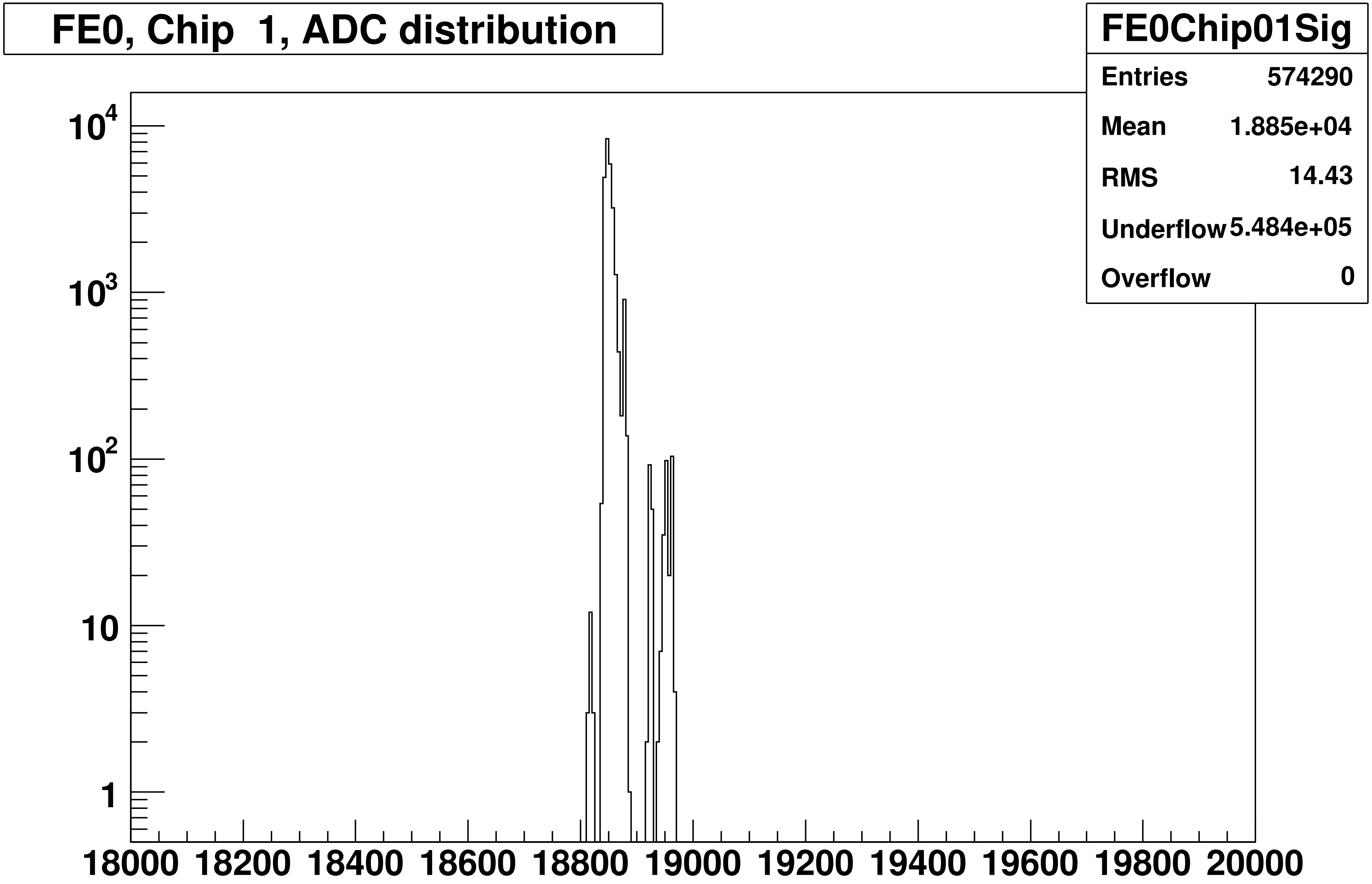
FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 574290
Mean 1.885e+04
RMS 14.43
Underflow 5.484e+05
Overflow 0
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10^3
10^2
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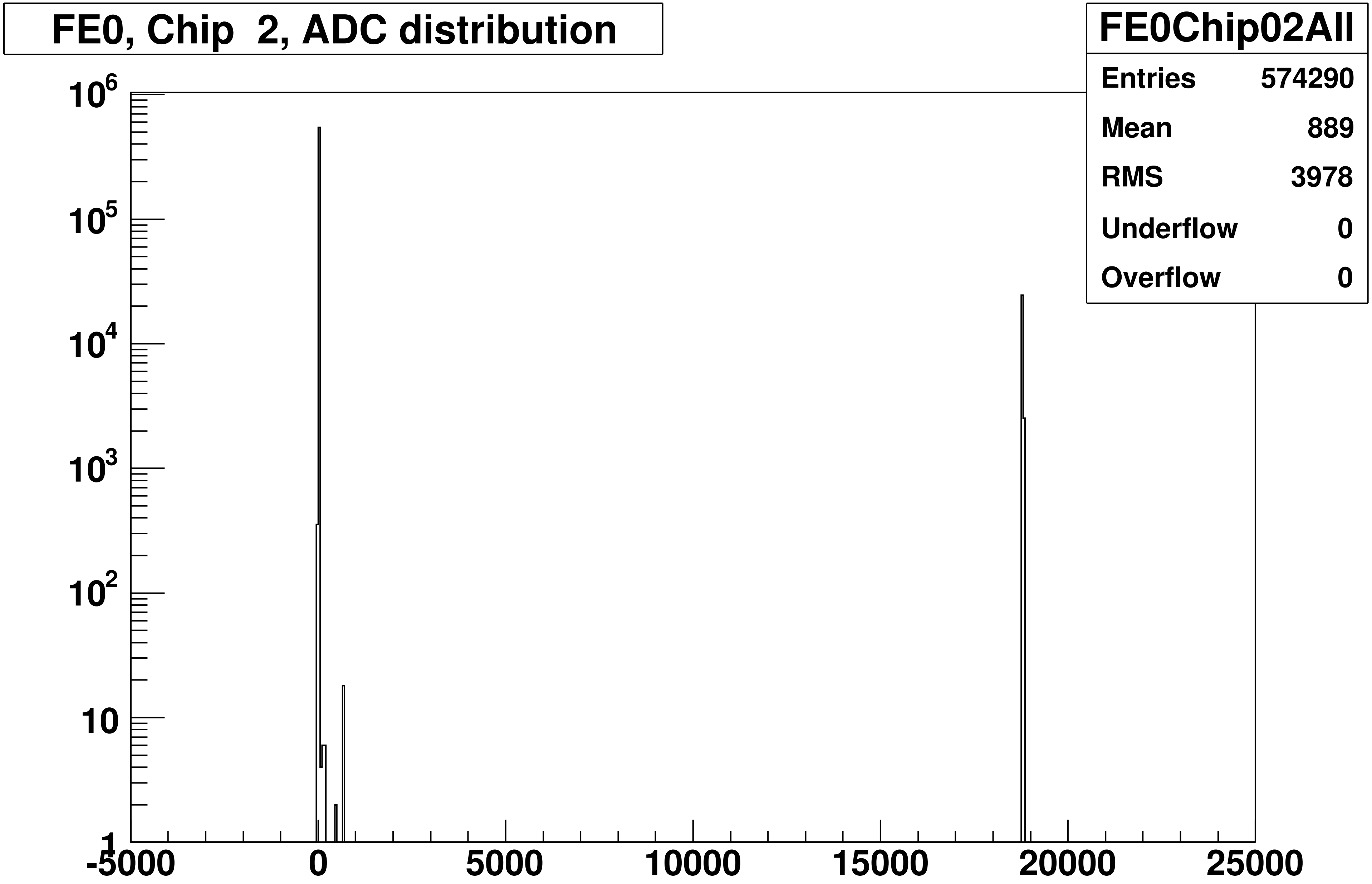
FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 574290
Mean 889
RMS 3978
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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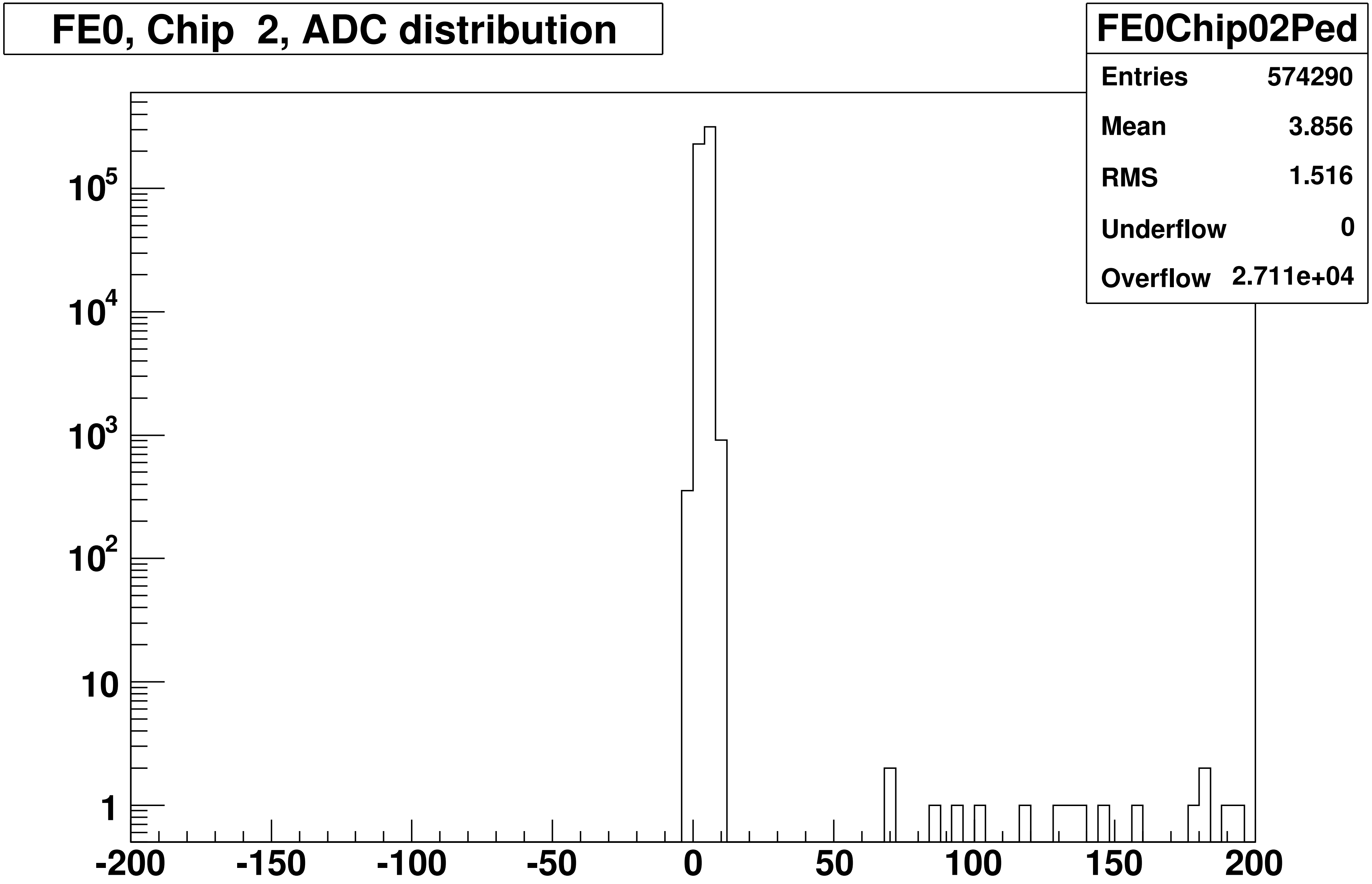
10000
15000
20000
25000



FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 574290
Mean 3.856
RMS 1.516
Underflow 0
Overflow 2.711e+04
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10^3
10^2
10
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-100
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150
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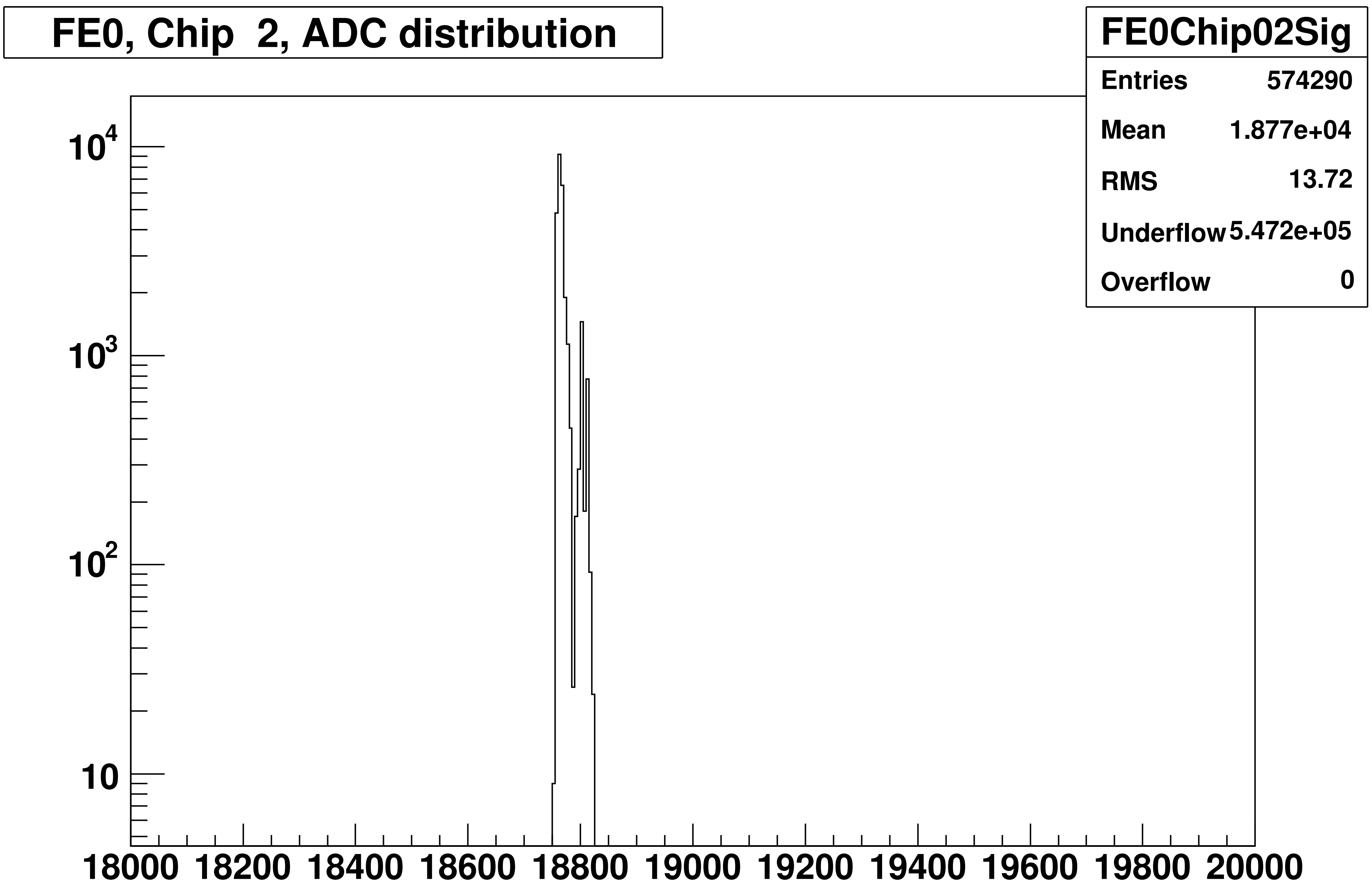
FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 574290
Mean 1.877e+04
RMS 13.72
Underflow 5.472e+05
Overflow 0
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10^3
10^2
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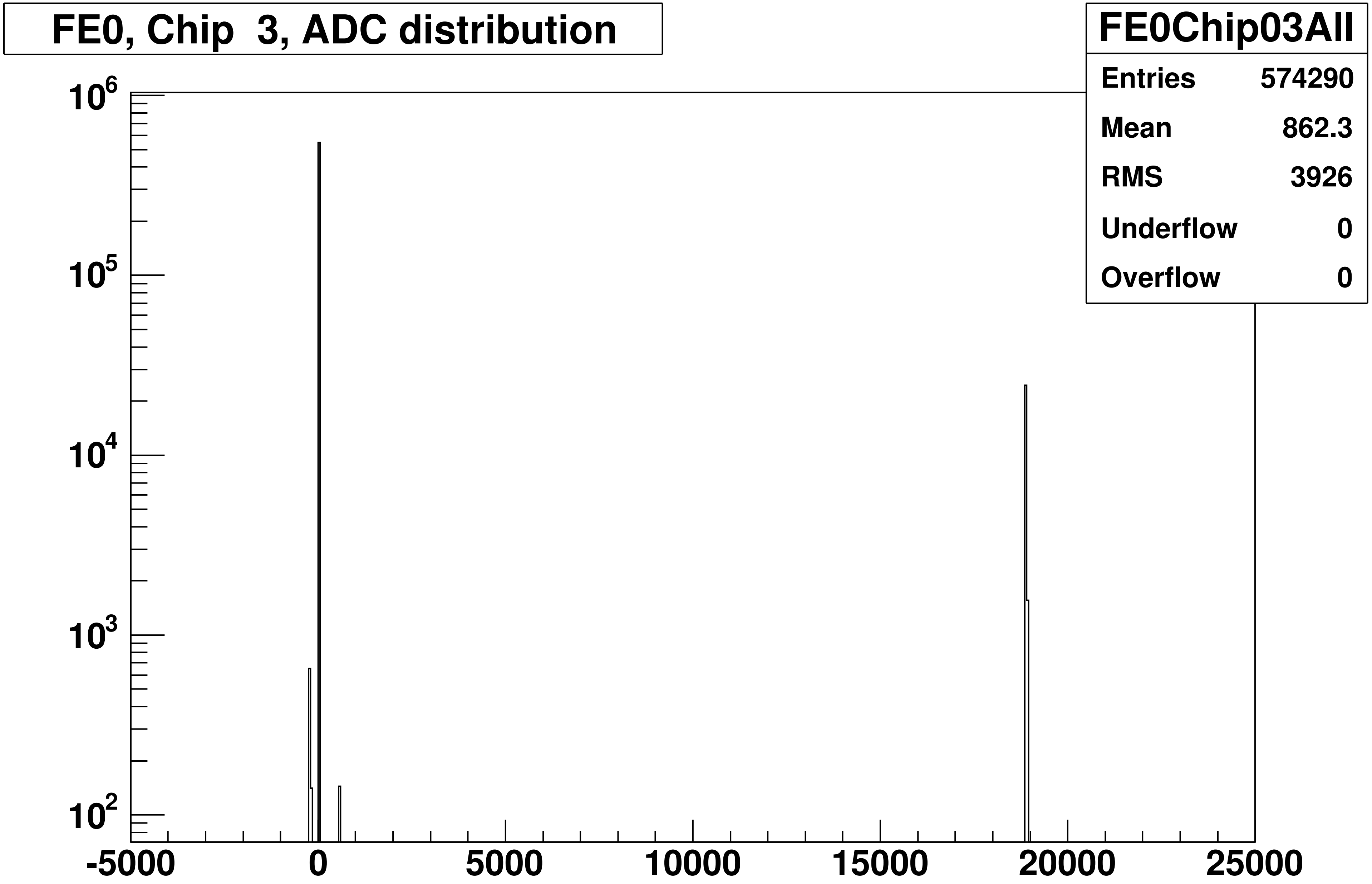
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 574290
Mean 862.3
RMS 3926
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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15000
20000
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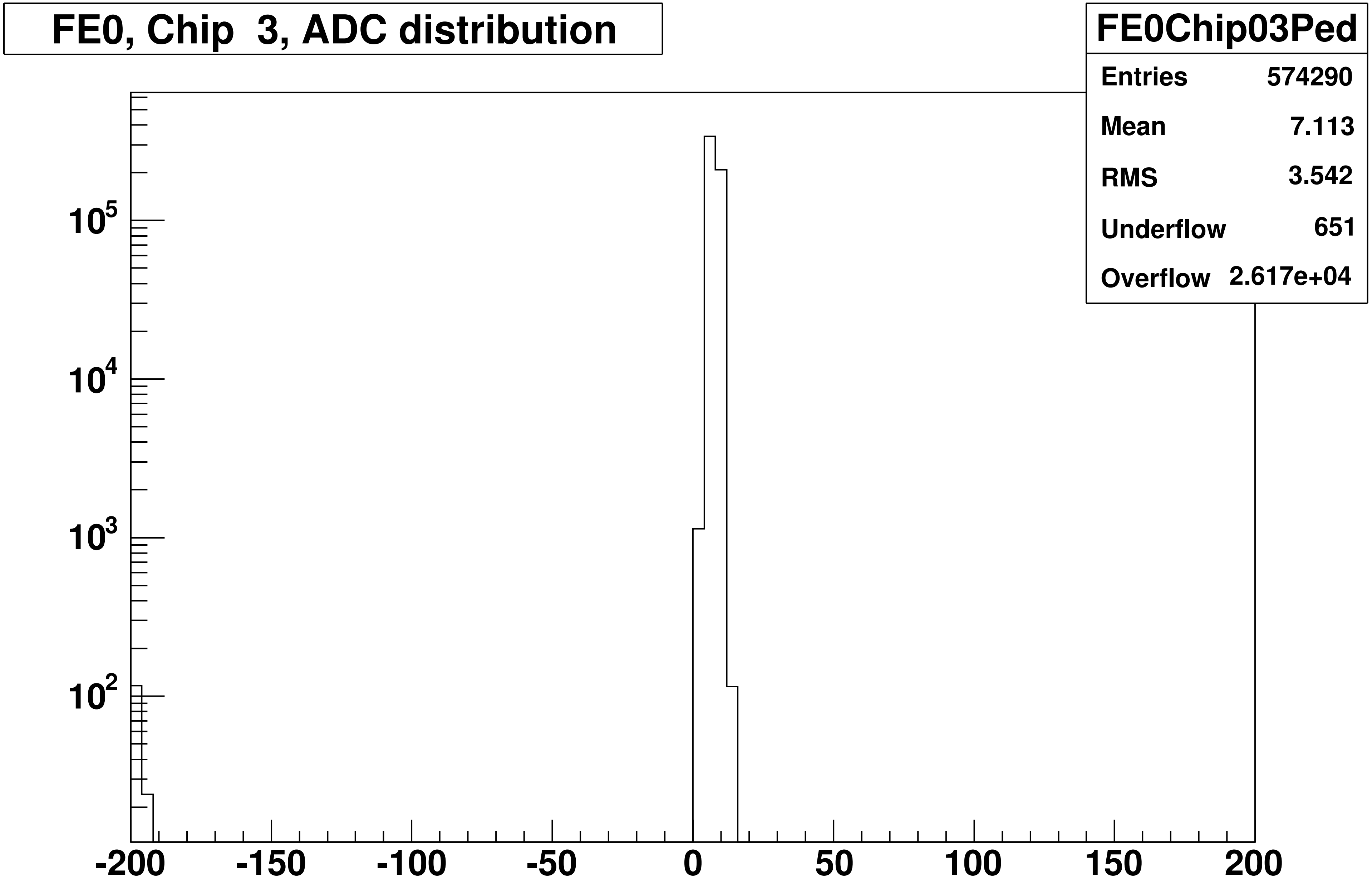

FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 574290
Mean 7.113
RMS 3.542
Underflow 651
Overflow 2.617e+04
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10^4
10^3
10^2
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-100
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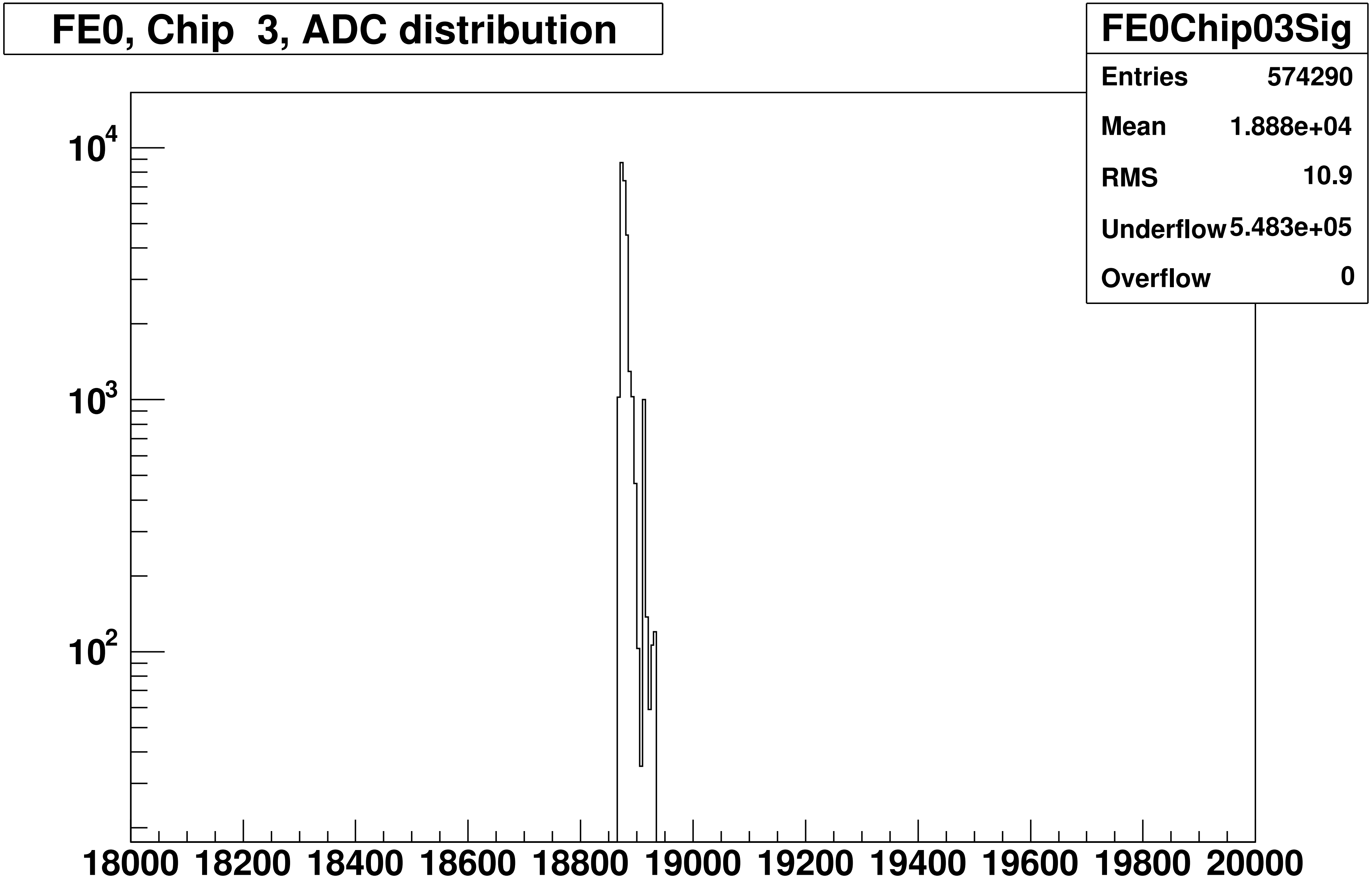
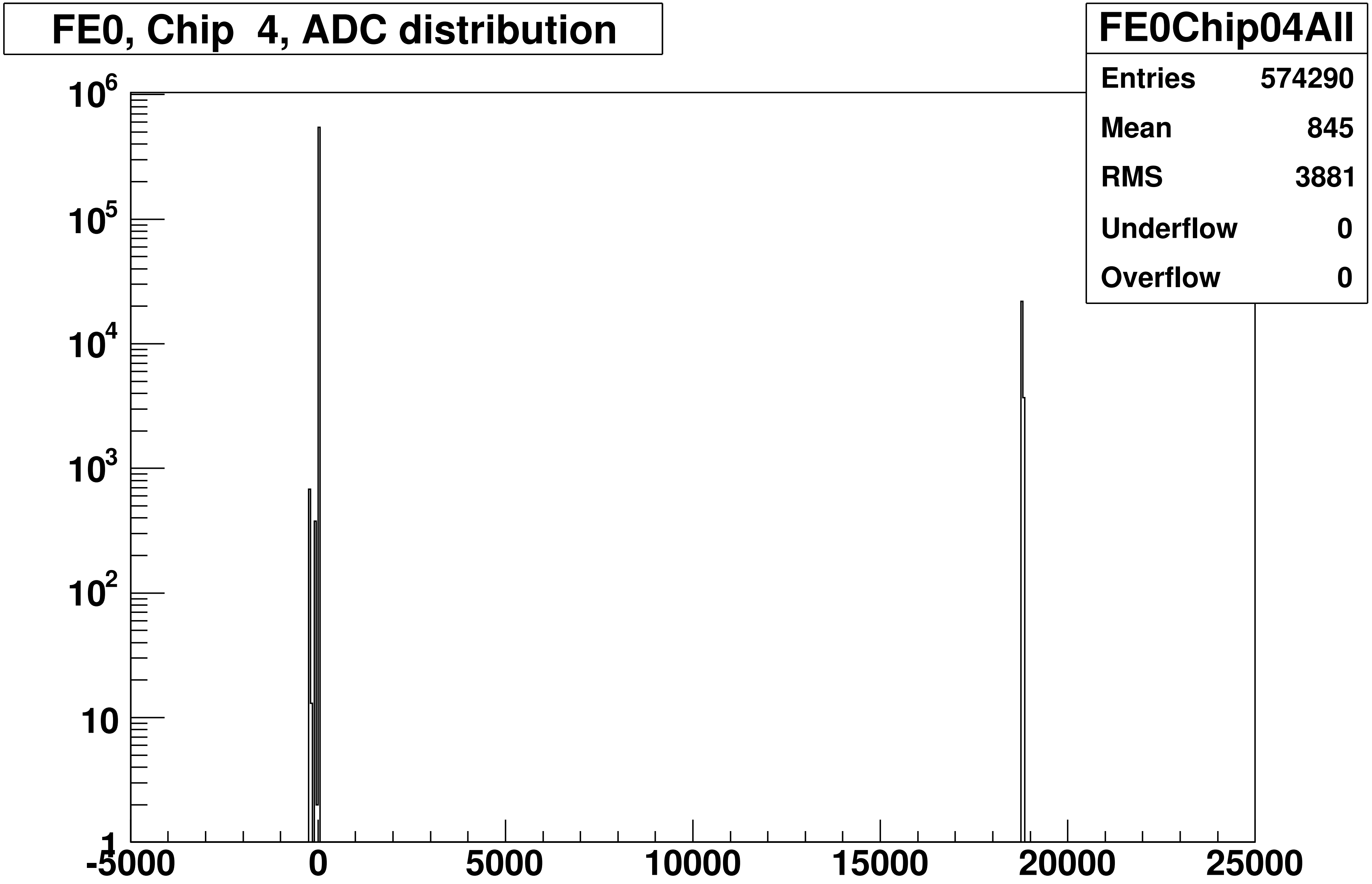
0
50
100
150
200


FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 574290
Mean 1.888e+04
RMS 10.9
Underflow 5.483e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 574290
Mean 845
RMS 3881
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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0
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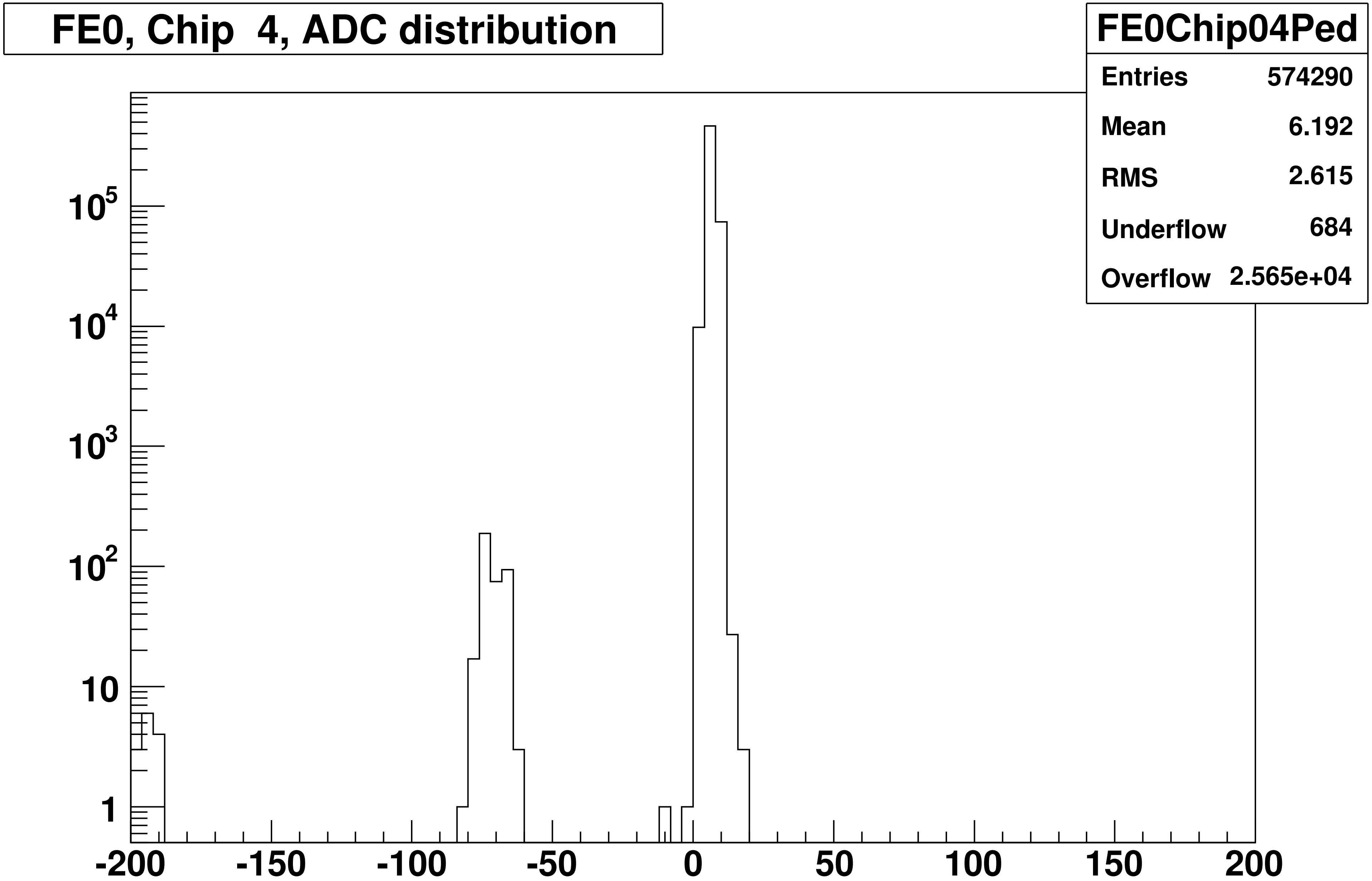
15000
20000
25000


FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 574290
Mean 6.192
RMS 2.615
Underflow 684
Overflow 2.565e+04
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10^4
10^3
10^2
10
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-100
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0
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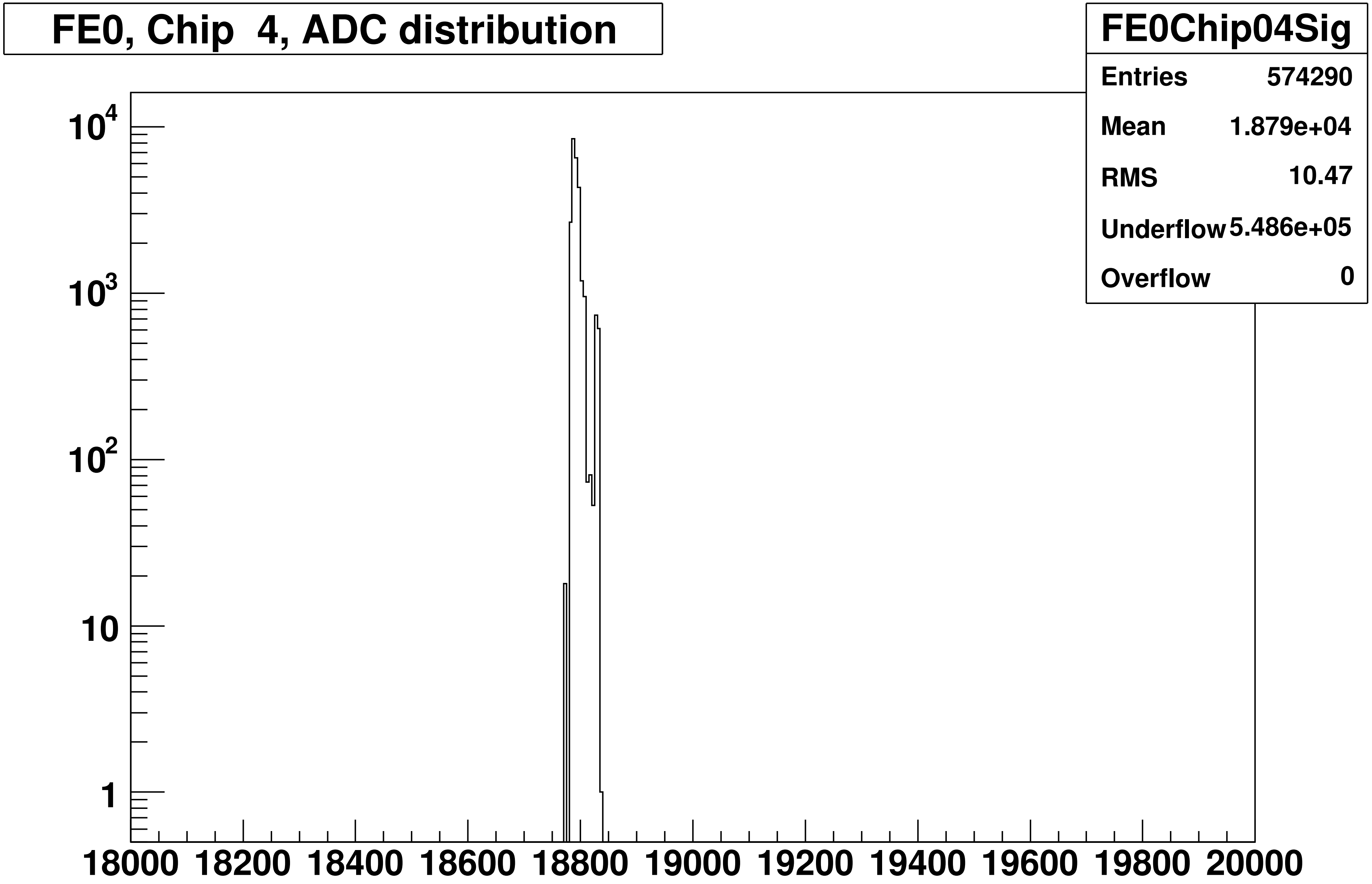
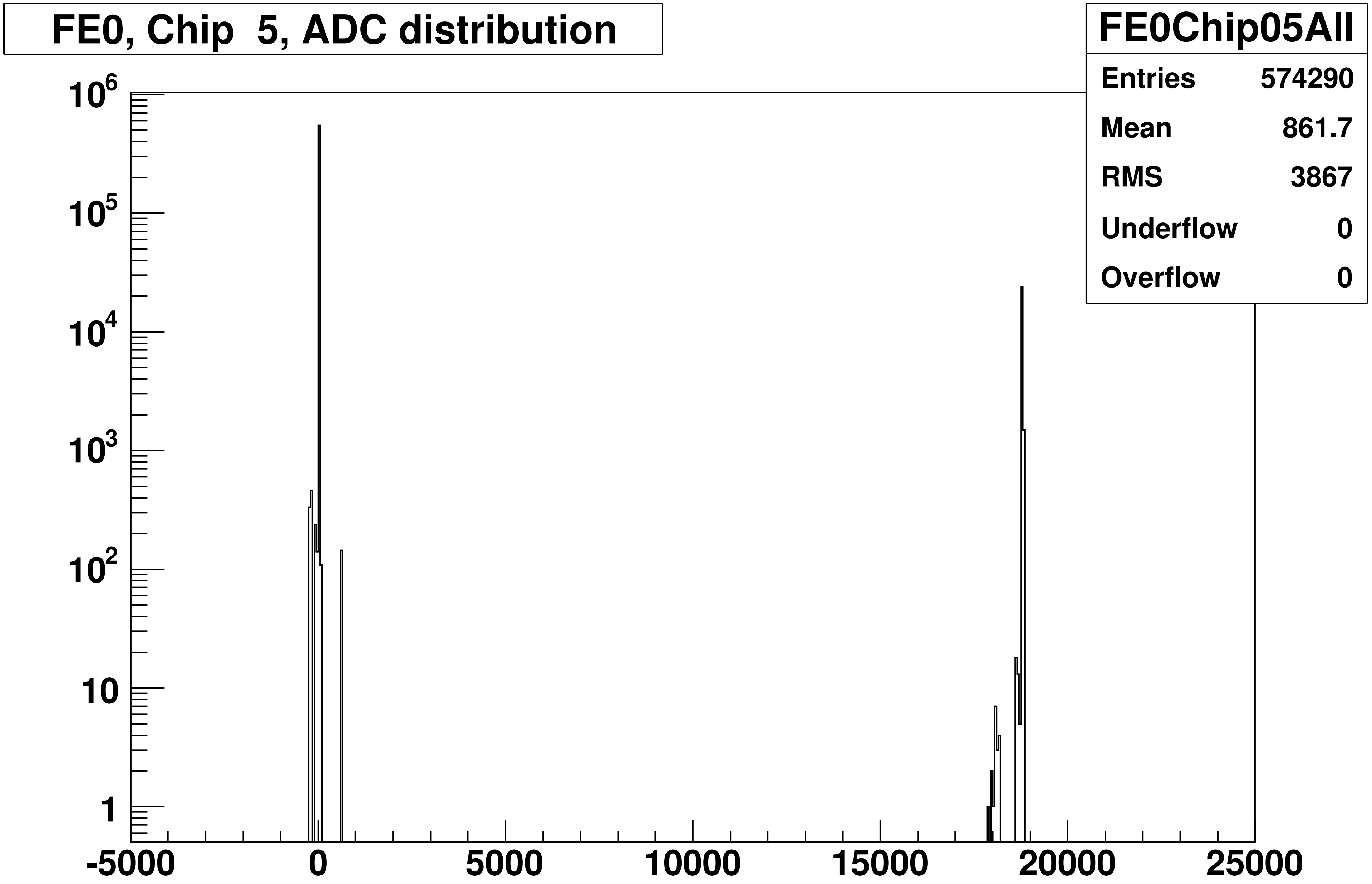
100
150
200


FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 574290
Mean 1.879e+04
RMS 10.47
Underflow 5.486e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 574290
Mean 861.7
RMS 3867
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
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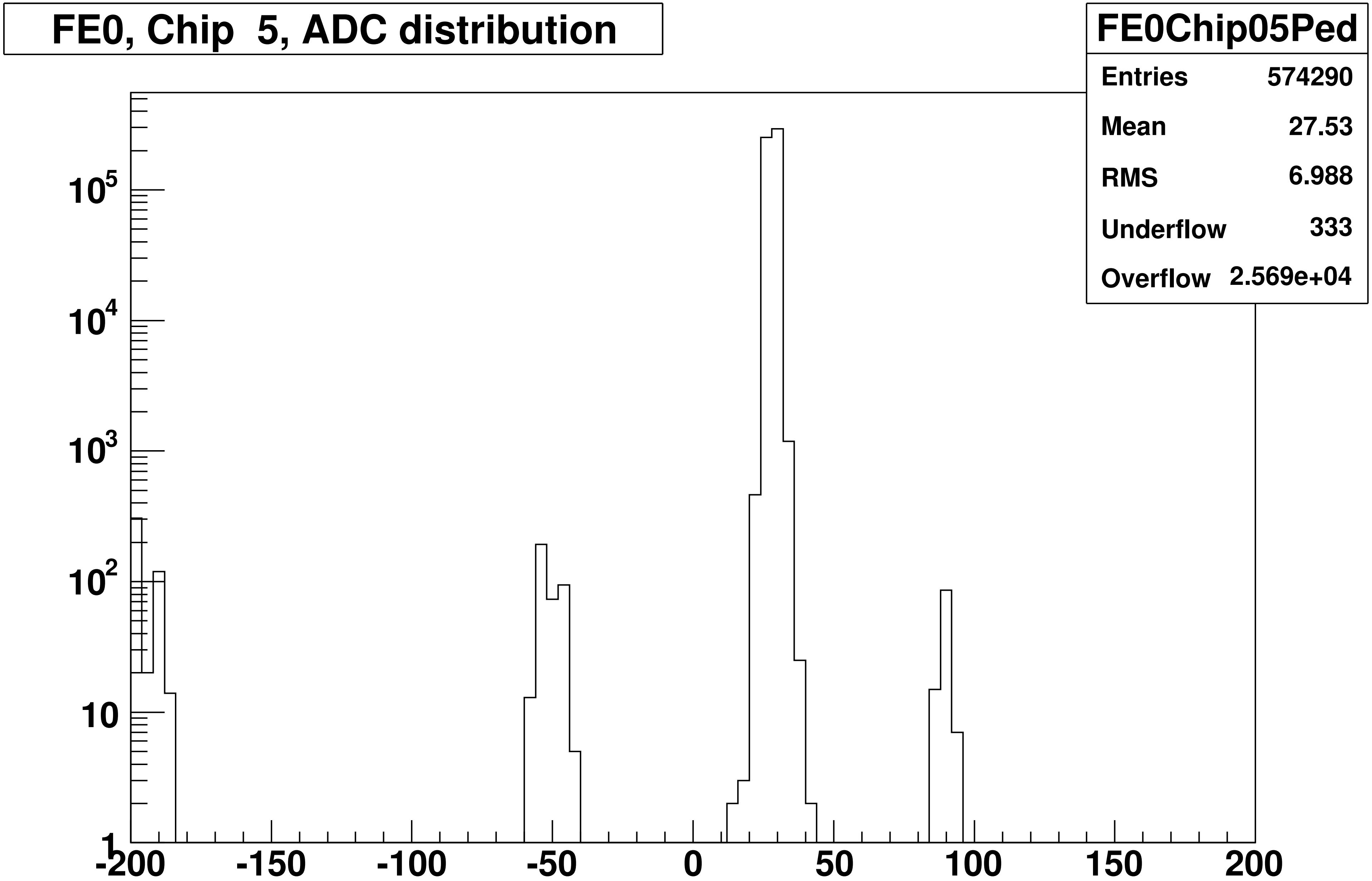
10000
15000
20000
25000


FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 574290
Mean 27.53
RMS 6.988
Underflow 333
Overflow 2.569e+04
10^5
10^4
10^3
10^2
10
1
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-100
-50
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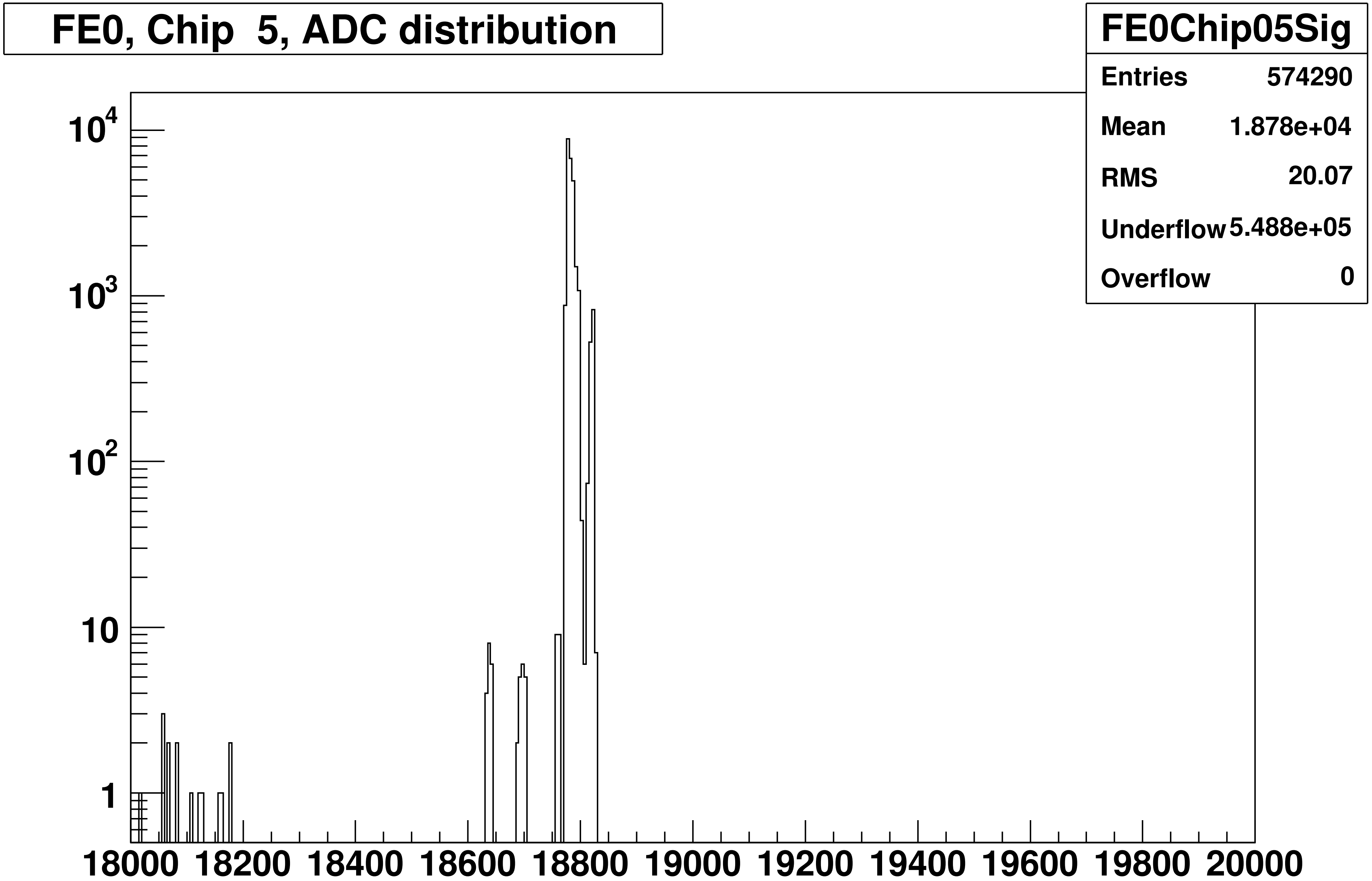
100
150
200


FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 574290
Mean 1.878e+04
RMS 20.07
Underflow 5.488e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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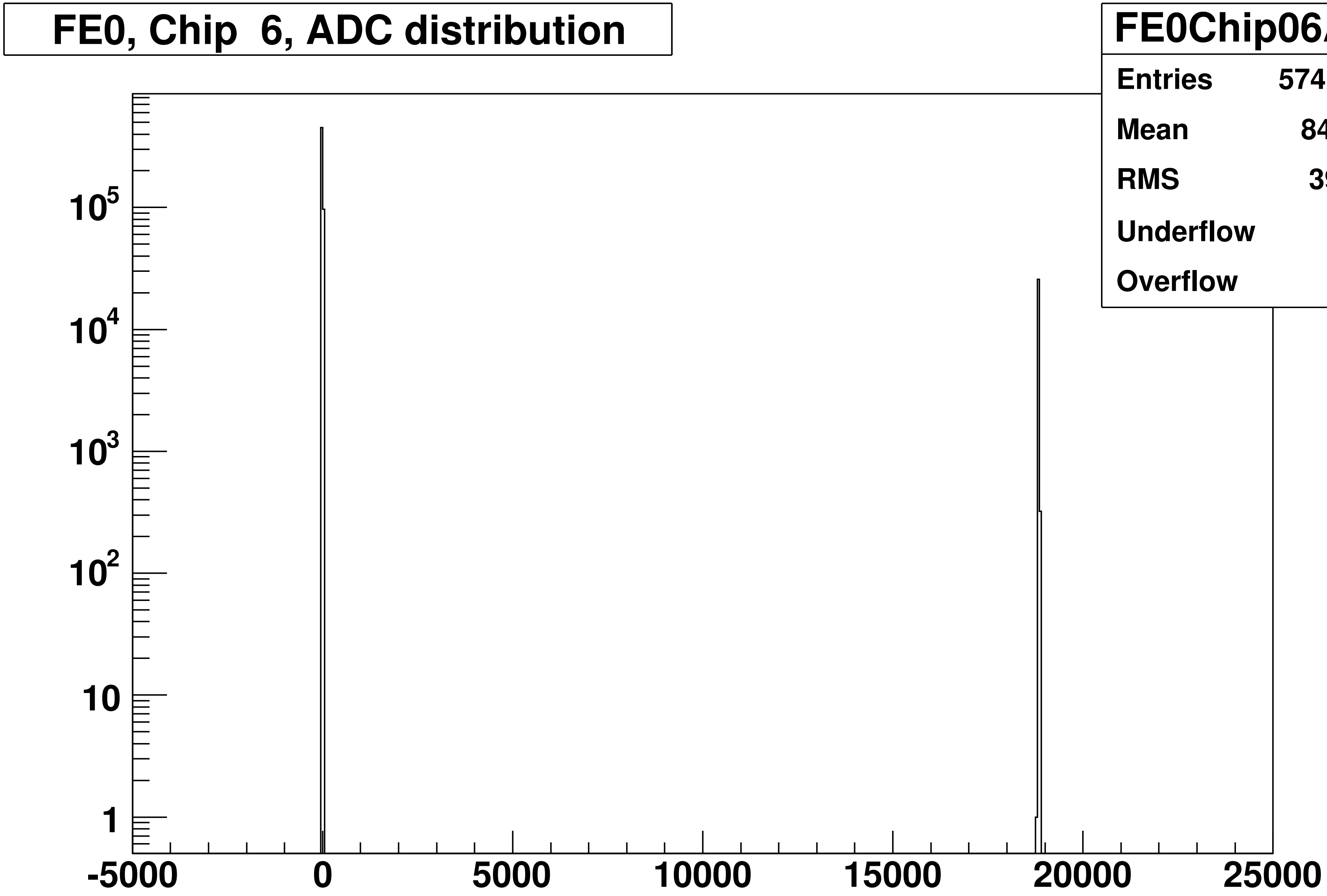
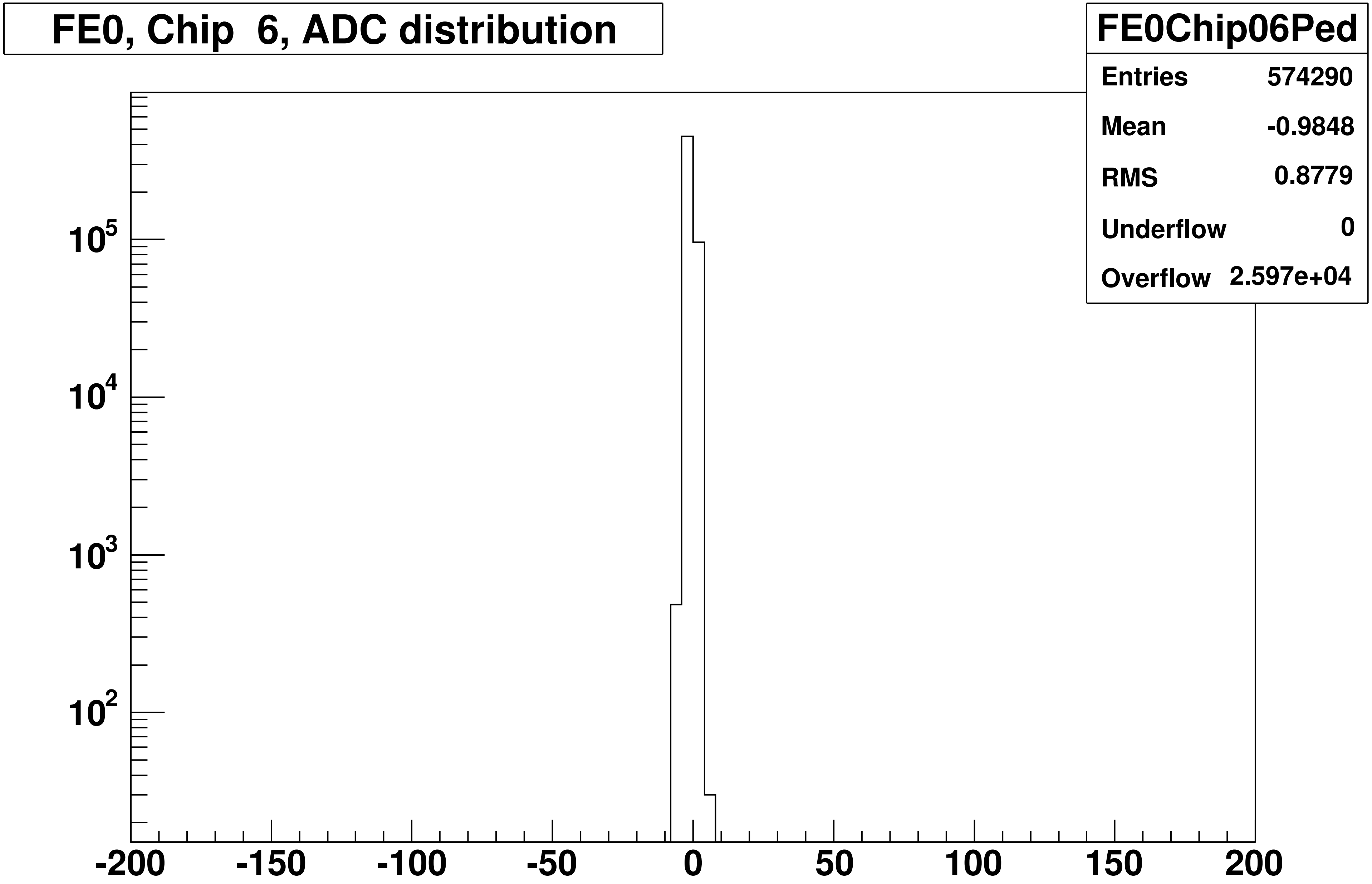
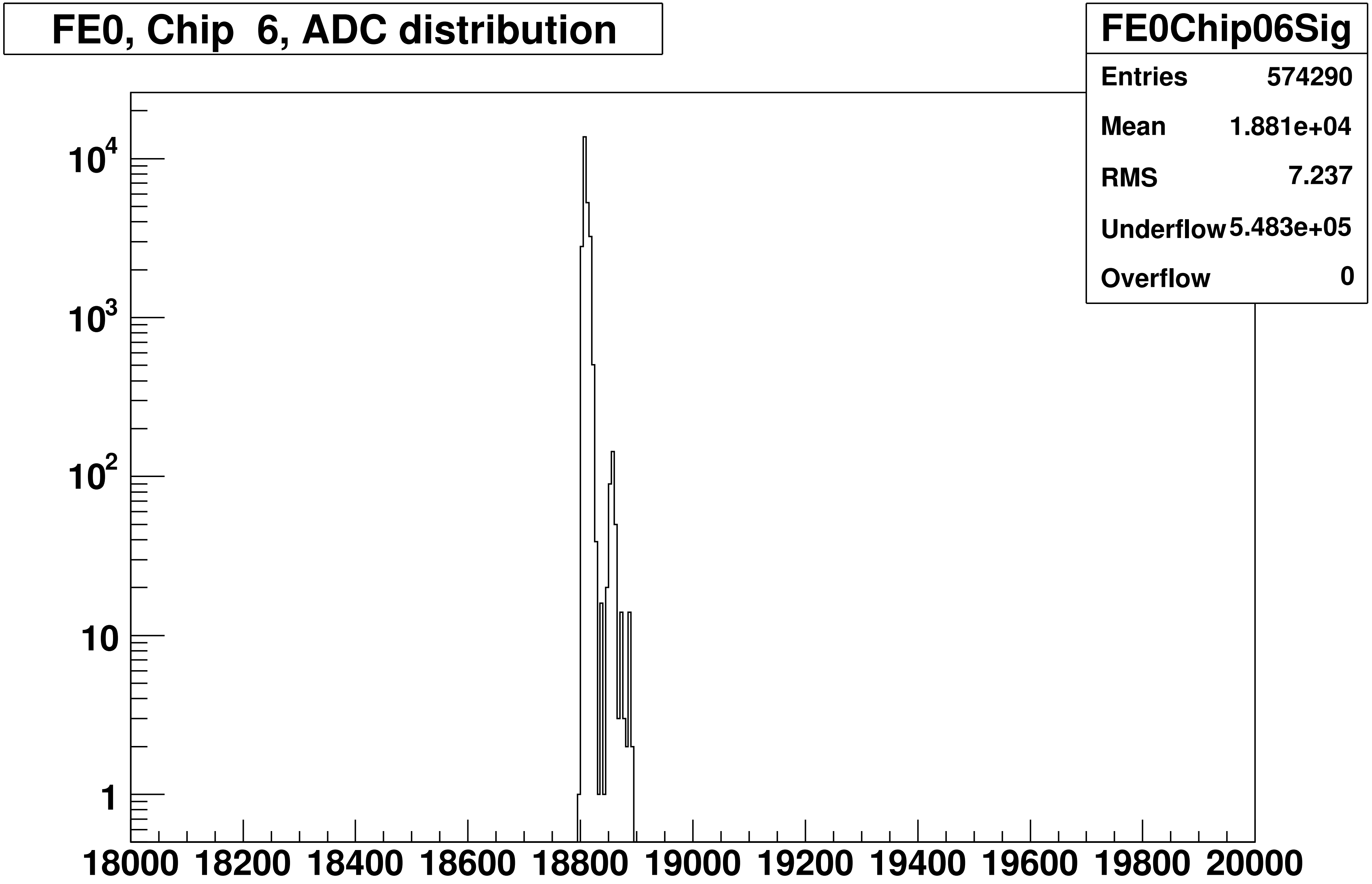
19400
19600
19800
20000


FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 574290
Mean 849.8
RMS 3909
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 574290
Mean -0.9848
RMS 0.8779
Underflow 0
Overflow 2.597e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 574290
Mean 1.881e+04
RMS 7.237
Underflow 5.483e+05
Overflow 0
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10^3
10^2
10
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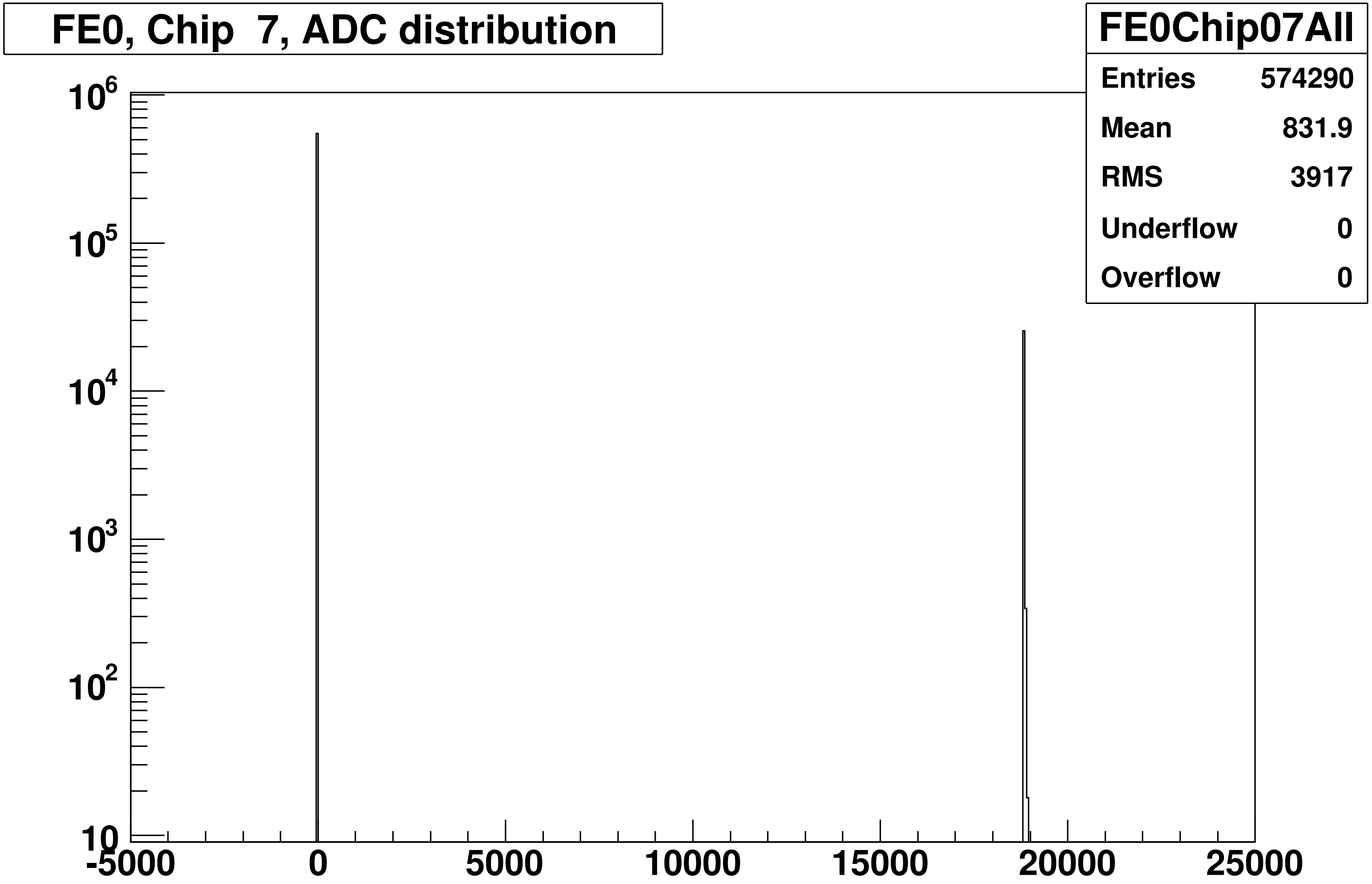
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 574290
Mean 831.9
RMS 3917
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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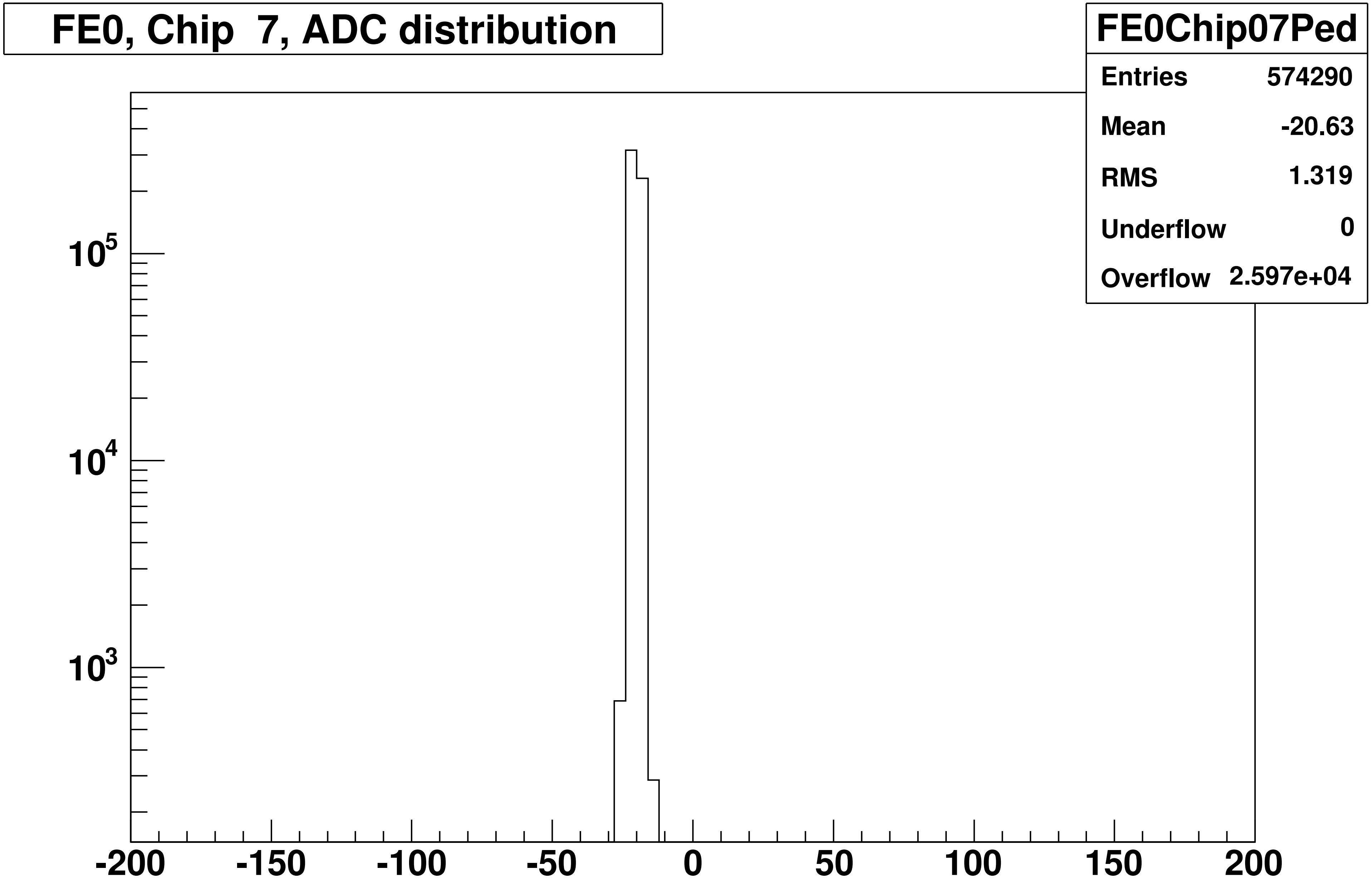
10000
15000
20000
25000



FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 574290
Mean -20.63
RMS 1.319
Underflow 0
Overflow 2.597e+04
10^5
10^4
10^3
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-100
-50
0
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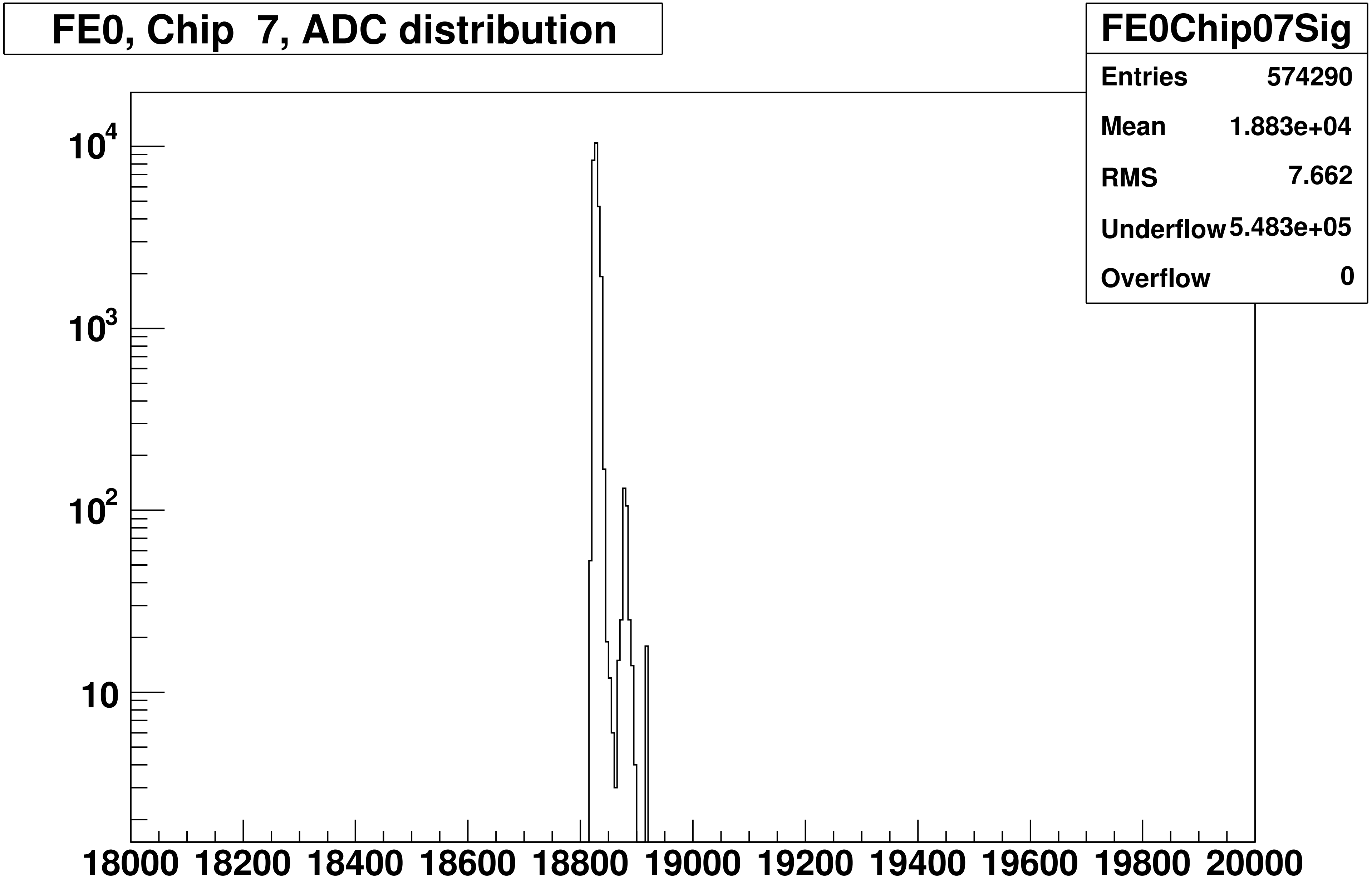
100
150
200


FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 574290
Mean 1.883e+04
RMS 7.662
Underflow 5.483e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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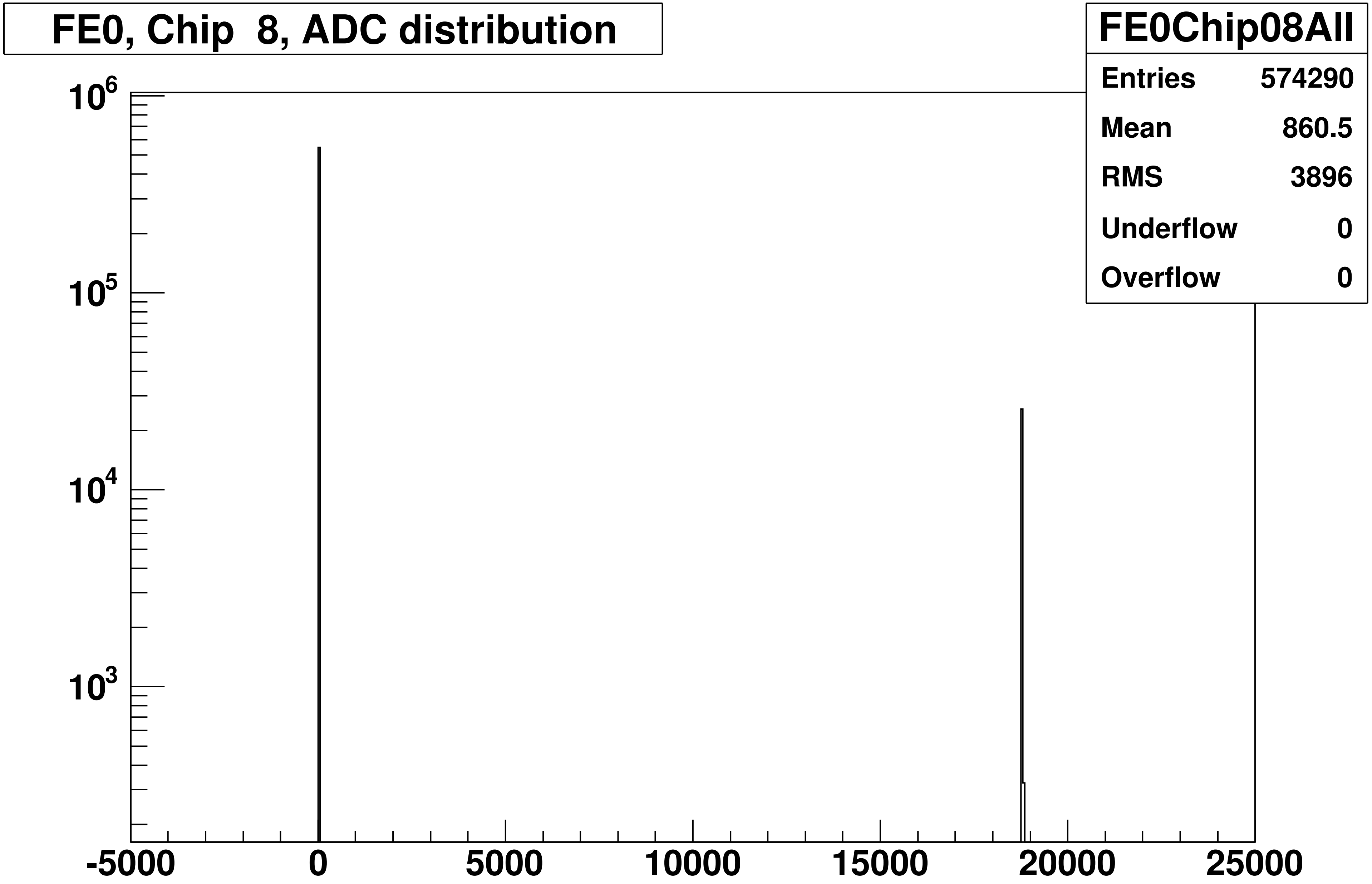
19600
19800
20000


FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 574290
Mean 860.5
RMS 3896
Underflow 0
Overflow 0
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10^5
10^4
10^3
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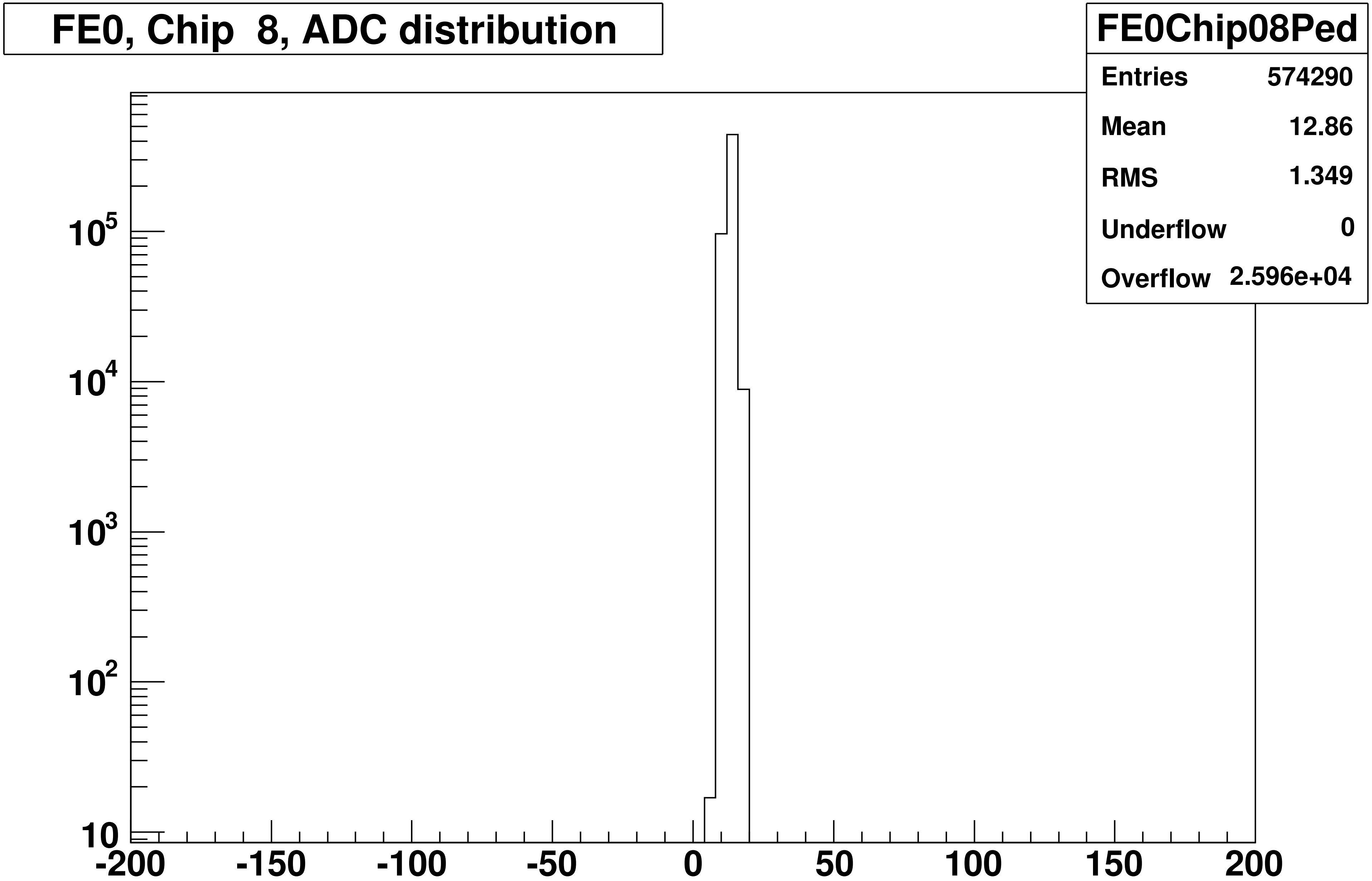
15000
20000
25000



FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 574290
Mean 12.86
RMS 1.349
Underflow 0
Overflow 2.596e+04
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10^2
10^3
10^4
10^5
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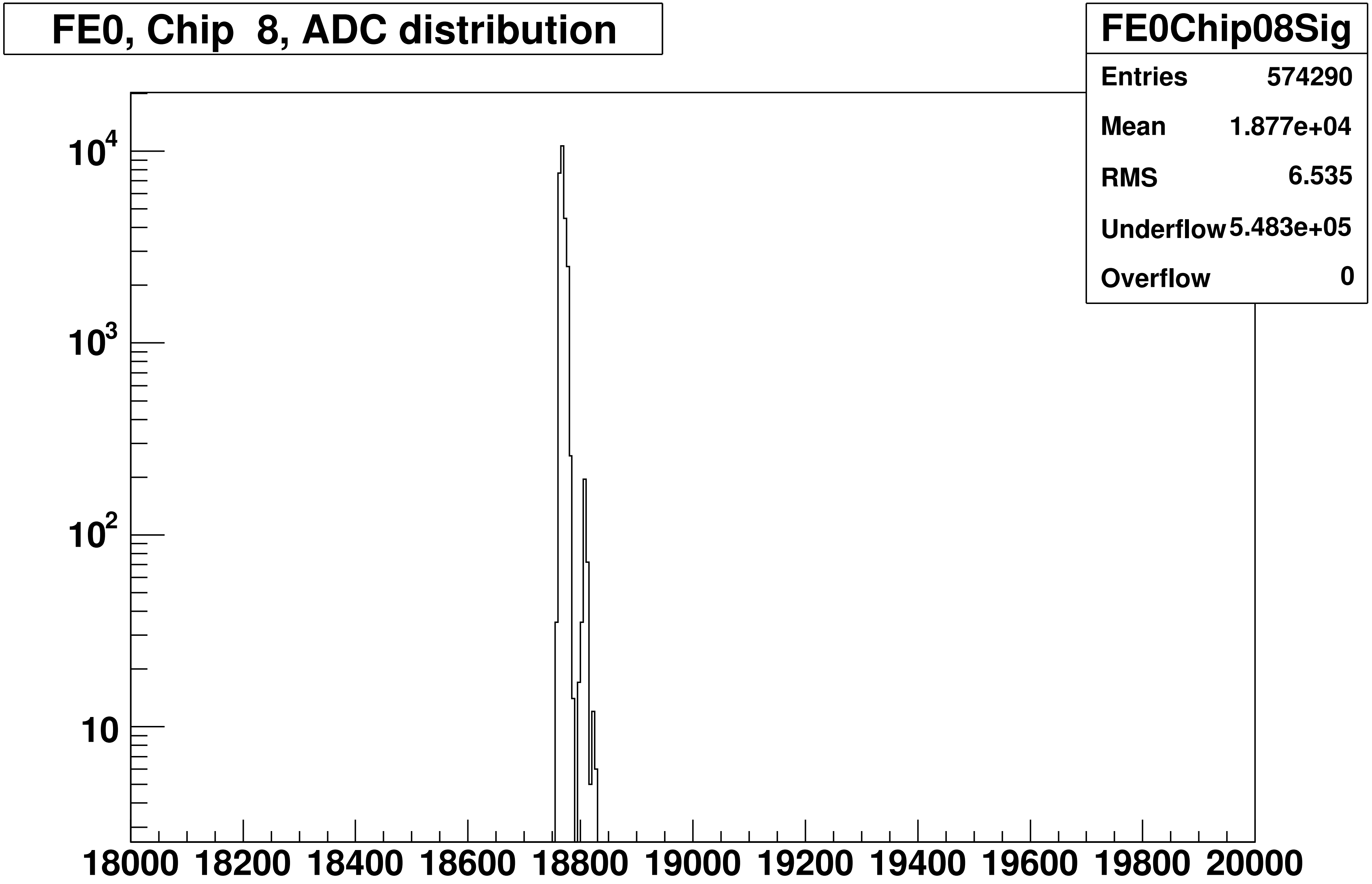
100
150
200


FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 574290
Mean 1.877e+04
RMS 6.535
Underflow 5.483e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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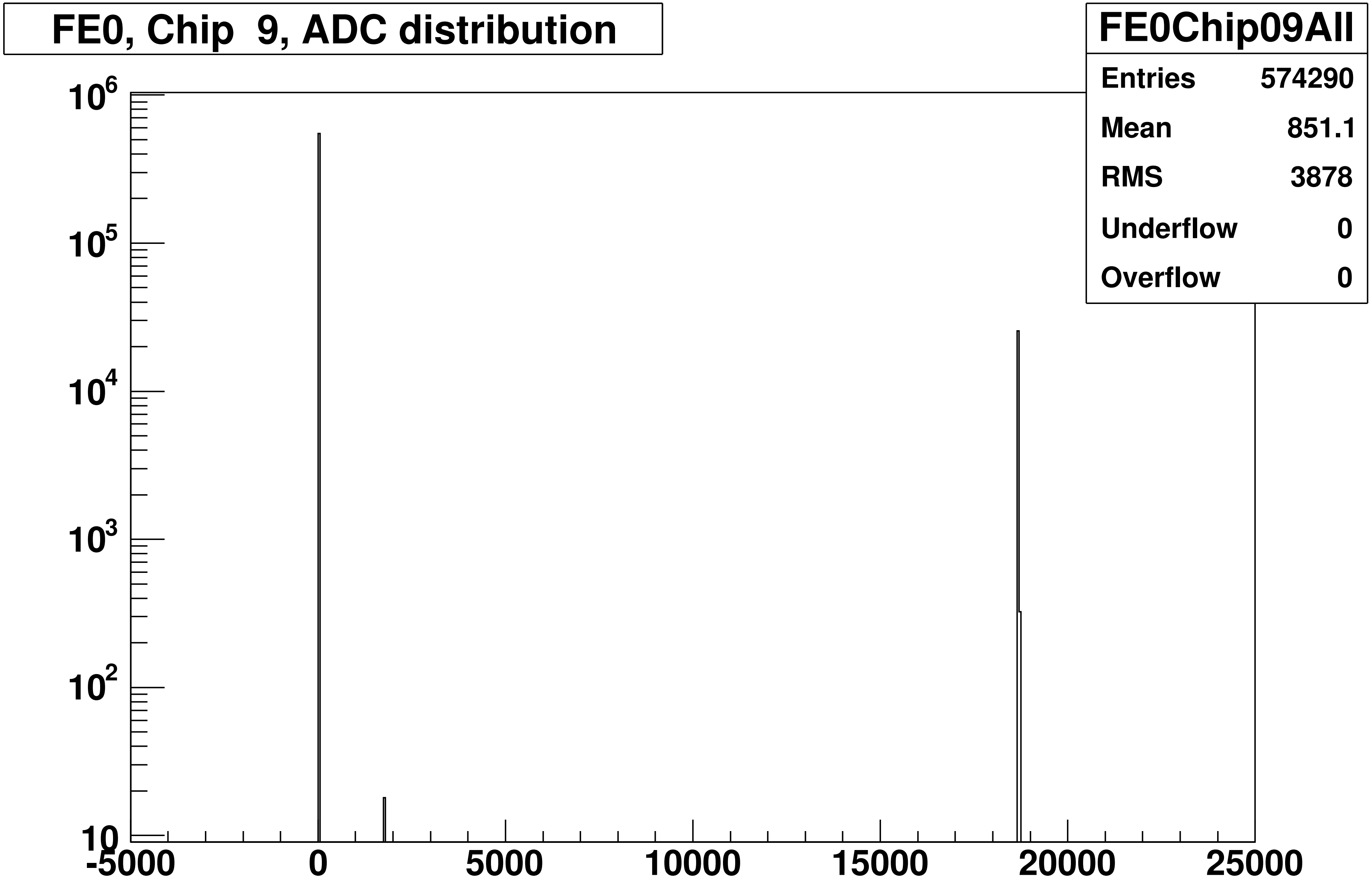
19600
19800
20000


FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 574290
Mean 851.1
RMS 3878
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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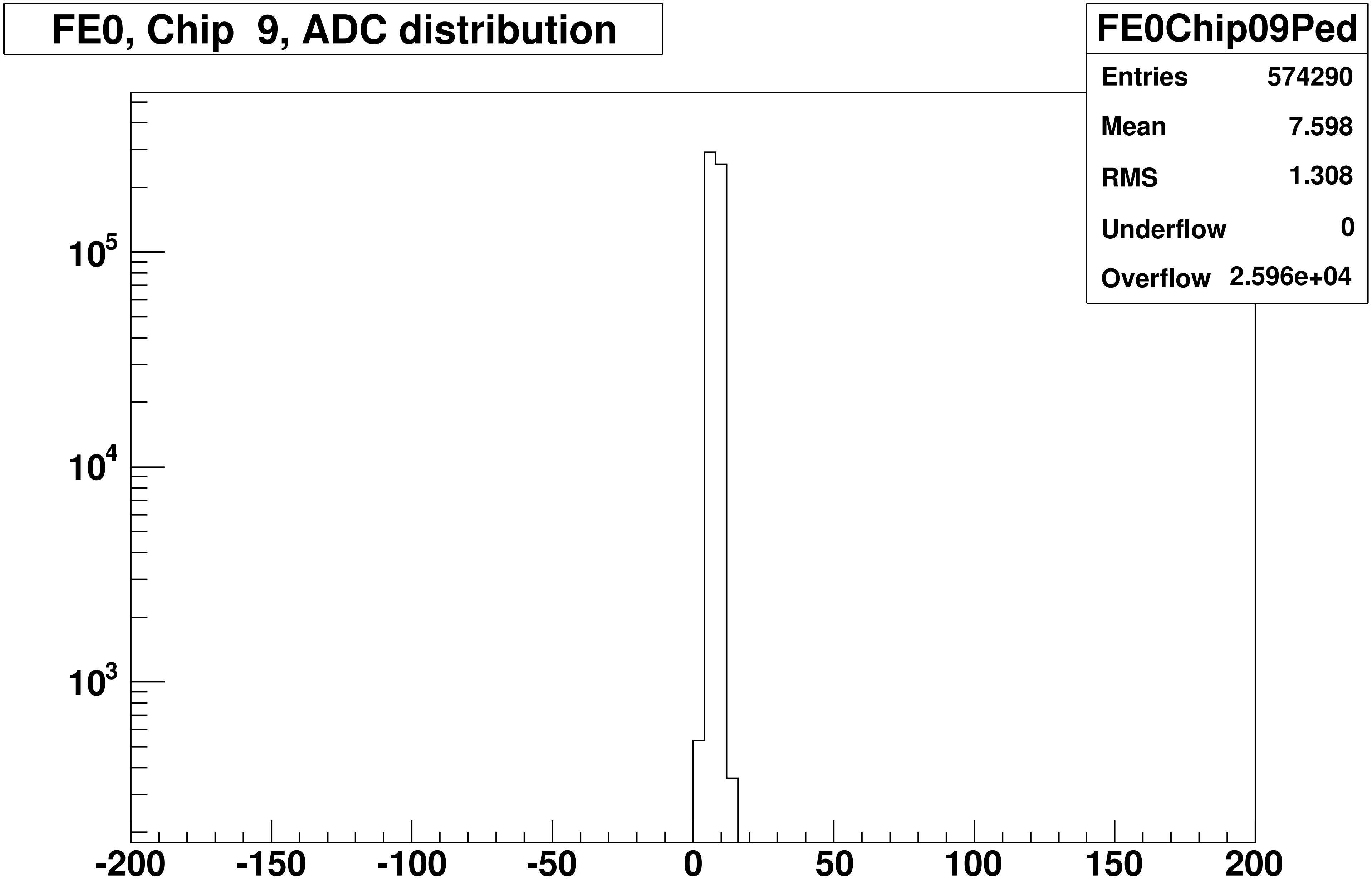
20000
25000



FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 574290
Mean 7.598
RMS 1.308
Underflow 0
Overflow 2.596e+04
10^5
10^4
10^3
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150
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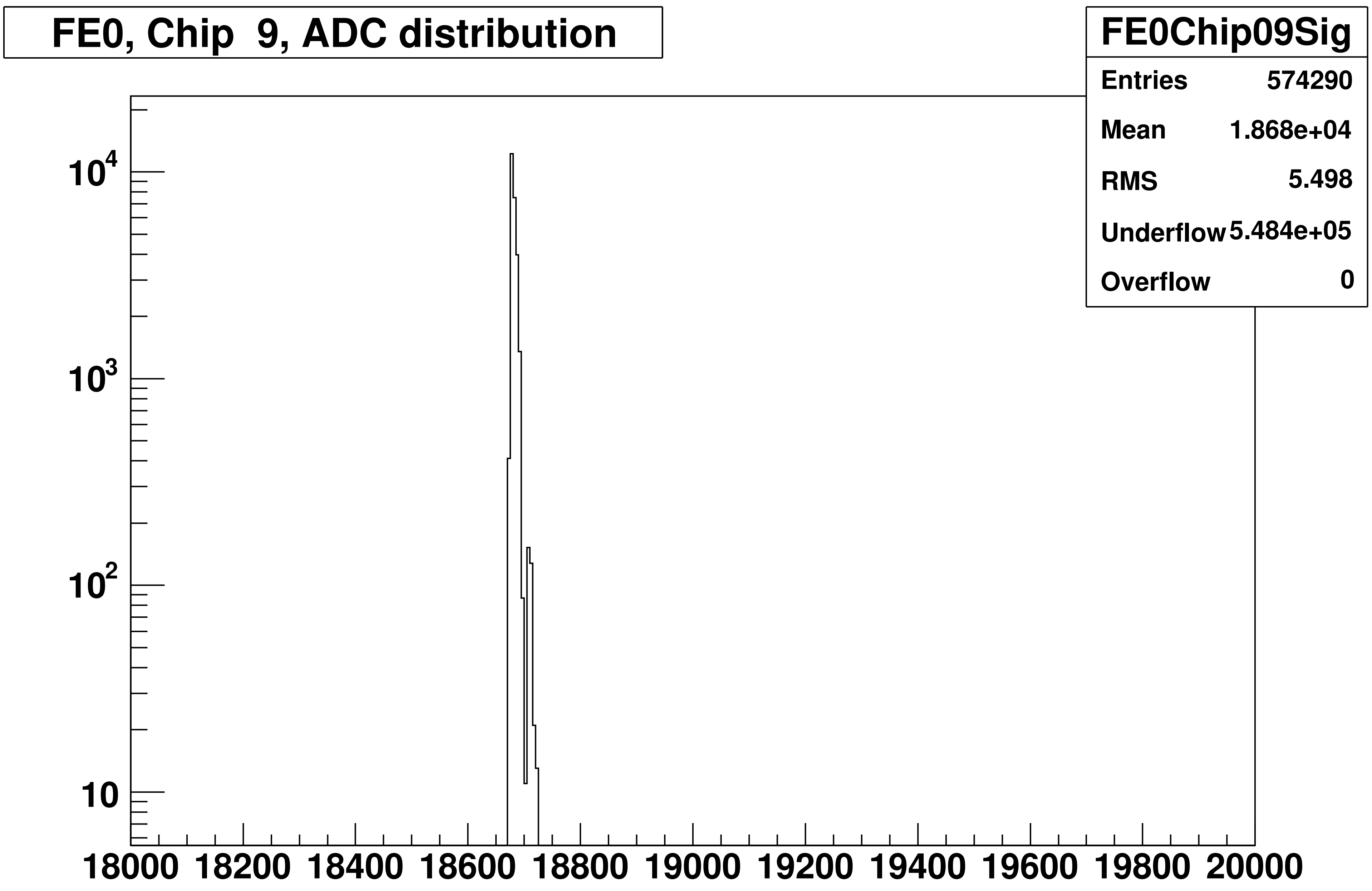
FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 574290
Mean 1.868e+04
RMS 5.498
Underflow 5.484e+05
Overflow 0
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10^3
10^2
10
18000
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18400
18600
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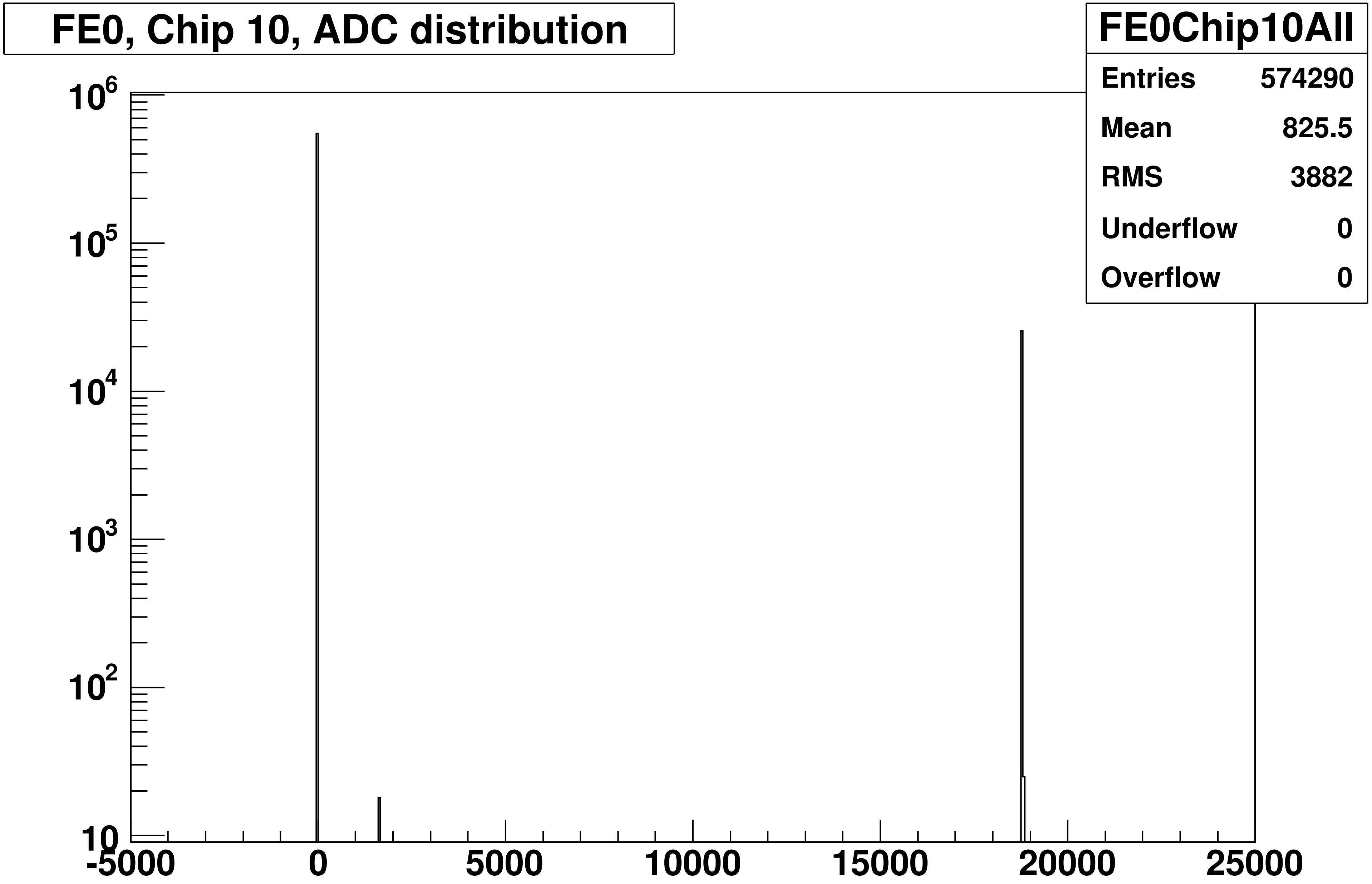
FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 574290
Mean 825.5
RMS 3882
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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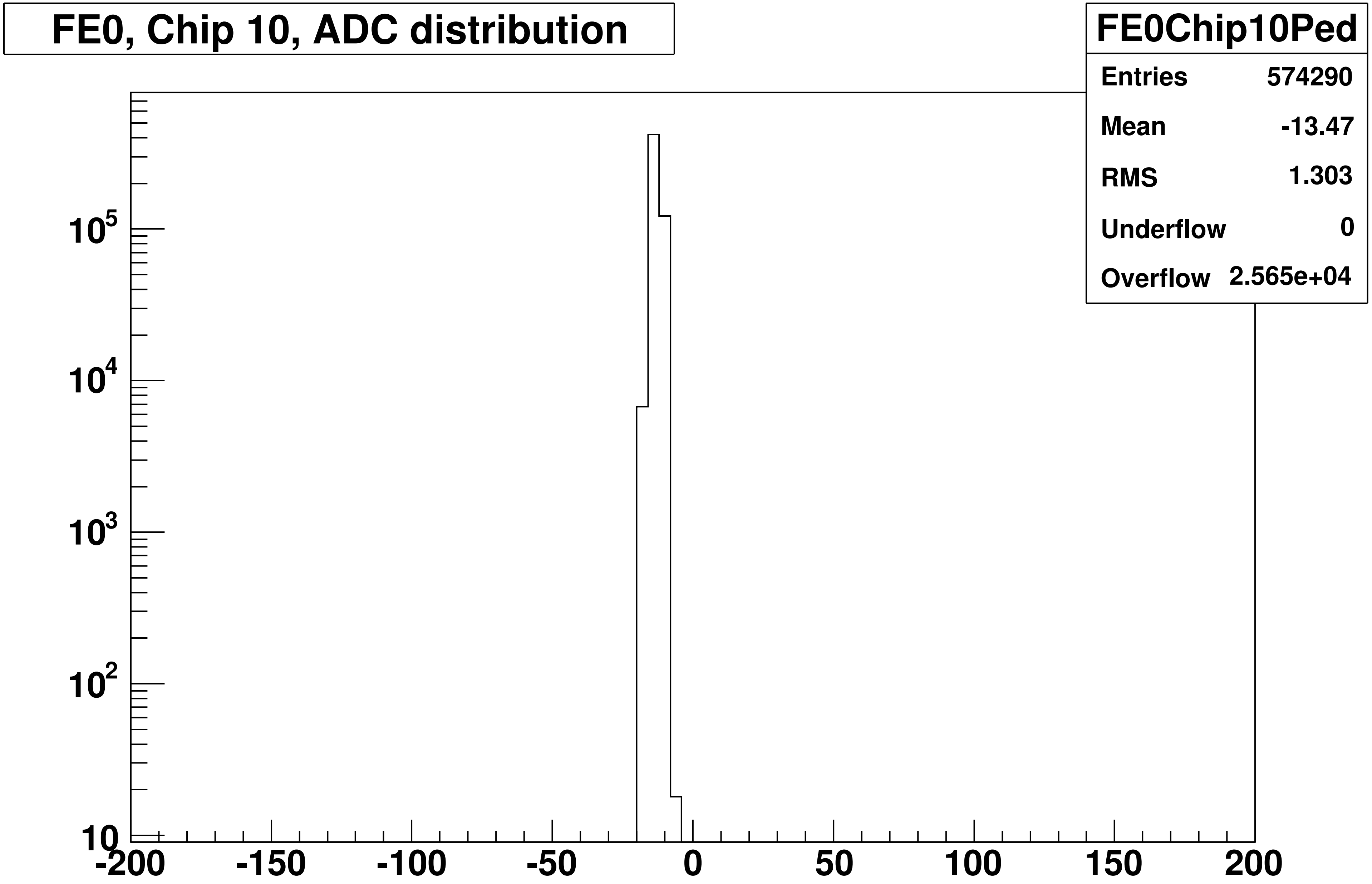
10000
15000
20000
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 574290
Mean -13.47
RMS 1.303
Underflow 0
Overflow 2.565e+04
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10^4
10^3
10^2
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-100
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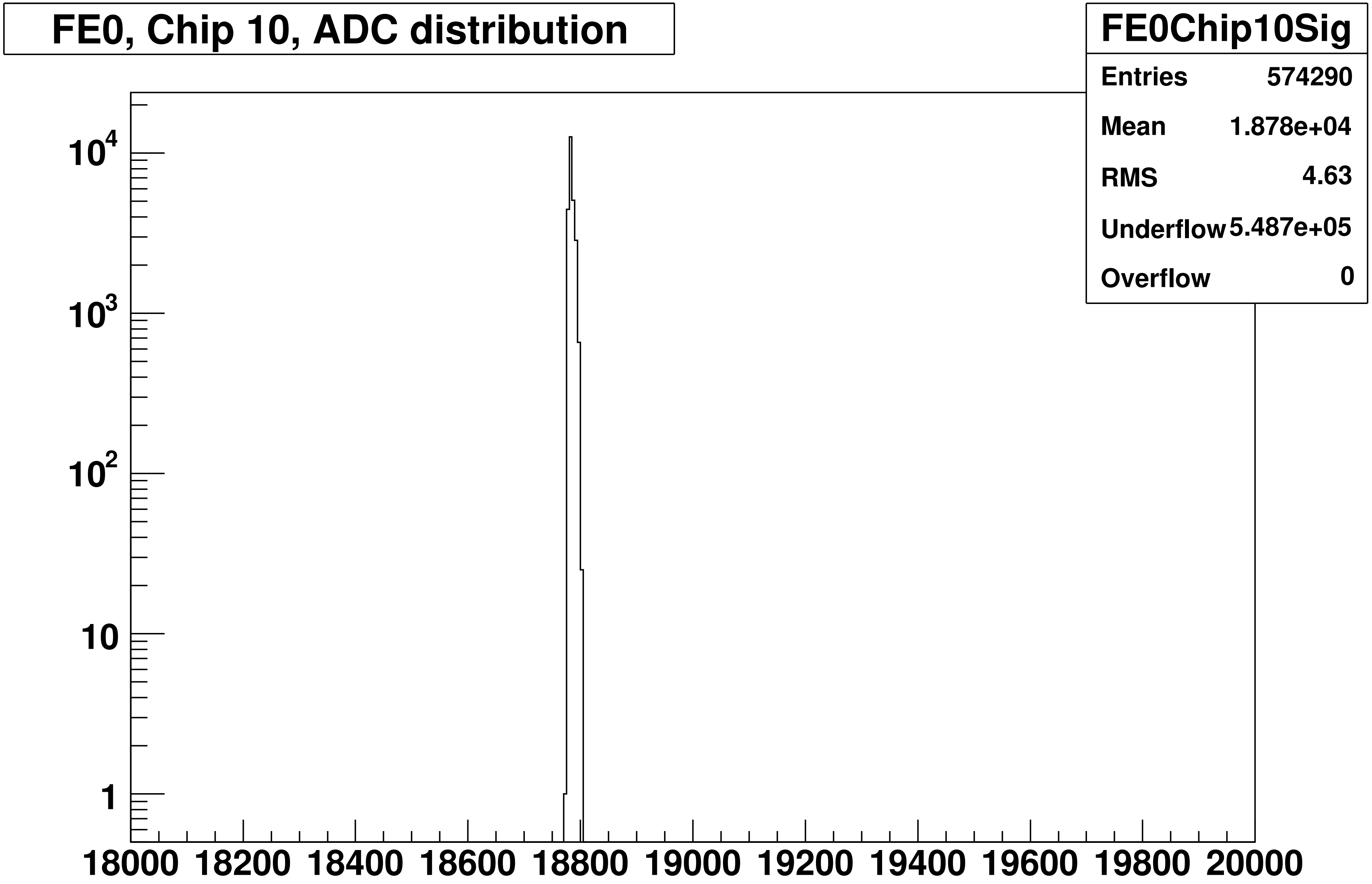
150
200


FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 574290
Mean 1.878e+04
RMS 4.63
Underflow 5.487e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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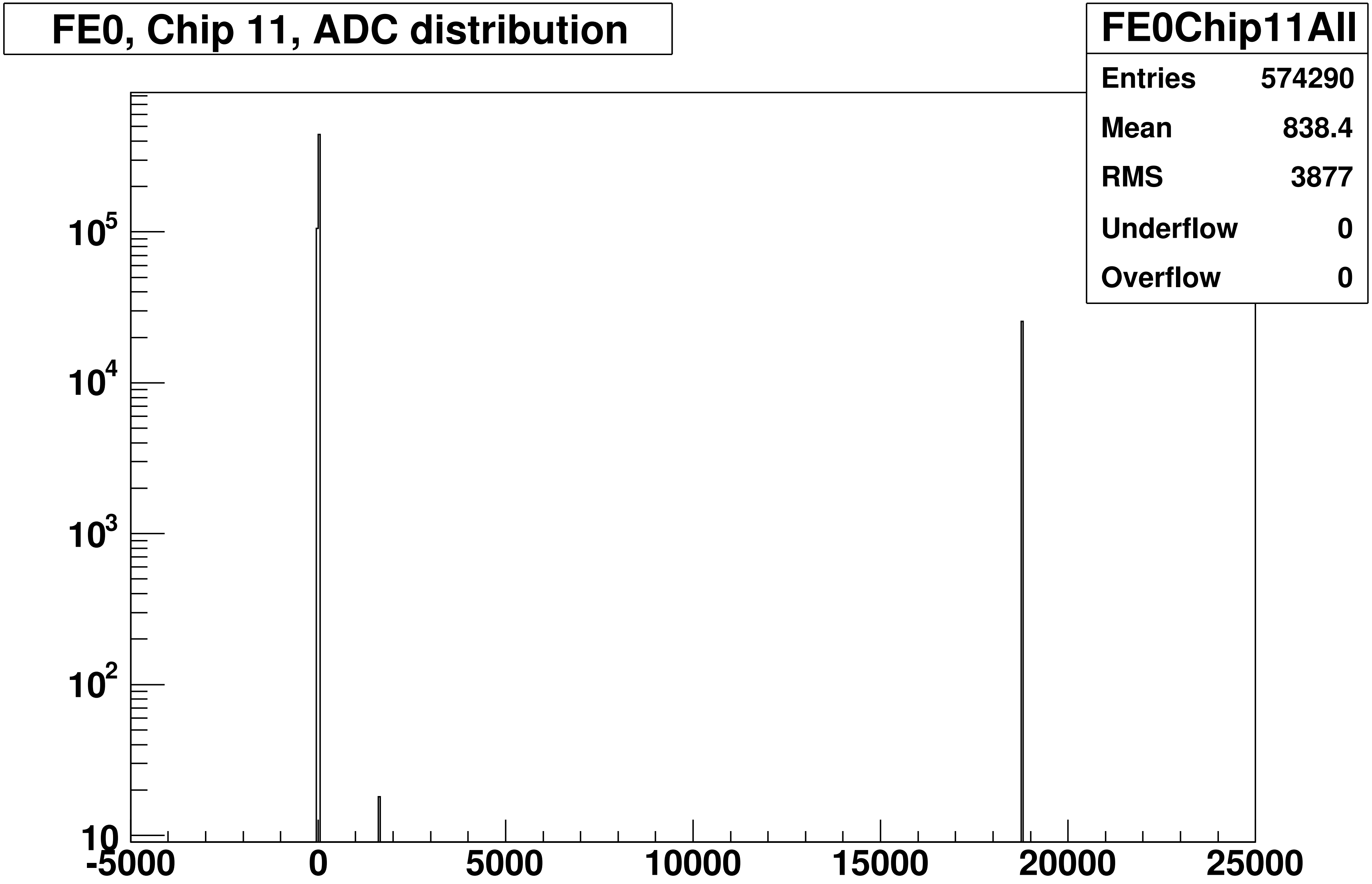
20000


FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 574290
Mean 838.4
RMS 3877
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
-5000
0
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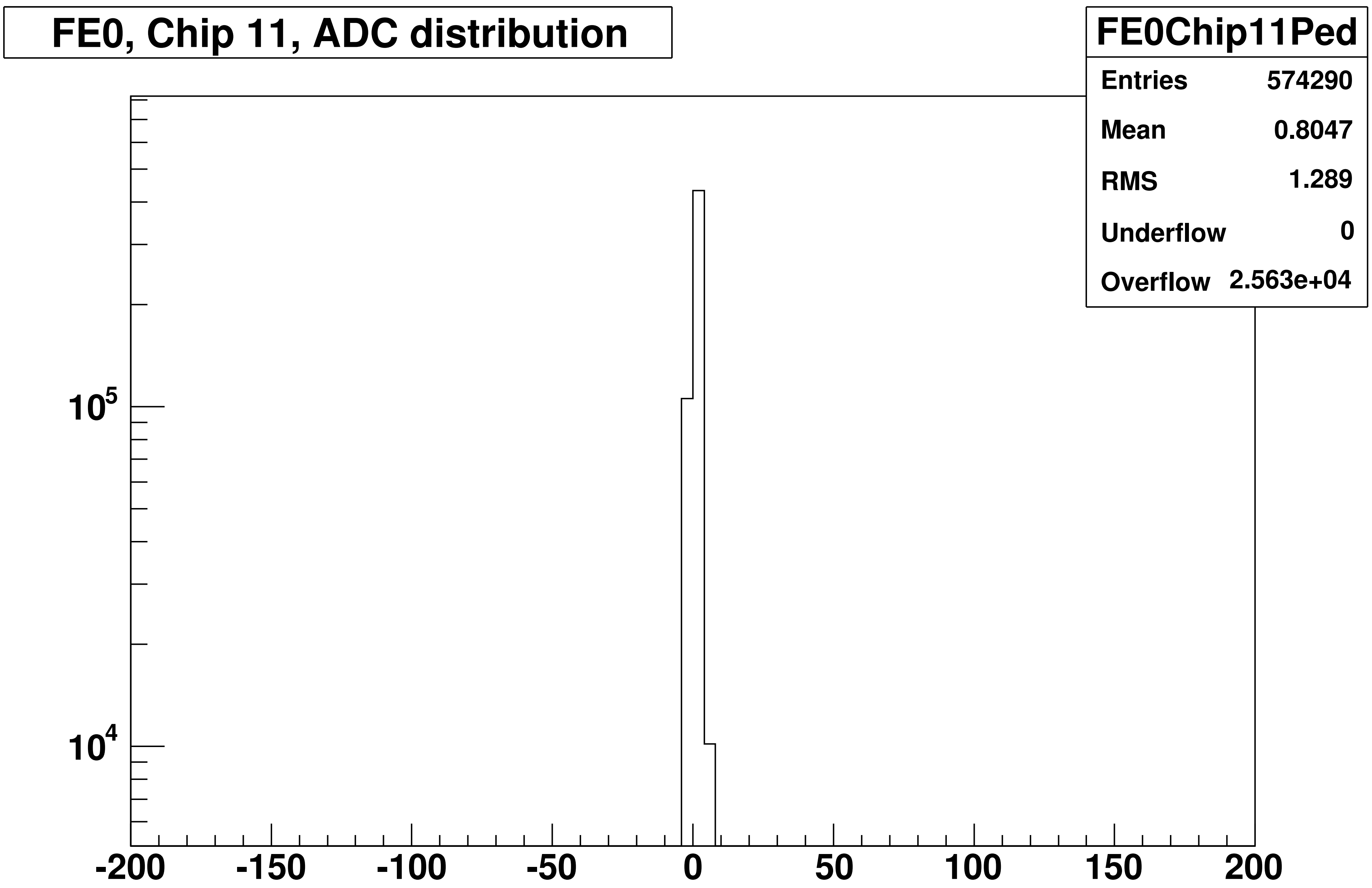
10000
15000
20000
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 574290
Mean 0.8047
RMS 1.289
Underflow 0
Overflow 2.563e+04
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10^4
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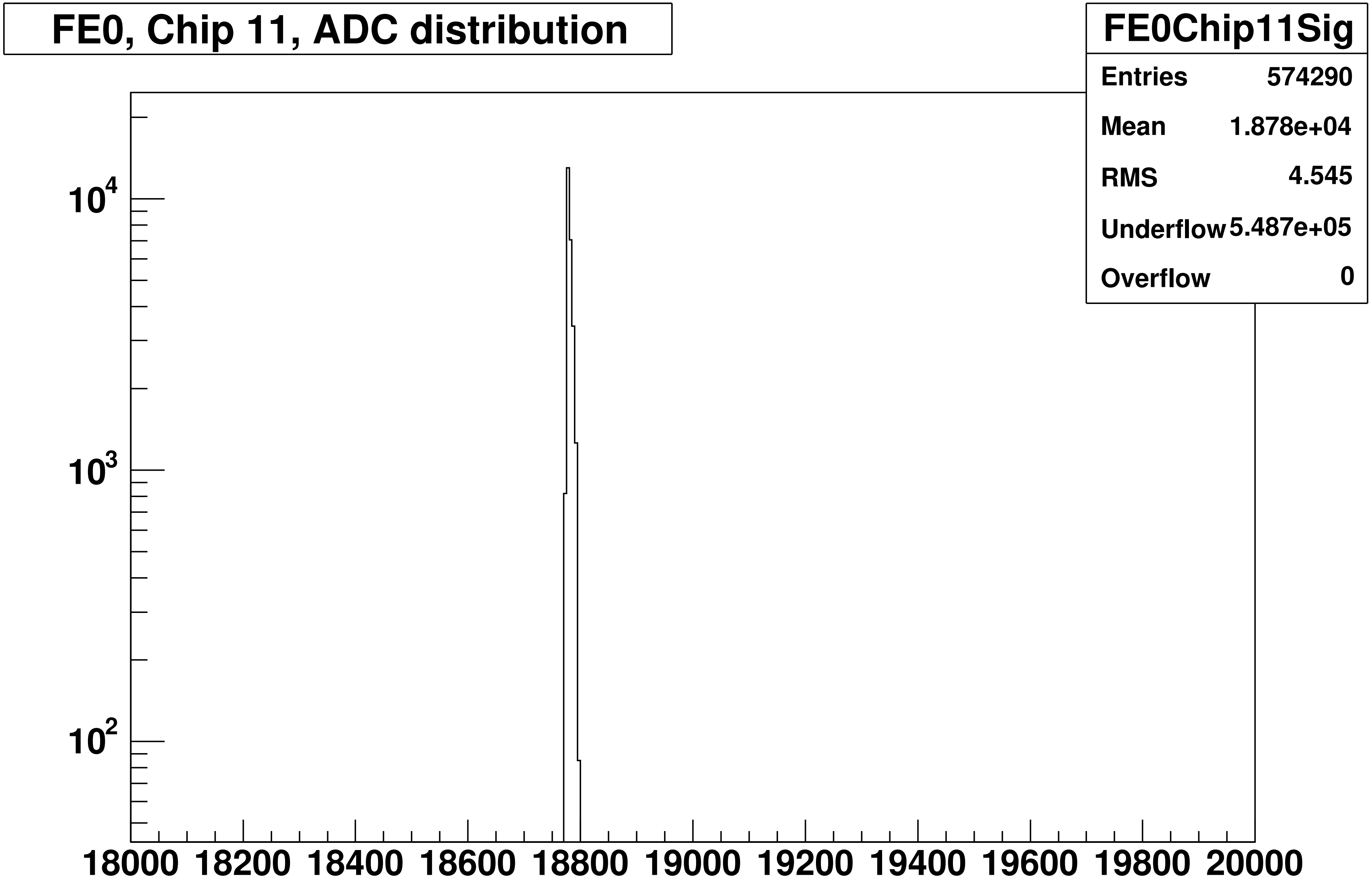
-100
-50
0
50
100
150
200



FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 574290
Mean 1.878e+04
RMS 4.545
Underflow 5.487e+05
Overflow 0
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10^3
10^2
18000
18200
18400
18600
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19000
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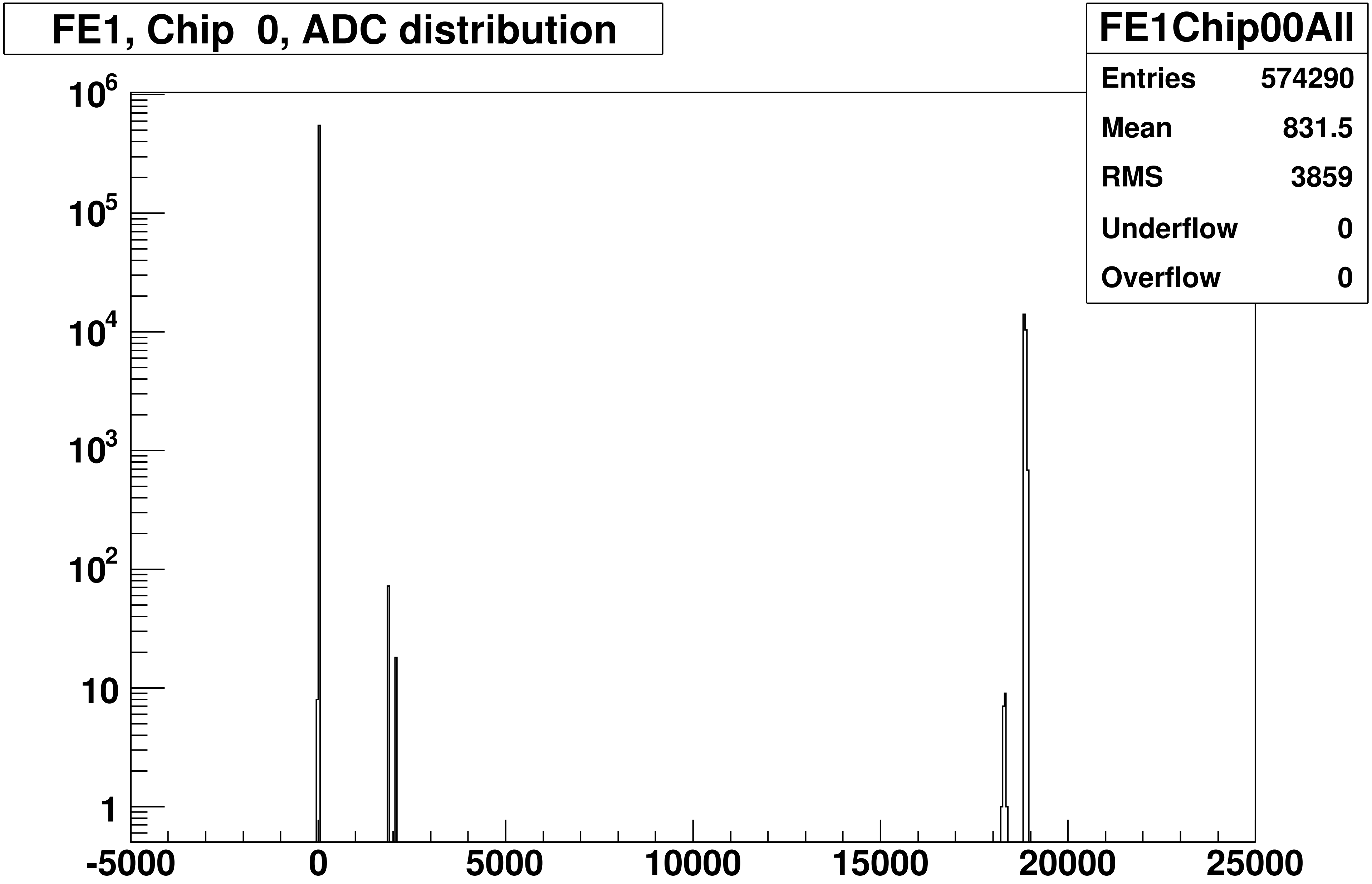
19600
19800
20000



FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 574290
Mean 831.5
RMS 3859
Underflow 0
Overflow 0
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10^4
10^3
10^2
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5000
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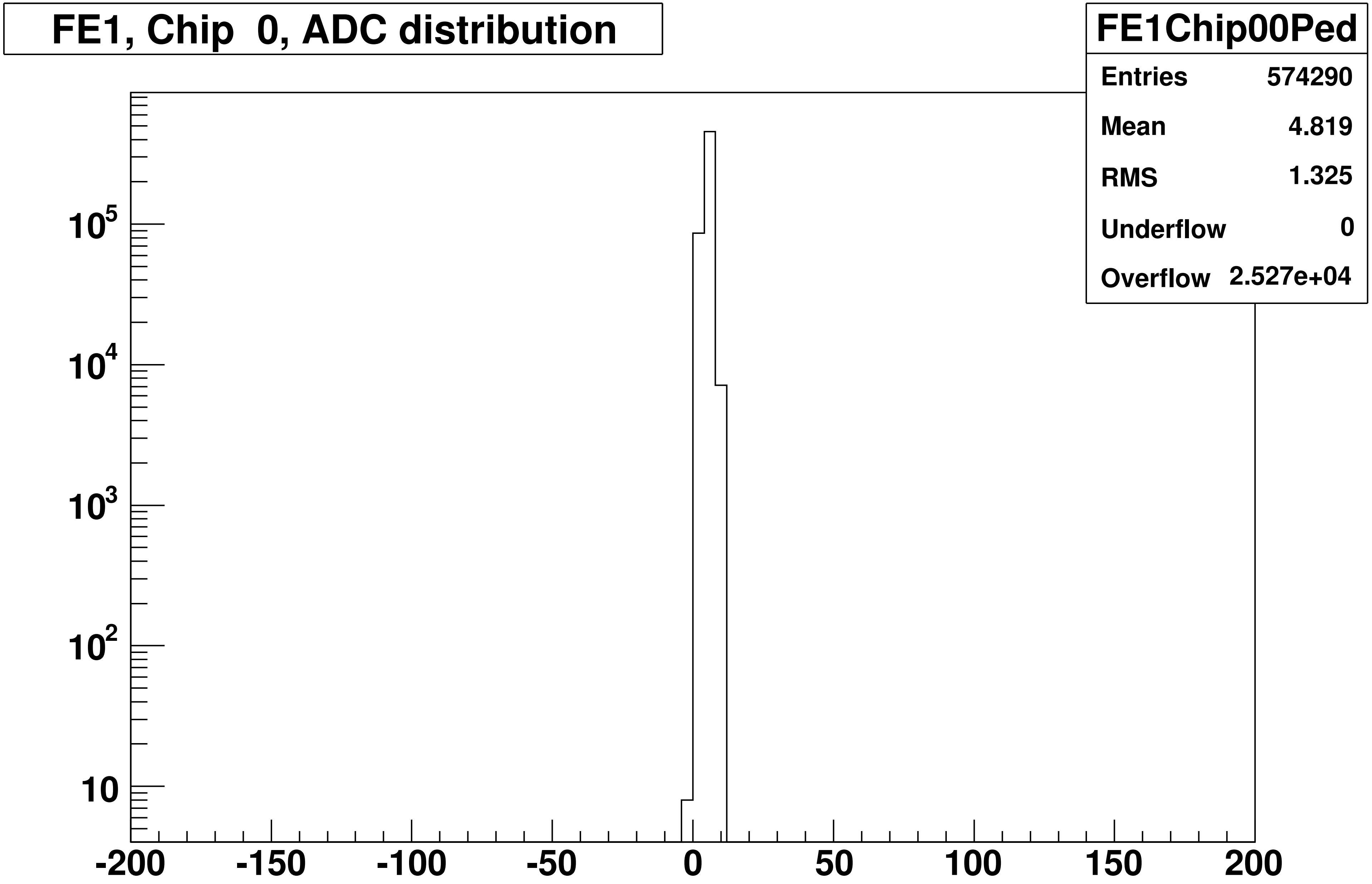
20000
25000


FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 574290
Mean 4.819
RMS 1.325
Underflow 0
Overflow 2.527e+04
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10^3
10^4
10^5
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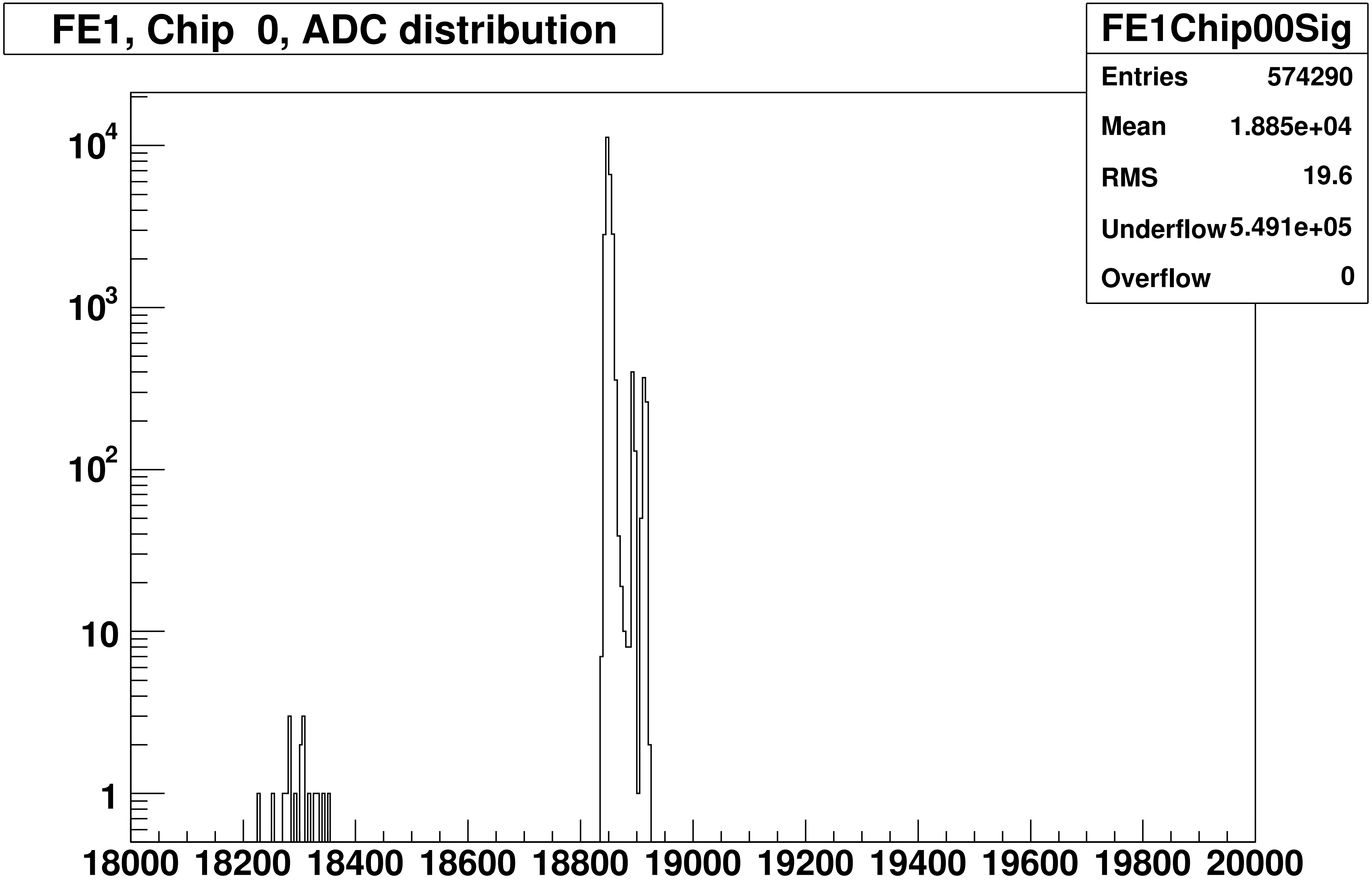
-100
-50
0
50
100
150
200


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 574290
Mean 1.885e+04
RMS 19.6
Underflow 5.491e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
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18400
18600
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19000
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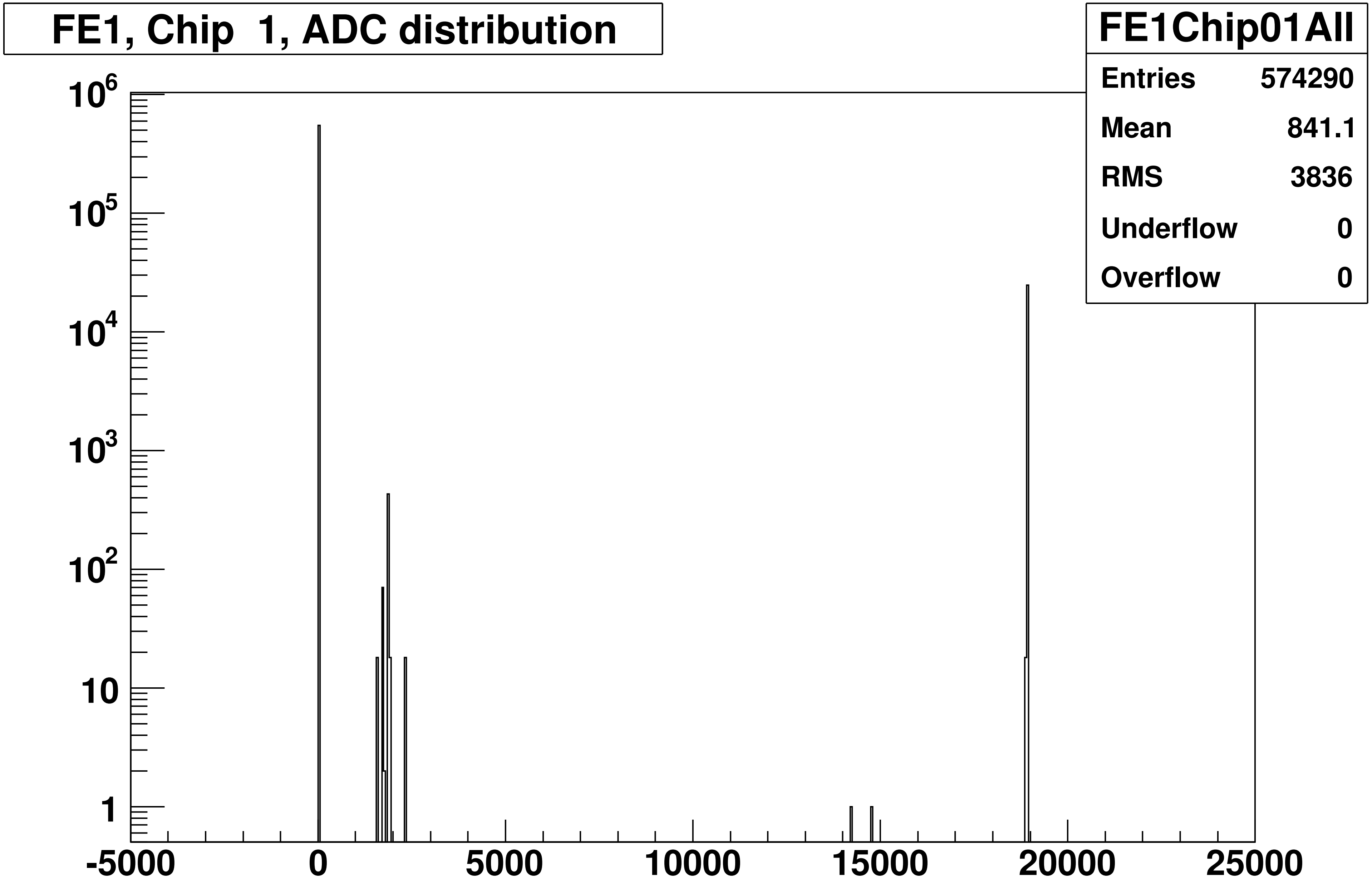
19400
19600
19800
20000



FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 574290
Mean 841.1
RMS 3836
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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-5000
0
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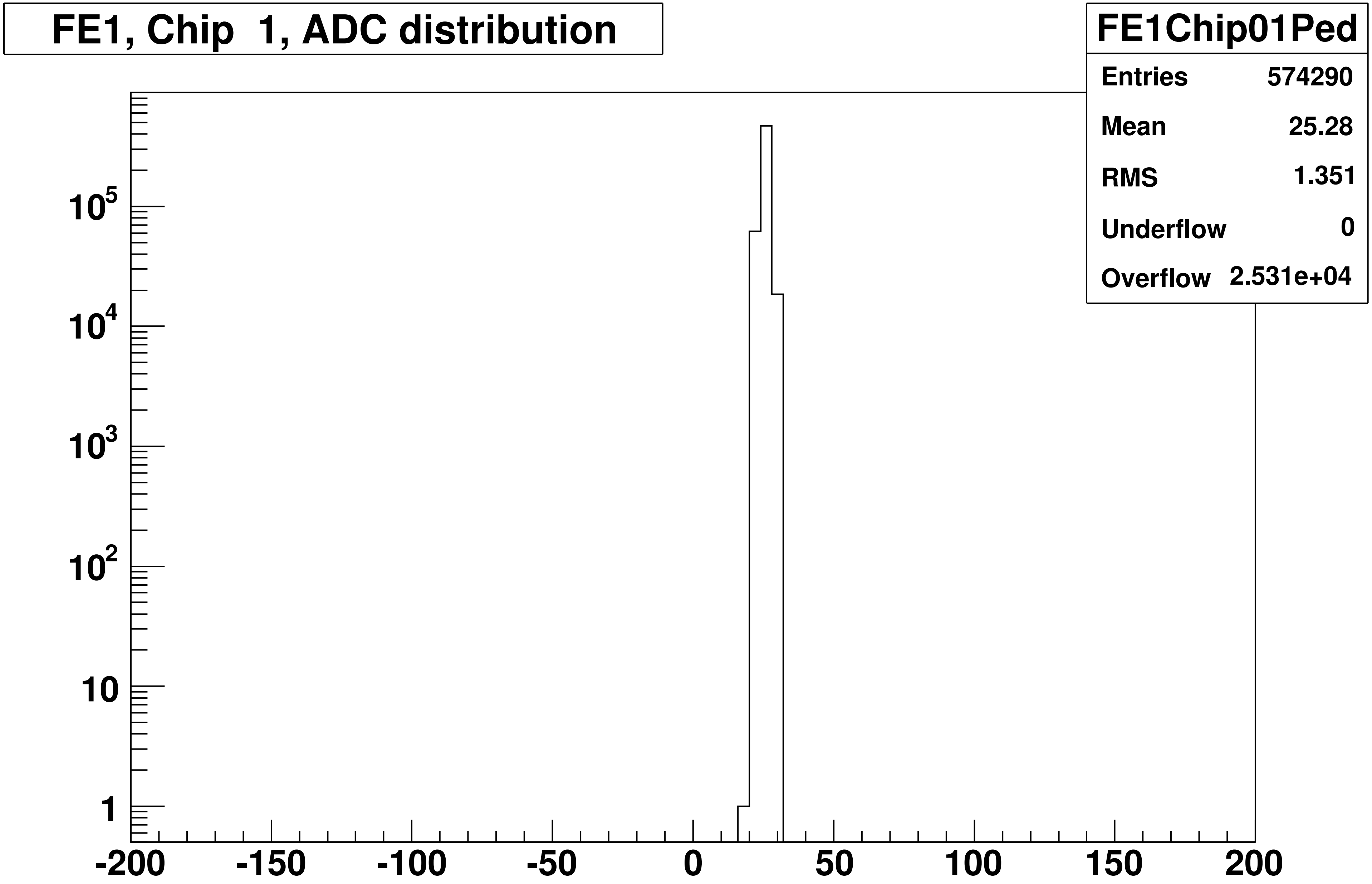
15000
20000
25000



FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 574290
Mean 25.28
RMS 1.351
Underflow 0
Overflow 2.531e+04
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10^3
10^2
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-200
-150
-100
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100
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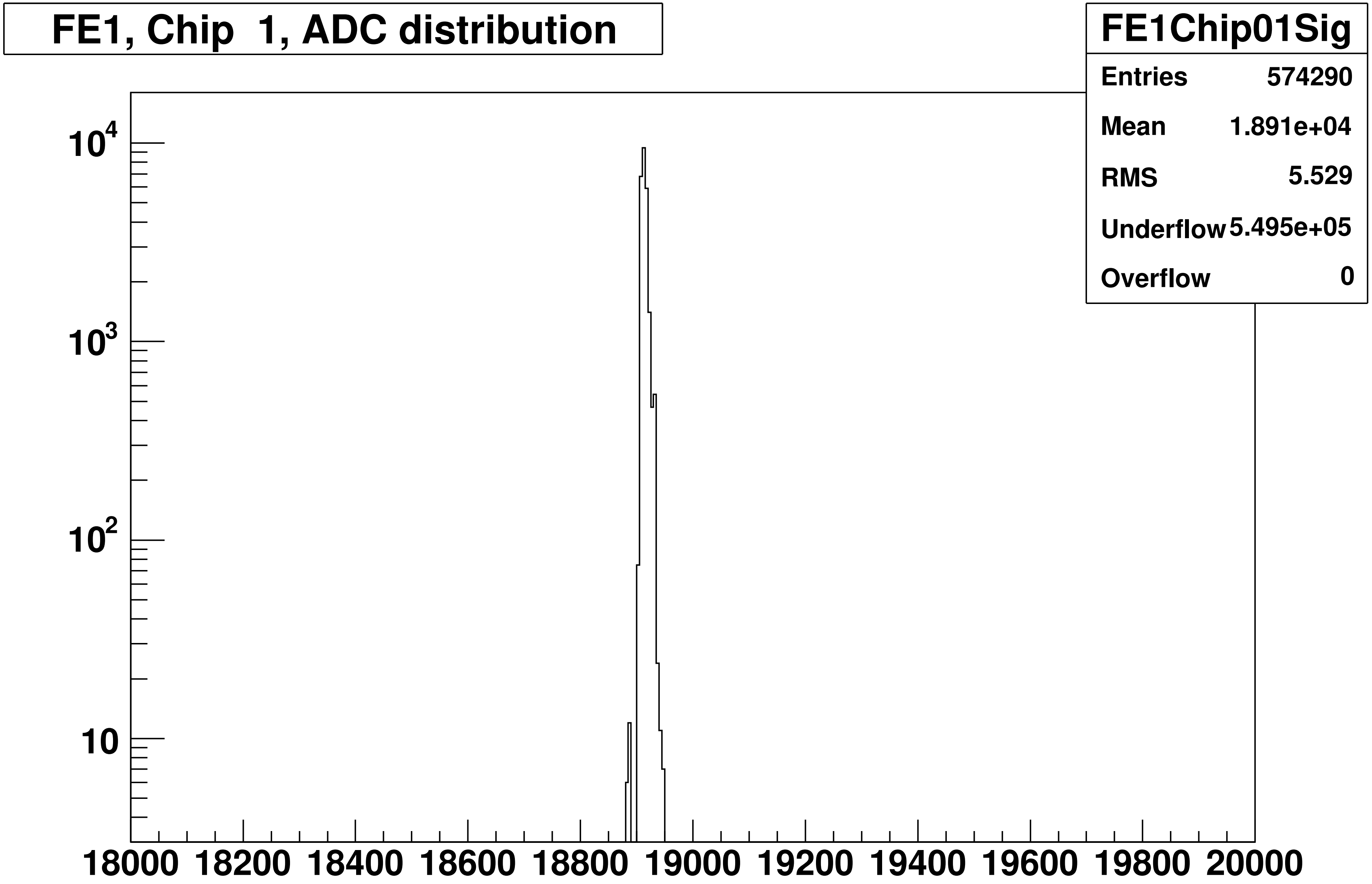
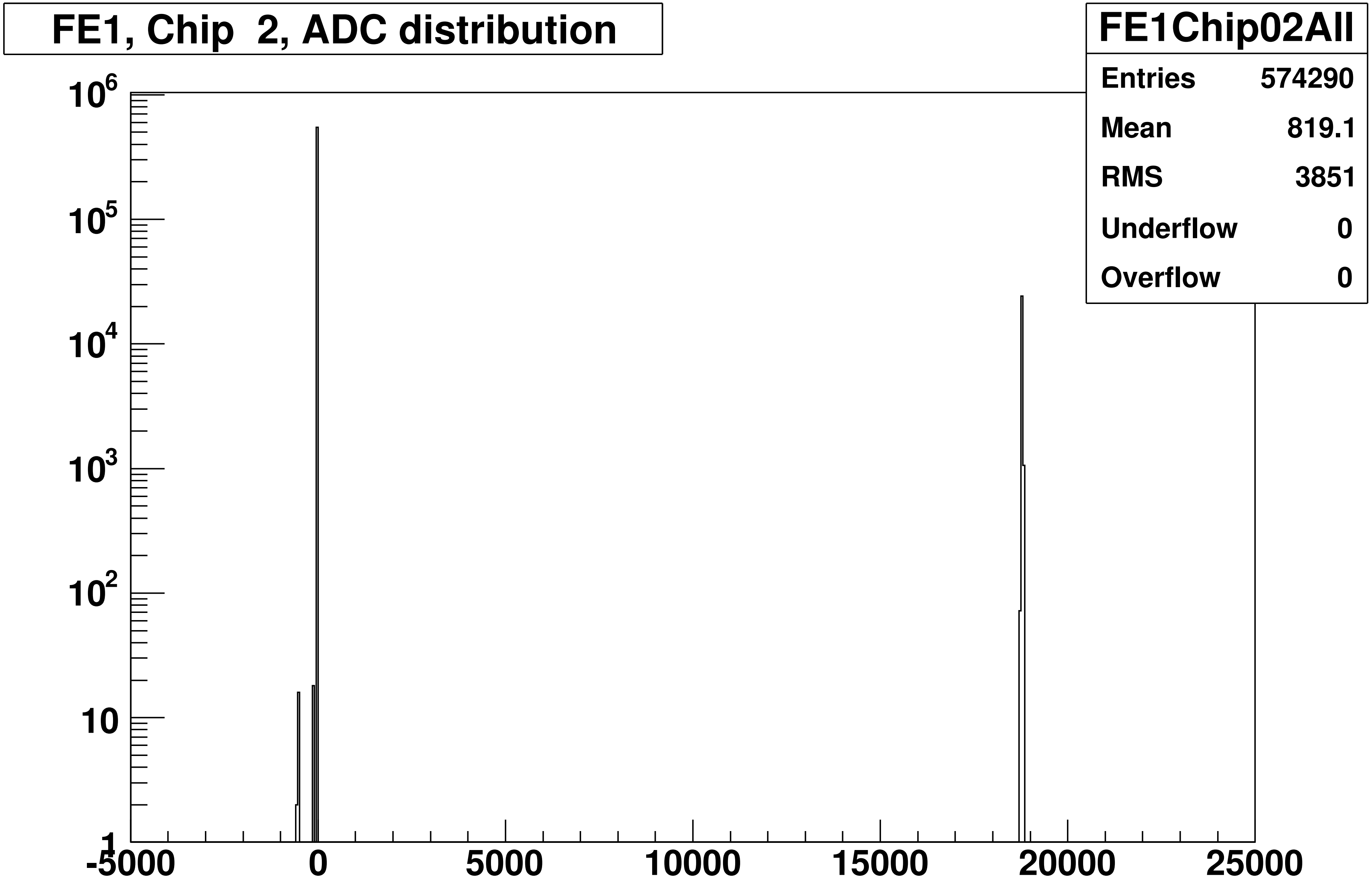

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 574290
Mean 1.891e+04
RMS 5.529
Underflow 5.495e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 574290
Mean 819.1
RMS 3851
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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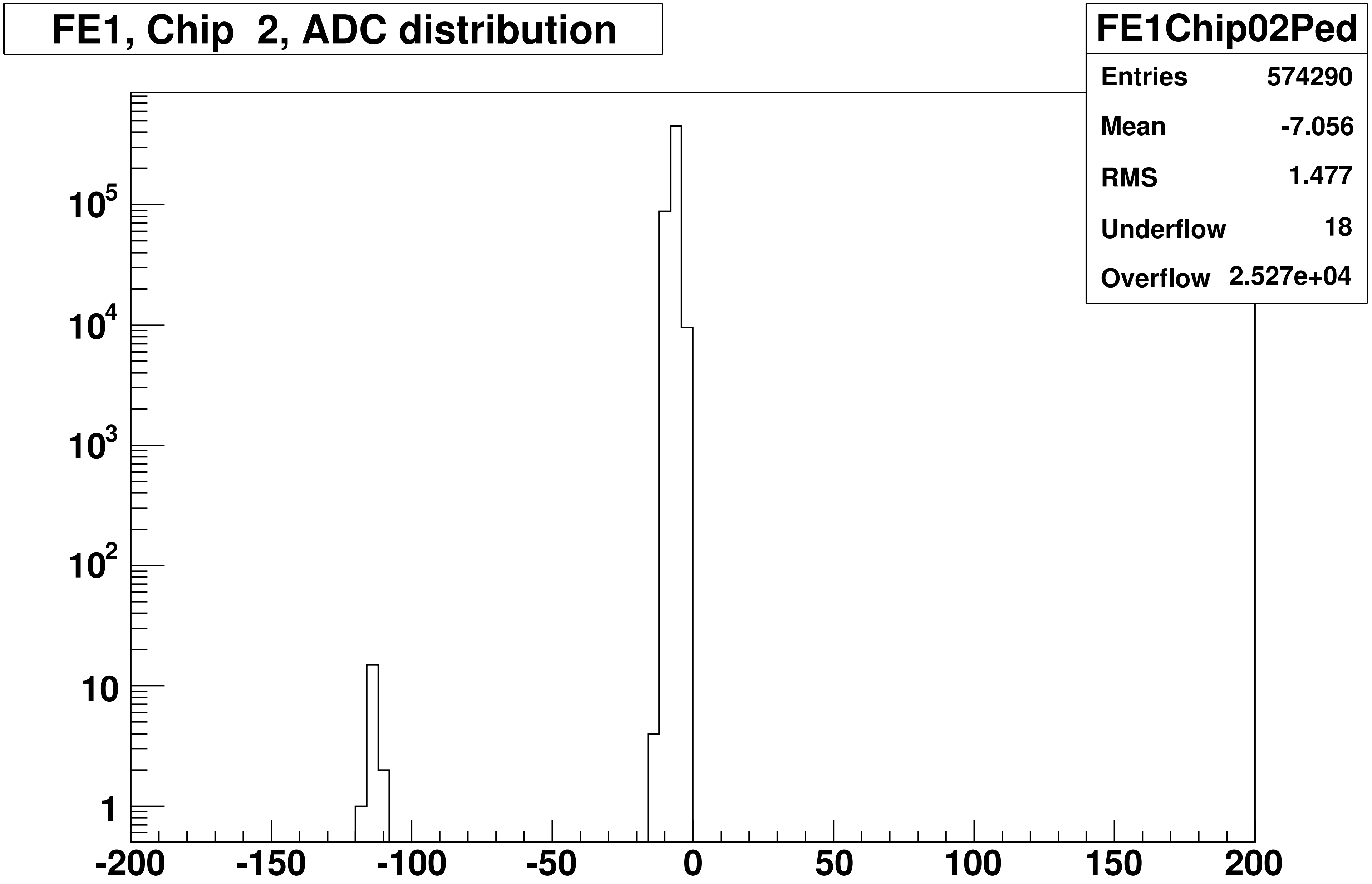
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 574290
Mean -7.056
RMS 1.477
Underflow 18
Overflow 2.527e+04
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10^4
10^3
10^2
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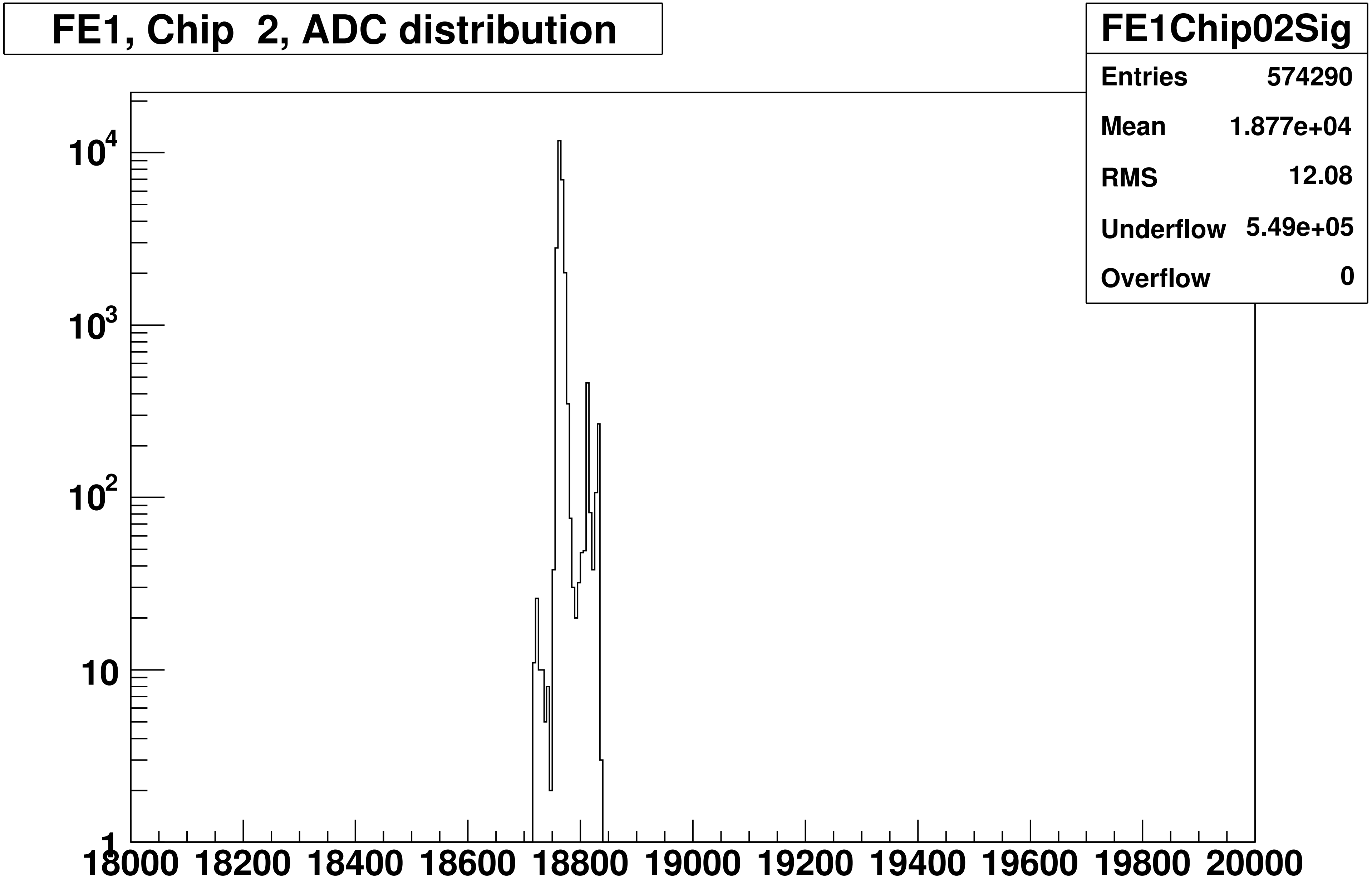

FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 574290
Mean 1.877e+04
RMS 12.08
Underflow 5.49e+05
Overflow 0
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10^3
10^2
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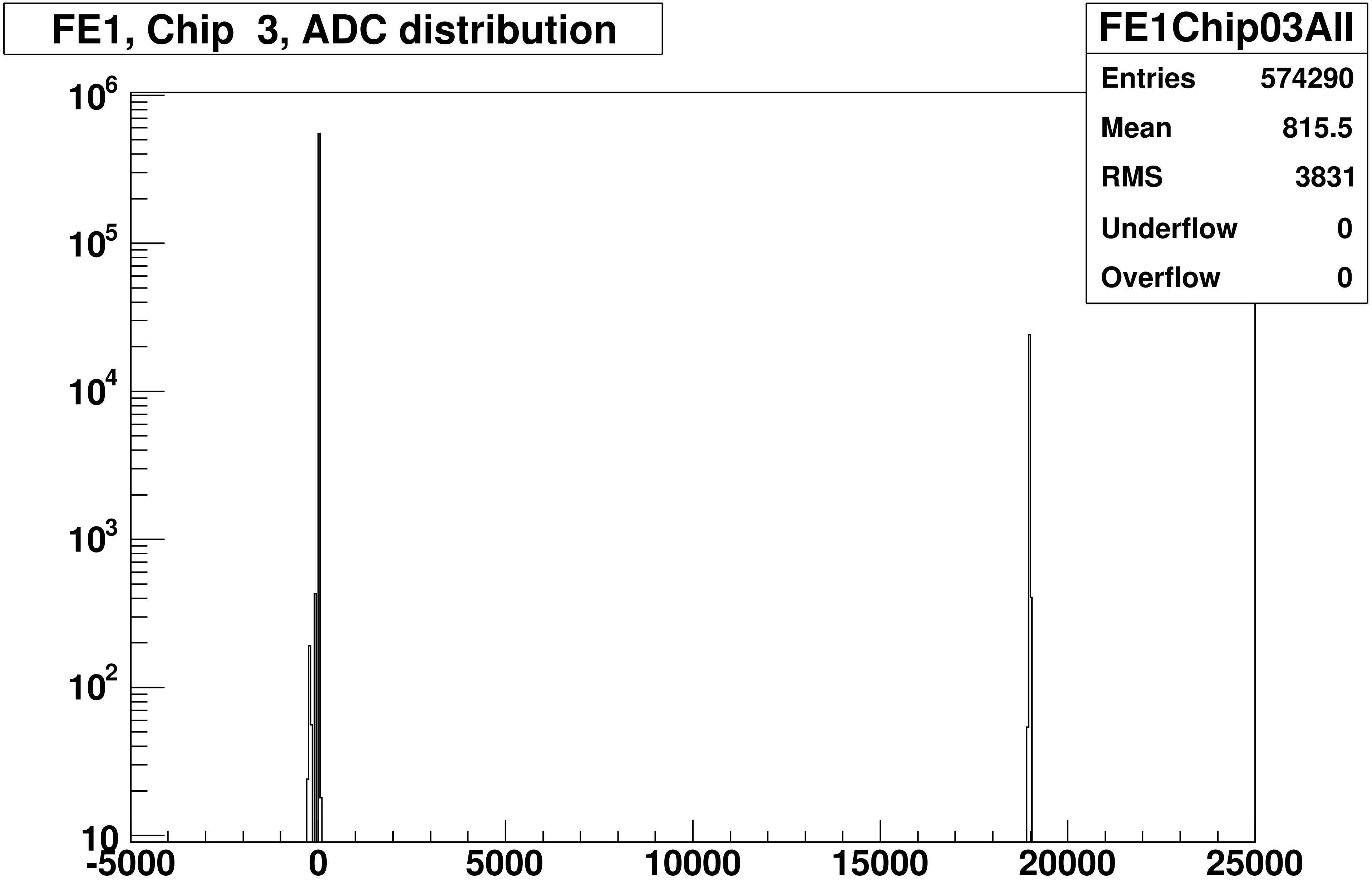
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 574290
Mean 815.5
RMS 3831
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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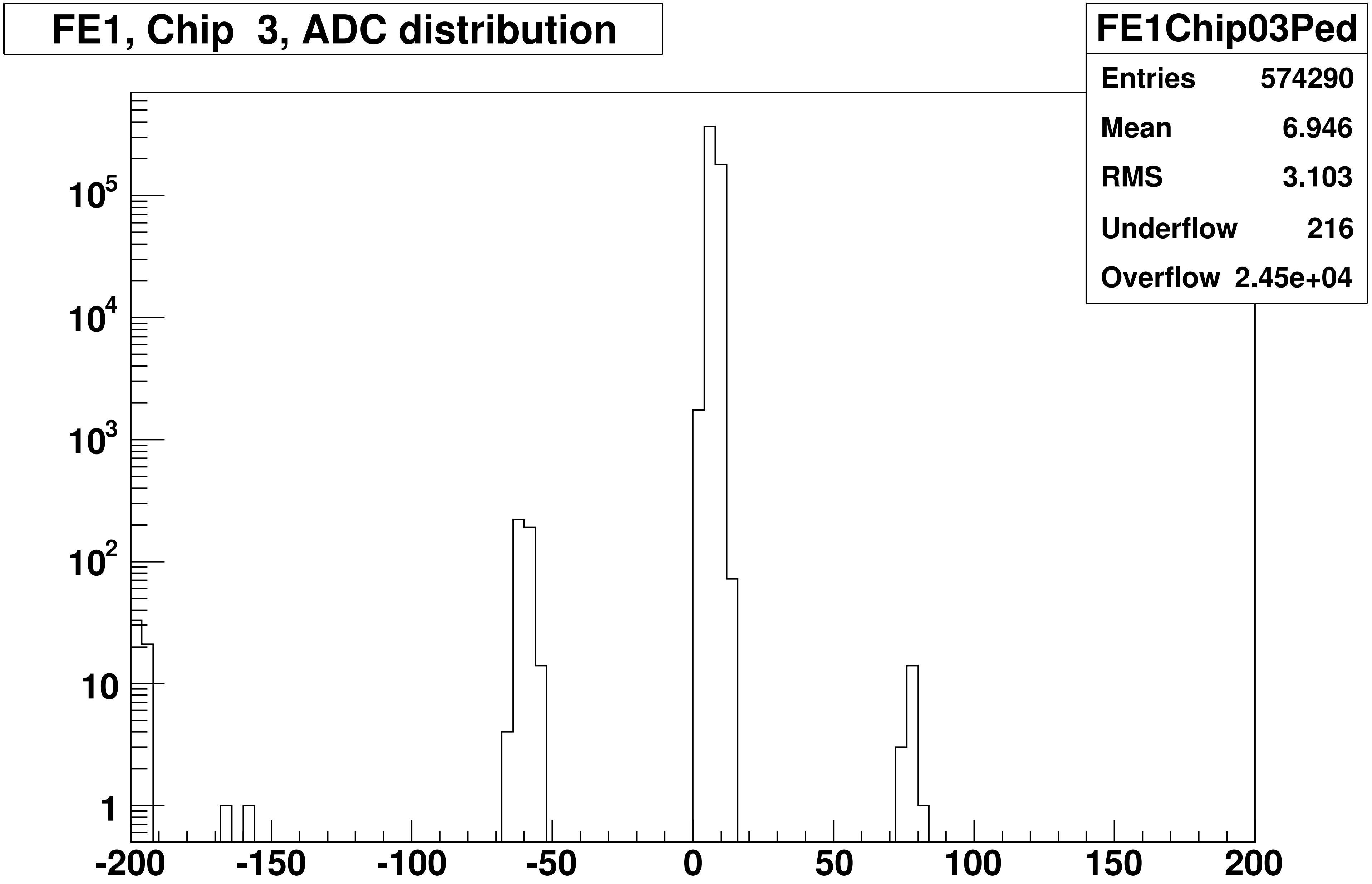
FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 574290
Mean 6.946
RMS 3.103
Underflow 216
Overflow 2.45e+04
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10^4
10^3
10^2
10
1
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-150
-100
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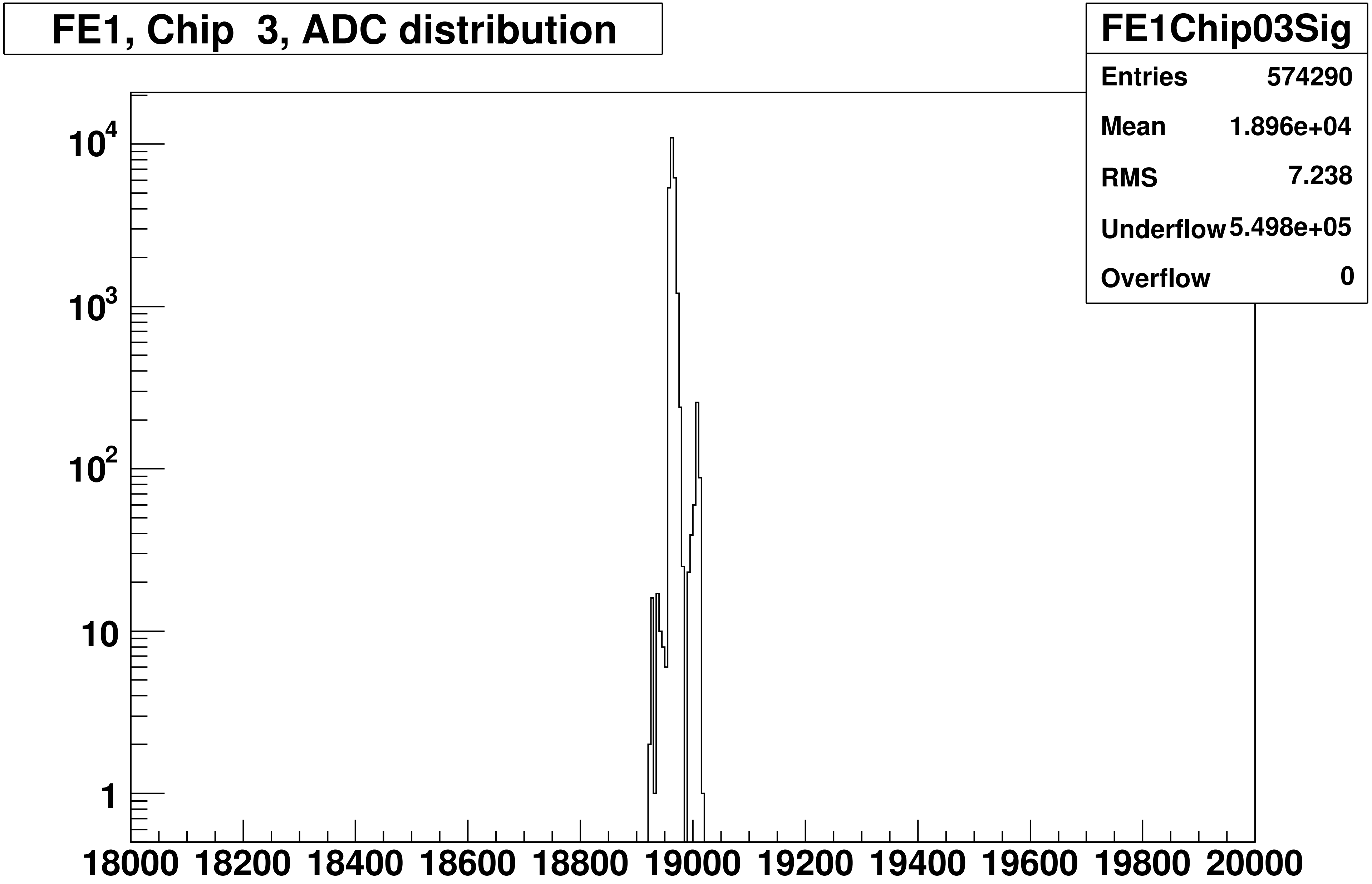
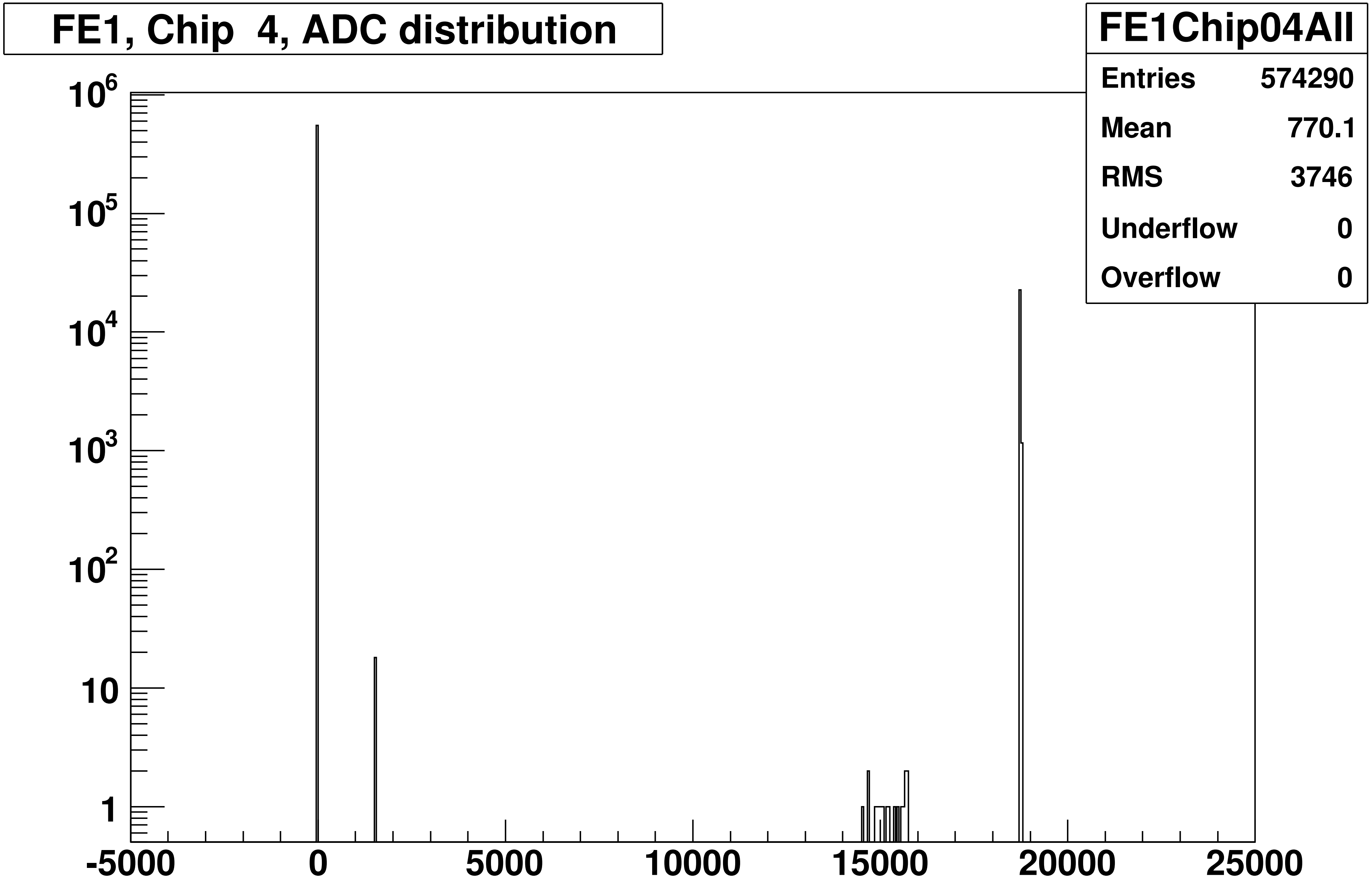
100
150
200


FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 574290
Mean 1.896e+04
RMS 7.238
Underflow 5.498e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 574290
Mean 770.1
RMS 3746
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
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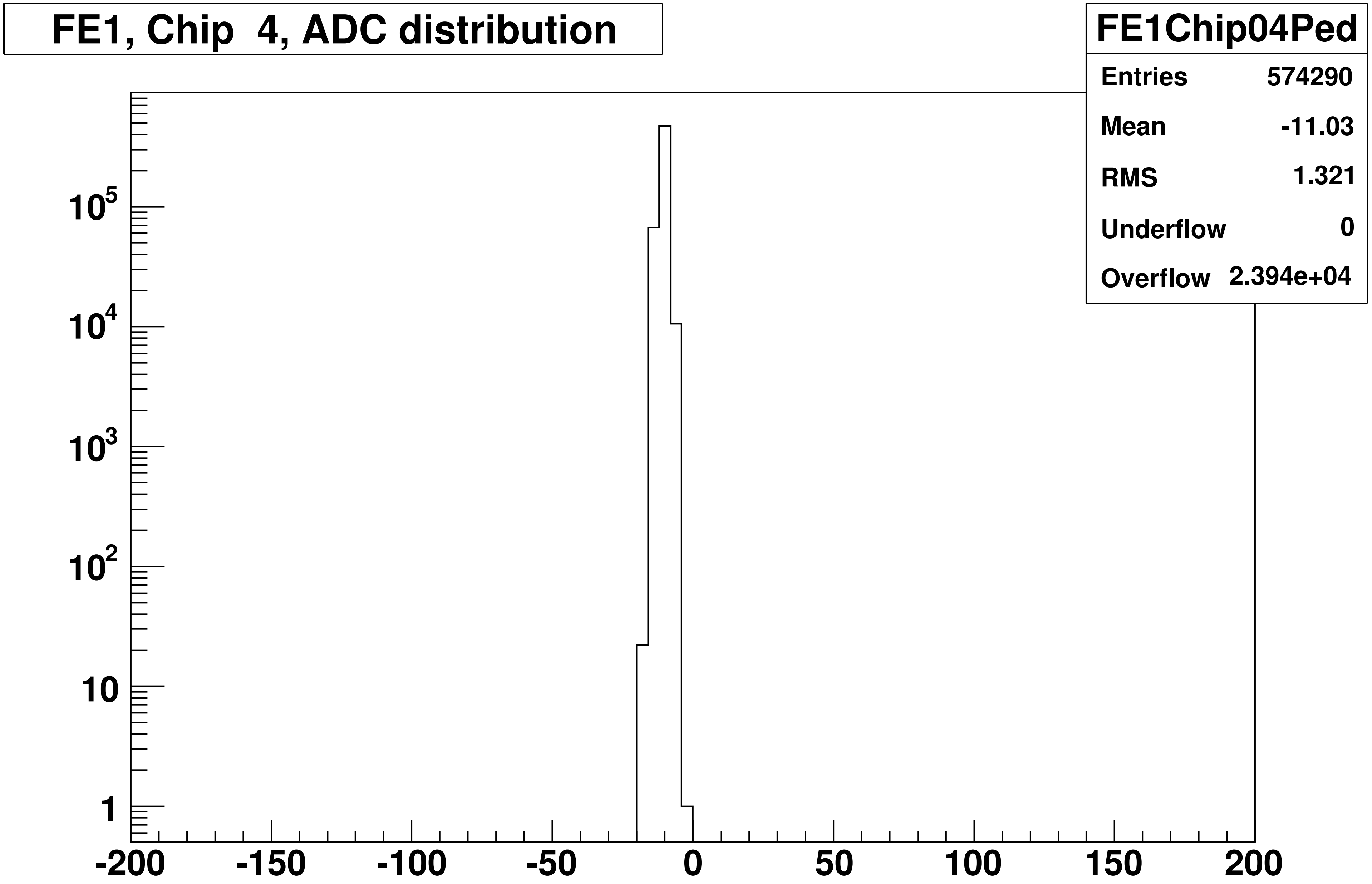
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 574290
Mean -11.03
RMS 1.321
Underflow 0
Overflow 2.394e+04
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10^3
10^2
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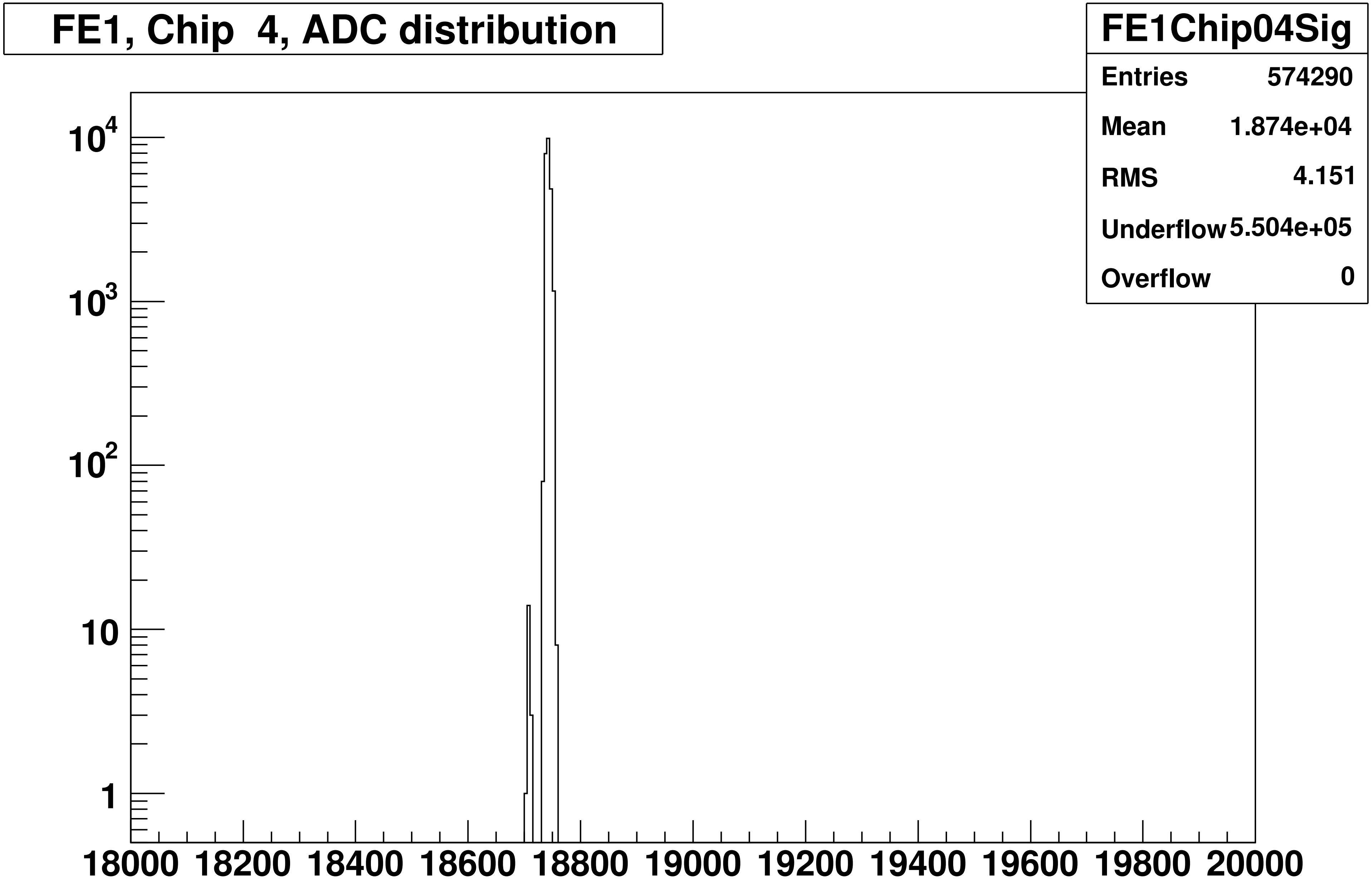
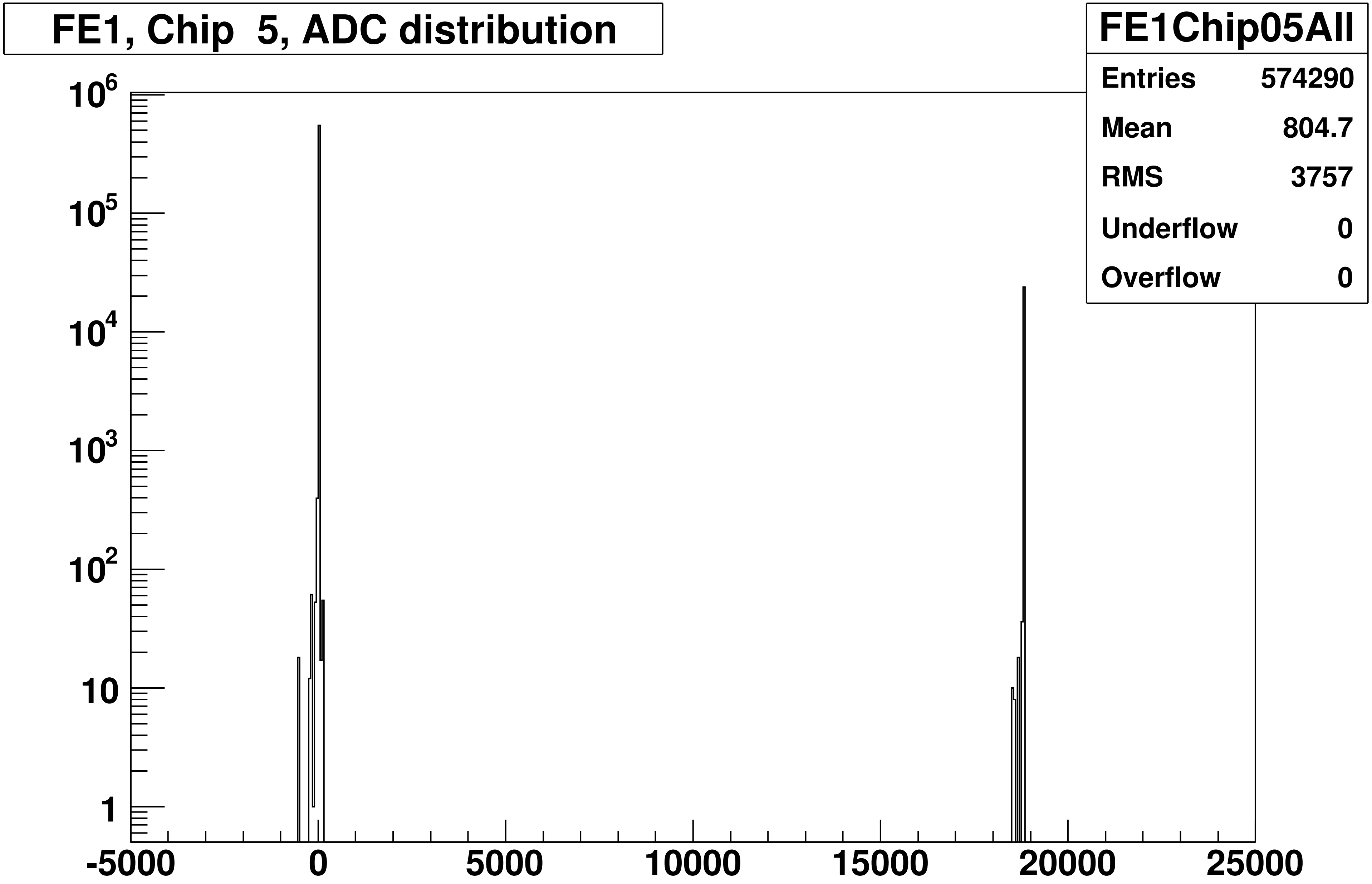
150
200



FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 574290
Mean 1.874e+04
RMS 4.151
Underflow 5.504e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 574290
Mean 804.7
RMS 3757
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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10000
15000
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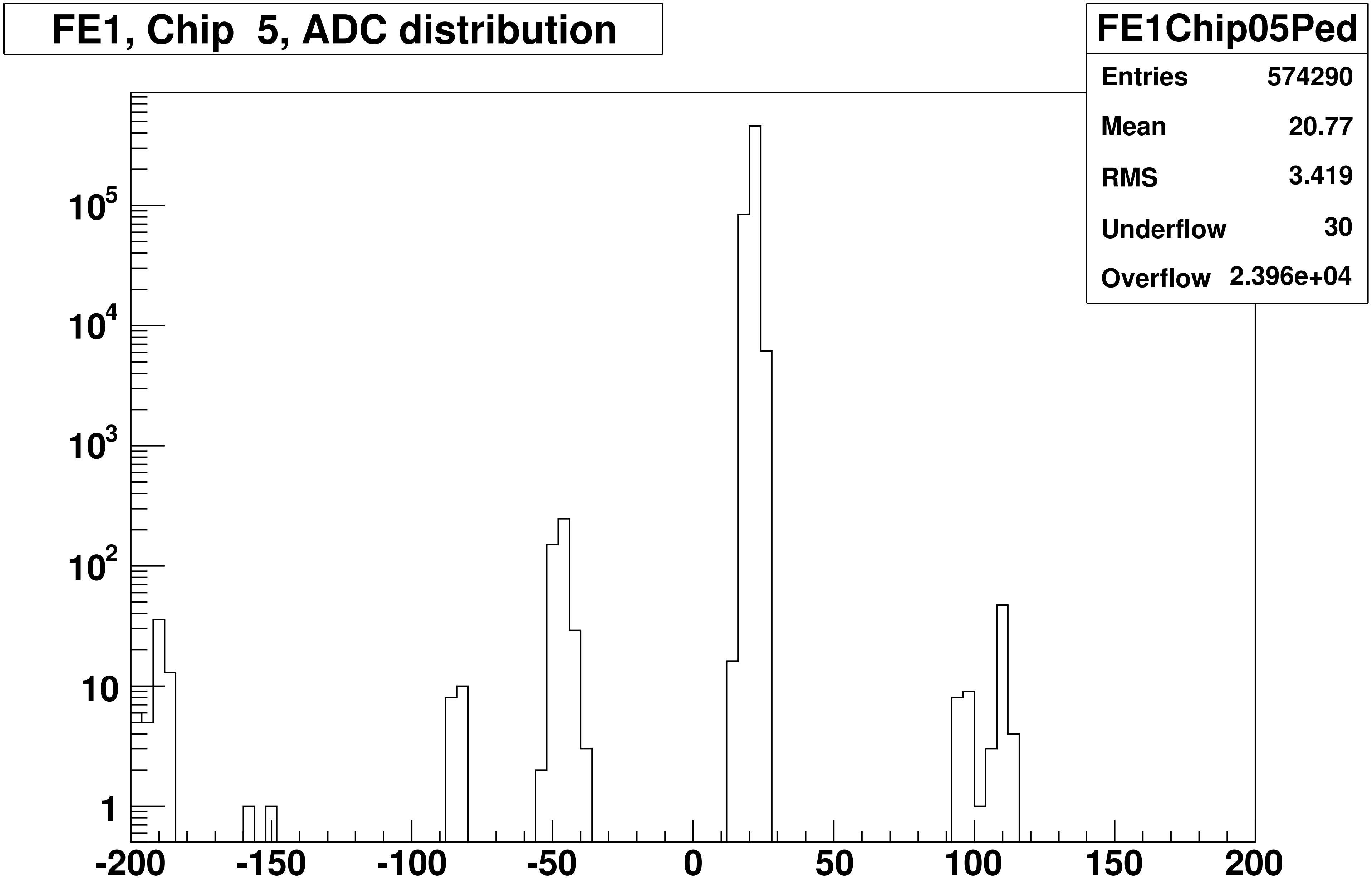
FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 574290
Mean 20.77
RMS 3.419
Underflow 30
Overflow 2.396e+04
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10^4
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10^2
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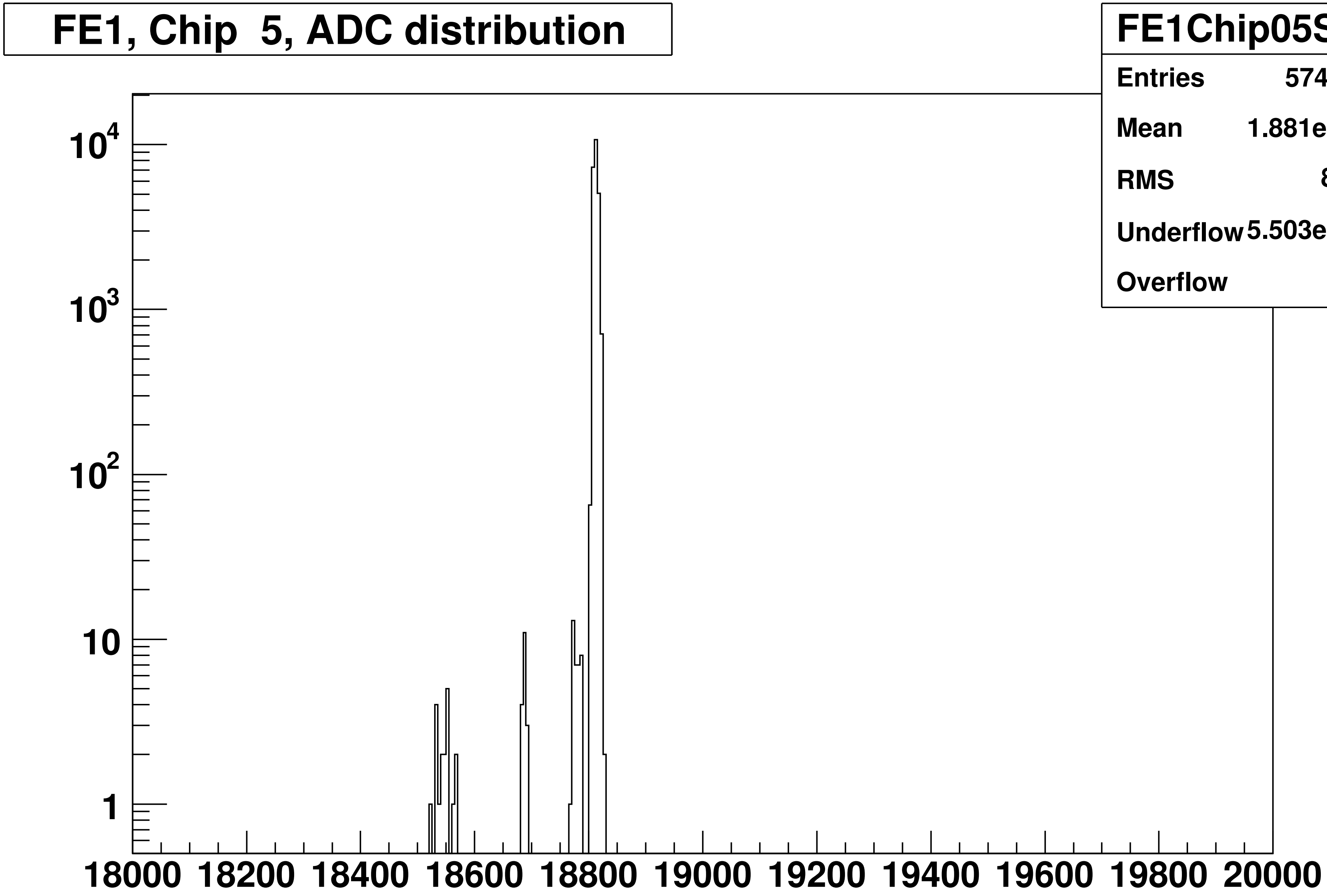
150
200



FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 574290
Mean 1.881e+04
RMS 8.96
Underflow 5.503e+05
Overflow 0
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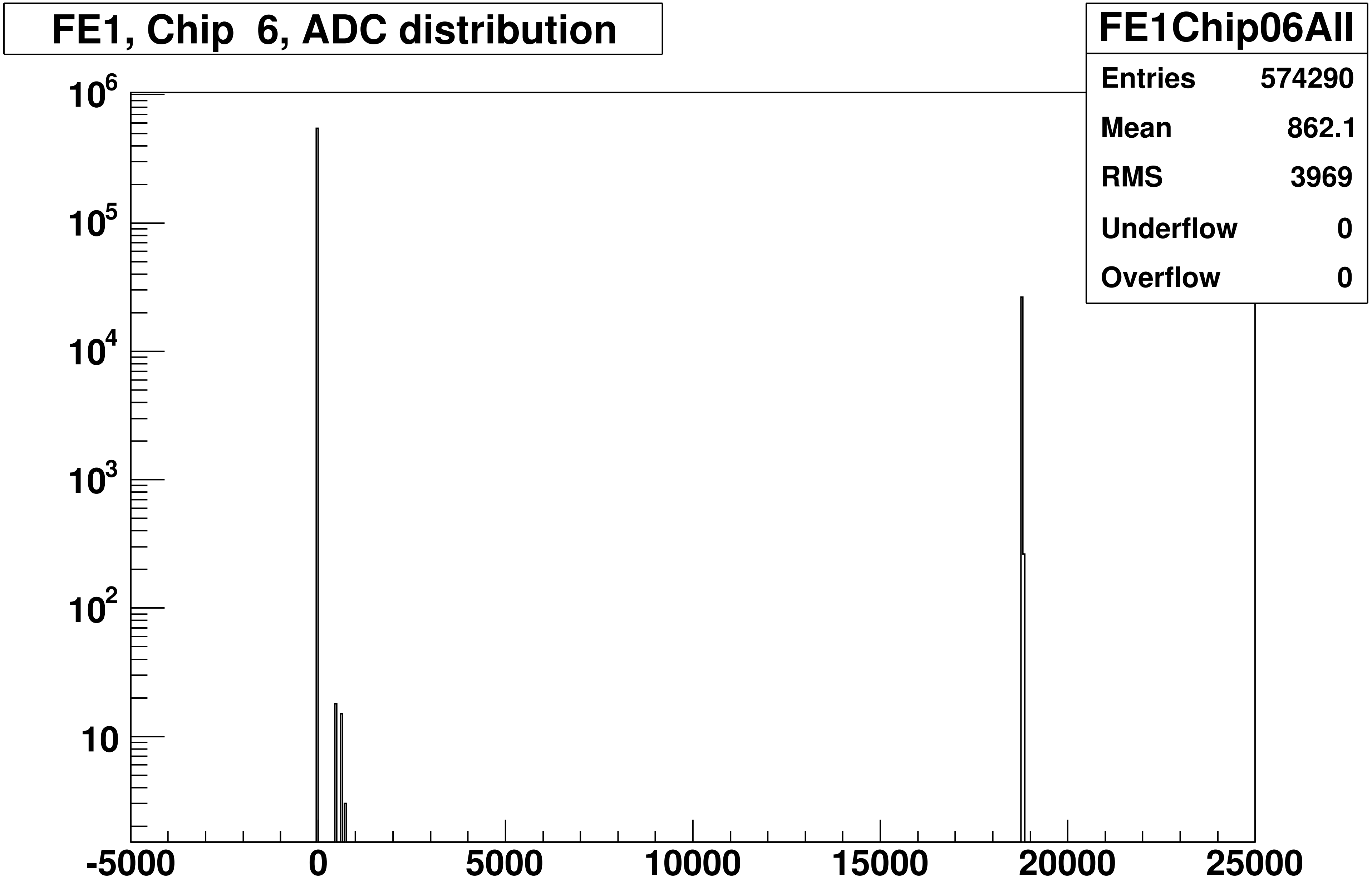
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 574290
Mean 862.1
RMS 3969
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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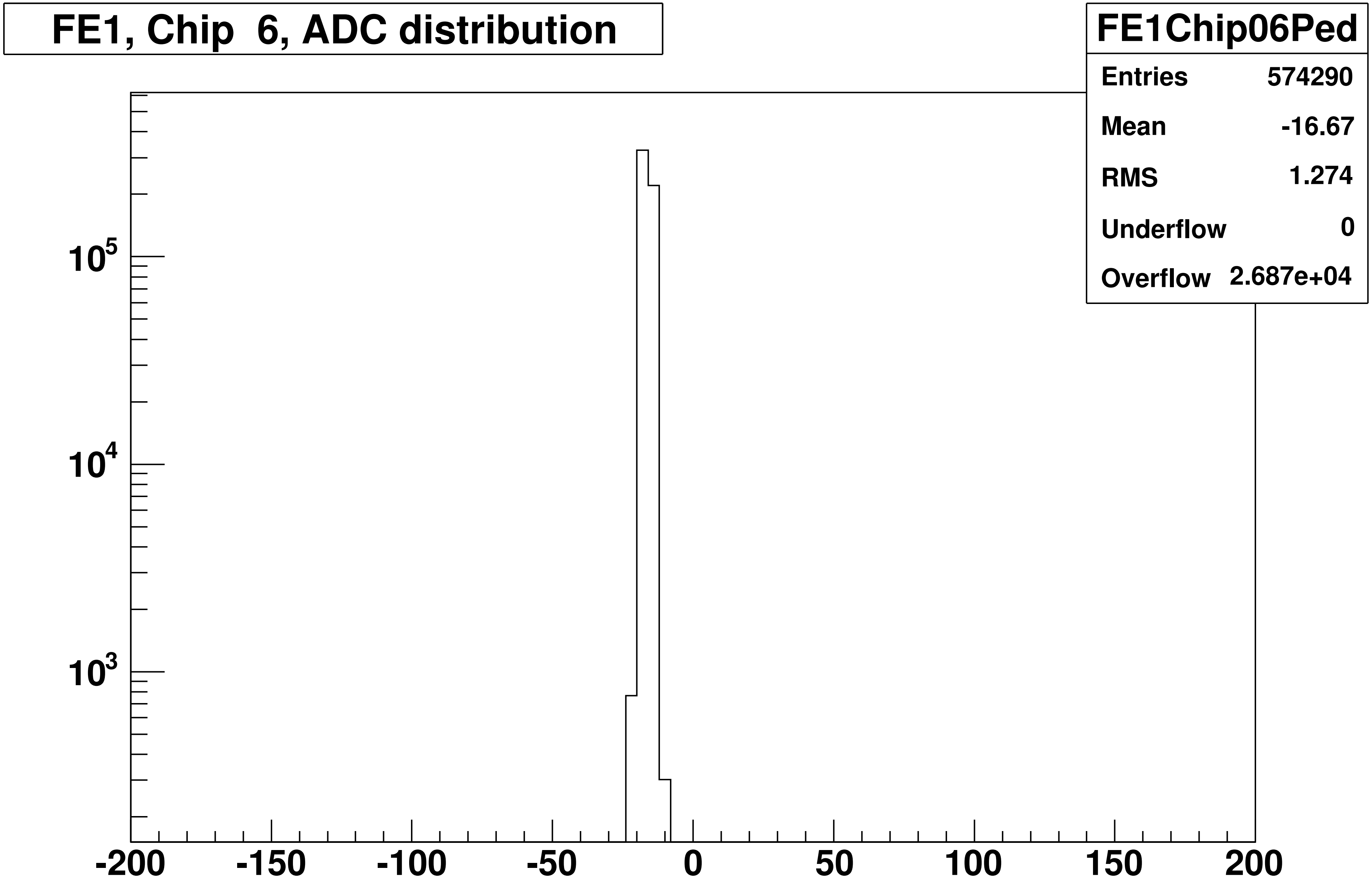
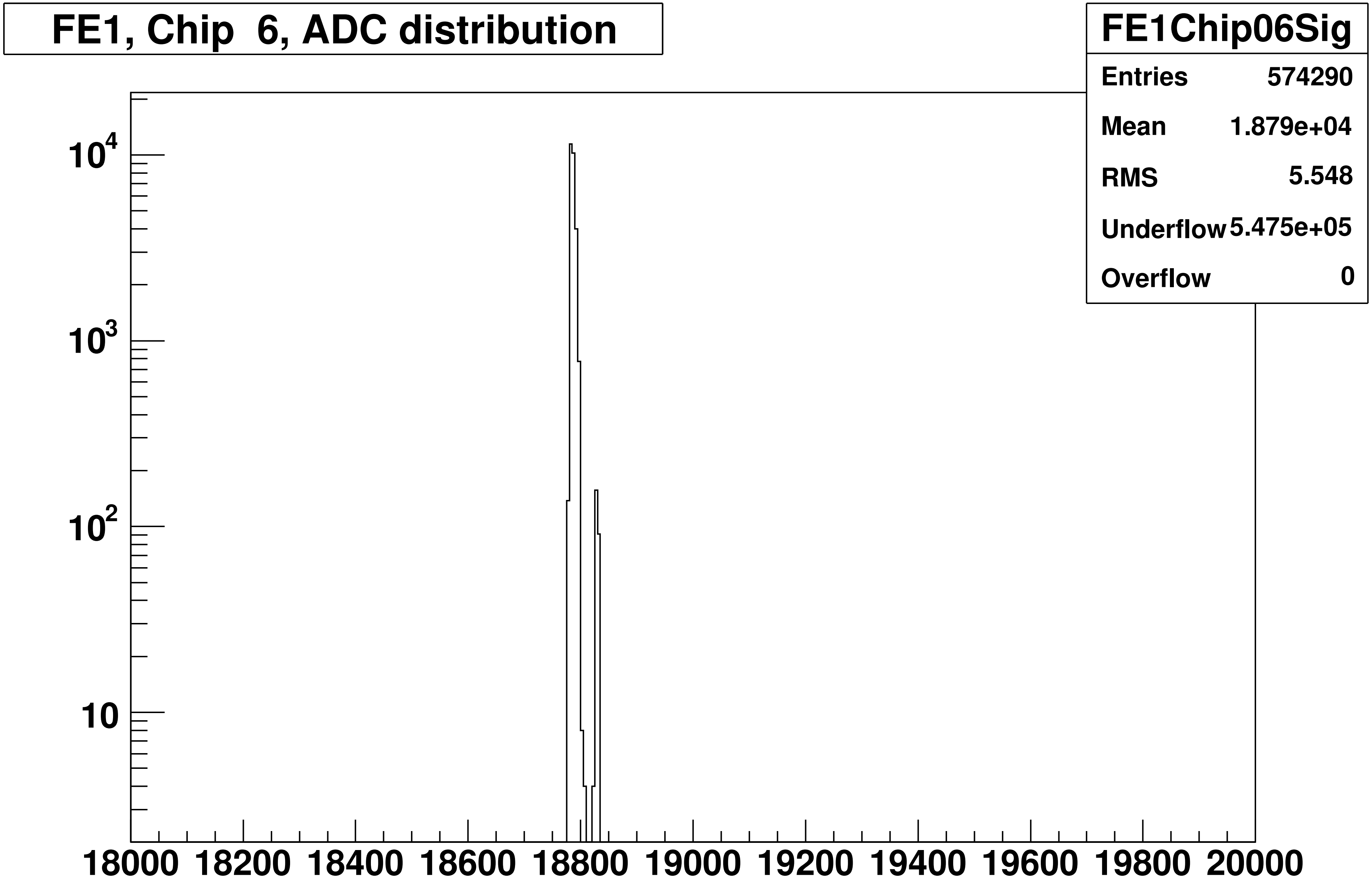
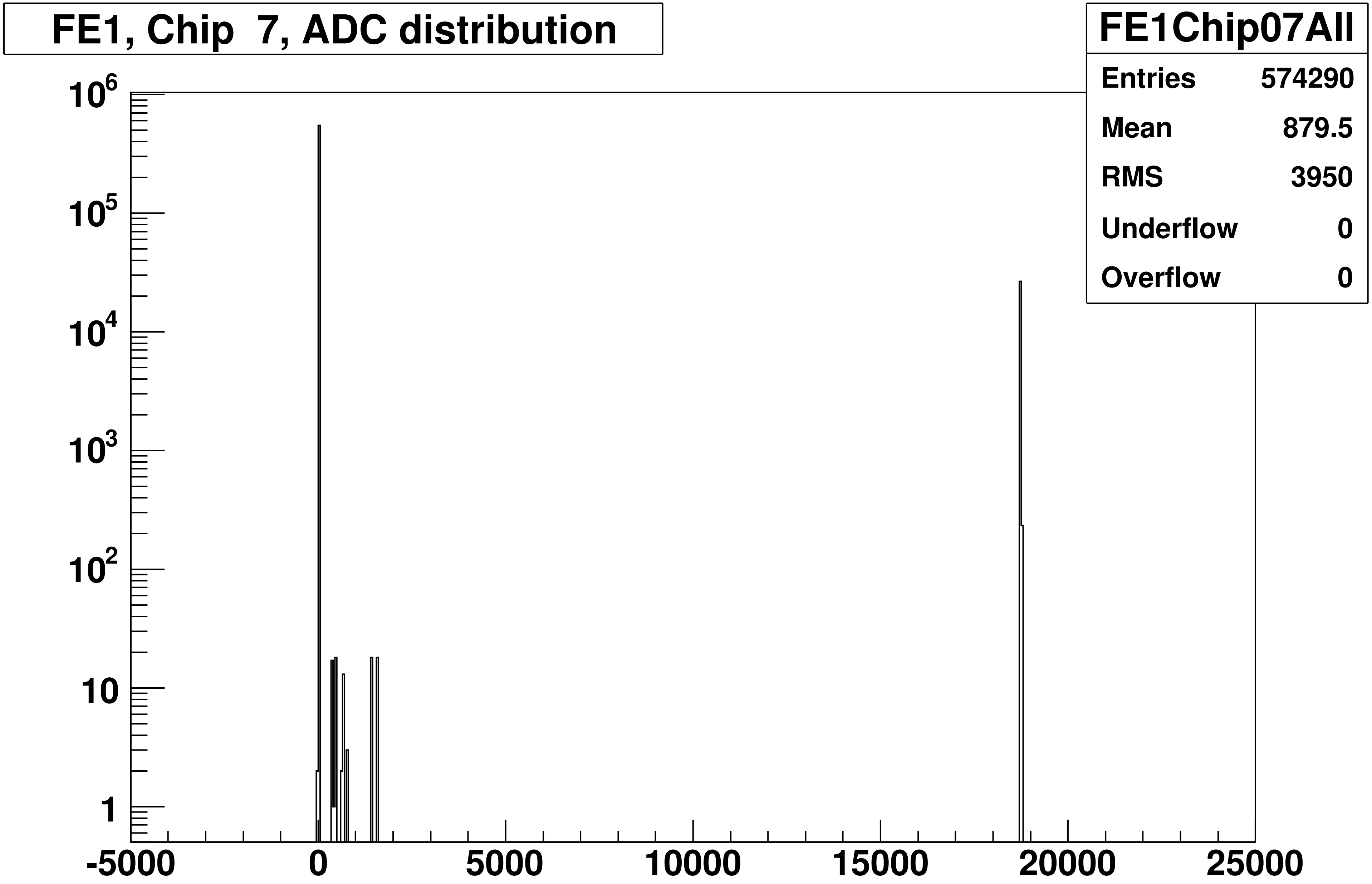
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 574290
Mean -16.67
RMS 1.274
Underflow 0
Overflow 2.687e+04
10^5
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 574290
Mean 1.879e+04
RMS 5.548
Underflow 5.475e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 574290
Mean 879.5
RMS 3950
Underflow 0
Overflow 0
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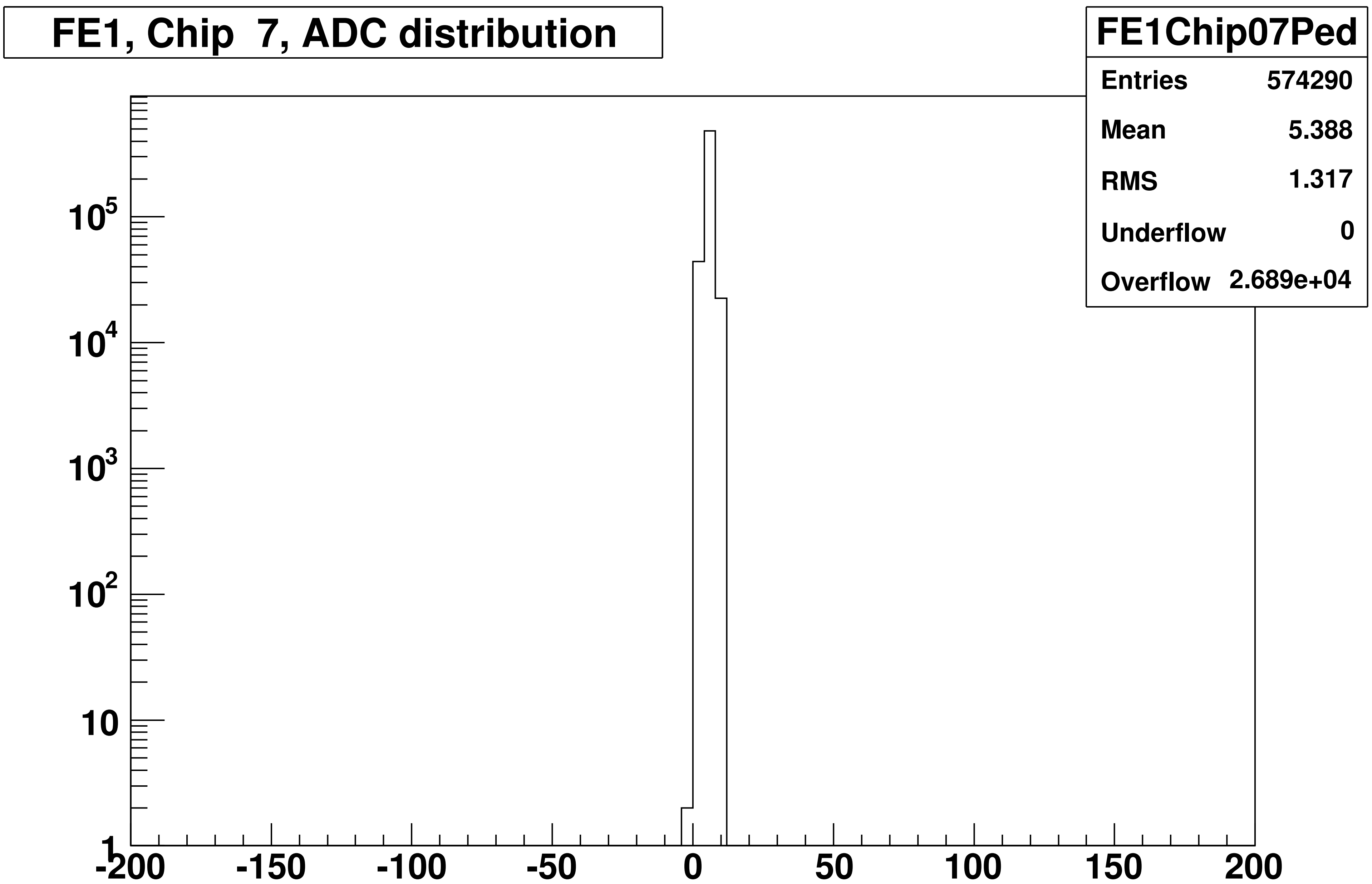
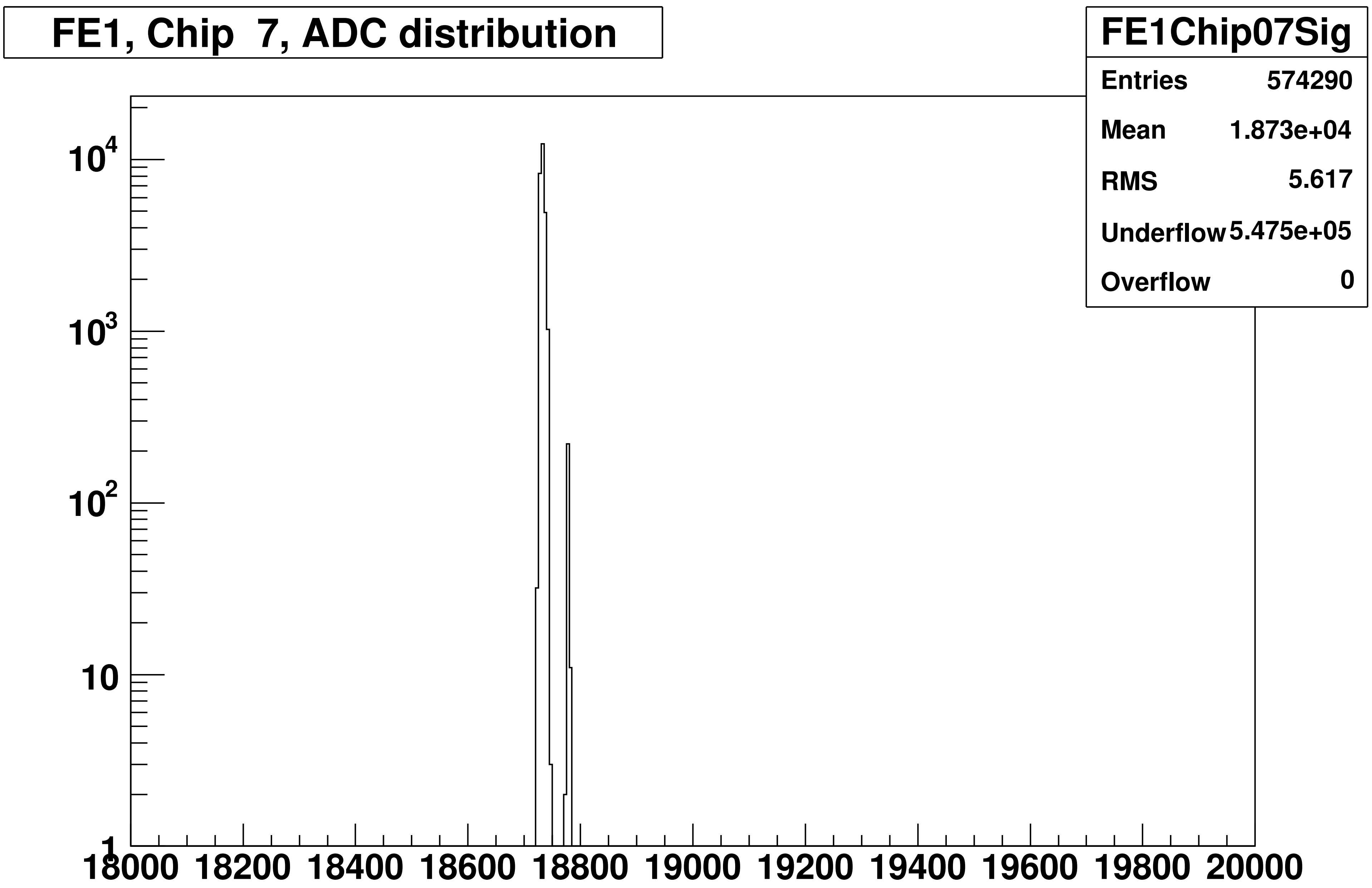
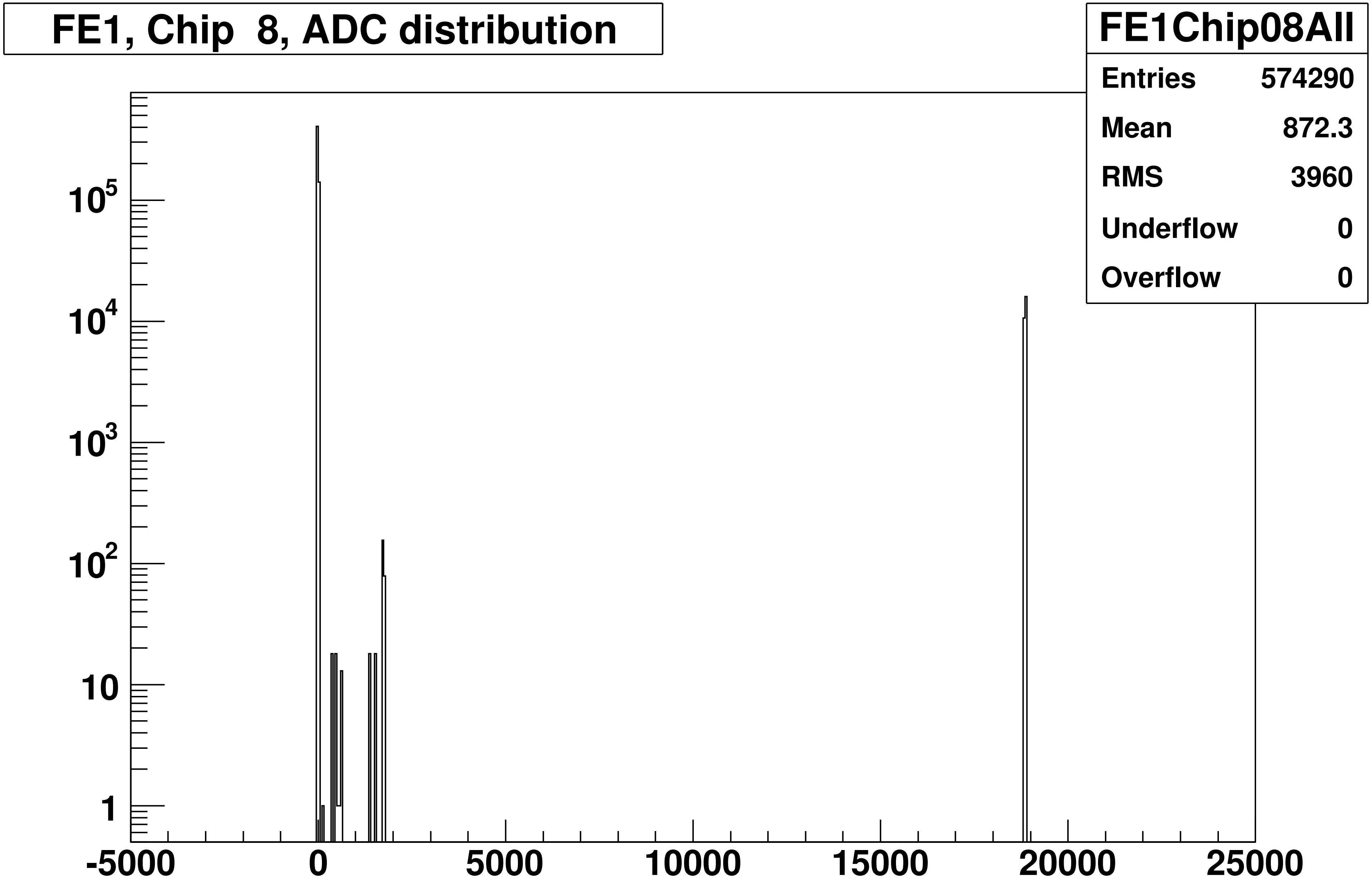
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 574290
Mean 5.388
RMS 1.317
Underflow 0
Overflow 2.689e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 574290
Mean 1.873e+04
RMS 5.617
Underflow 5.475e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 574290
Mean 872.3
RMS 3960
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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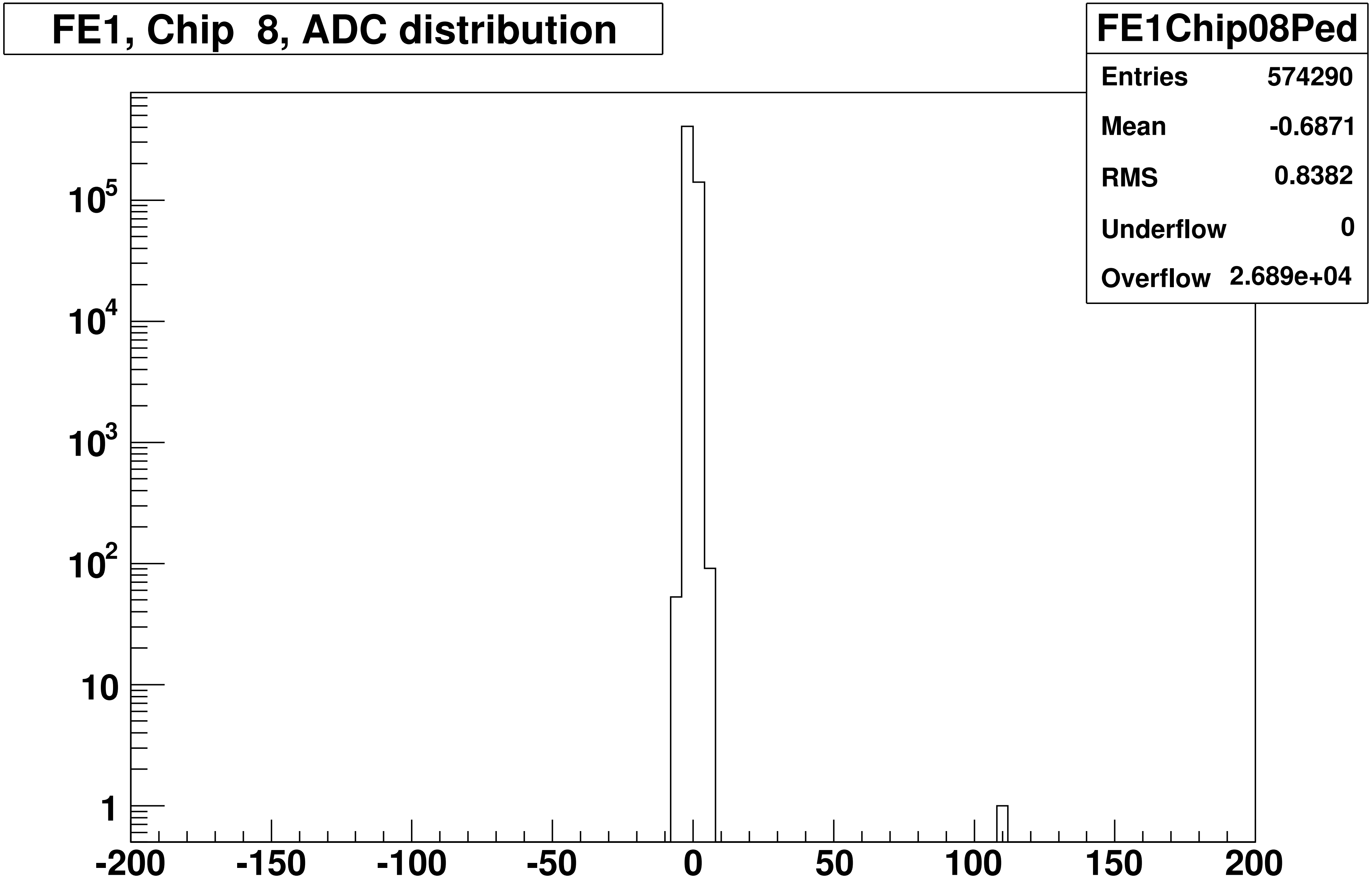
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 574290
Mean -0.6871
RMS 0.8382
Underflow 0
Overflow 2.689e+04
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10^4
10^3
10^2
10
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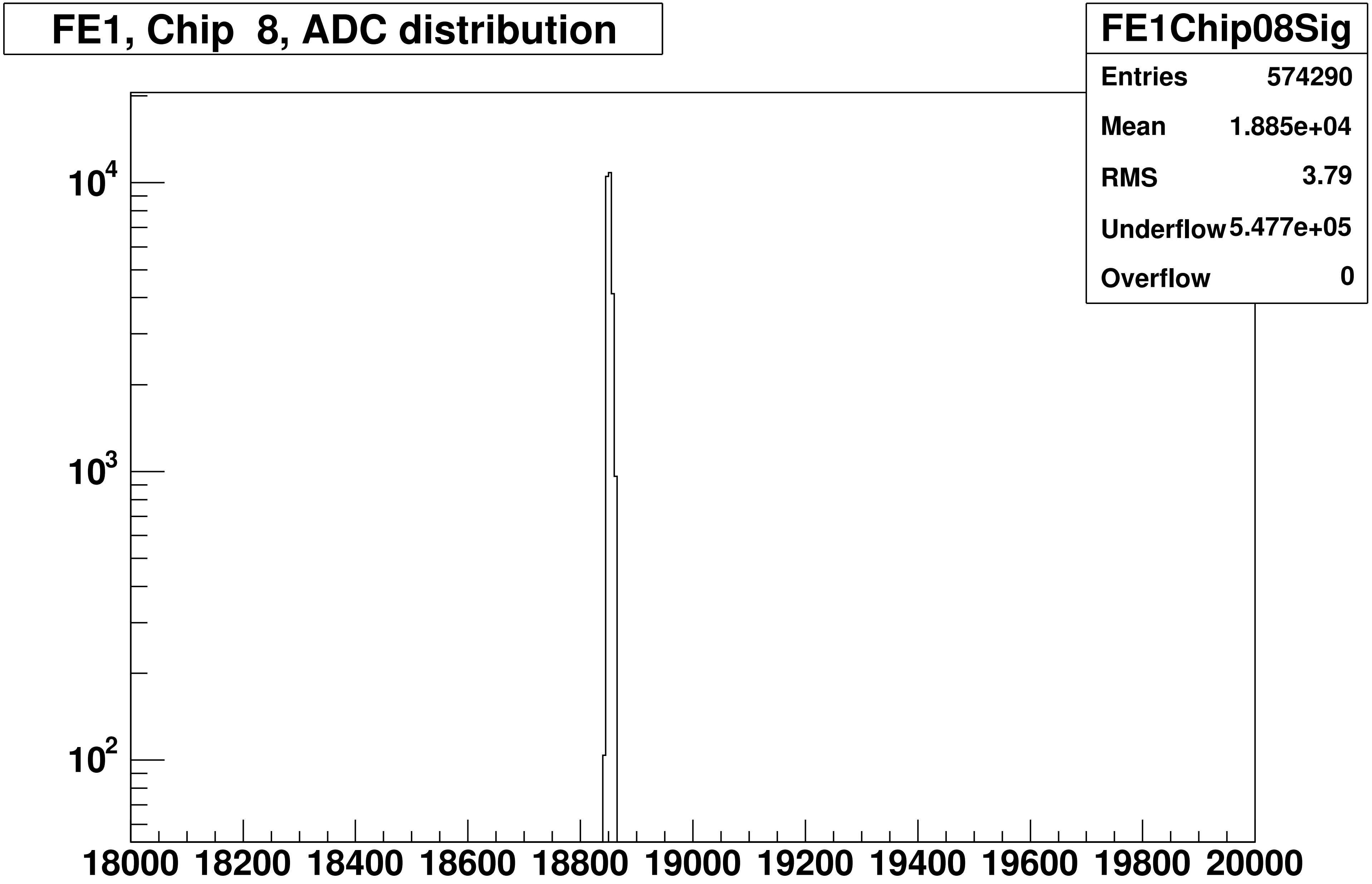
100
150
200


FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 574290
Mean 1.885e+04
RMS 3.79
Underflow 5.477e+05
Overflow 0
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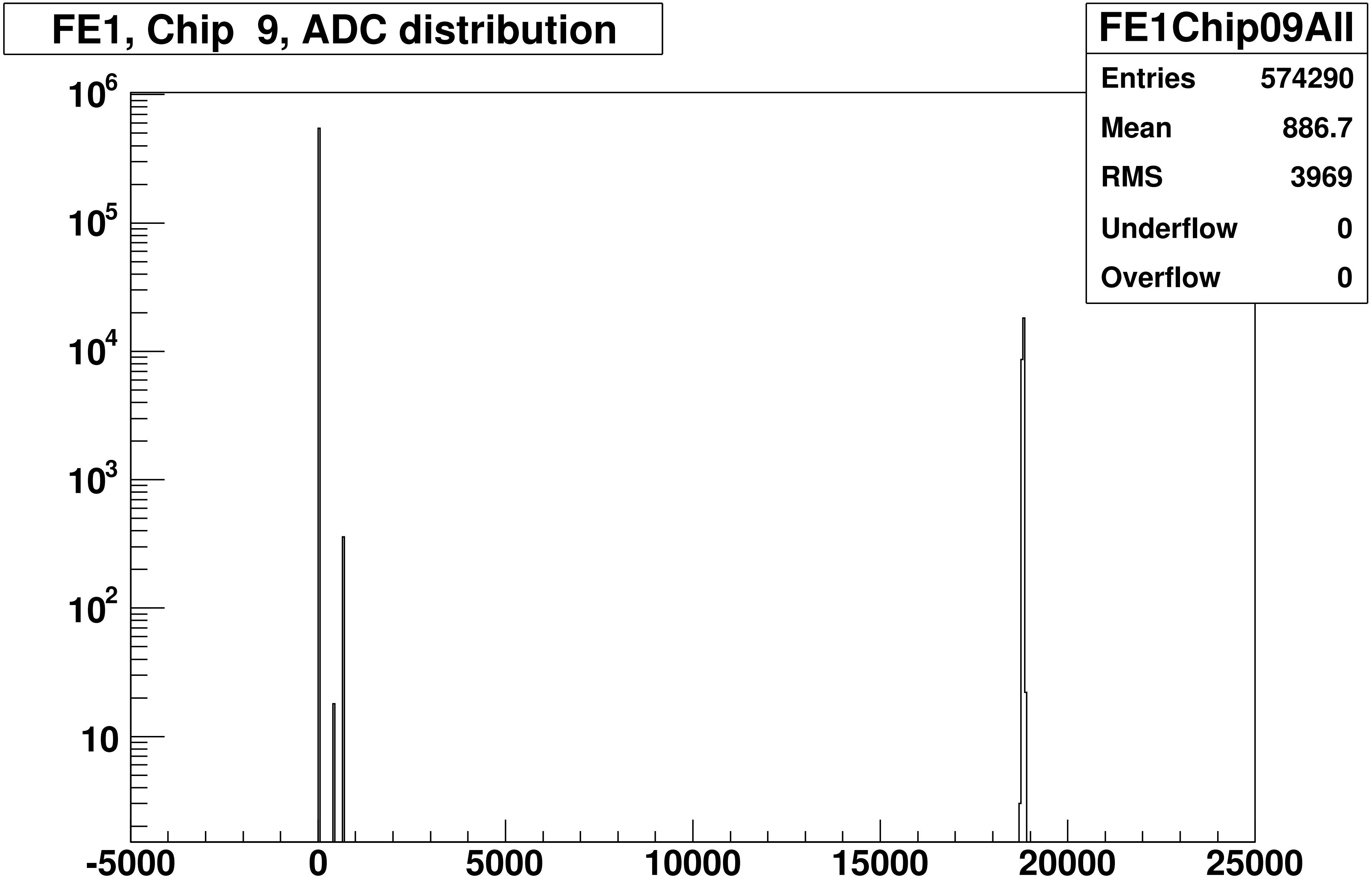
10^3
10^2
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 574290
Mean 886.7
RMS 3969
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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10000
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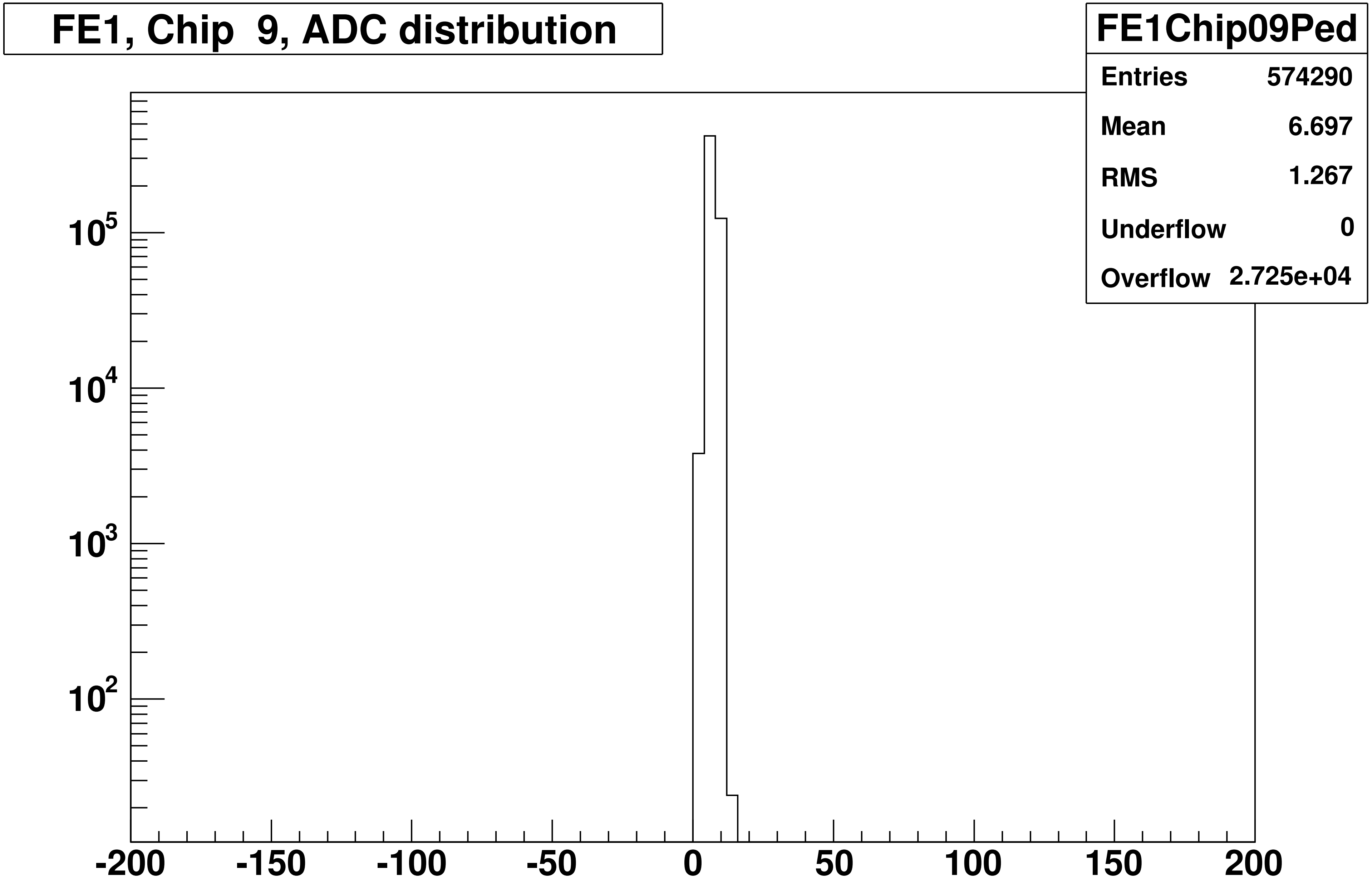
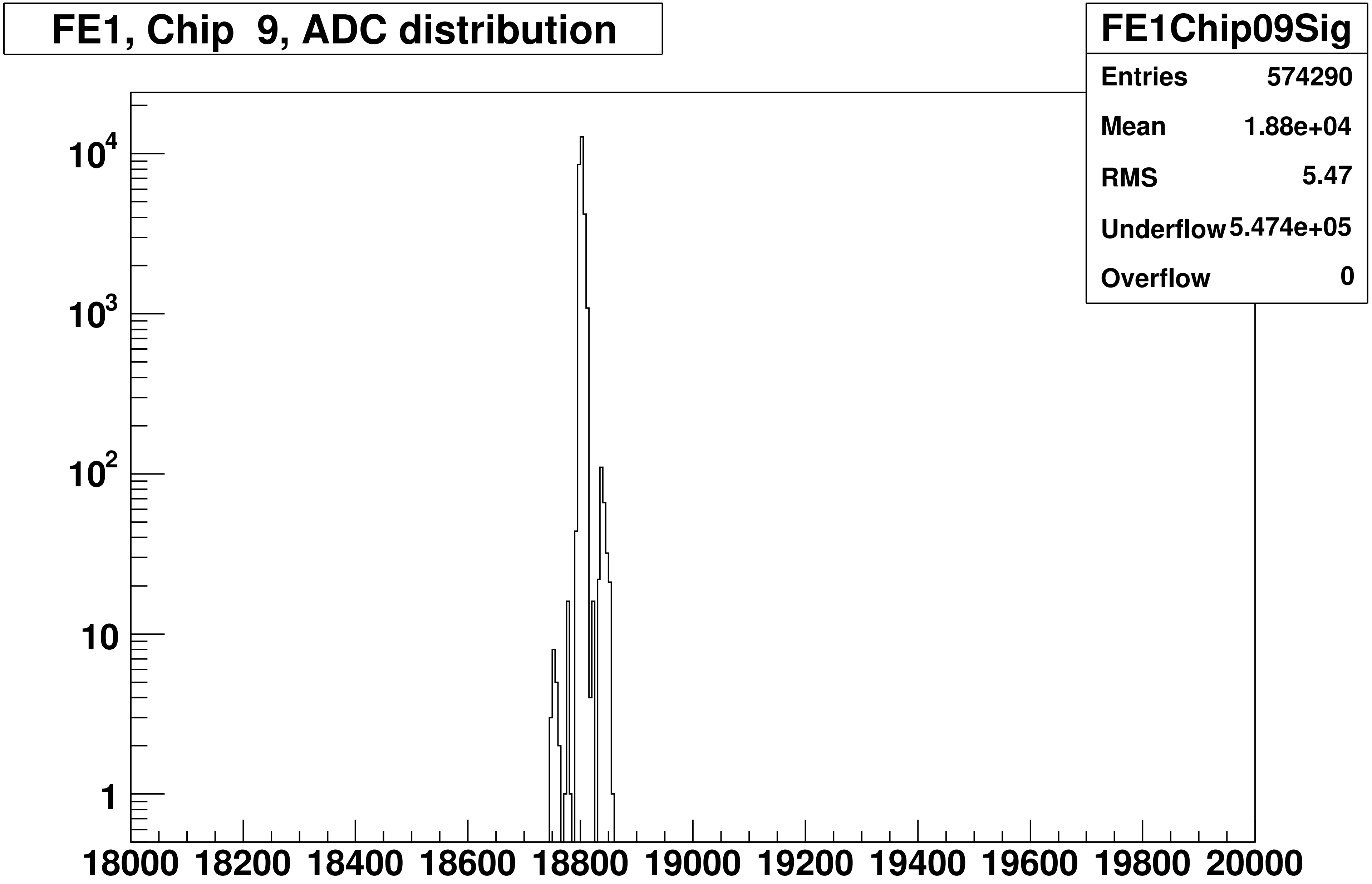
25000



FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 574290
Mean 6.697
RMS 1.267
Underflow 0
Overflow 2.725e+04
10^5
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200


FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 574290
Mean 1.88e+04
RMS 5.47
Underflow 5.474e+05
Overflow 0
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10^3
10^2
10
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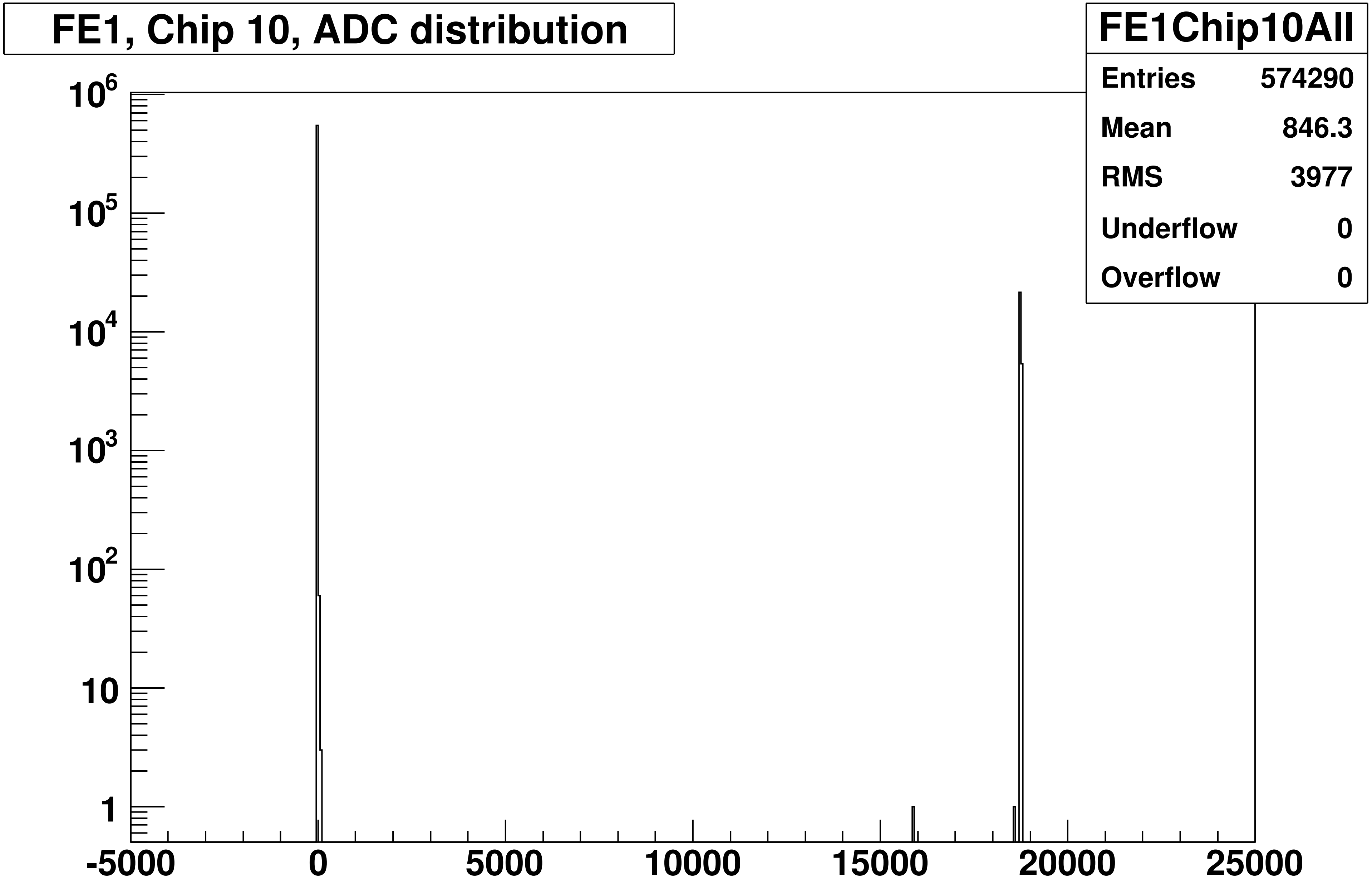
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 574290
Mean 846.3
RMS 3977
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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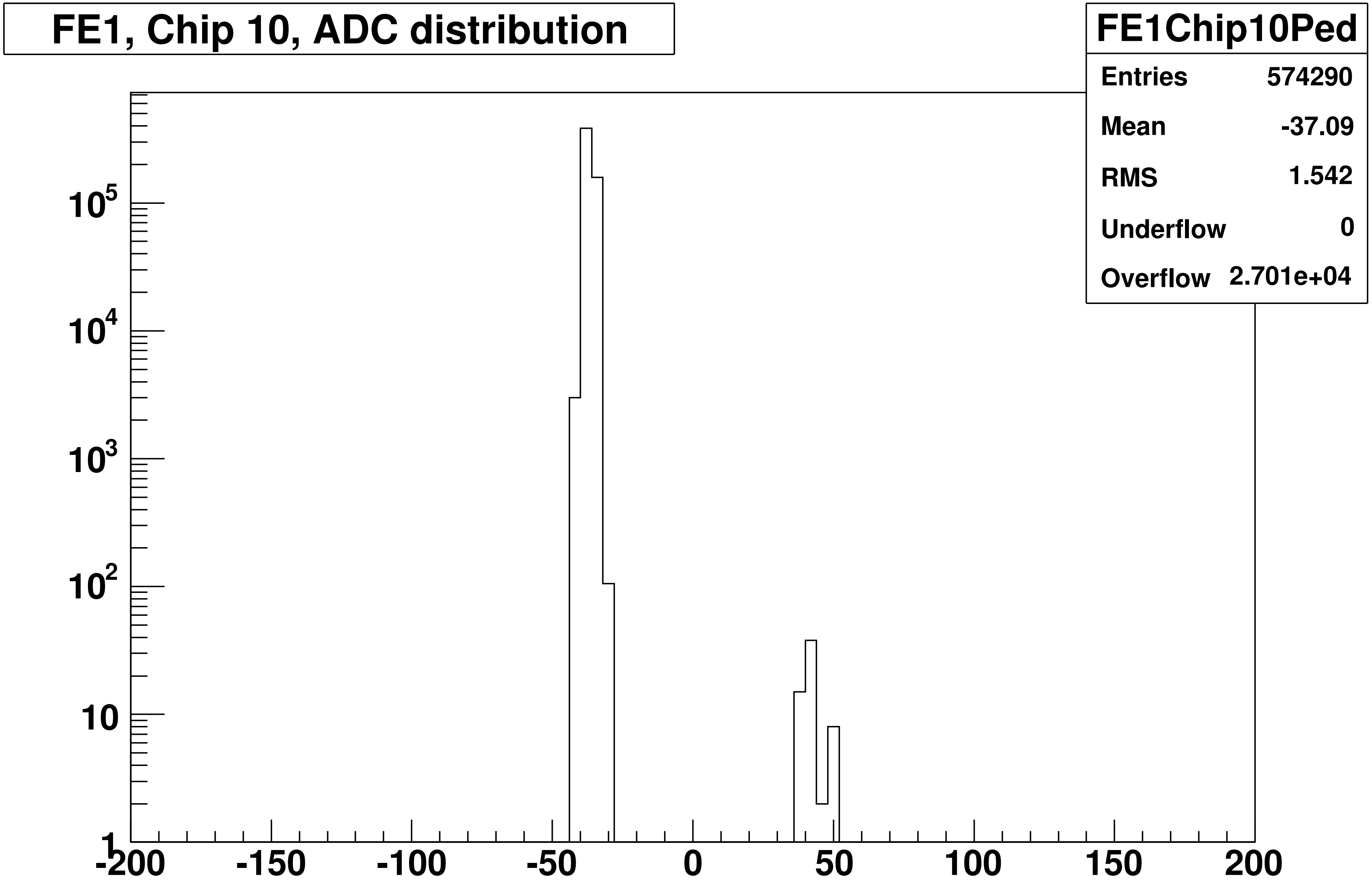

FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 574290
Mean -37.09
RMS 1.542
Underflow 0
Overflow 2.701e+04
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10^4
10^3
10^2
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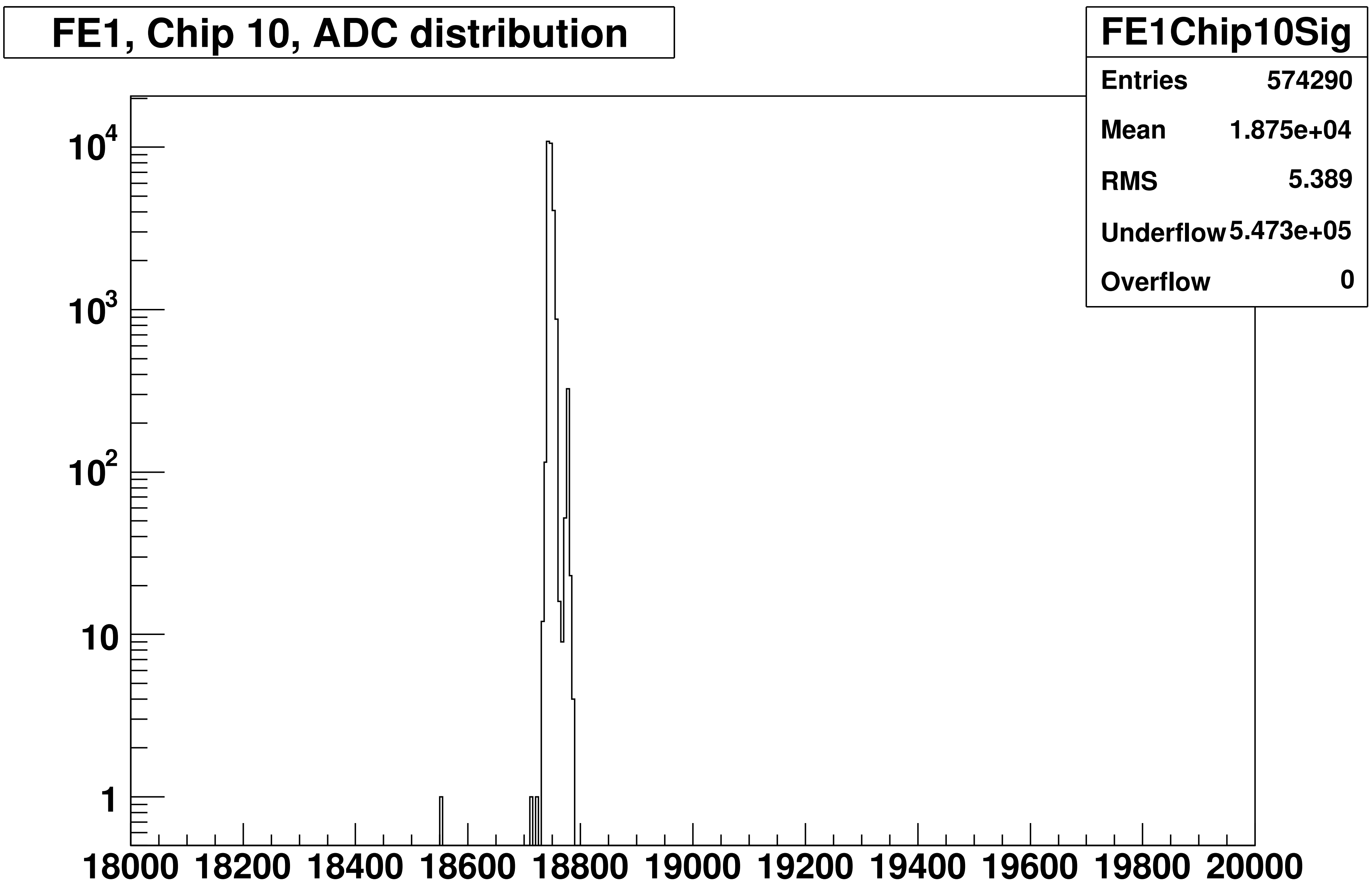
150
200


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 574290
Mean 1.875e+04
RMS 5.389
Underflow 5.473e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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19000
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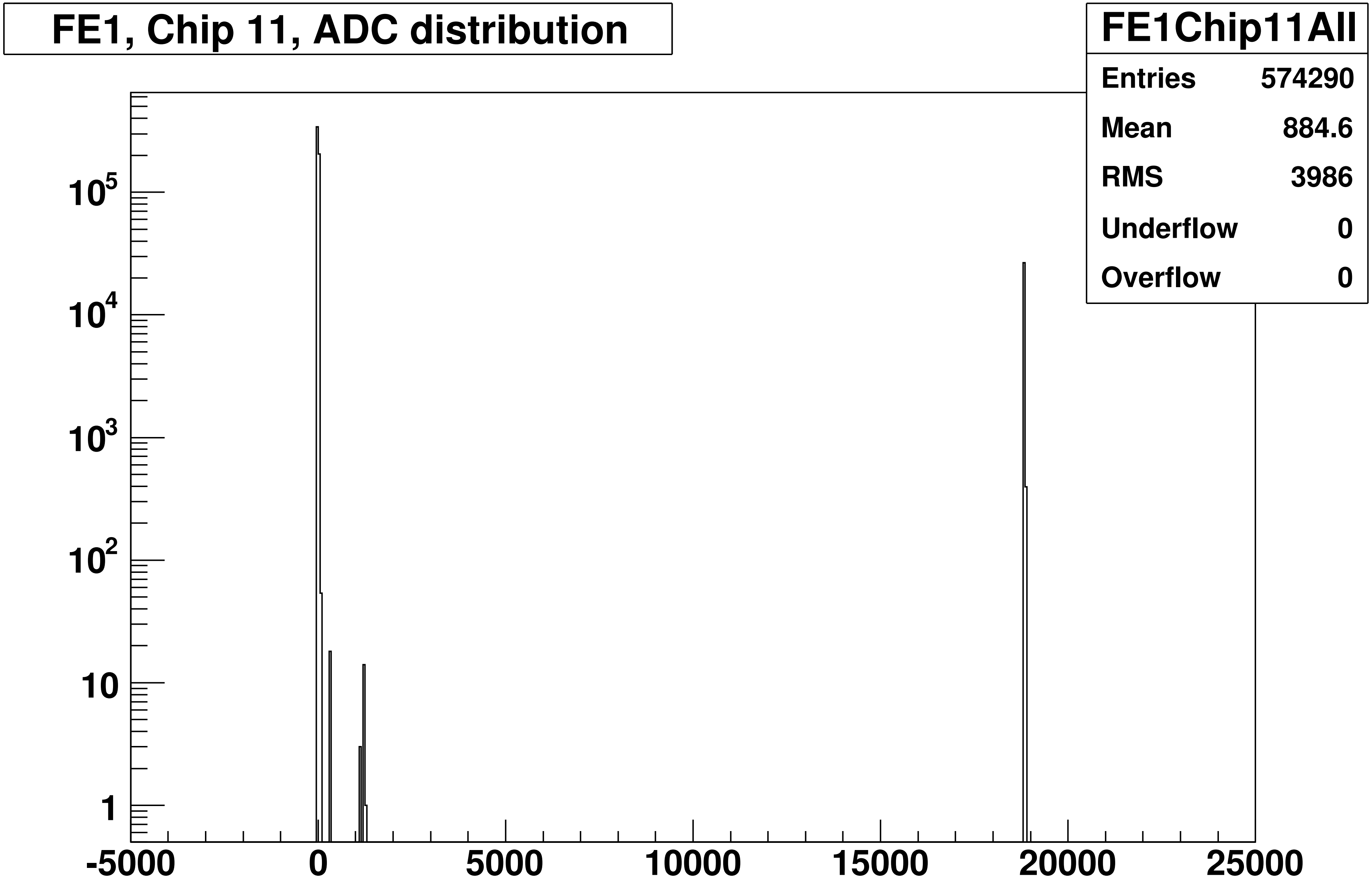
19800
20000


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 574290
Mean 884.6
RMS 3986
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
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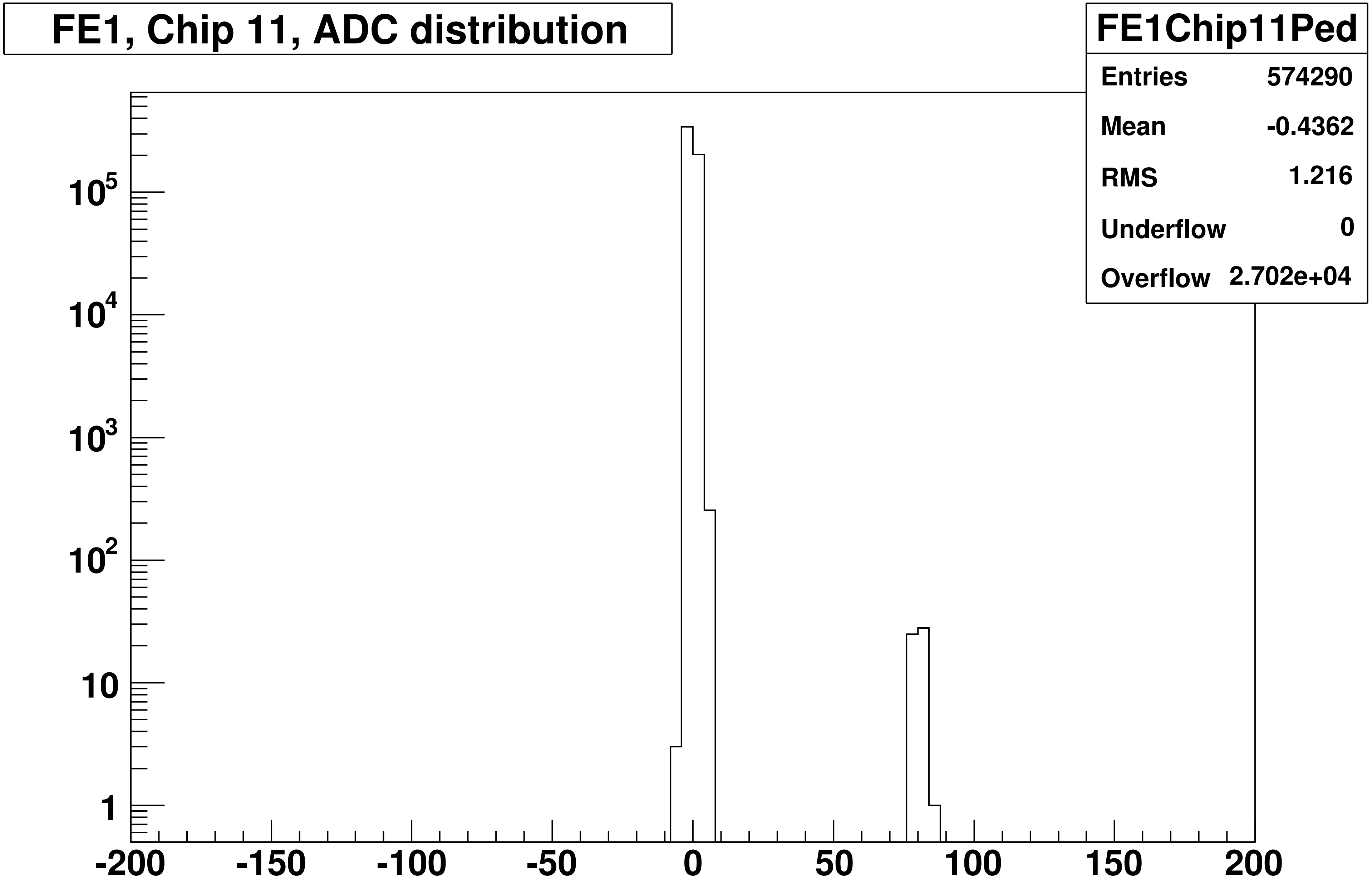
FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 574290
Mean -0.4362
RMS 1.216
Underflow 0
Overflow 2.702e+04
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10^3
10^2
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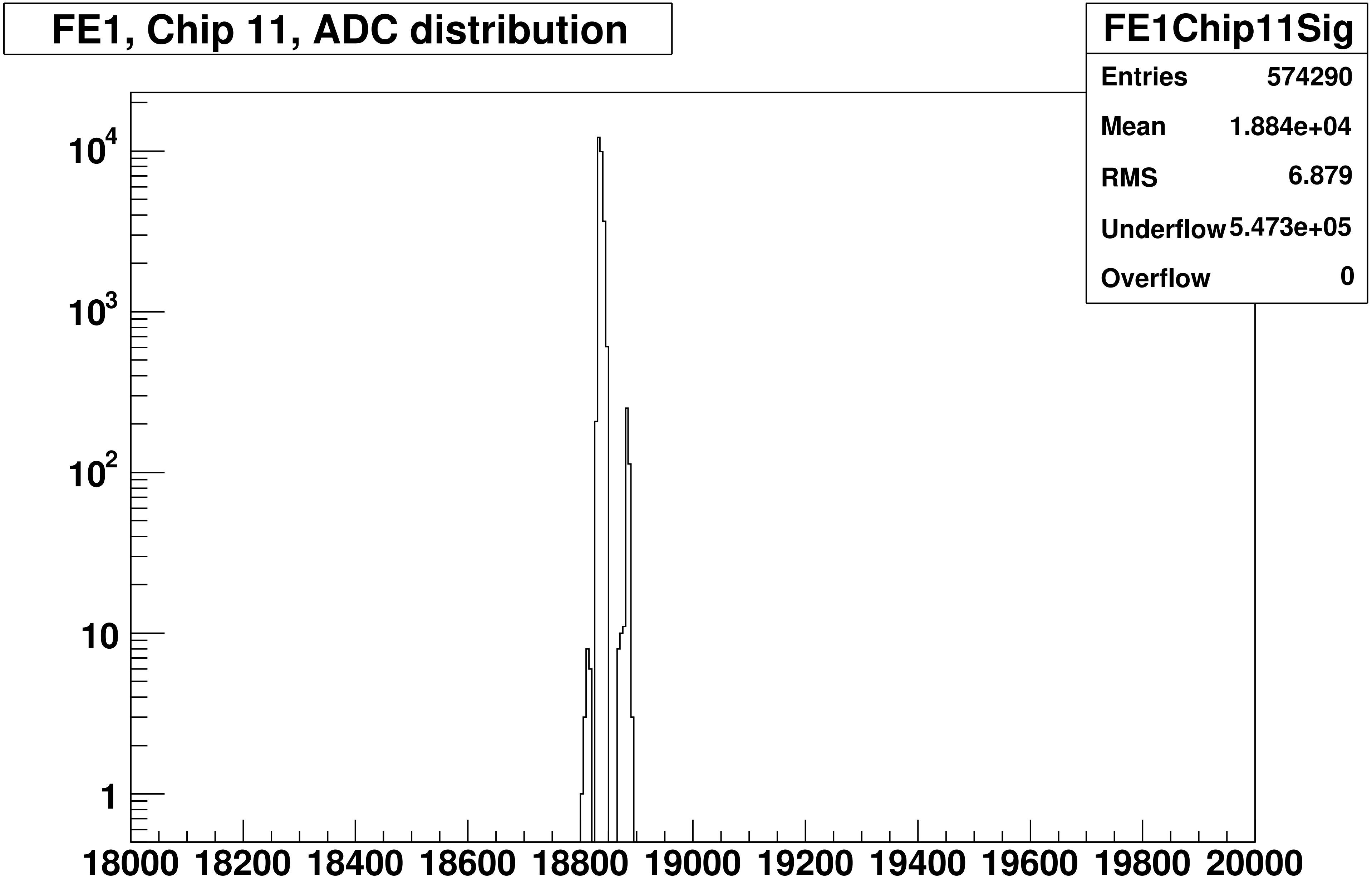
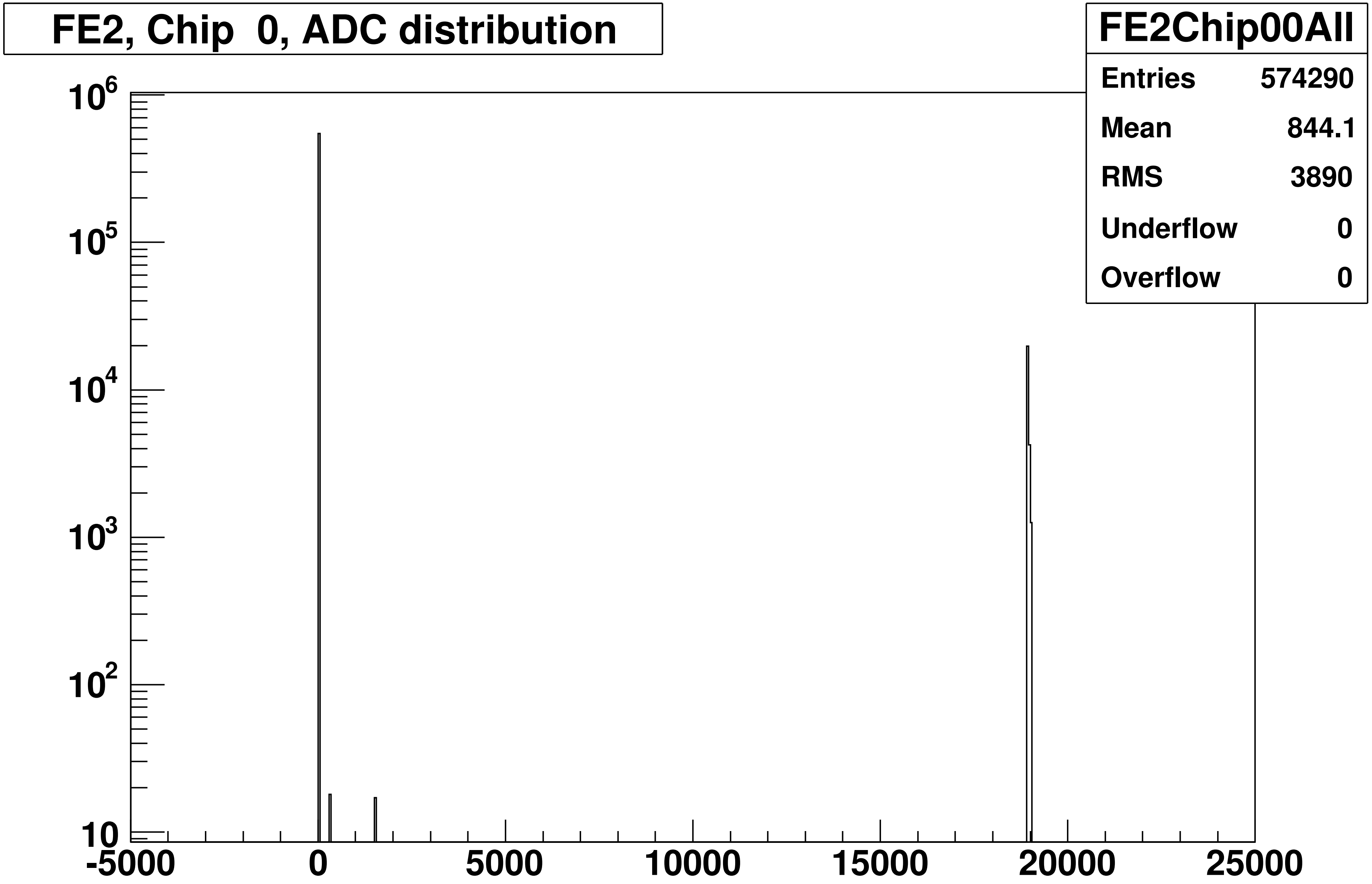
150
200


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 574290
Mean 1.884e+04
RMS 6.879
Underflow 5.473e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 574290
Mean 844.1
RMS 3890
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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10000
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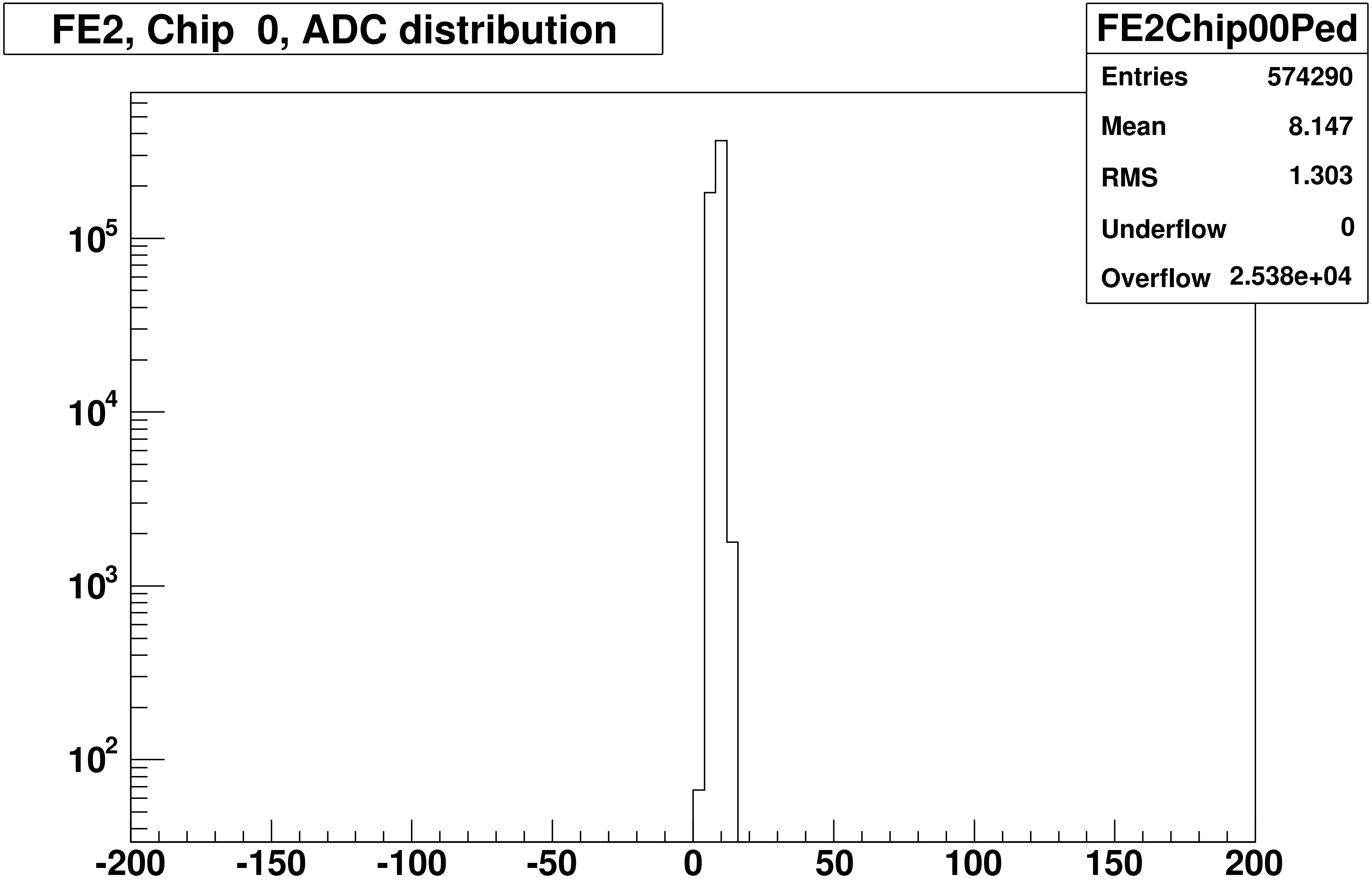
20000
25000


FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 574290
Mean 8.147
RMS 1.303
Underflow 0
Overflow 2.538e+04
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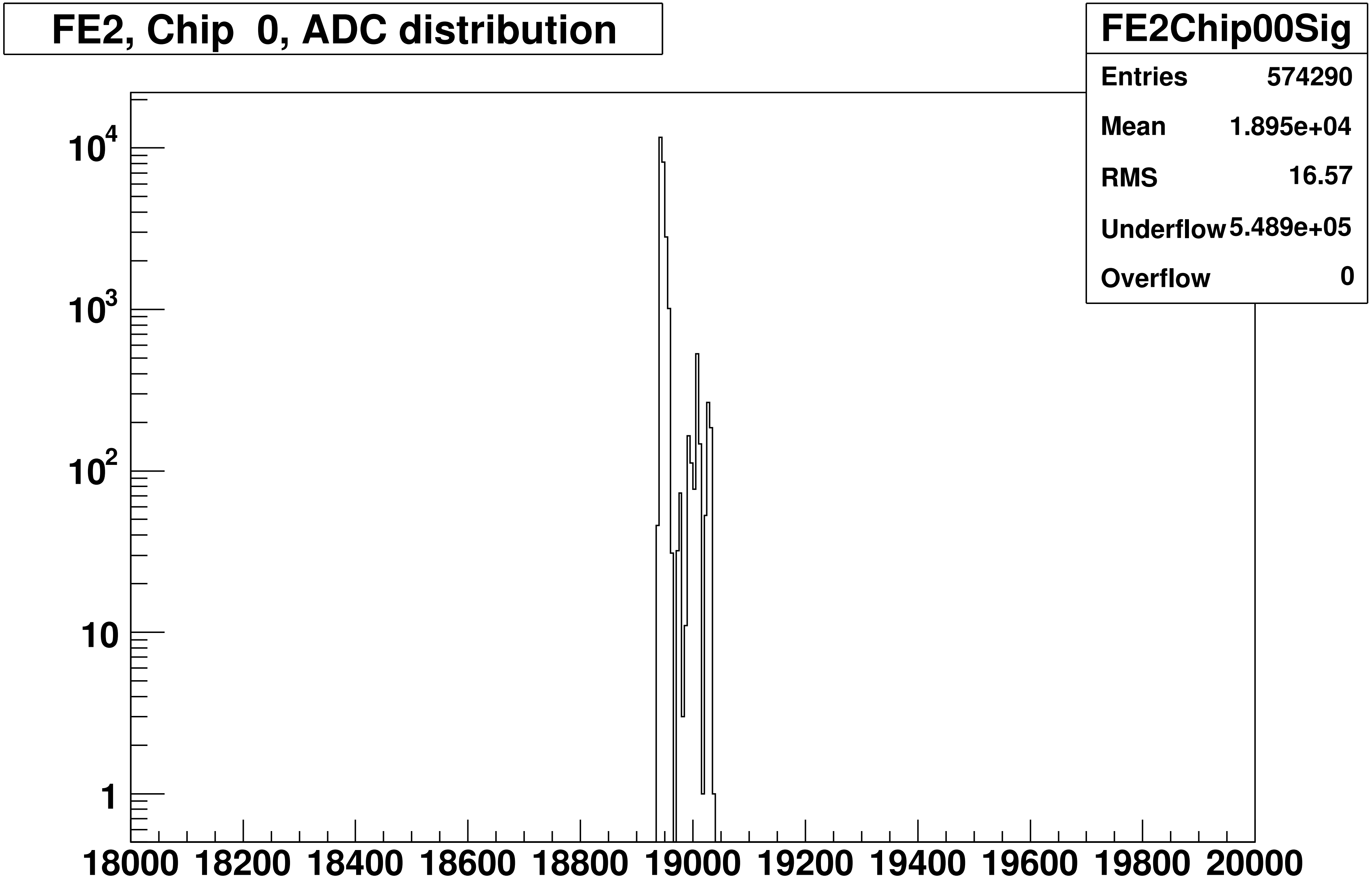
150
200


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 574290
Mean 1.895e+04
RMS 16.57
Underflow 5.489e+05
Overflow 0
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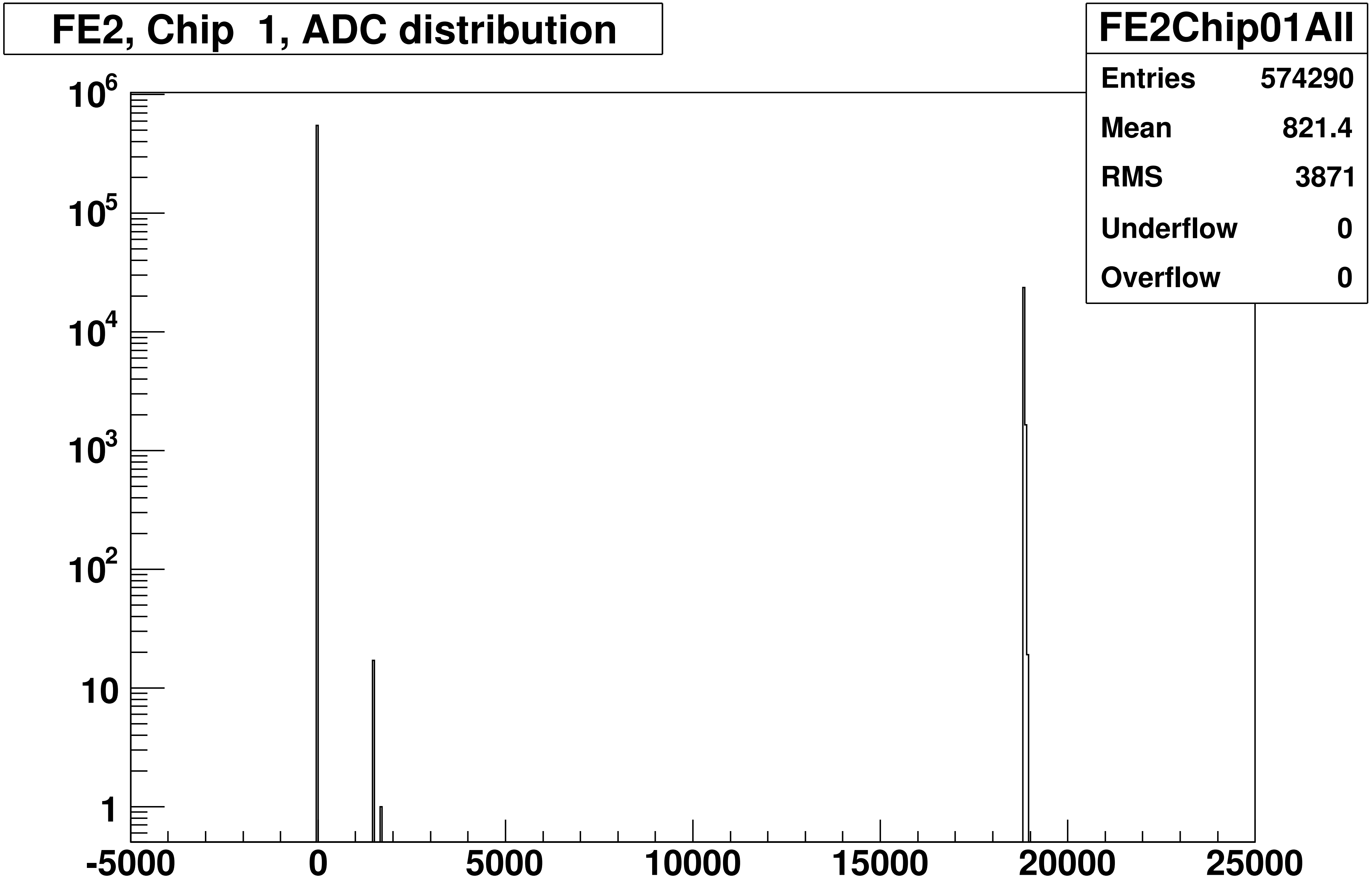
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 574290
Mean 821.4
RMS 3871
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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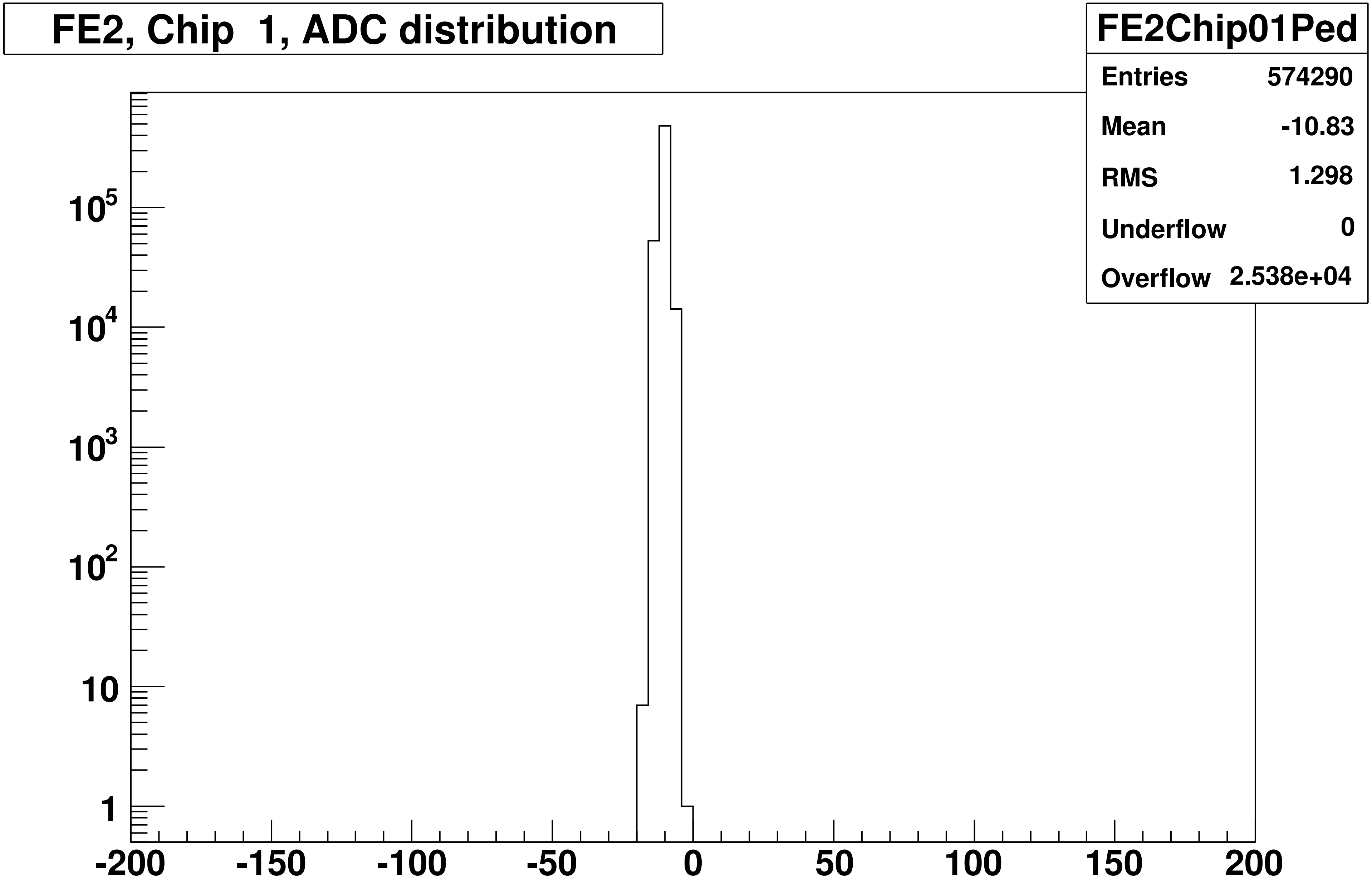
10000
15000
20000
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 574290
Mean -10.83
RMS 1.298
Underflow 0
Overflow 2.538e+04
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10^3
10^2
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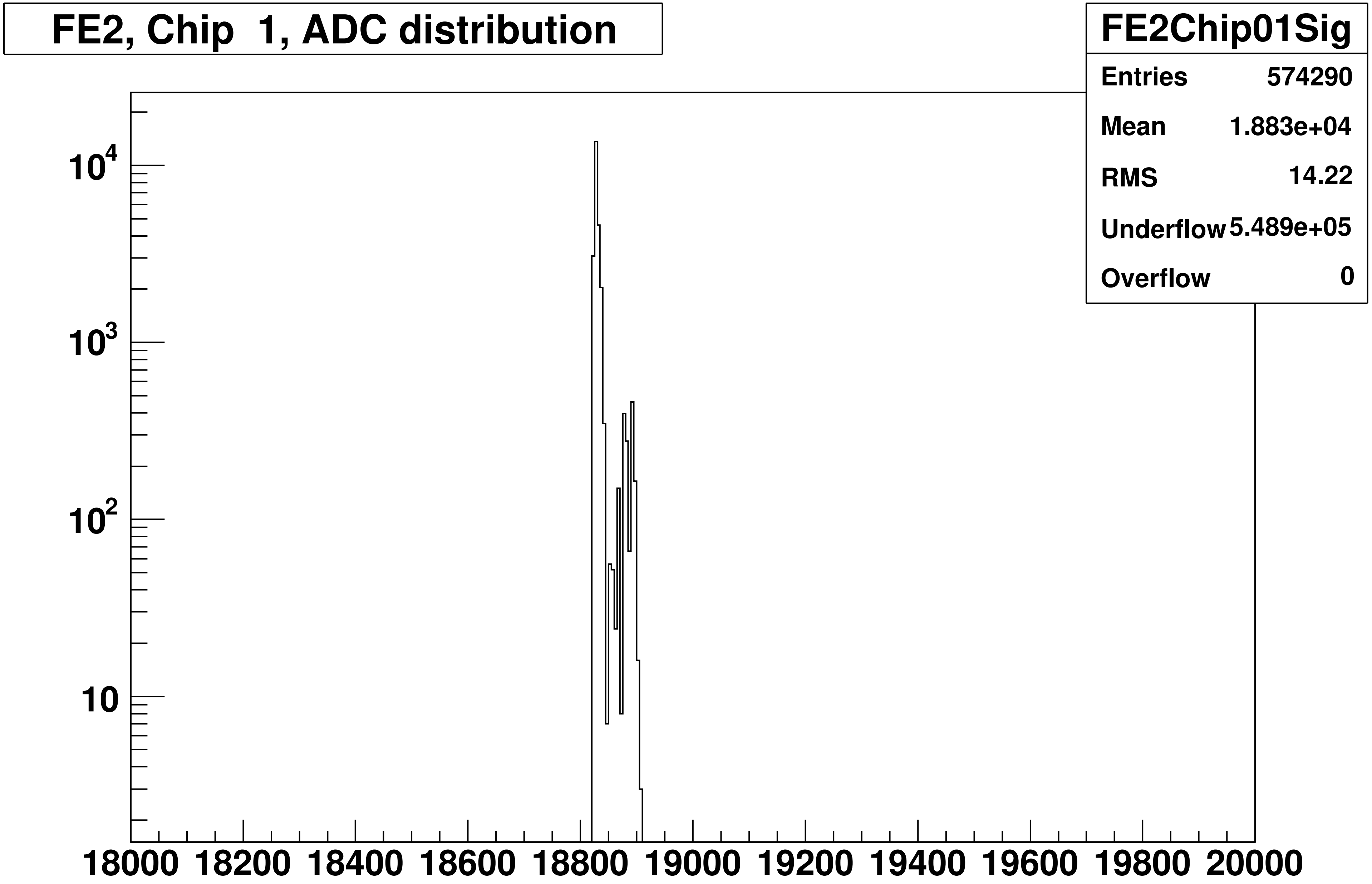
150
200


FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 574290
Mean 1.883e+04
RMS 14.22
Underflow 5.489e+05
Overflow 0
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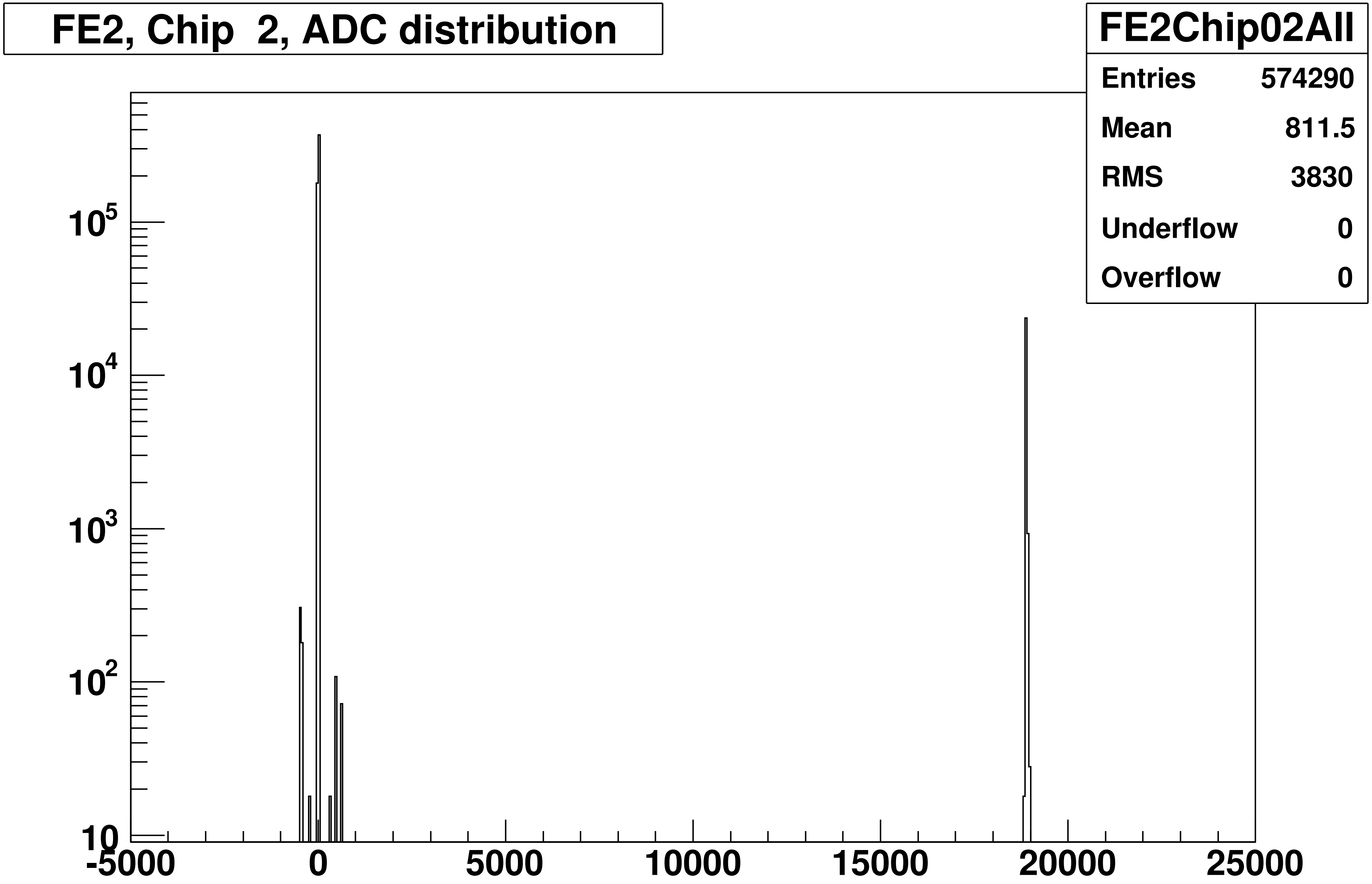
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 574290
Mean 811.5
RMS 3830
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
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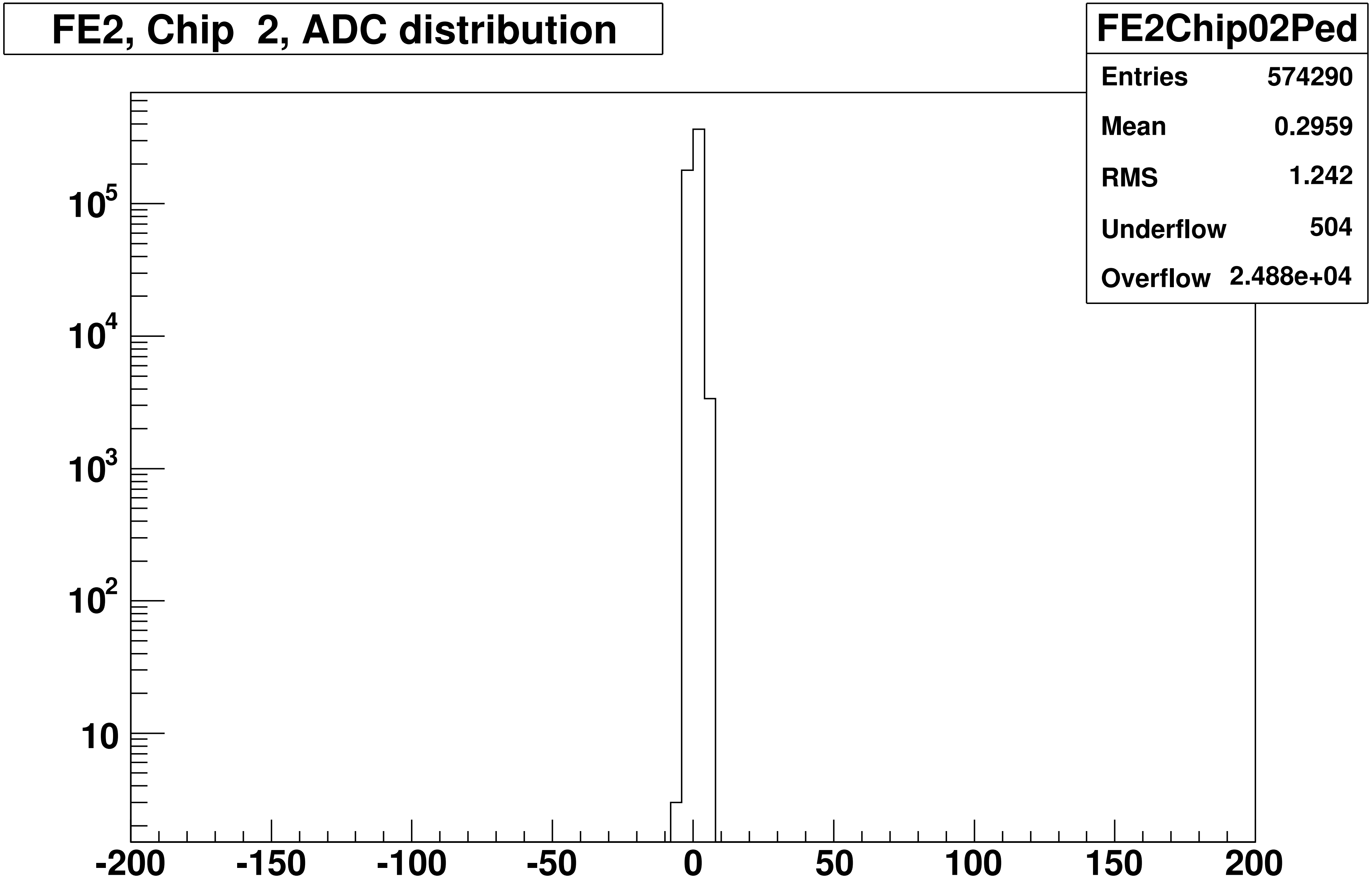
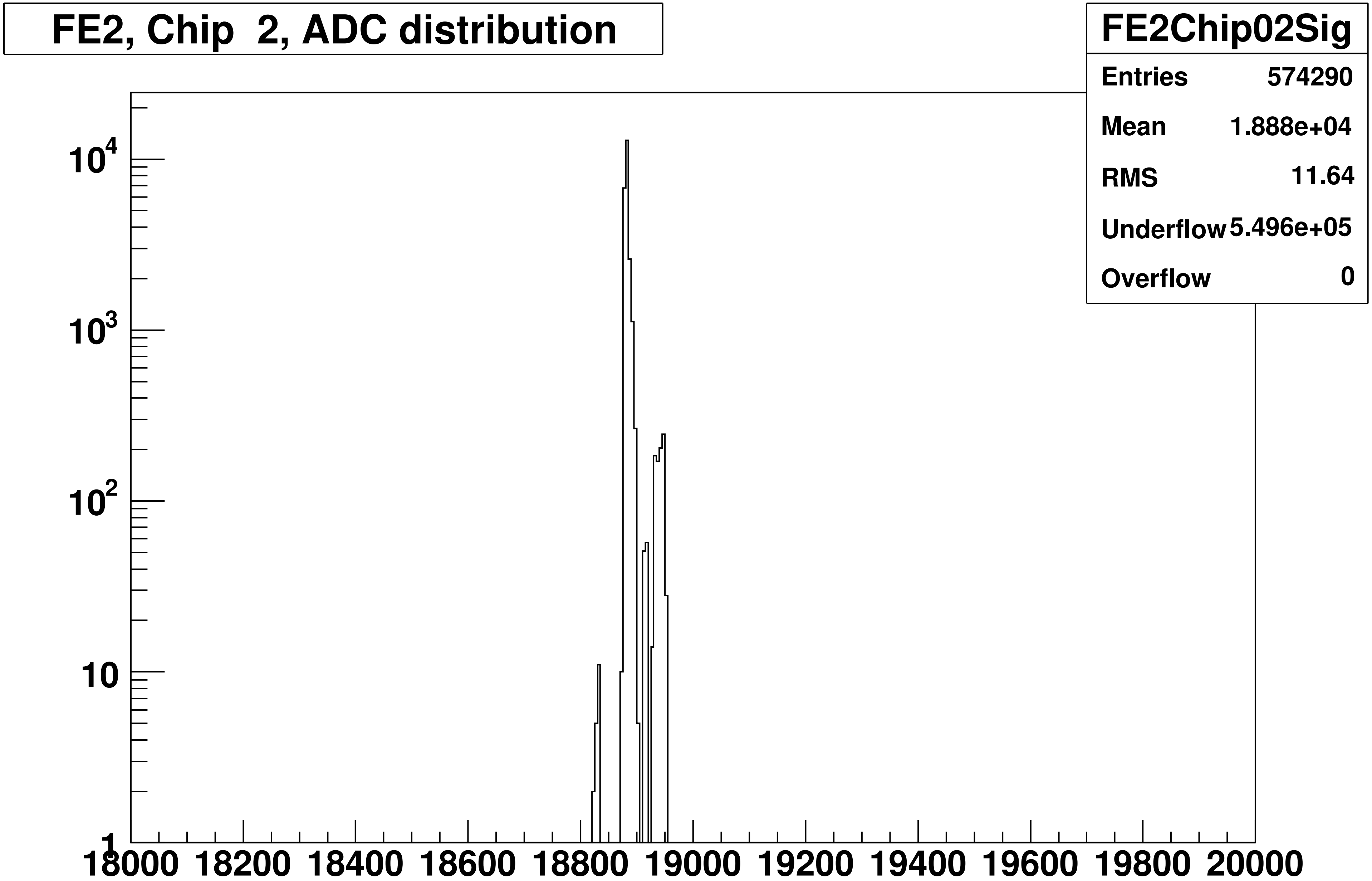
20000
25000


FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 574290
Mean 0.2959
RMS 1.242
Underflow 504
Overflow 2.488e+04
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 574290
Mean 1.888e+04
RMS 11.64
Underflow 5.496e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
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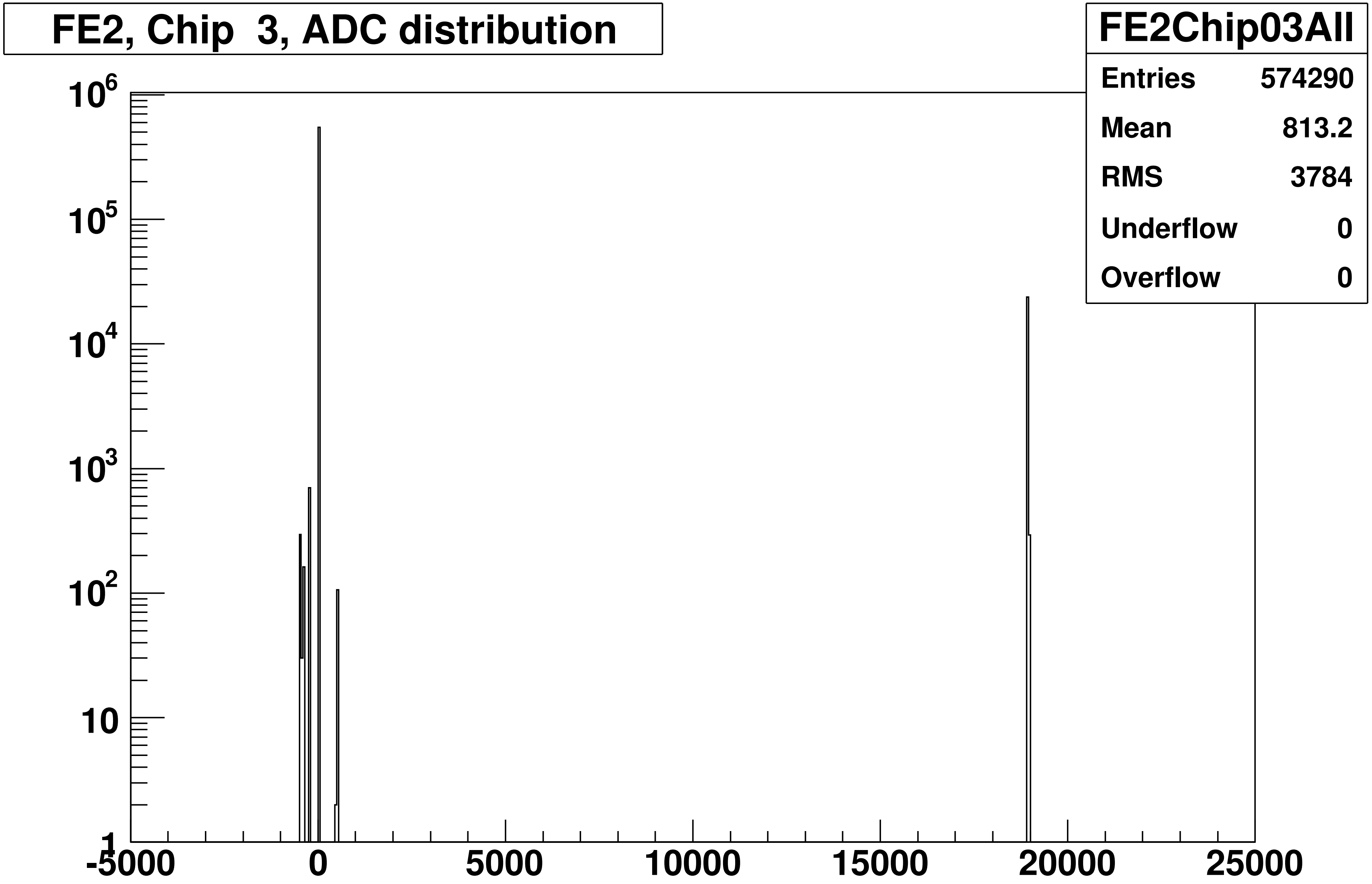
19600
19800
20000


FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 574290
Mean 813.2
RMS 3784
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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10000
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20000
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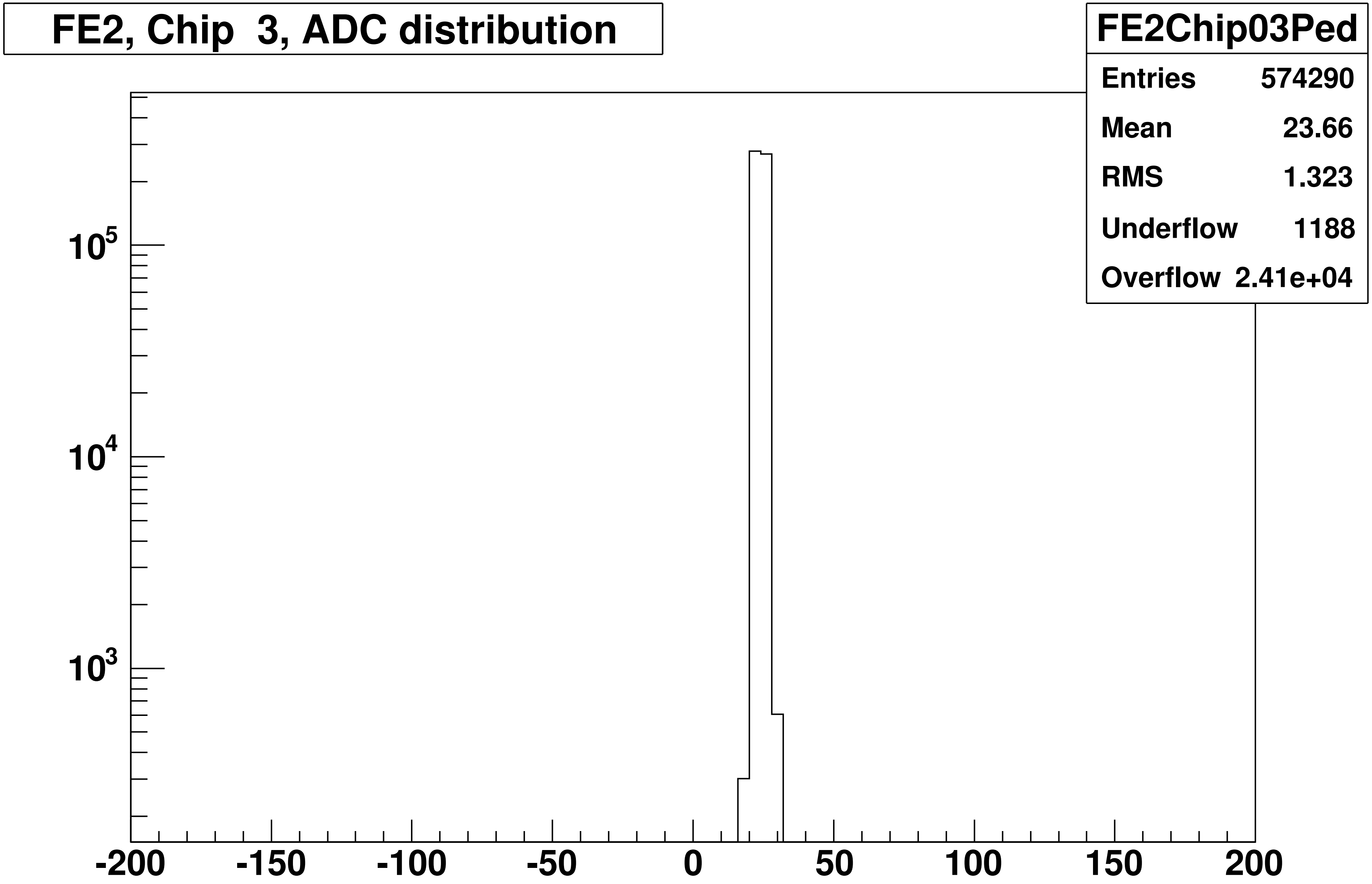
FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 574290
Mean 23.66
RMS 1.323
Underflow 1188
Overflow 2.41e+04
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10^4
10^3
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150
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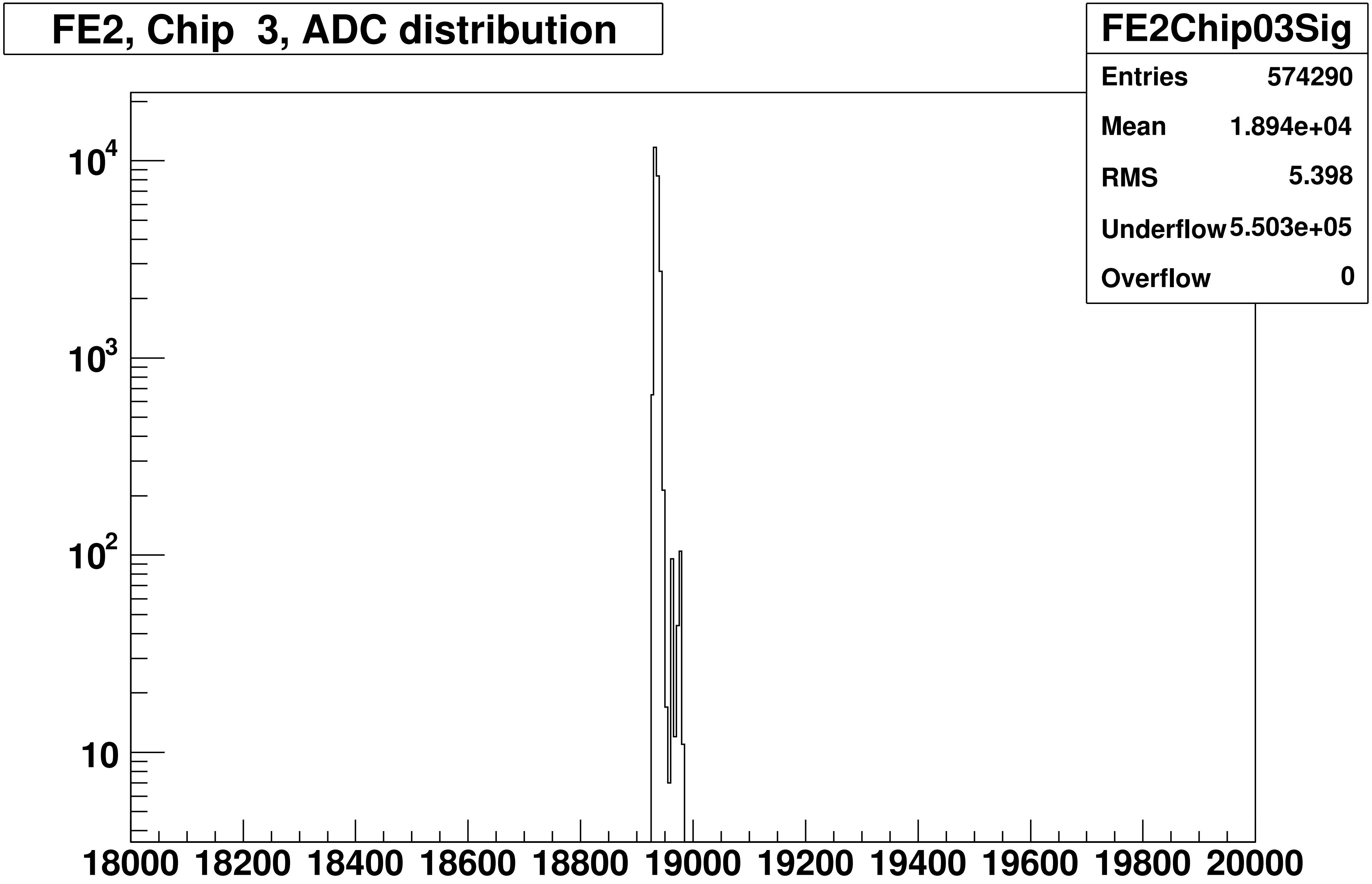
FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 574290
Mean 1.894e+04
RMS 5.398
Underflow 5.503e+05
Overflow 0
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18400
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19000
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19600
19800
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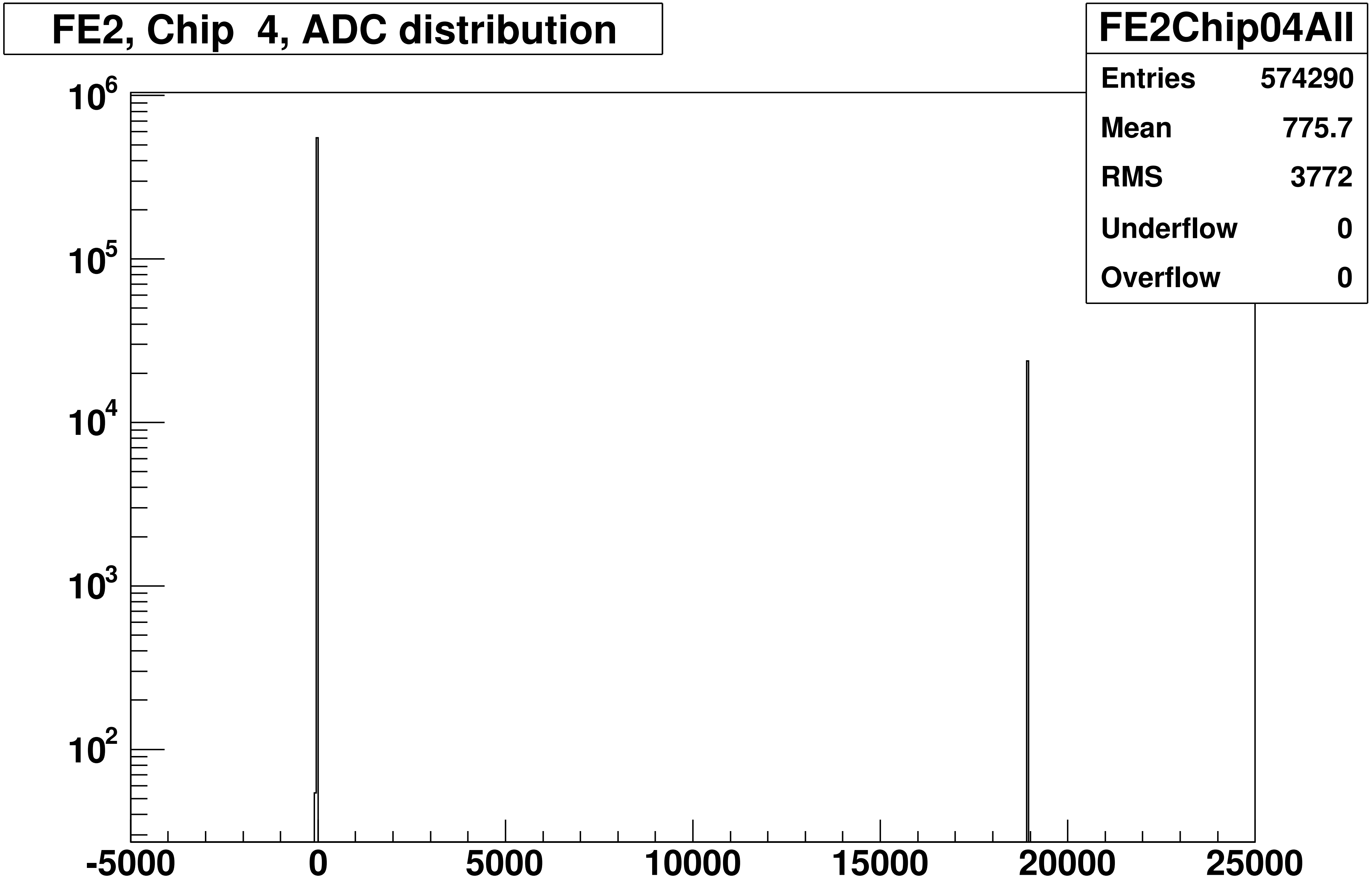
FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 574290
Mean 775.7
RMS 3772
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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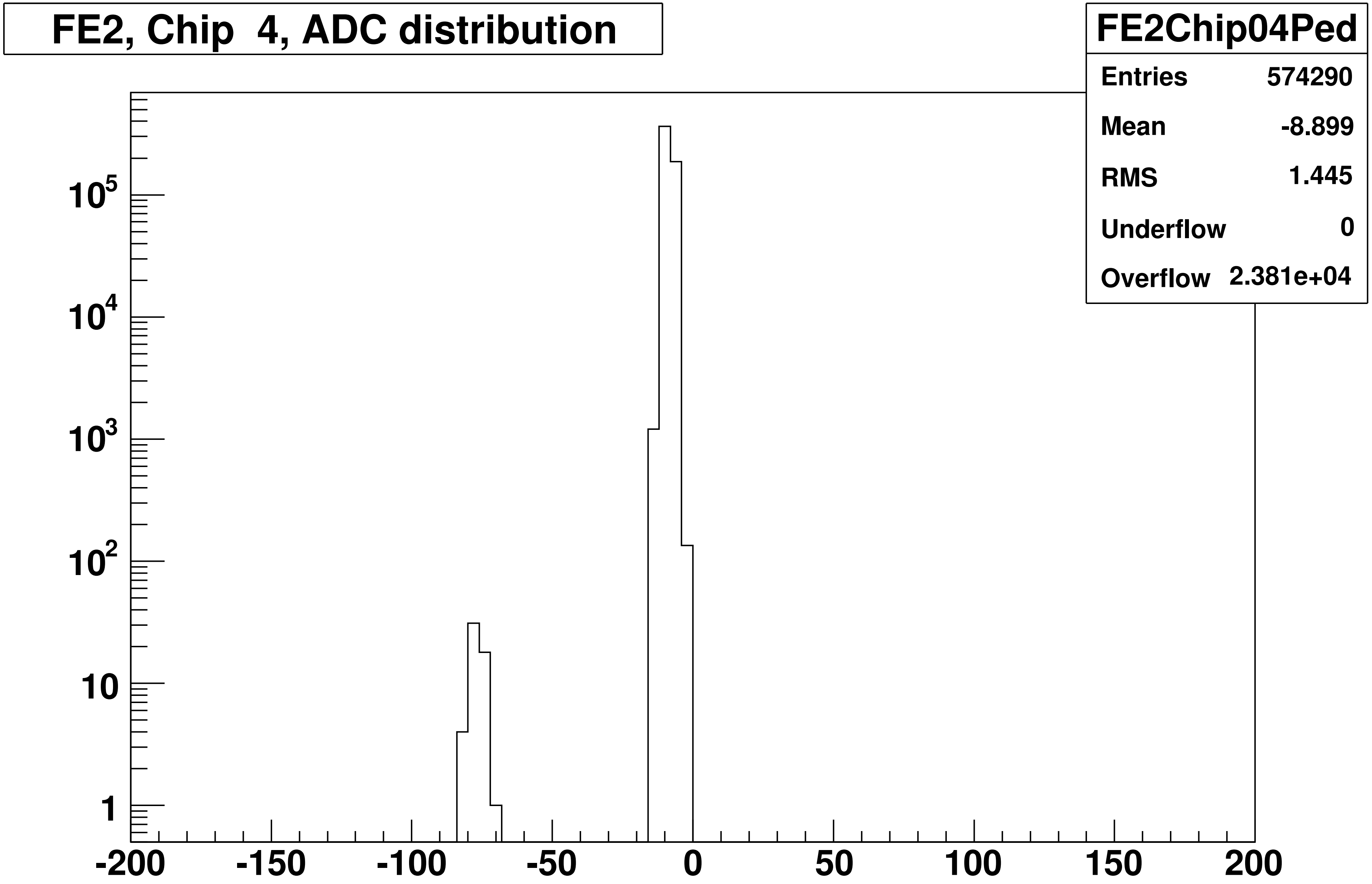
FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 574290
Mean -8.899
RMS 1.445
Underflow 0
Overflow 2.381e+04
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10^2
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150
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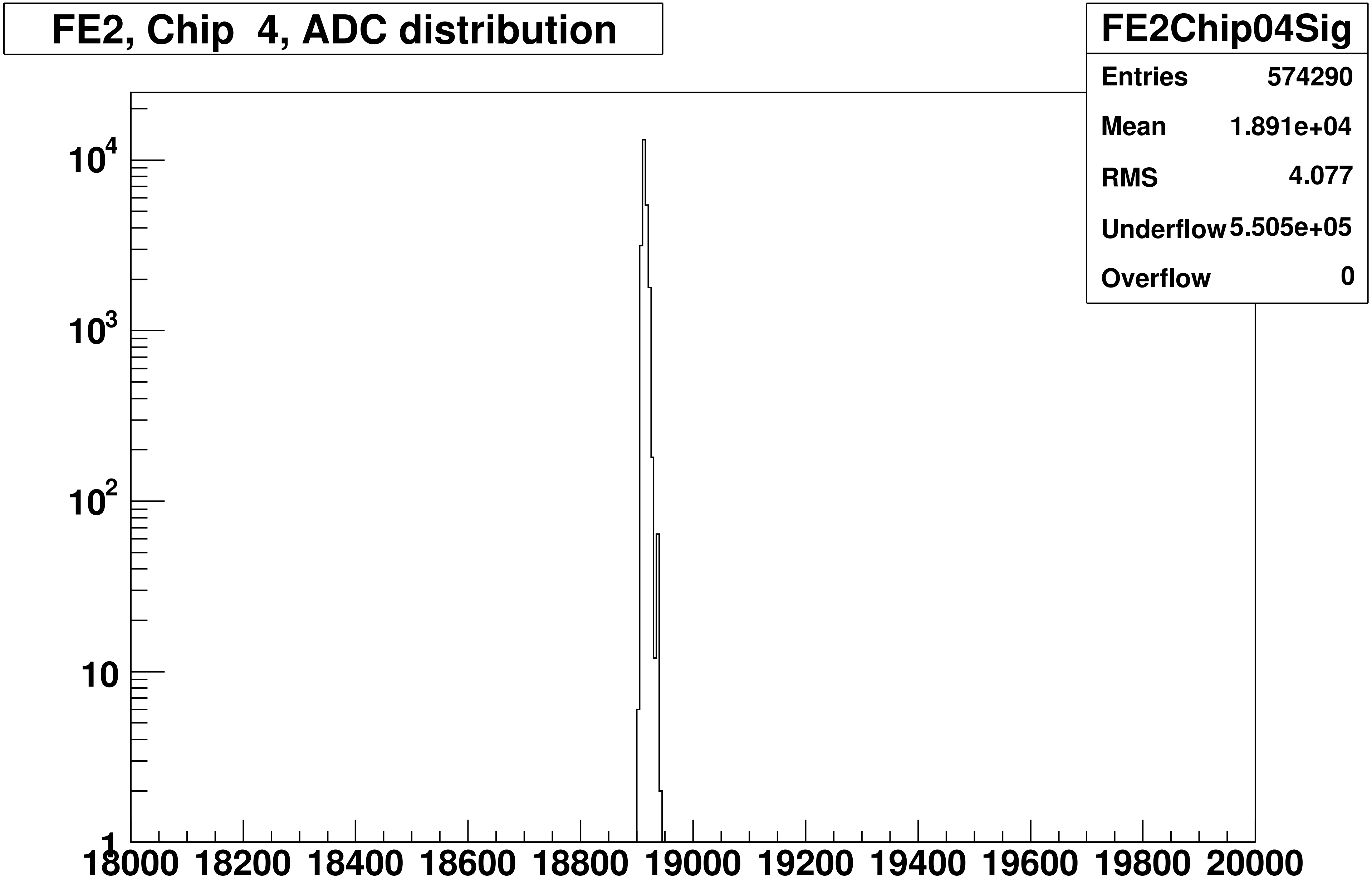

FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 574290
Mean 1.891e+04
RMS 4.077
Underflow 5.505e+05
Overflow 0
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10^3
10^2
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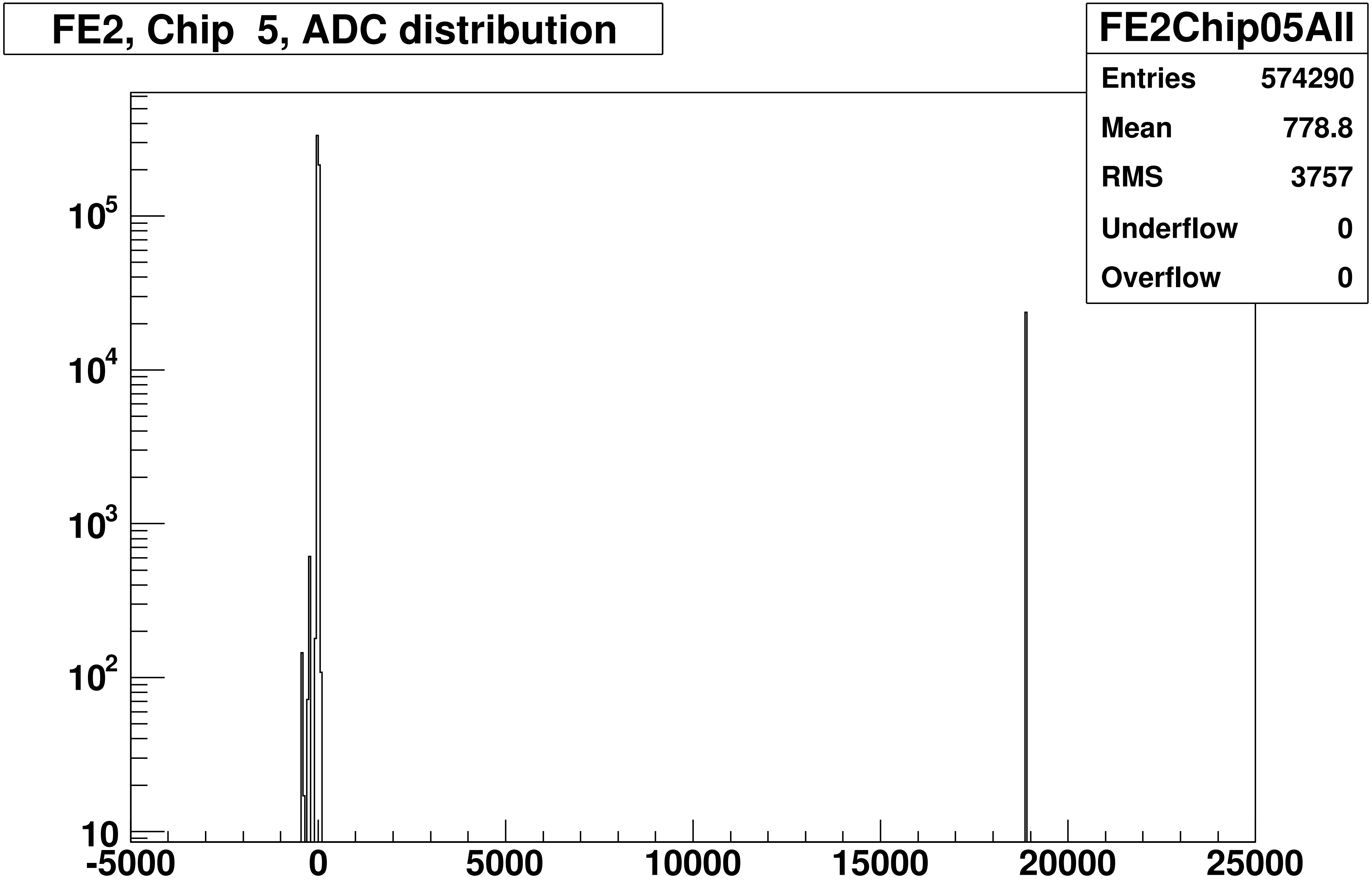
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 574290
Mean 778.8
RMS 3757
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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15000
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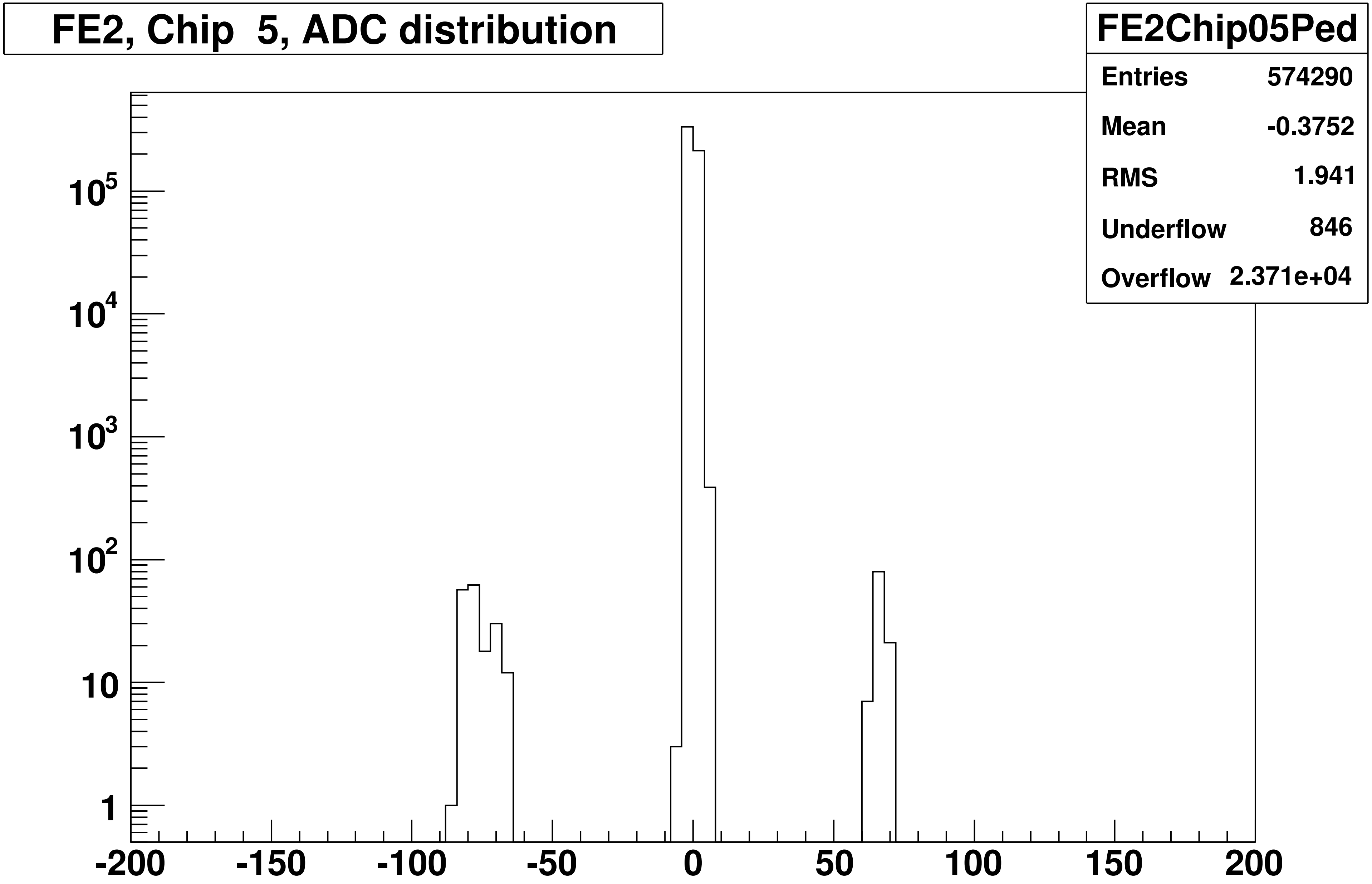
FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 574290
Mean -0.3752
RMS 1.941
Underflow 846
Overflow 2.371e+04
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10^3
10^4
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150
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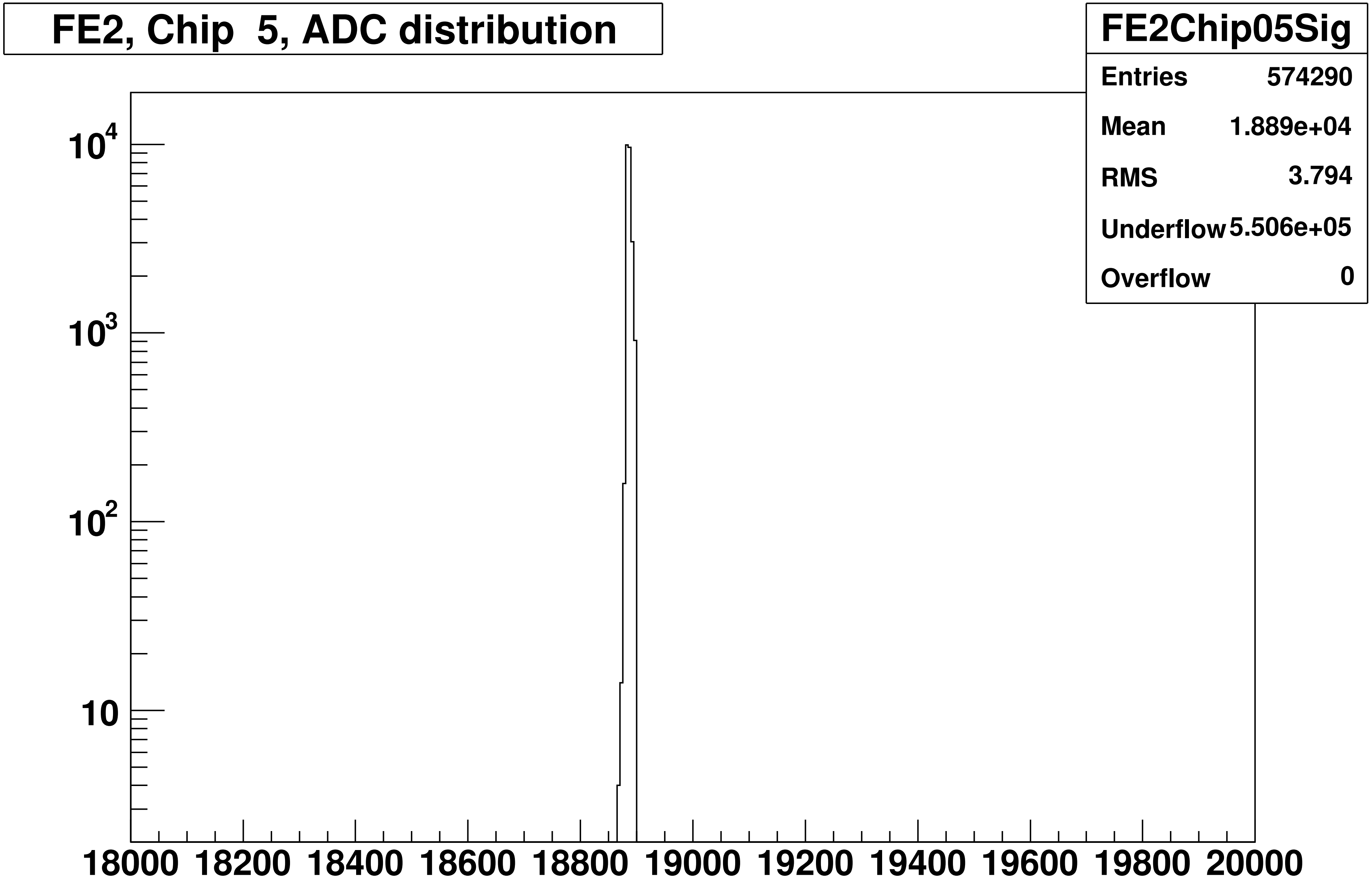

FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 574290
Mean 1.889e+04
RMS 3.794
Underflow 5.506e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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19600
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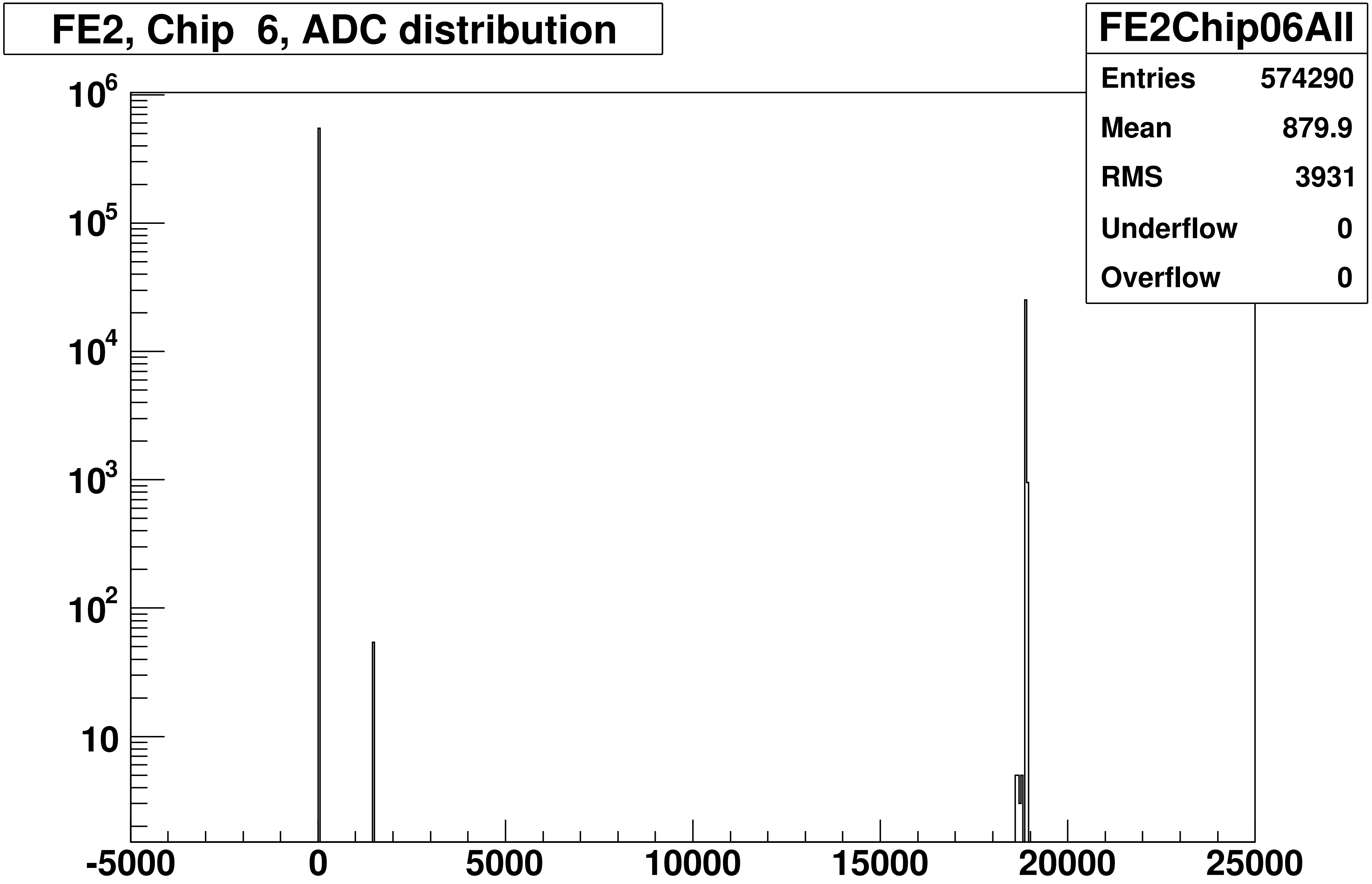
20000


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 574290
Mean 879.9
RMS 3931
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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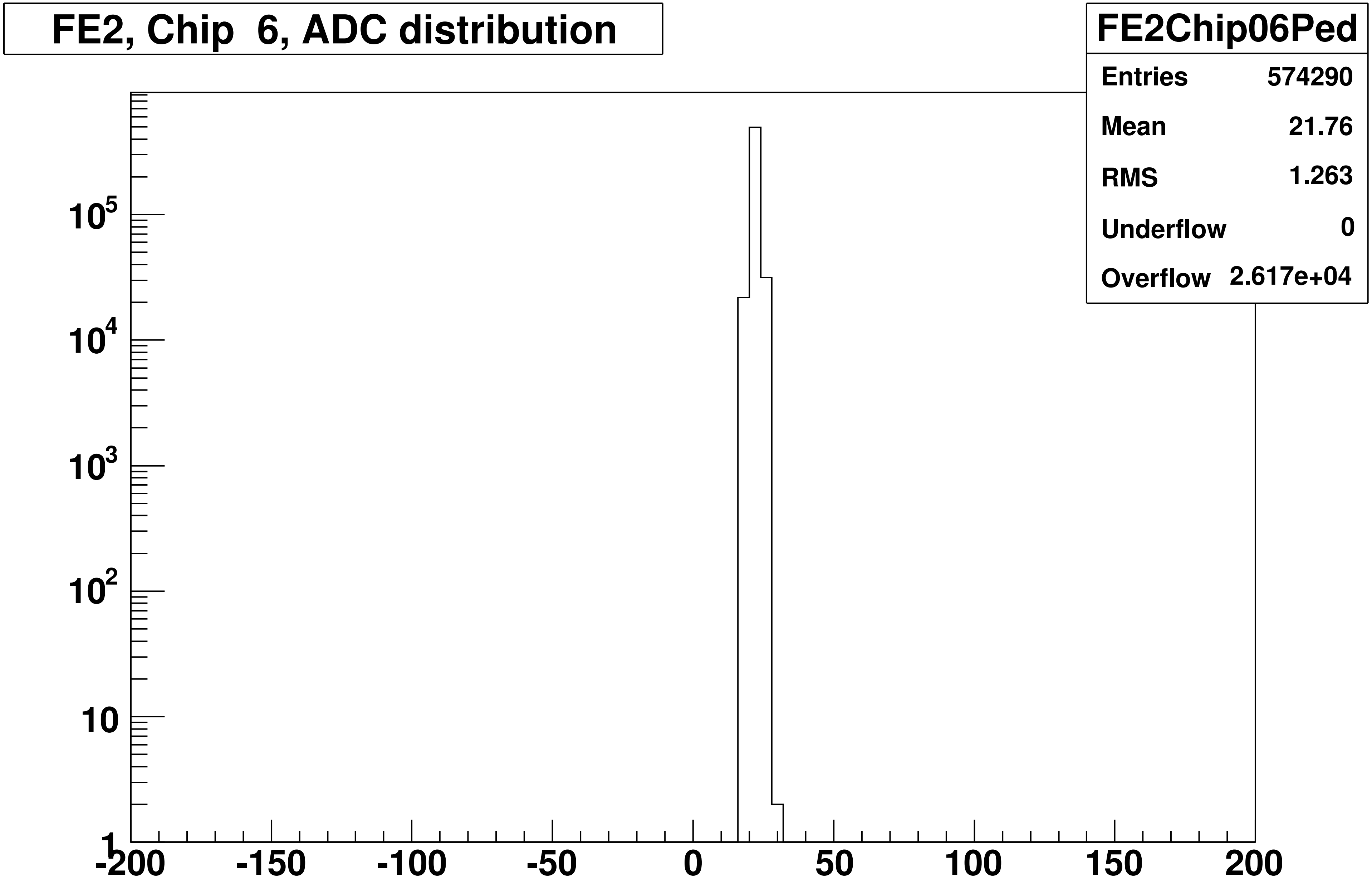
10000
15000
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 574290
Mean 21.76
RMS 1.263
Underflow 0
Overflow 2.617e+04
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10^3
10^2
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150
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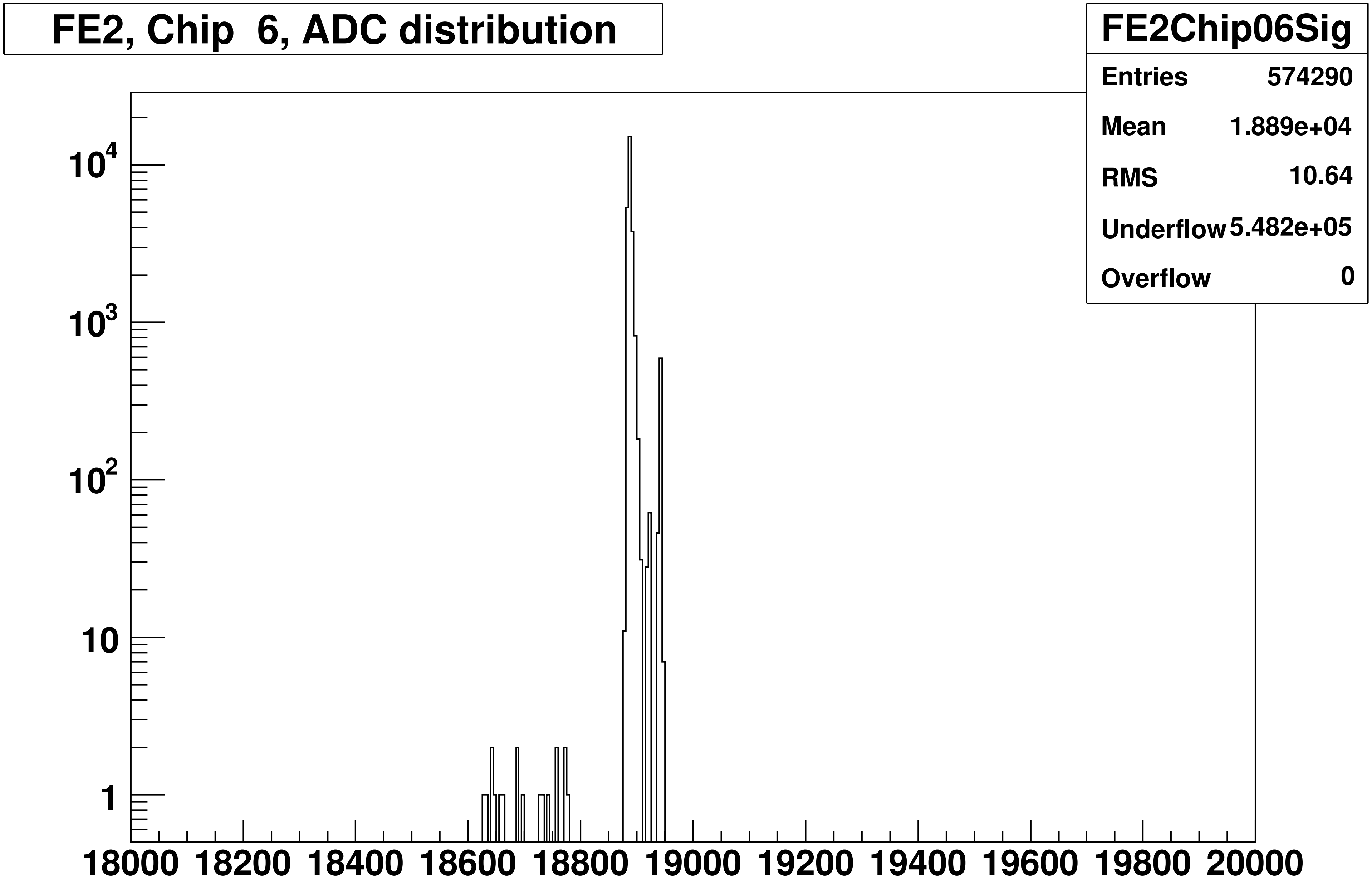
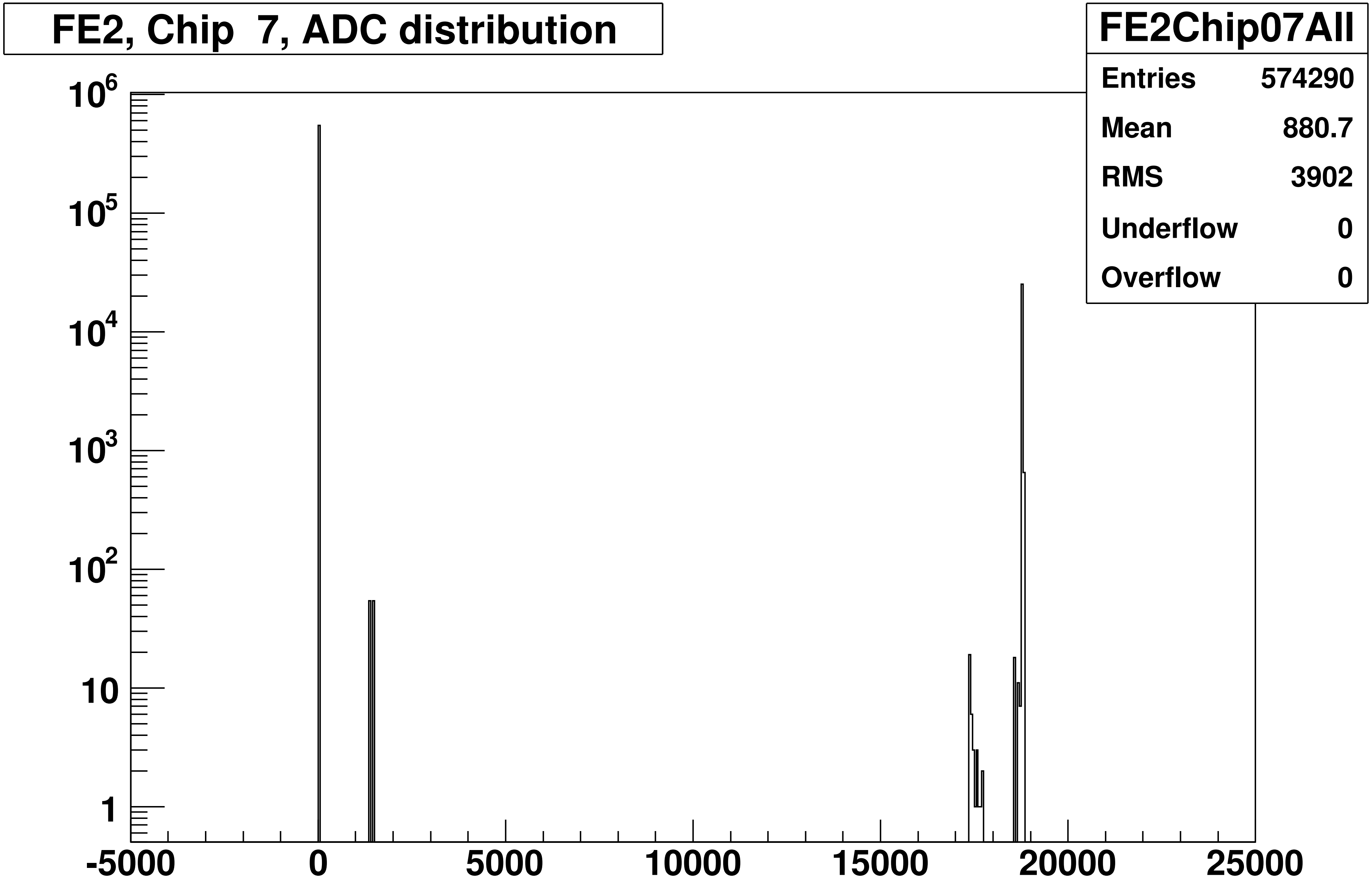

FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 574290
Mean 1.889e+04
RMS 10.64
Underflow 5.482e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 574290
Mean 880.7
RMS 3902
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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20000
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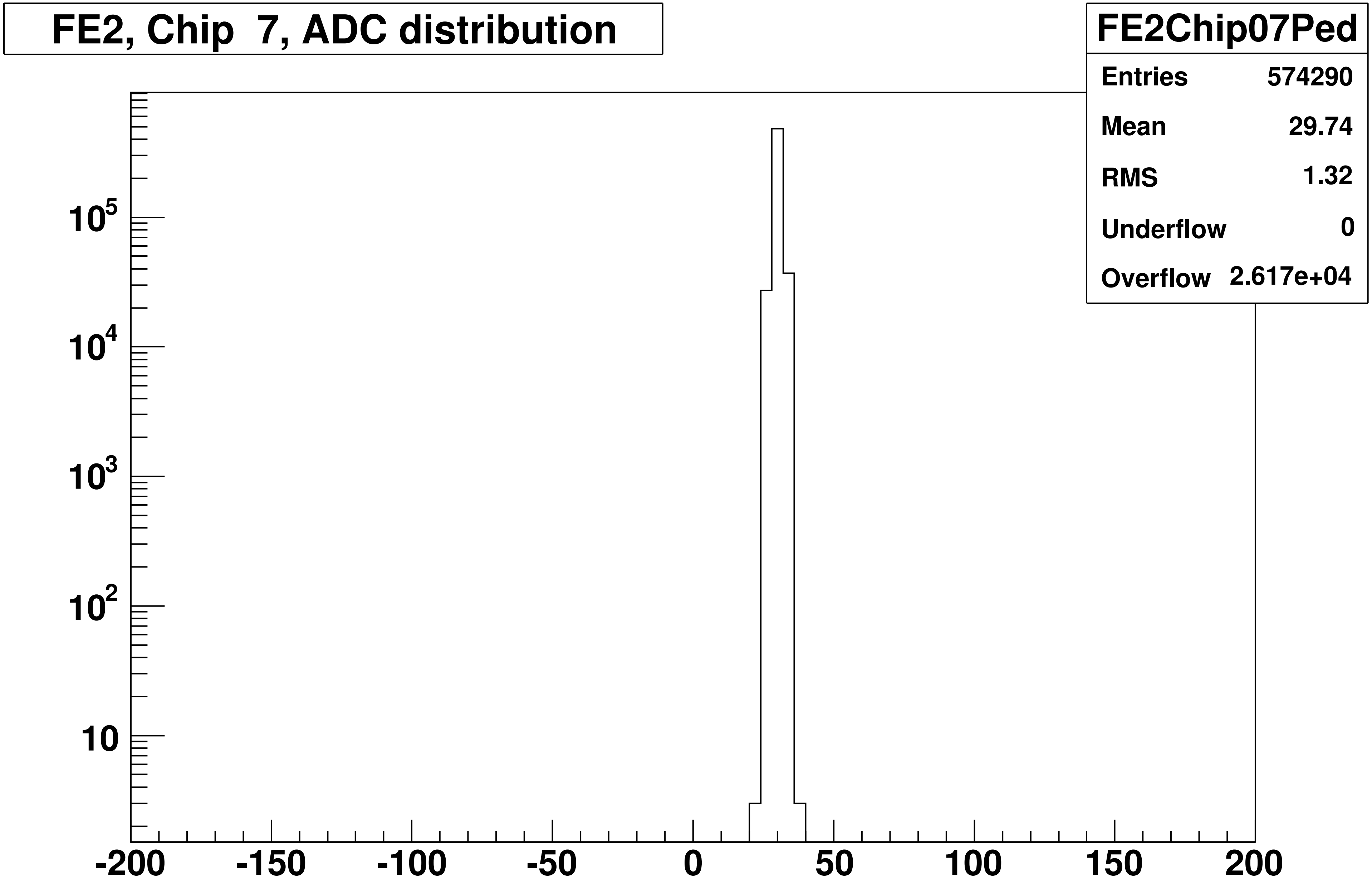

FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 574290
Mean 29.74
RMS 1.32
Underflow 0
Overflow 2.617e+04
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10^3
10^4
10^5
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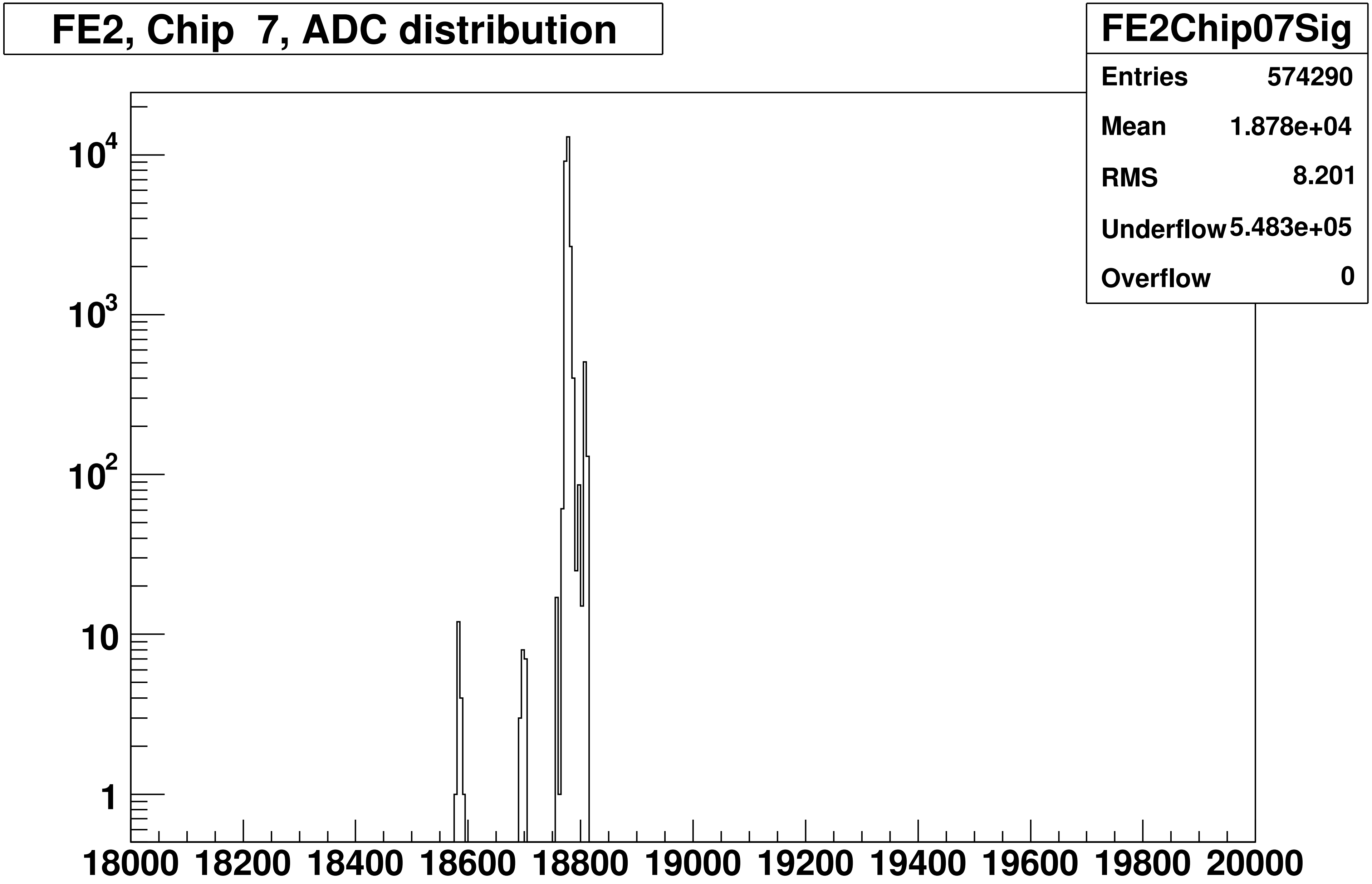
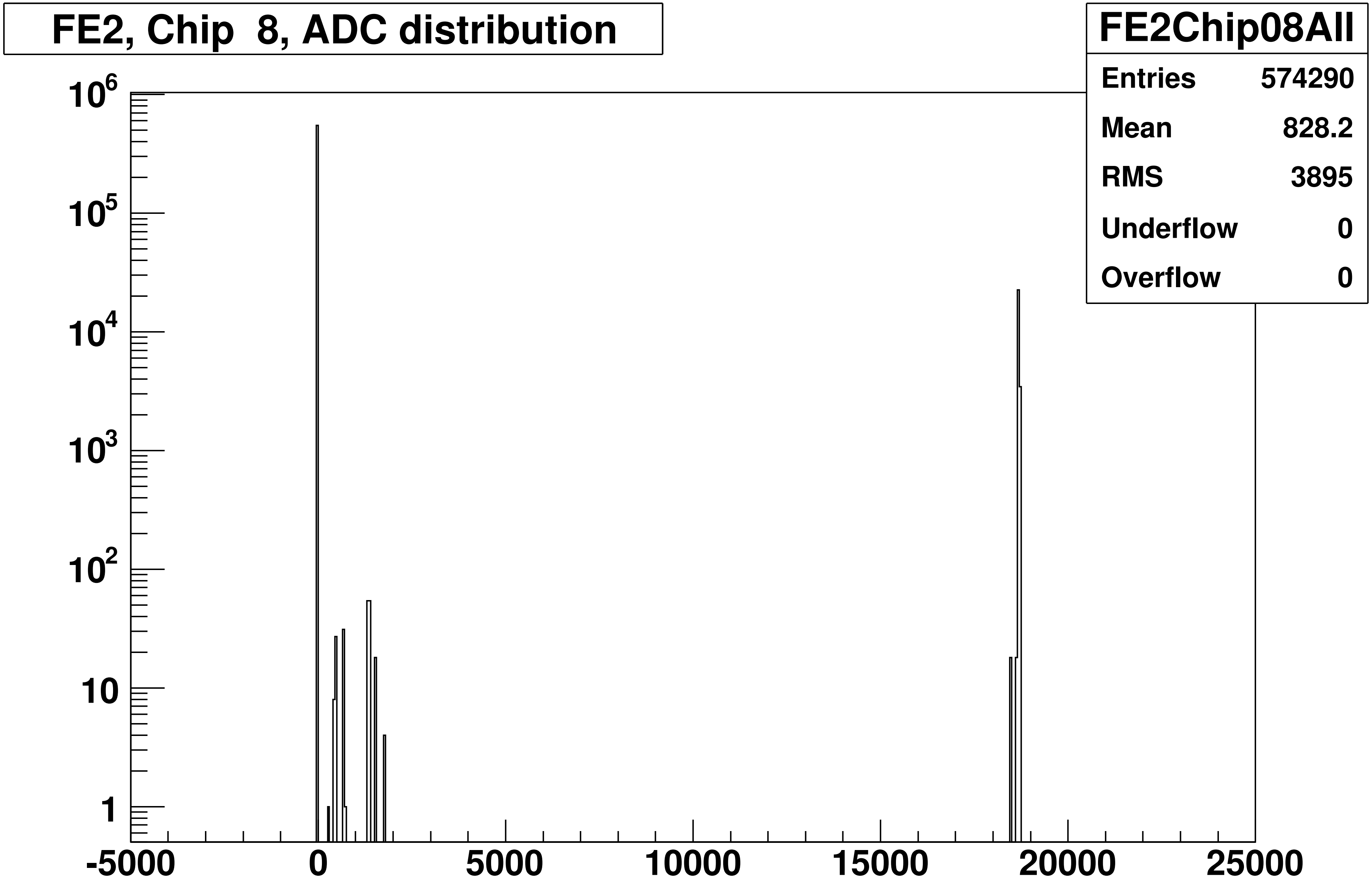
100
150
200


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 574290
Mean 1.878e+04
RMS 8.201
Underflow 5.483e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 574290
Mean 828.2
RMS 3895
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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15000
20000
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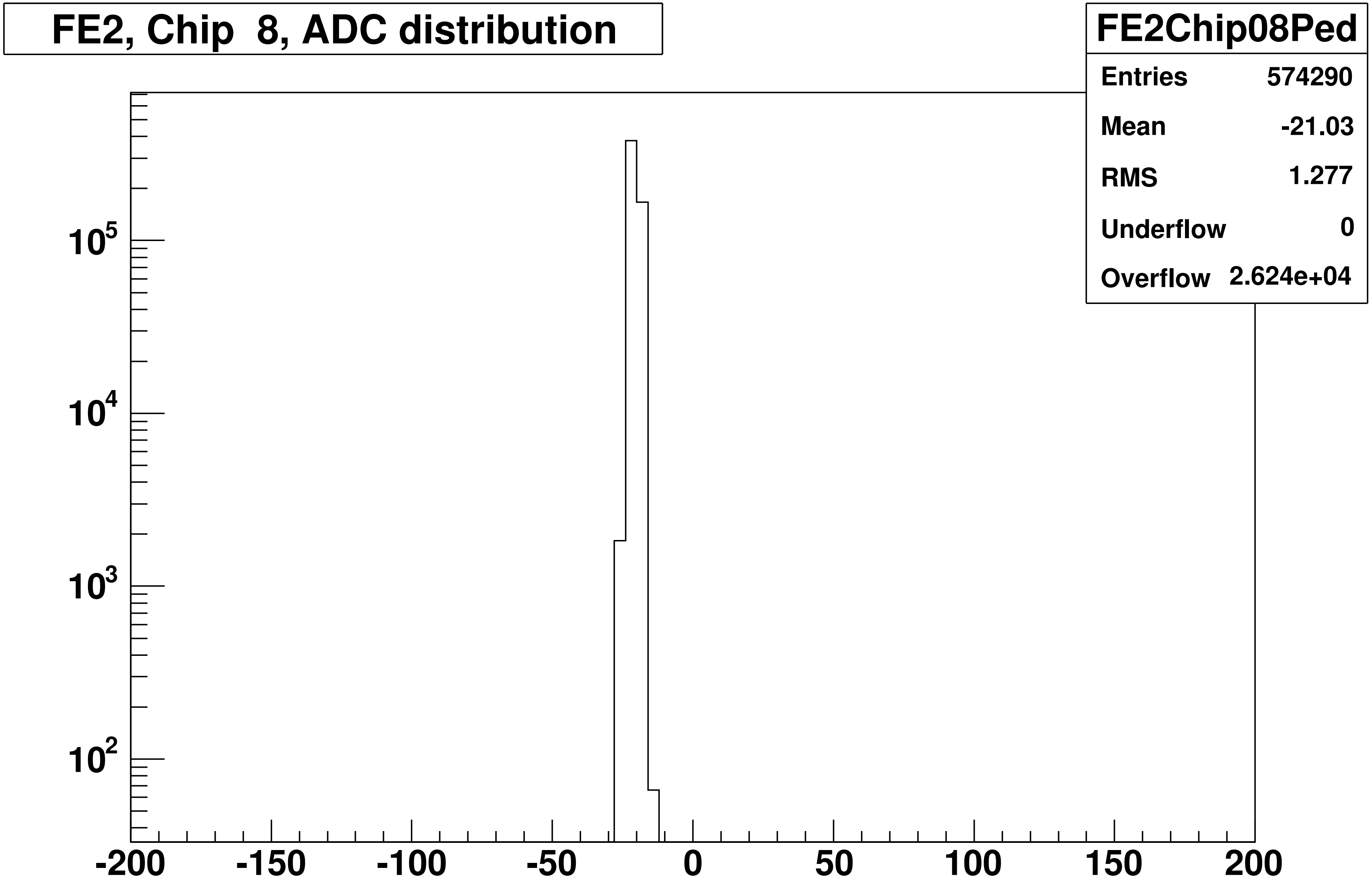
FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 574290
Mean -21.03
RMS 1.277
Underflow 0
Overflow 2.624e+04
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10^4
10^3
10^2
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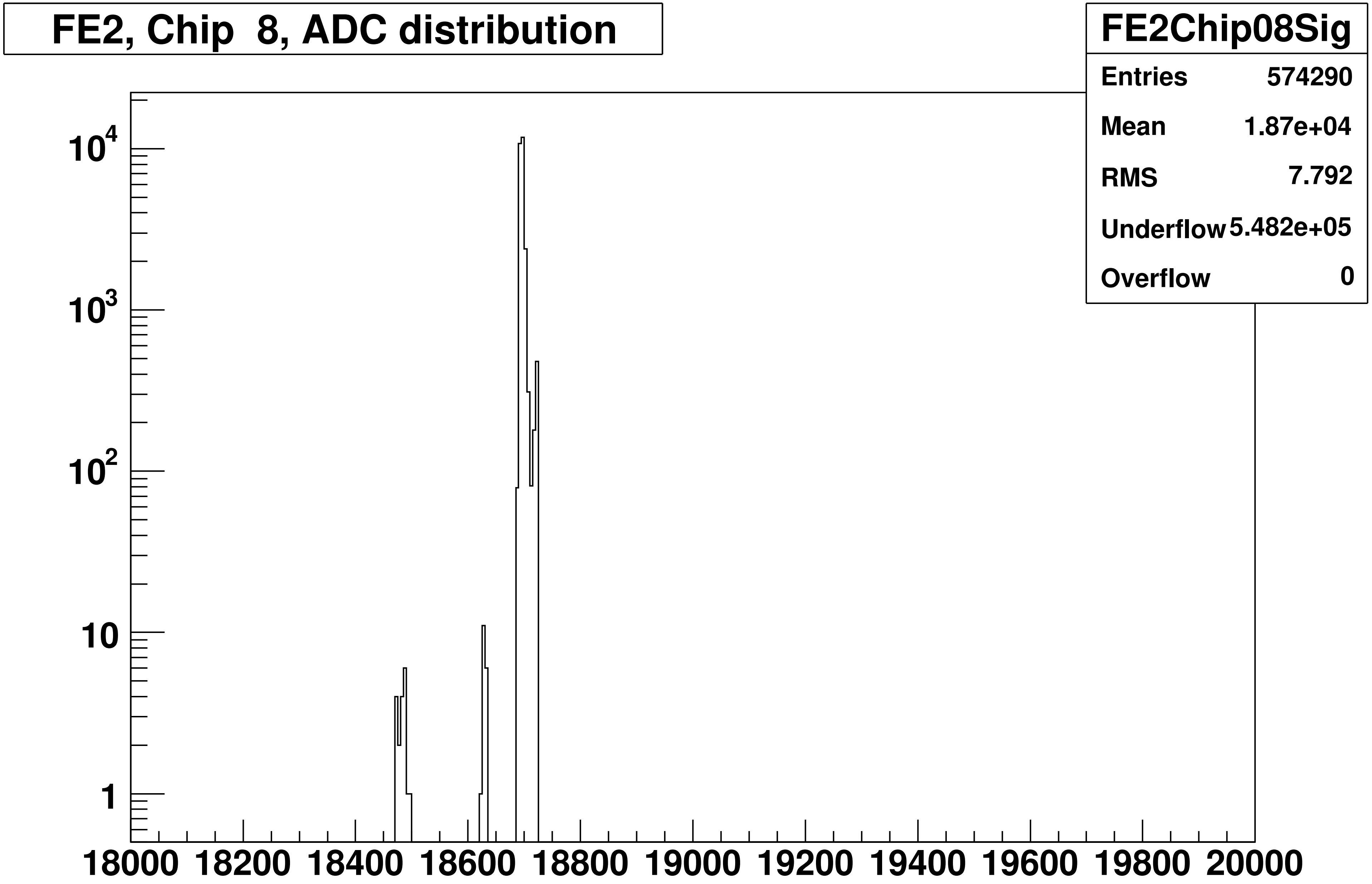
100
150
200


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 574290
Mean 1.87e+04
RMS 7.792
Underflow 5.482e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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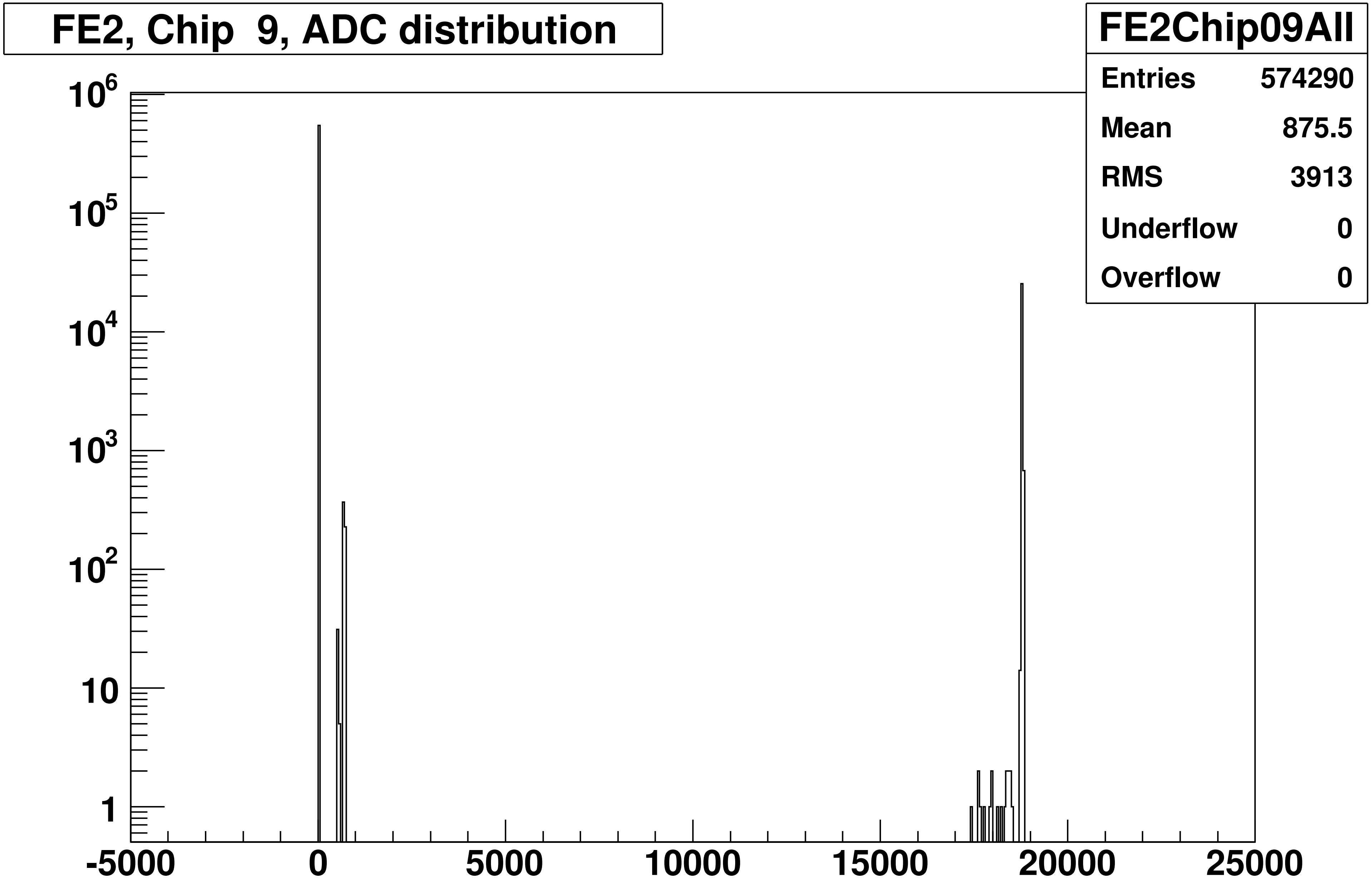
19800
20000


FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 574290
Mean 875.5
RMS 3913
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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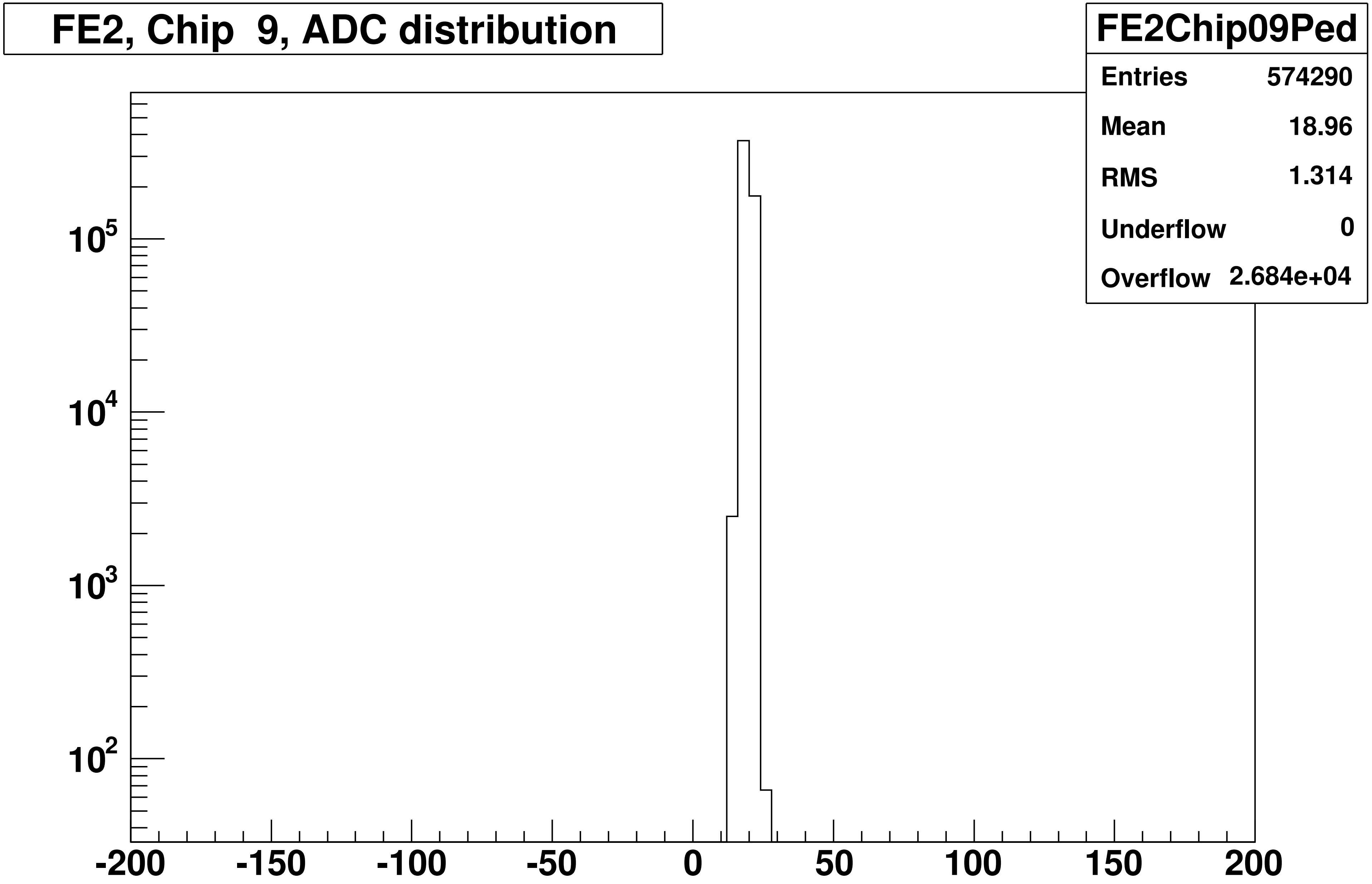
FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 574290
Mean 18.96
RMS 1.314
Underflow 0
Overflow 2.684e+04
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10^4
10^3
10^2
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100
150
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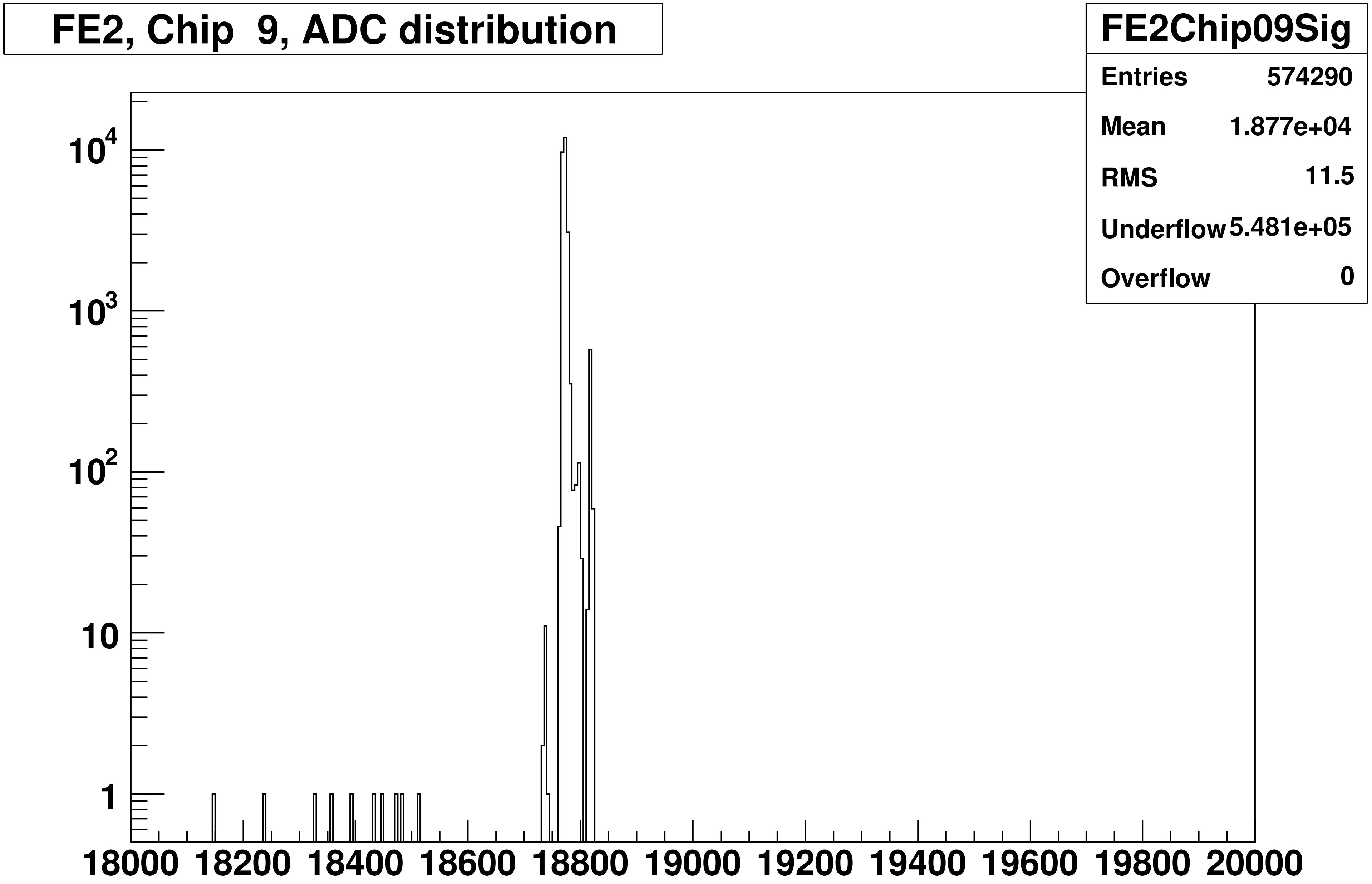
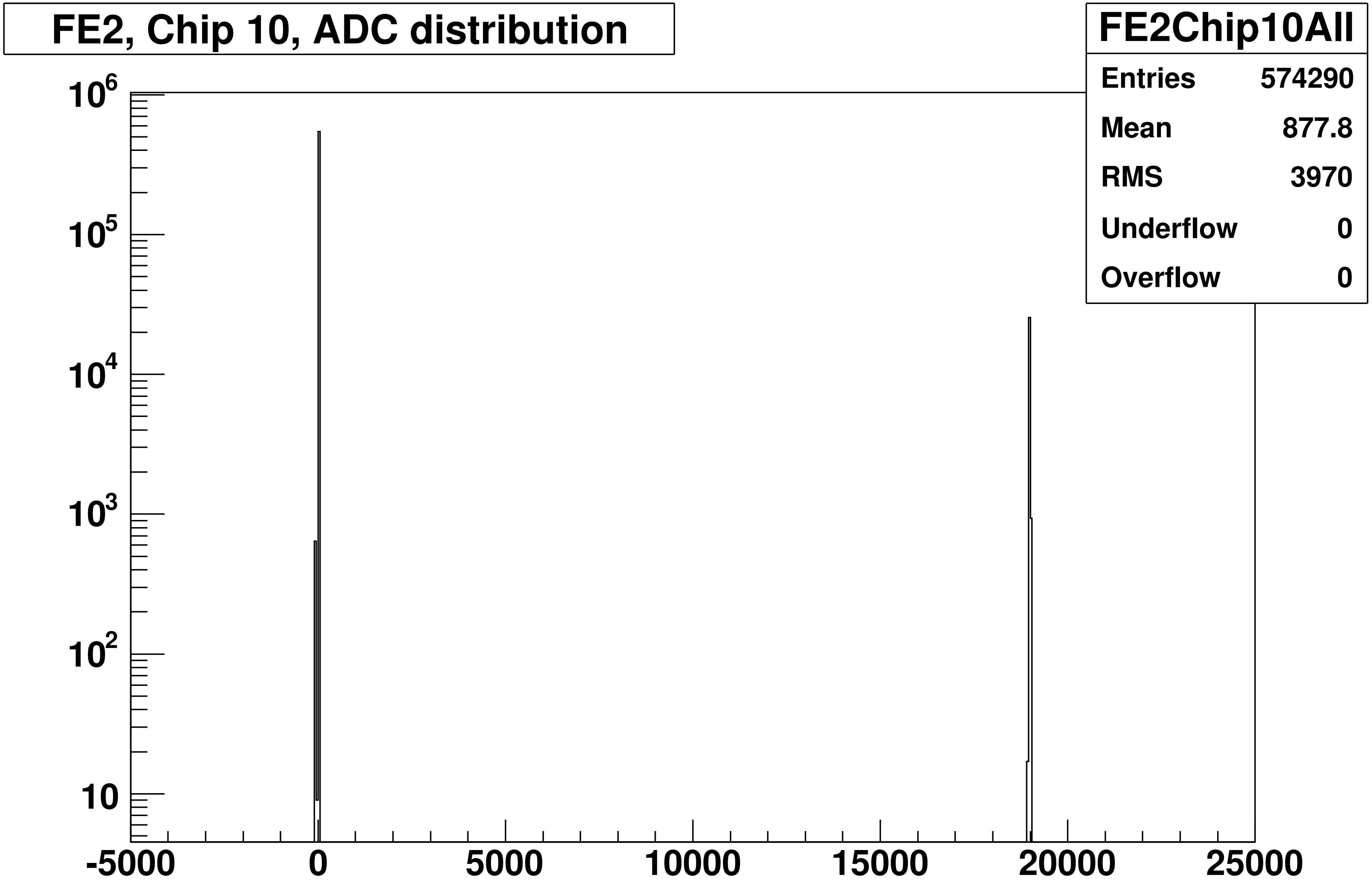

FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 574290
Mean 1.877e+04
RMS 11.5
Underflow 5.481e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 574290
Mean 877.8
RMS 3970
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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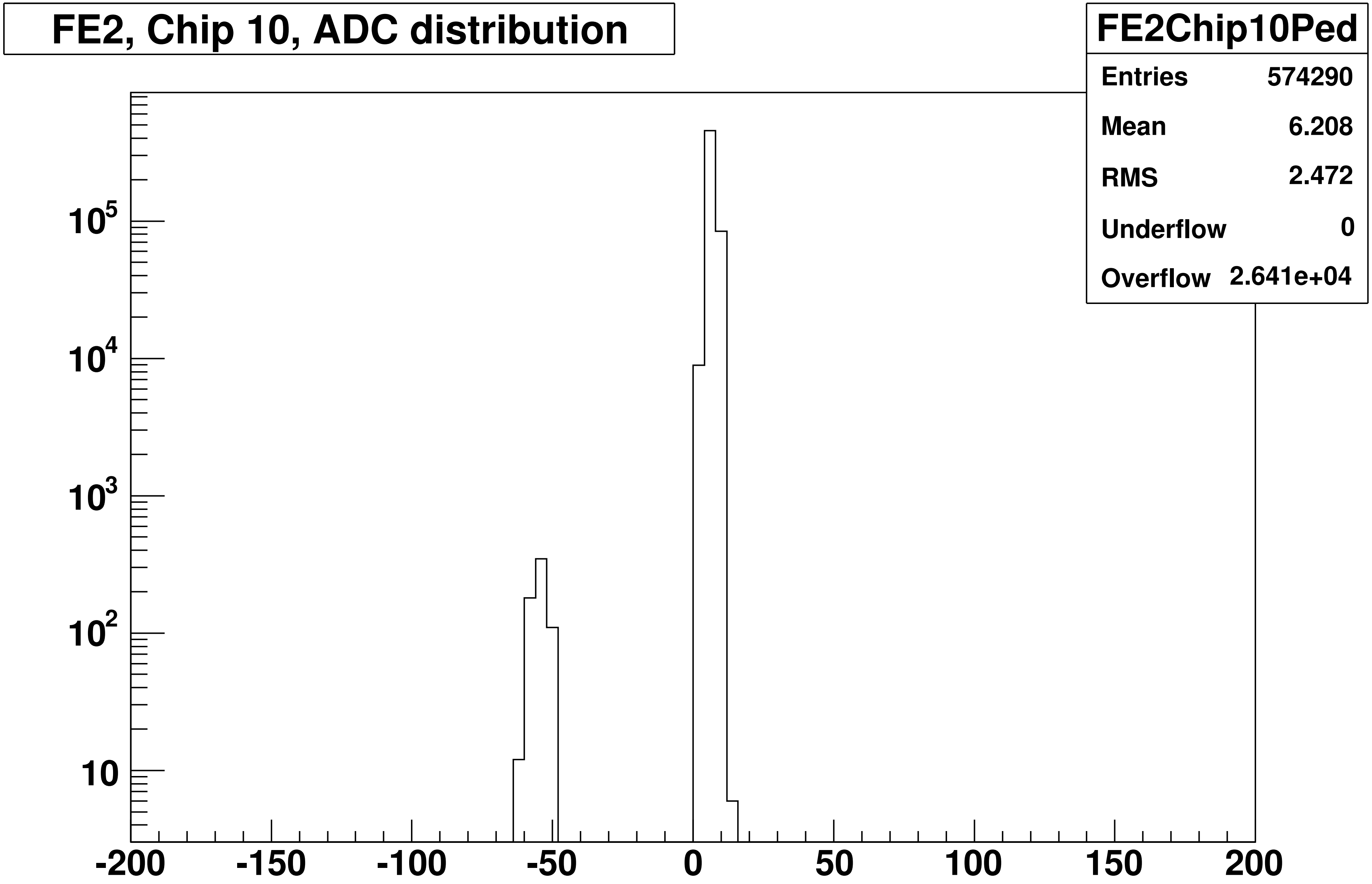
FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 574290
Mean 6.208
RMS 2.472
Underflow 0
Overflow 2.641e+04
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10^3
10^4
10^5
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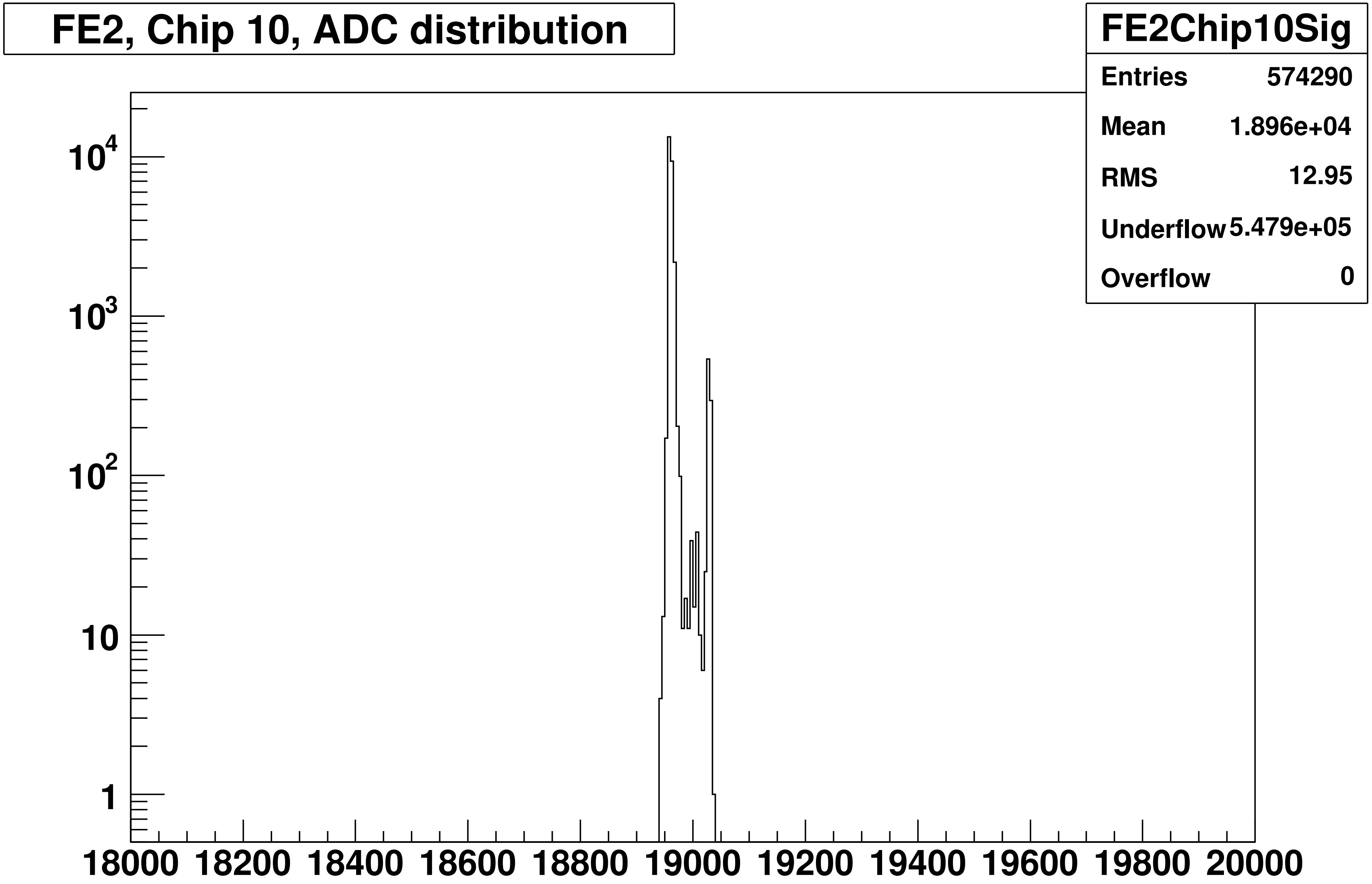
150
200


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 574290
Mean 1.896e+04
RMS 12.95
Underflow 5.479e+05
Overflow 0
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10^3
10^2
10
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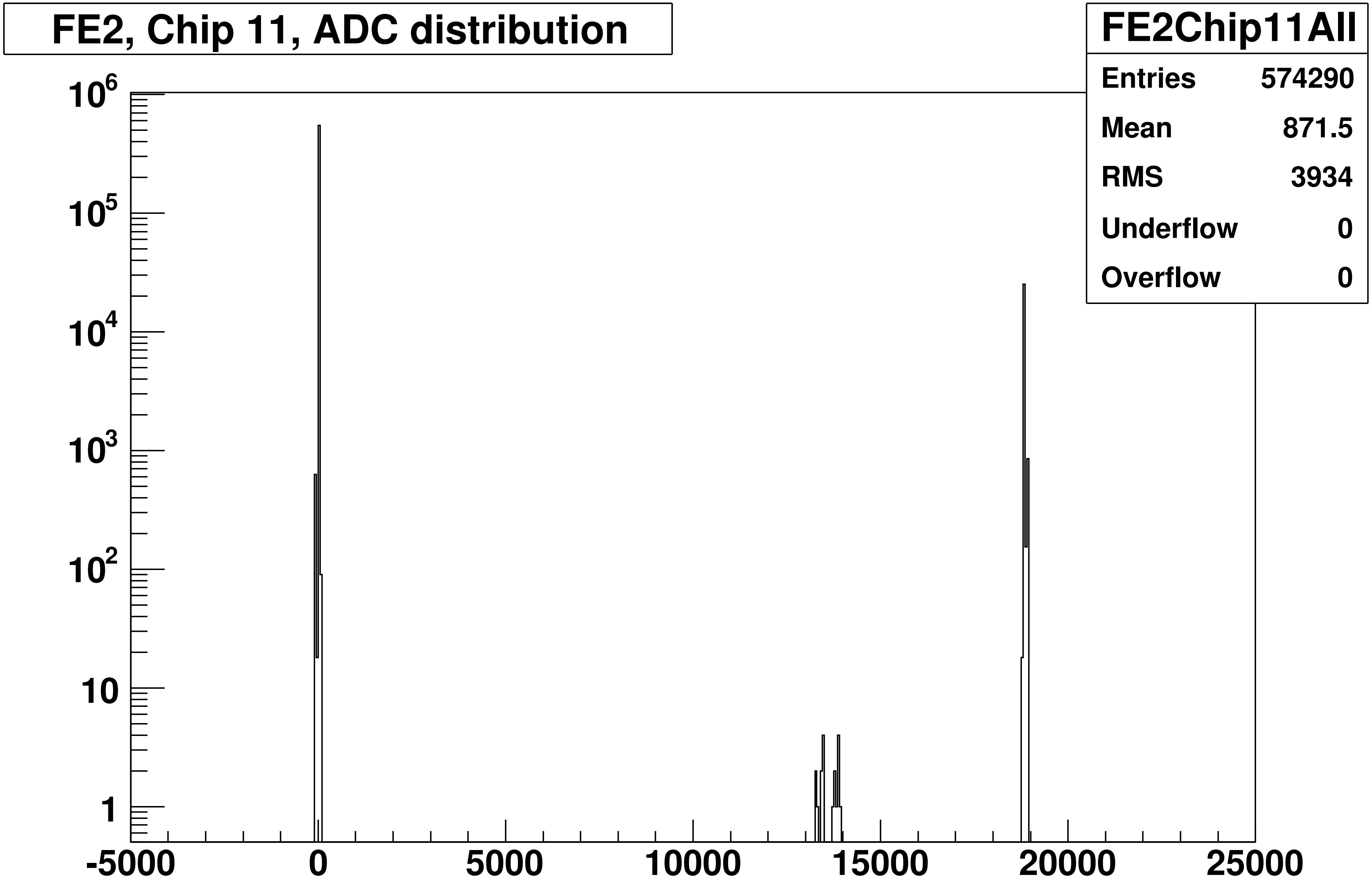
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 574290
Mean 871.5
RMS 3934
Underflow 0
Overflow 0
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10^3
10^2
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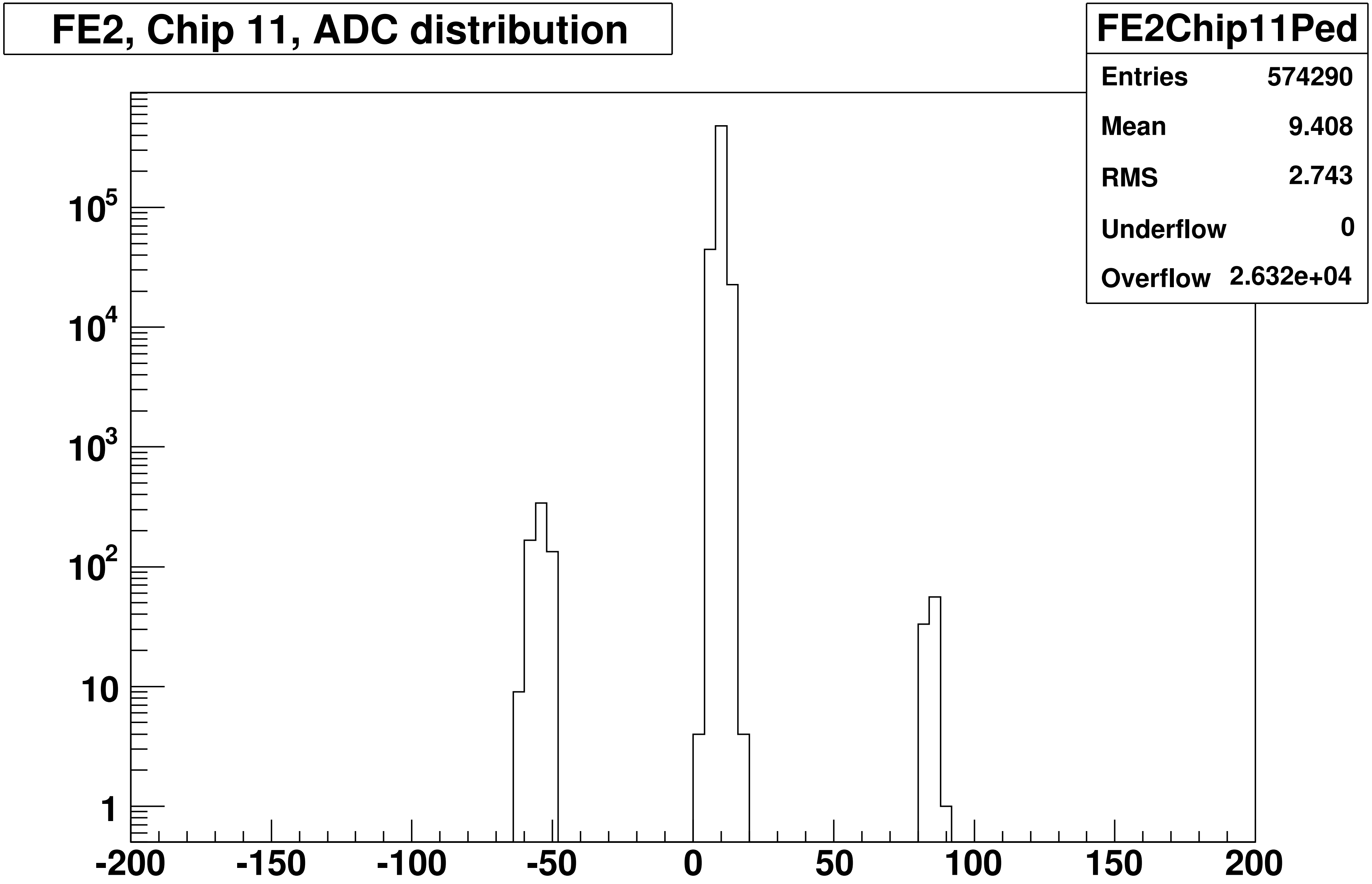
FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 574290
Mean 9.408
RMS 2.743
Underflow 0
Overflow 2.632e+04
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10^3
10^2
10
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-100
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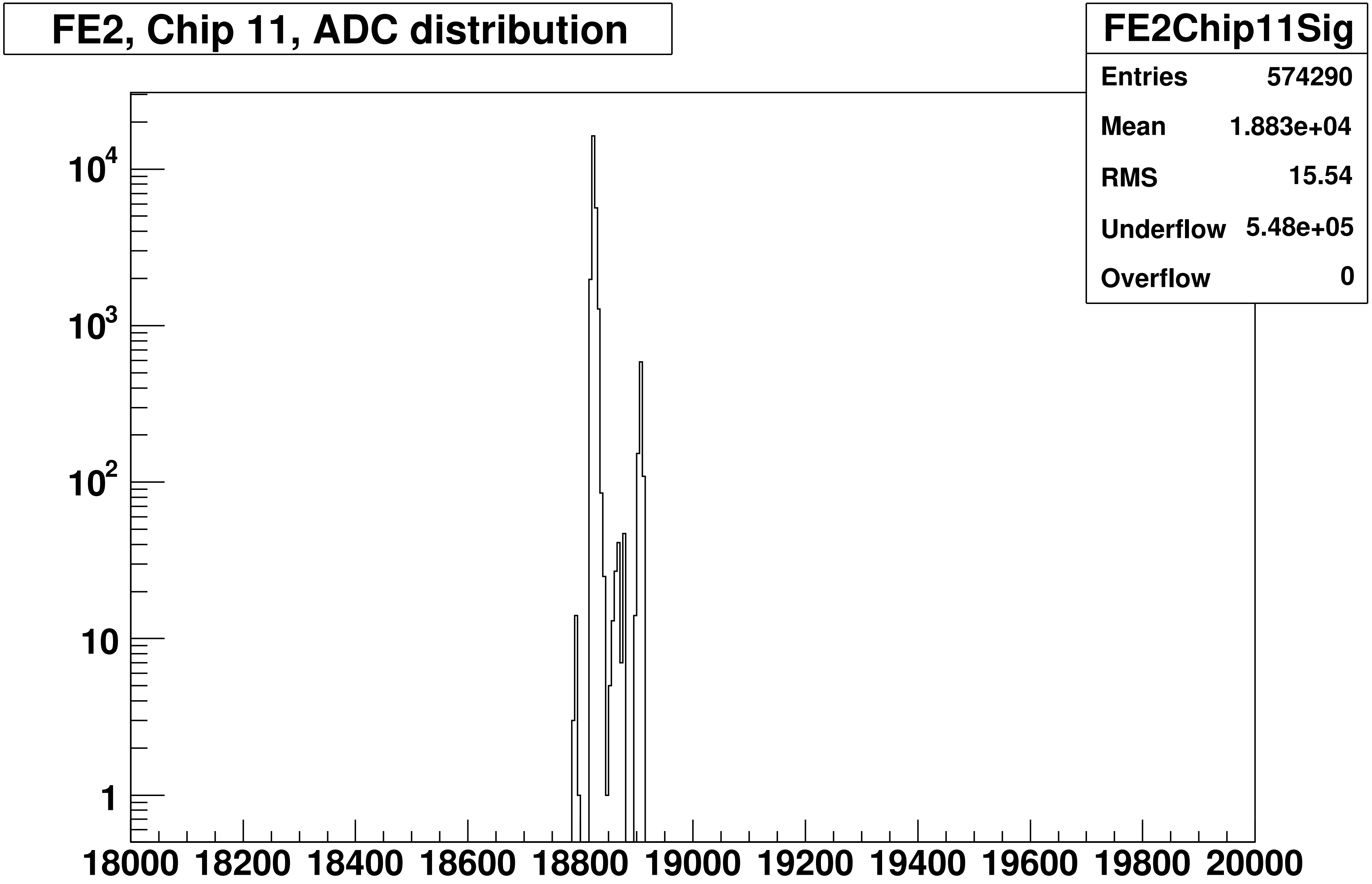
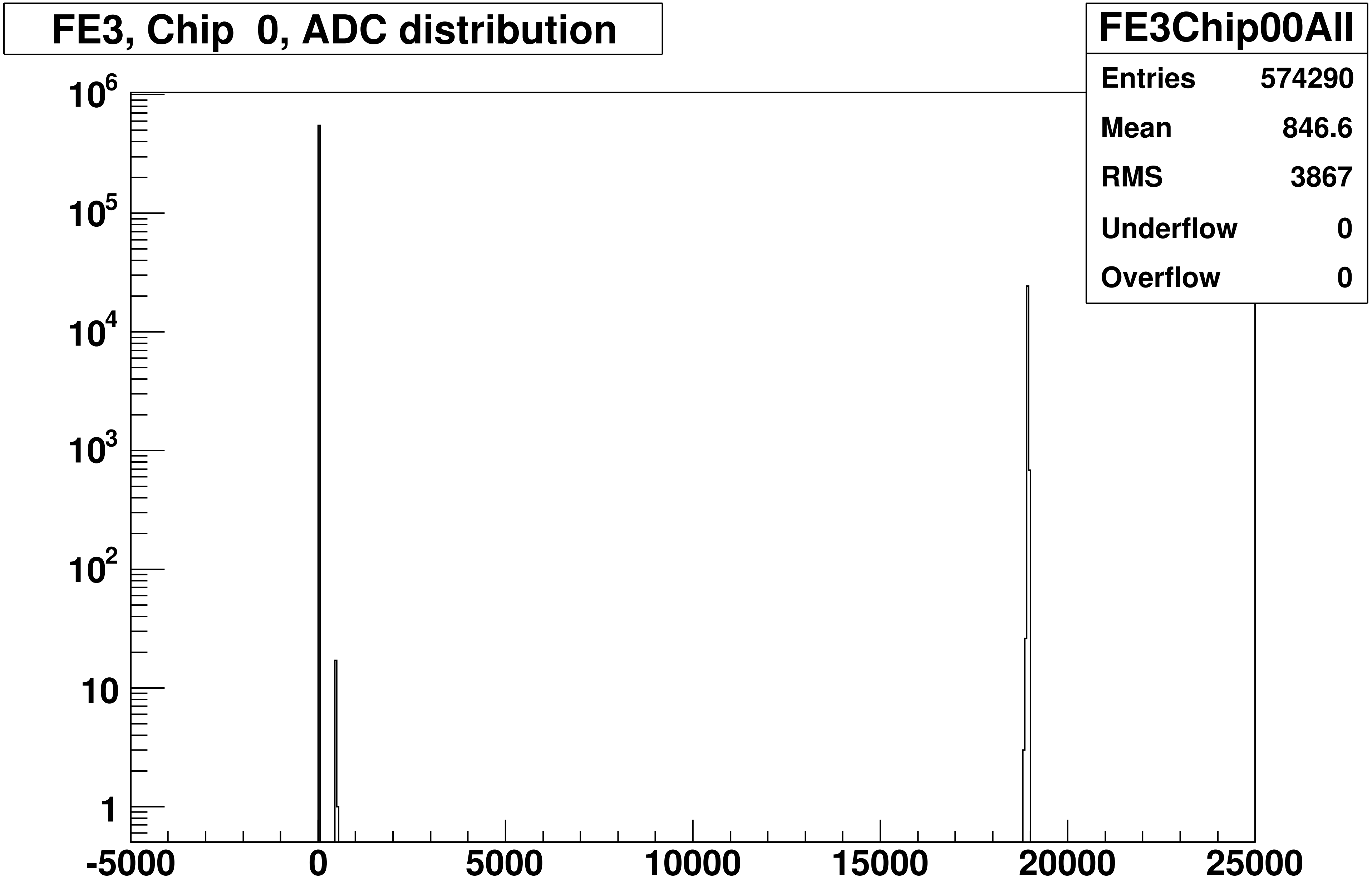
150
200



FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 574290
Mean 1.883e+04
RMS 15.54
Underflow 5.48e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 574290
Mean 846.6
RMS 3867
Underflow 0
Overflow 0
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10^3
10^2
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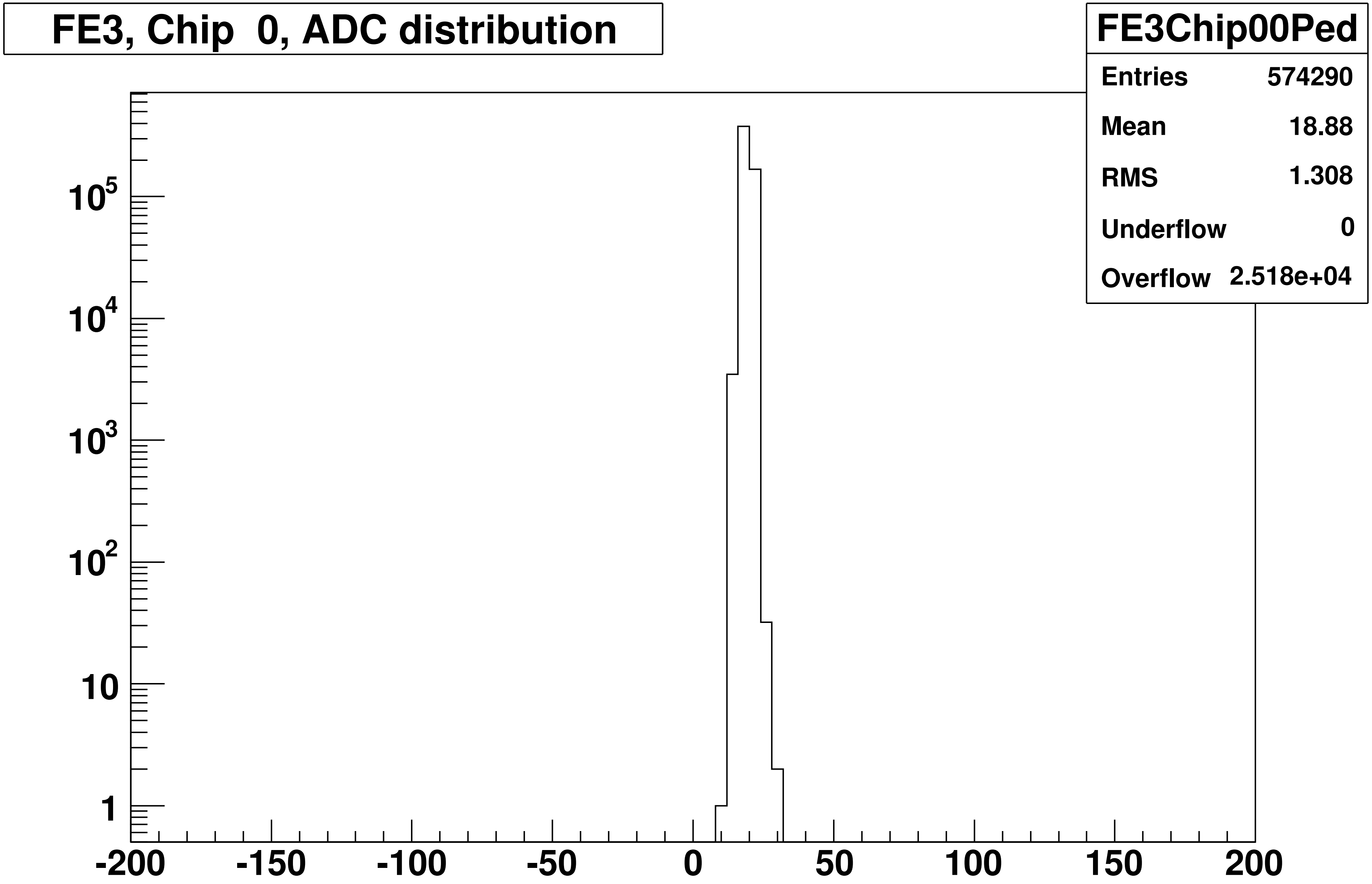
25000



FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 574290
Mean 18.88
RMS 1.308
Underflow 0
Overflow 2.518e+04
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10^3
10^2
10
1
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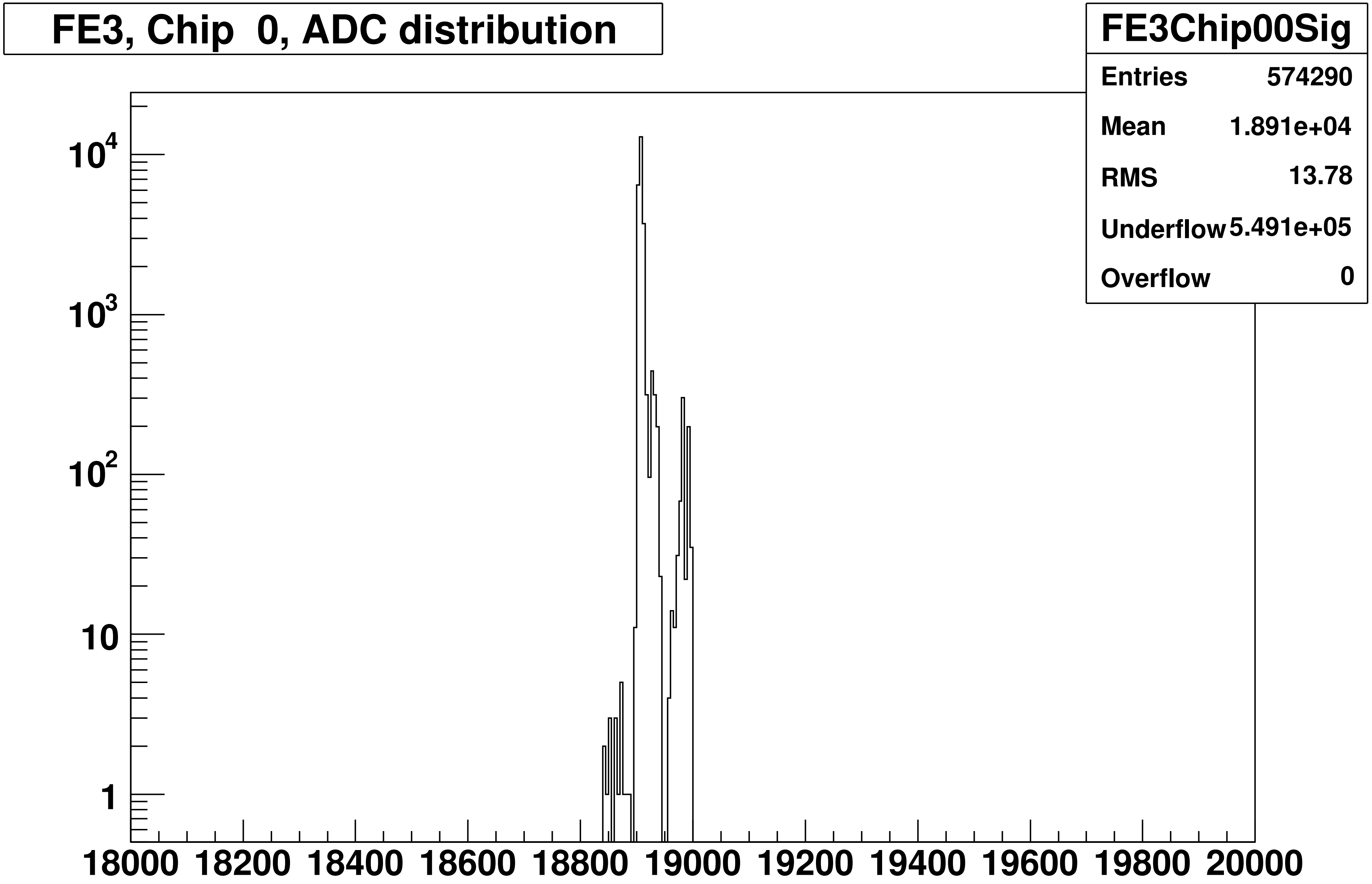
150
200


FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 574290
Mean 1.891e+04
RMS 13.78
Underflow 5.491e+05
Overflow 0
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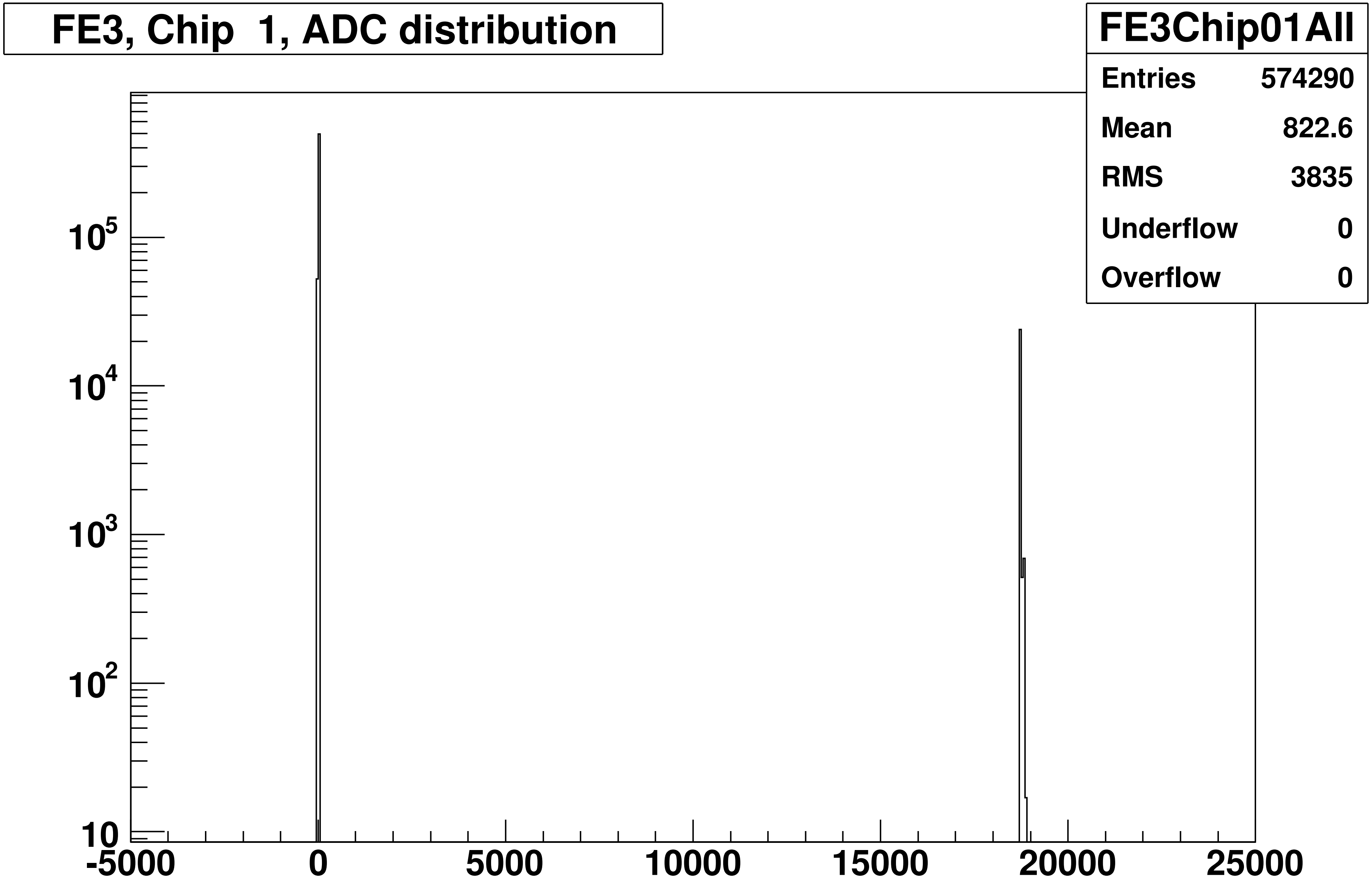
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 574290
Mean 822.6
RMS 3835
Underflow 0
Overflow 0
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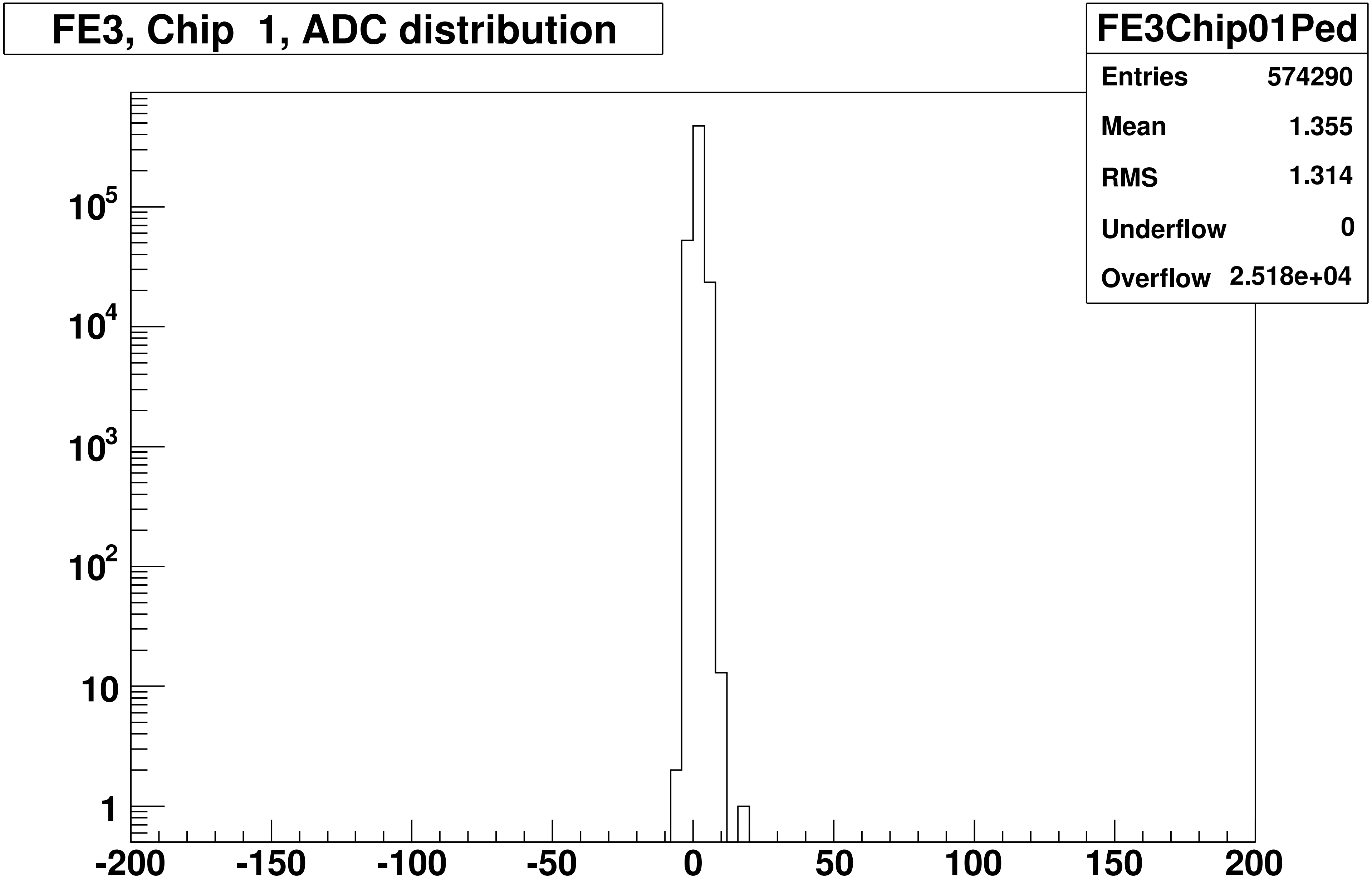
FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 574290
Mean 1.355
RMS 1.314
Underflow 0
Overflow 2.518e+04
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10^3
10^2
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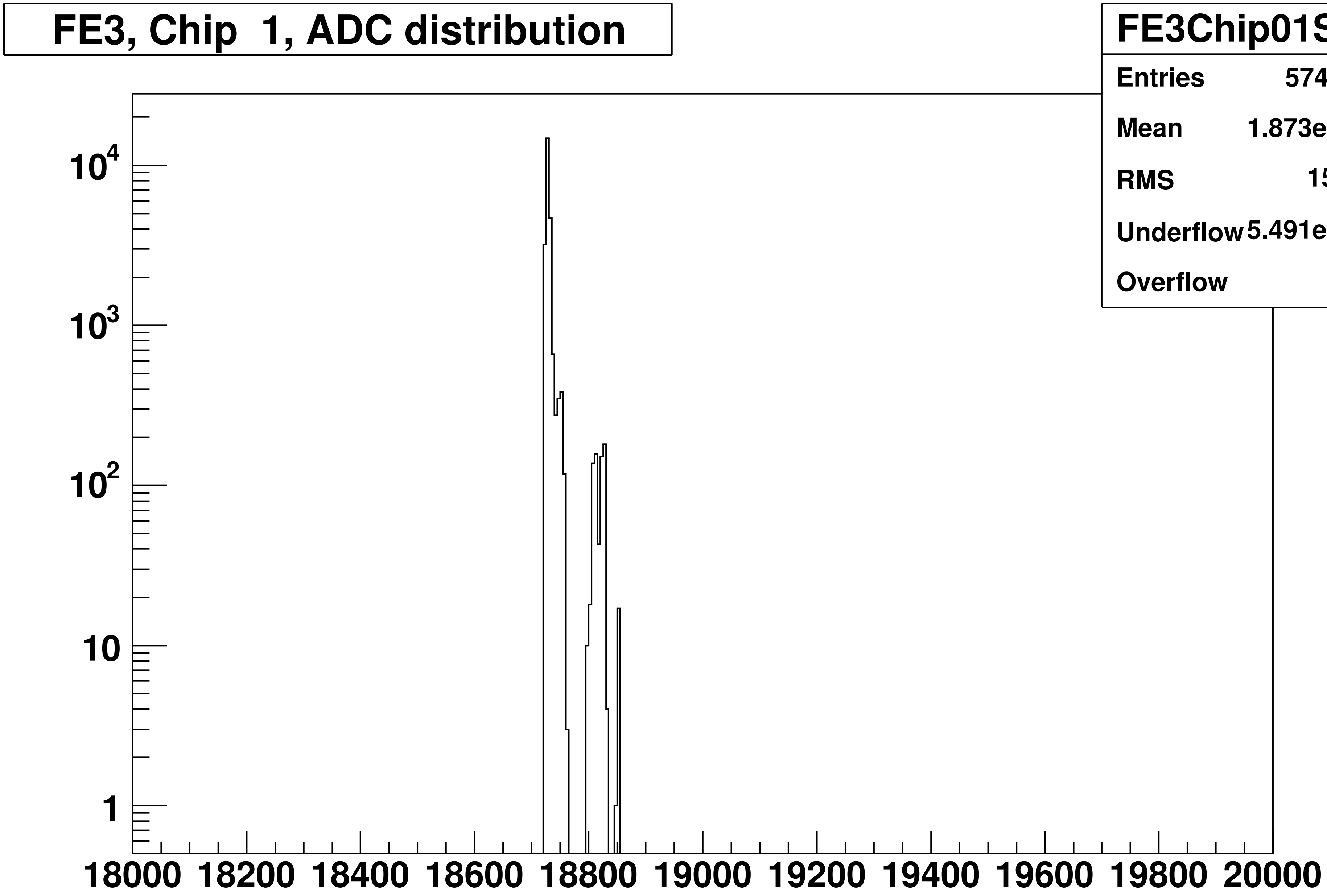

FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 574290
Mean 1.873e+04
RMS 15.87
Underflow 5.491e+05
Overflow 0
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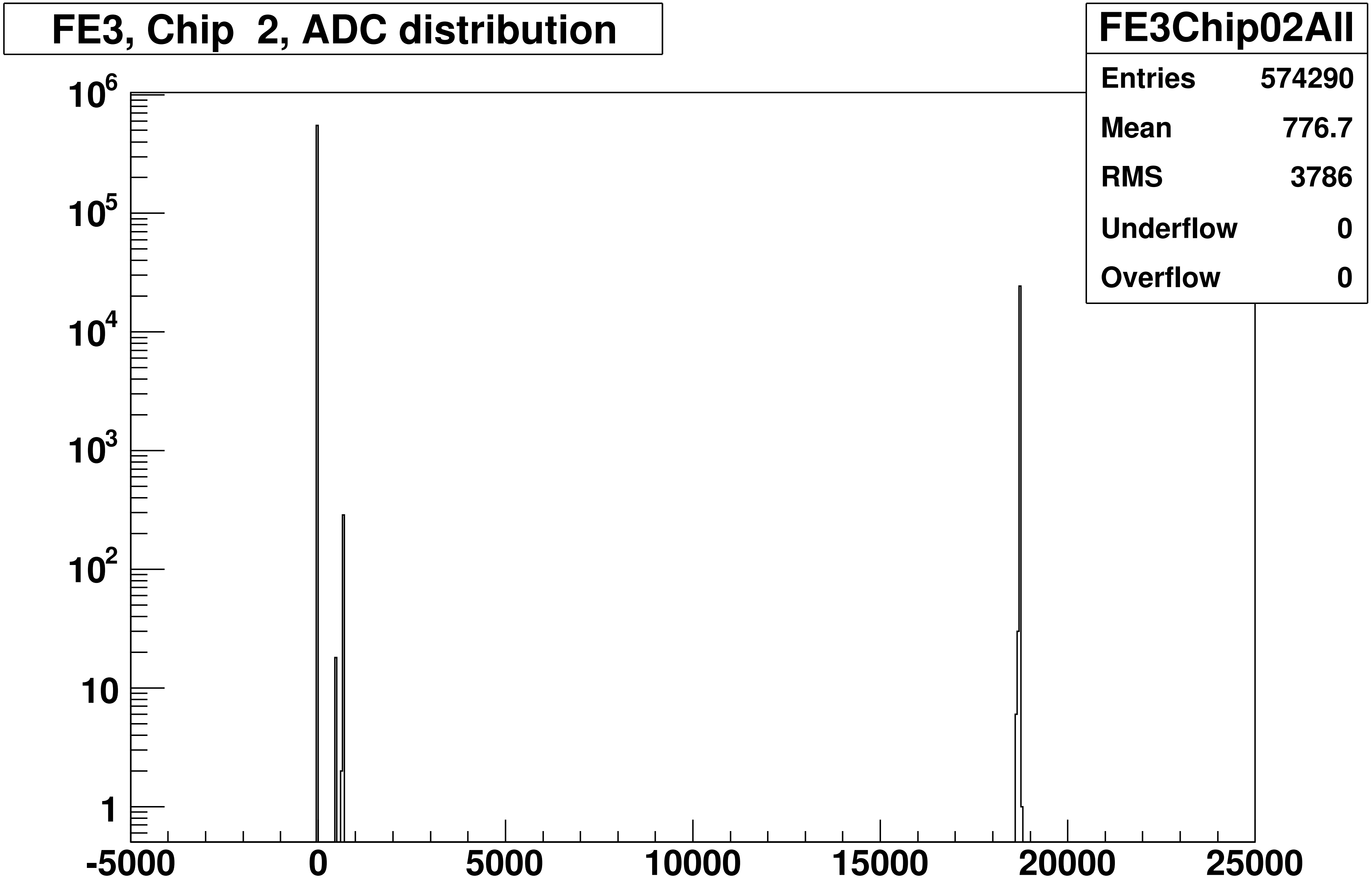
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 574290
Mean 776.7
RMS 3786
Underflow 0
Overflow 0
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10^4
10^3
10^2
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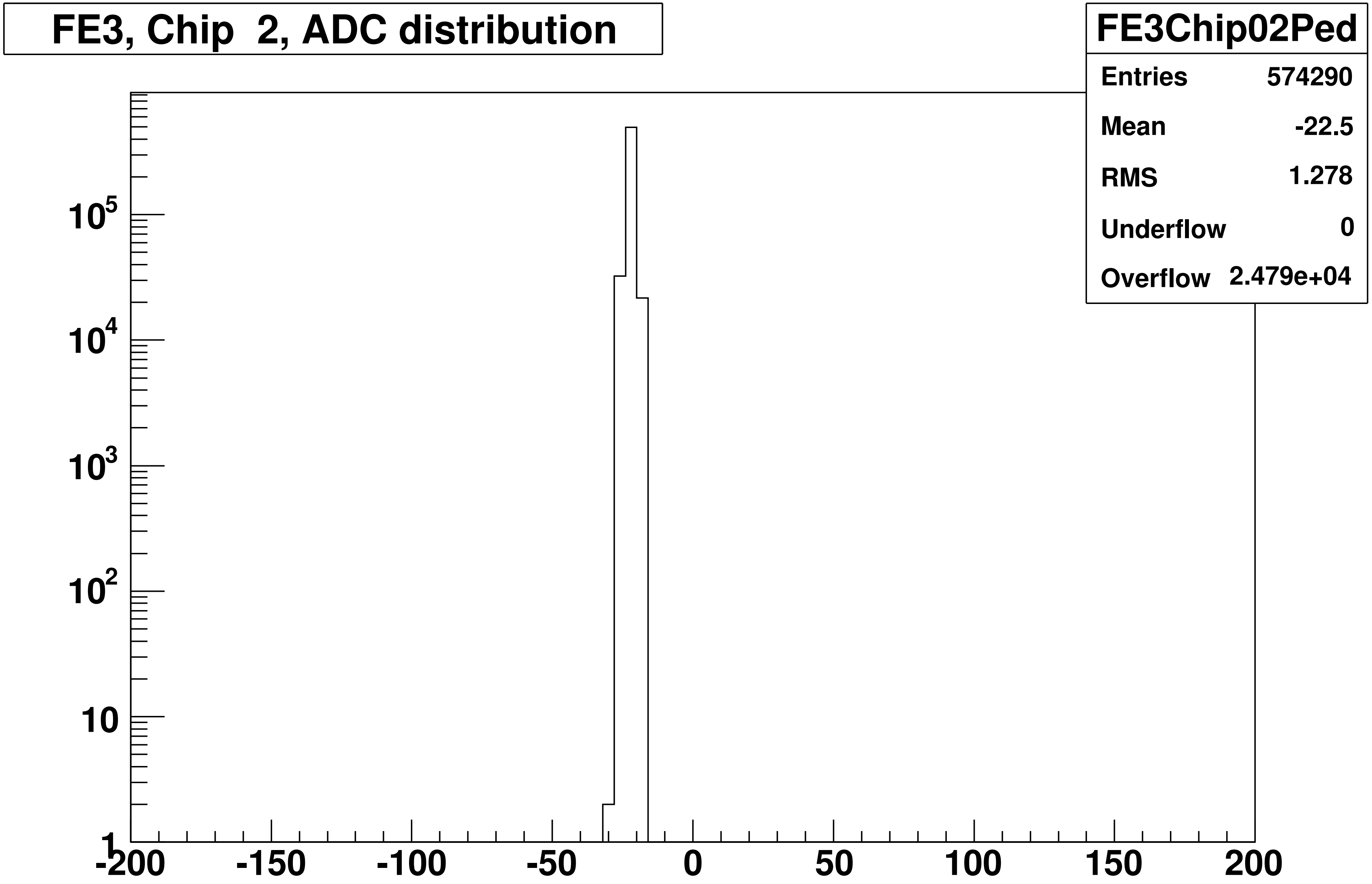
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 574290
Mean -22.5
RMS 1.278
Underflow 0
Overflow 2.479e+04
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10^4
10^3
10^2
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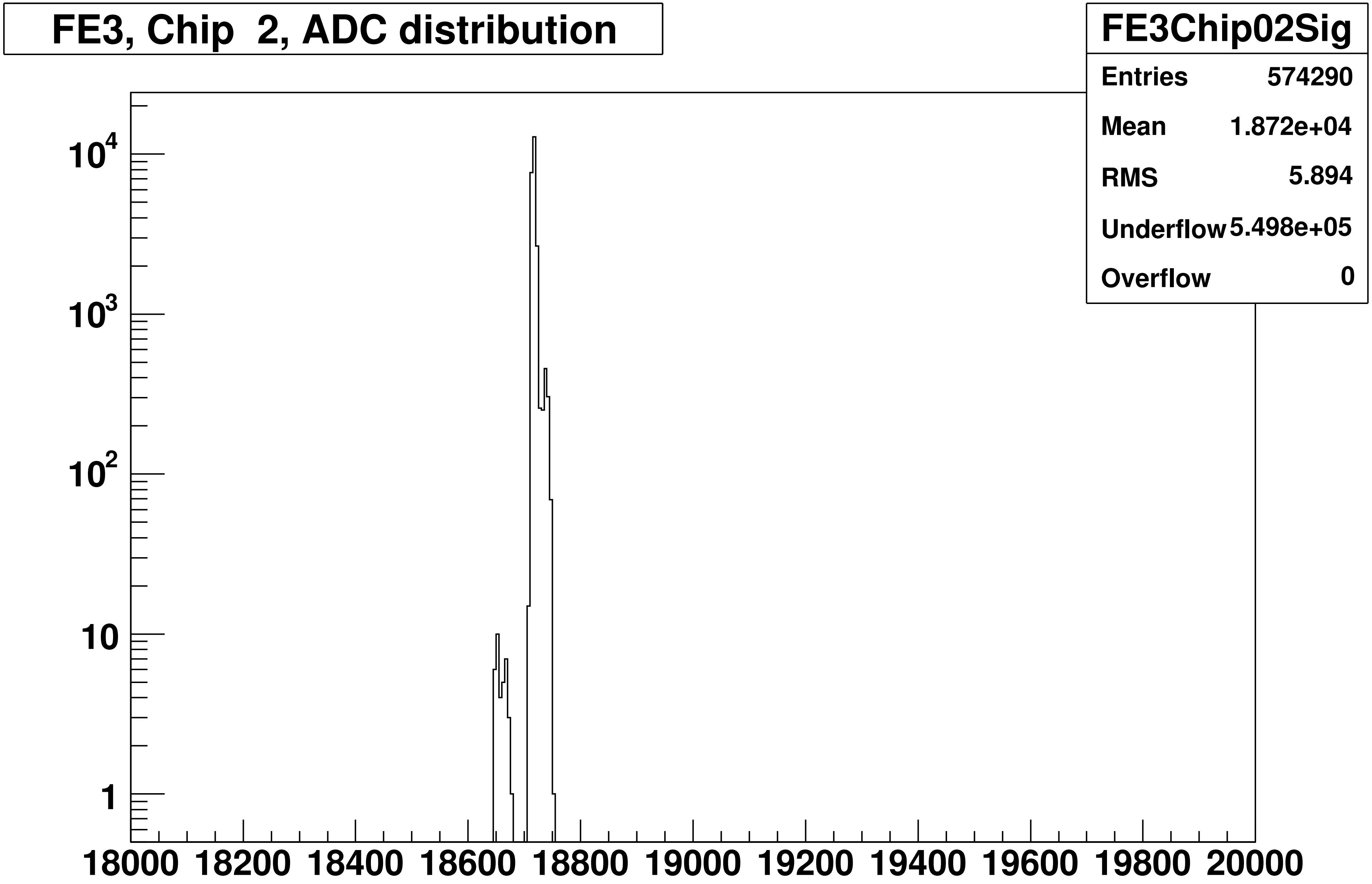
150
200


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 574290
Mean 1.872e+04
RMS 5.894
Underflow 5.498e+05
Overflow 0
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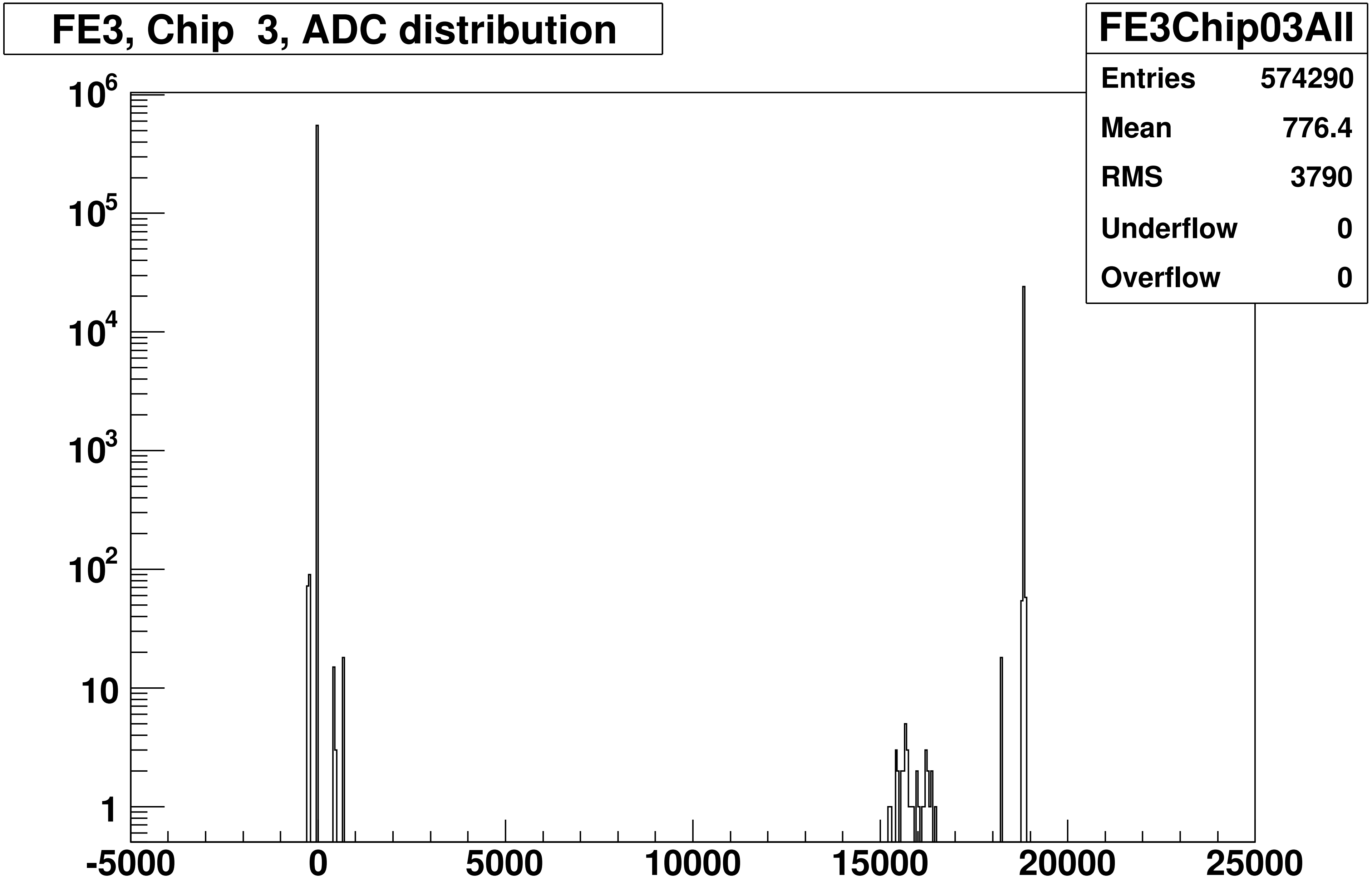
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 574290
Mean 776.4
RMS 3790
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
5000
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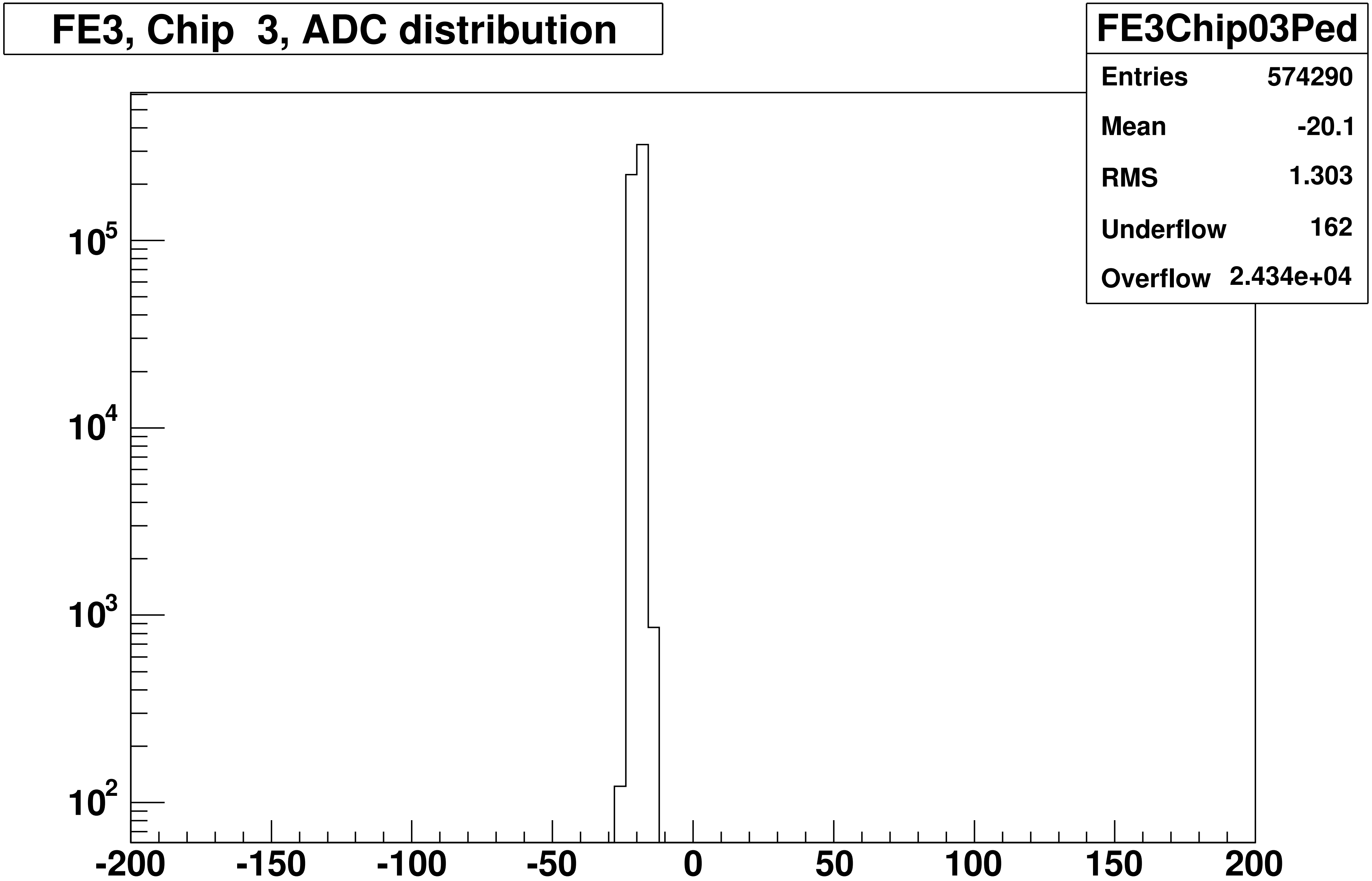
15000
20000
25000



FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 574290
Mean -20.1
RMS 1.303
Underflow 162
Overflow 2.434e+04
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10^4
10^3
10^2
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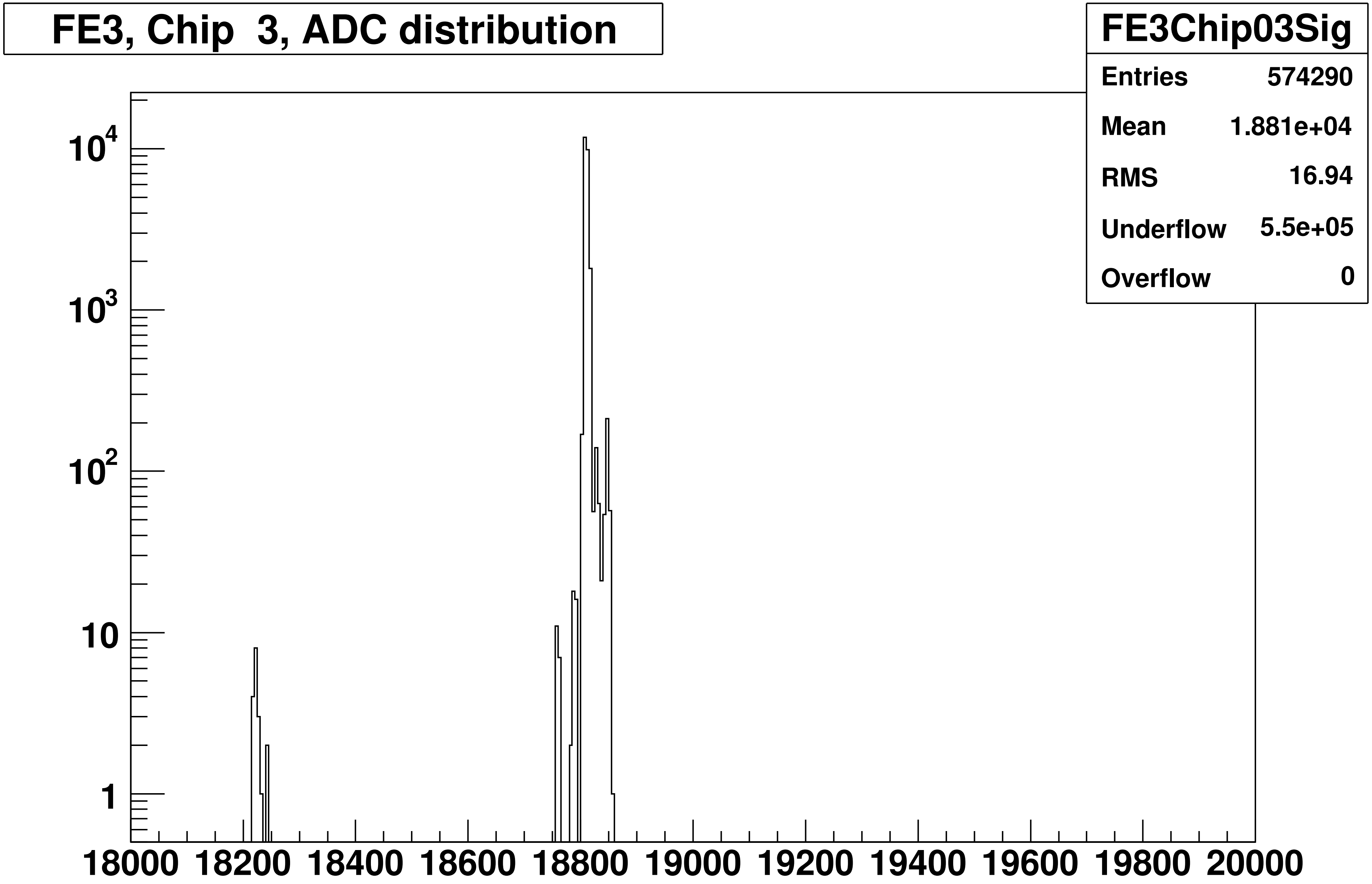
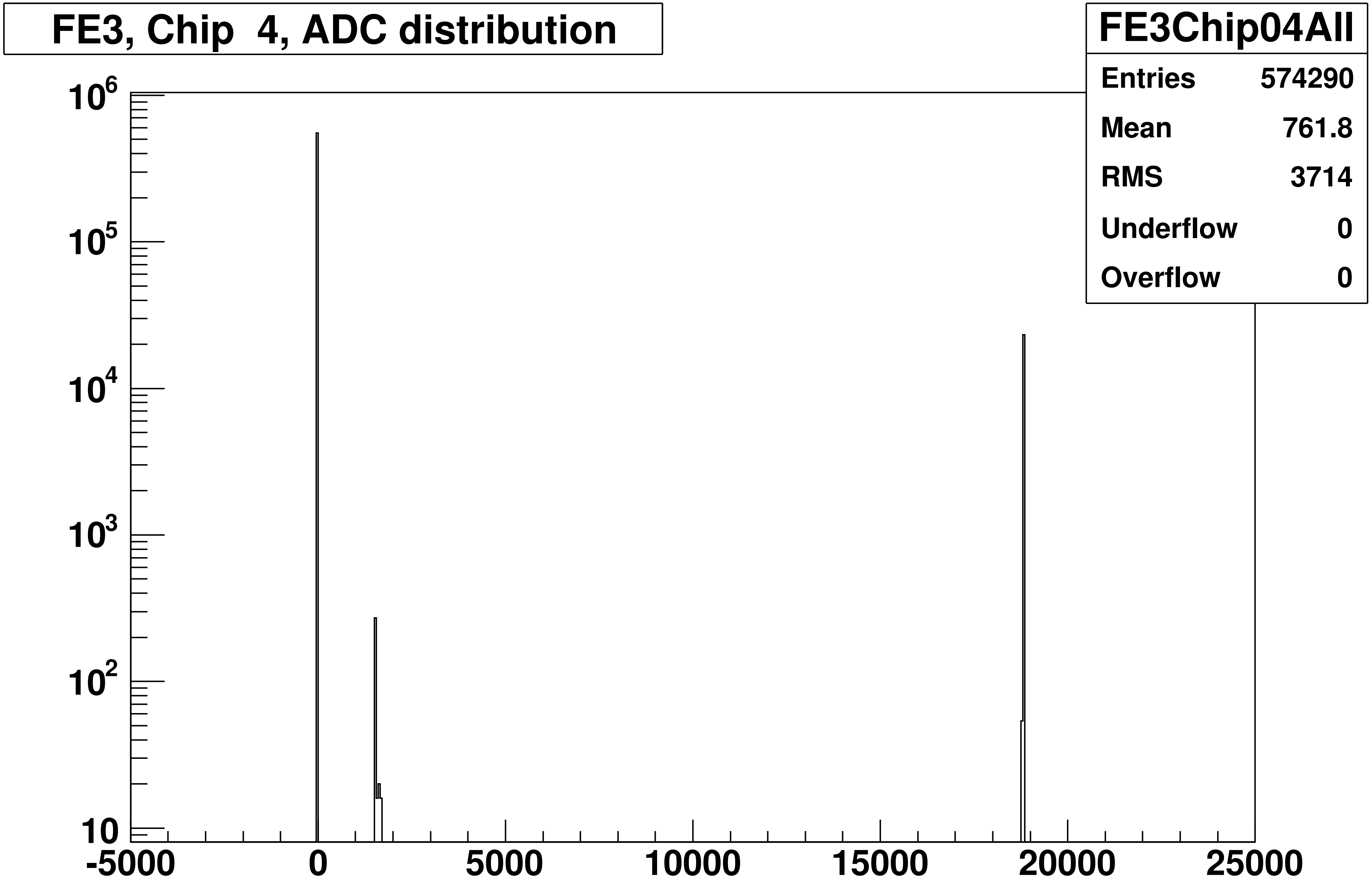
150
200


FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 574290
Mean 1.881e+04
RMS 16.94
Underflow 5.5e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 574290
Mean 761.8
RMS 3714
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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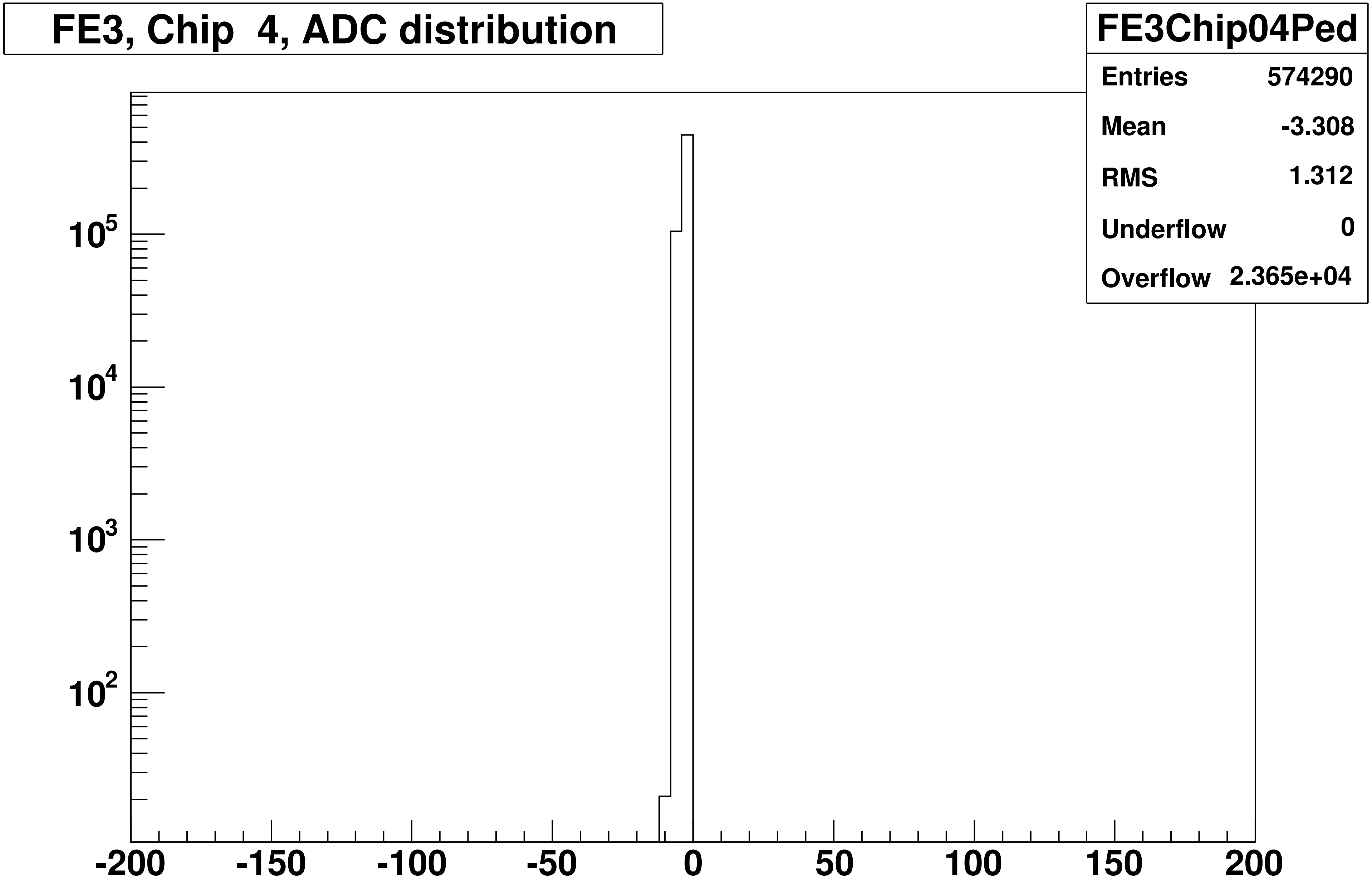
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 574290
Mean -3.308
RMS 1.312
Underflow 0
Overflow 2.365e+04
10^5
10^4
10^3
10^2
-200
-150
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100
150
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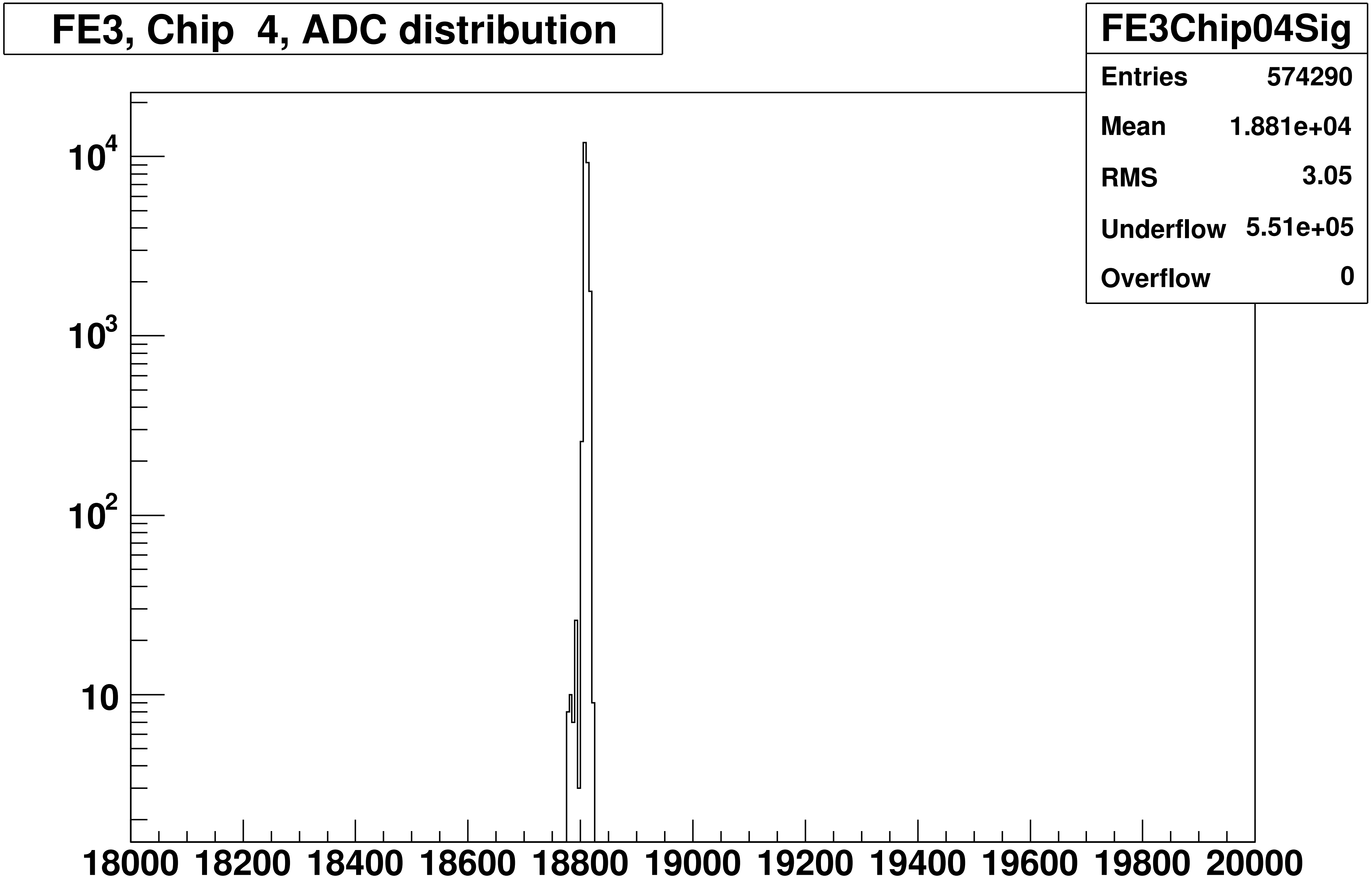

FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 574290
Mean 1.881e+04
RMS 3.05
Underflow 5.51e+05
Overflow 0
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10^3
10^2
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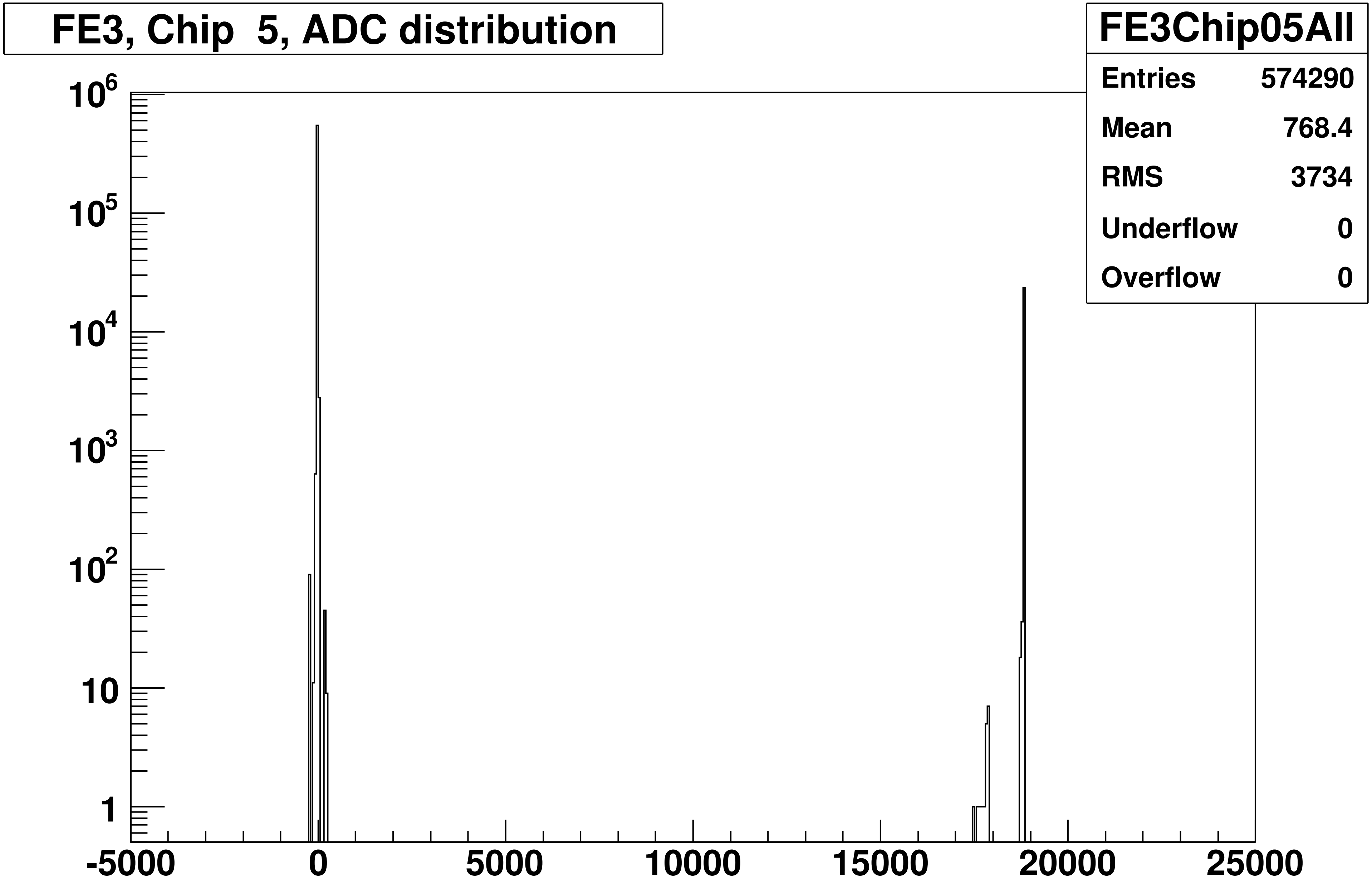
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 574290
Mean 768.4
RMS 3734
Underflow 0
Overflow 0
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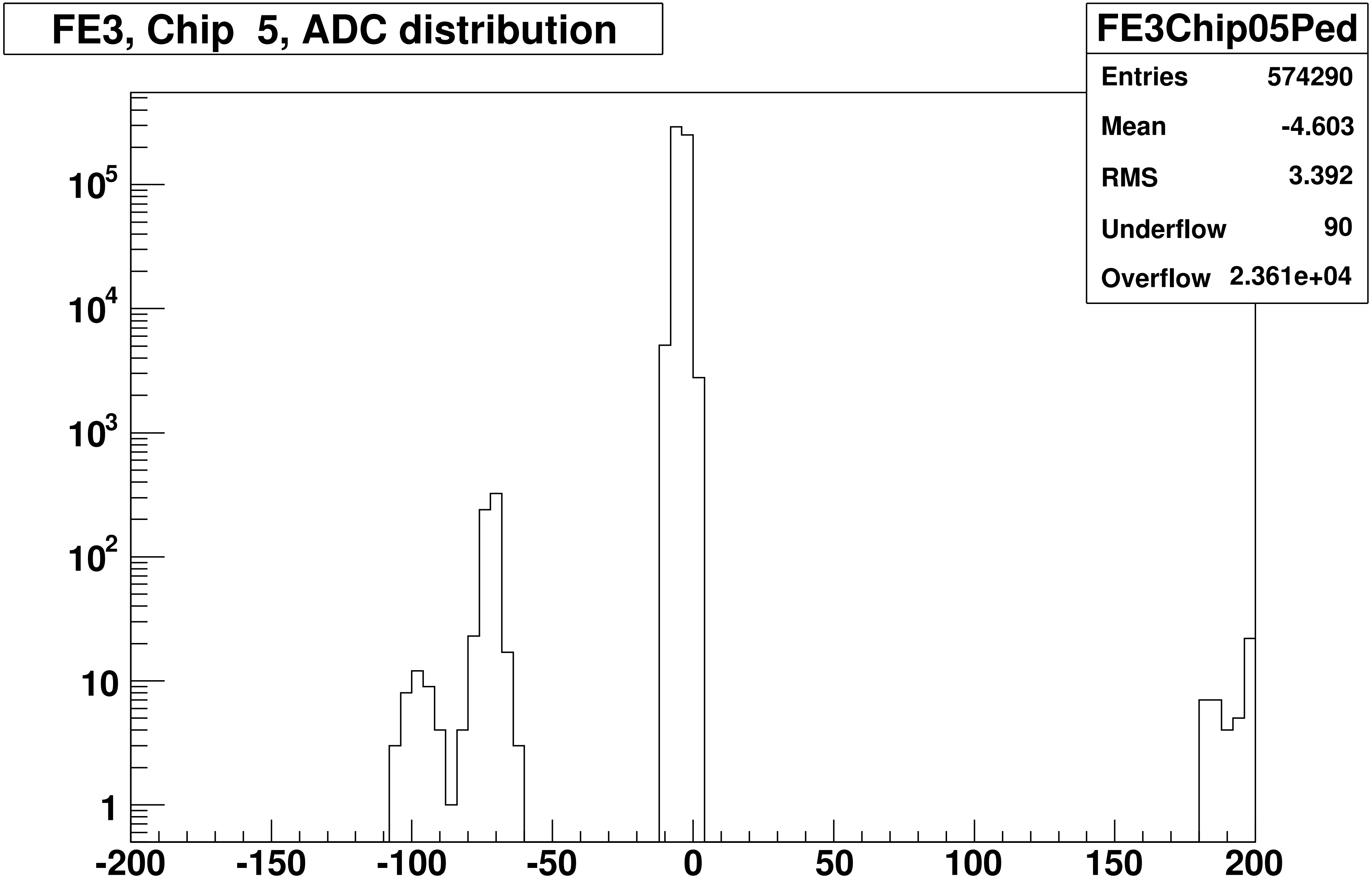
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 574290
Mean -4.603
RMS 3.392
Underflow 90
Overflow 2.361e+04
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10^2
10^3
10^4
10^5
-200
-150
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100
150
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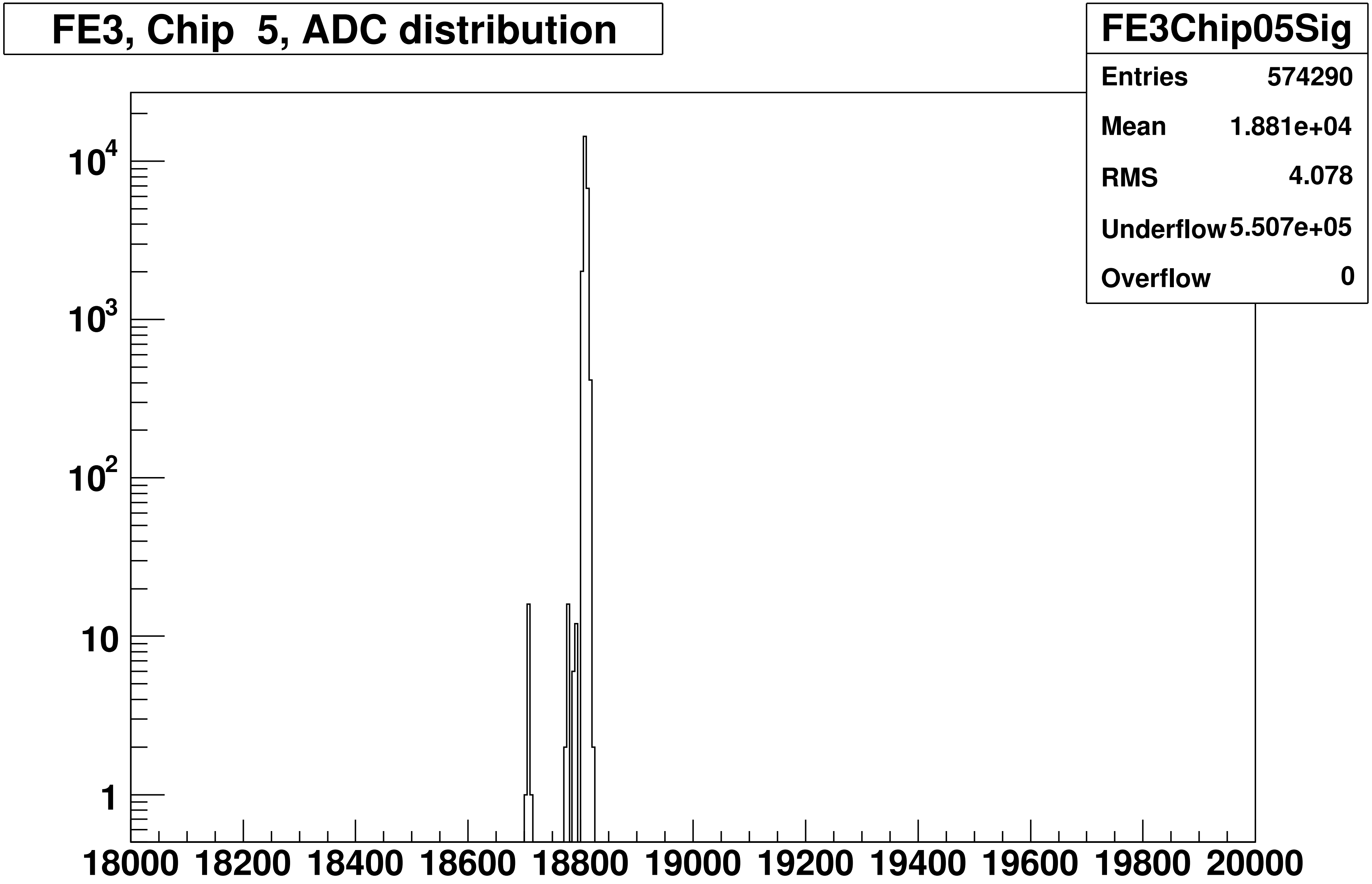
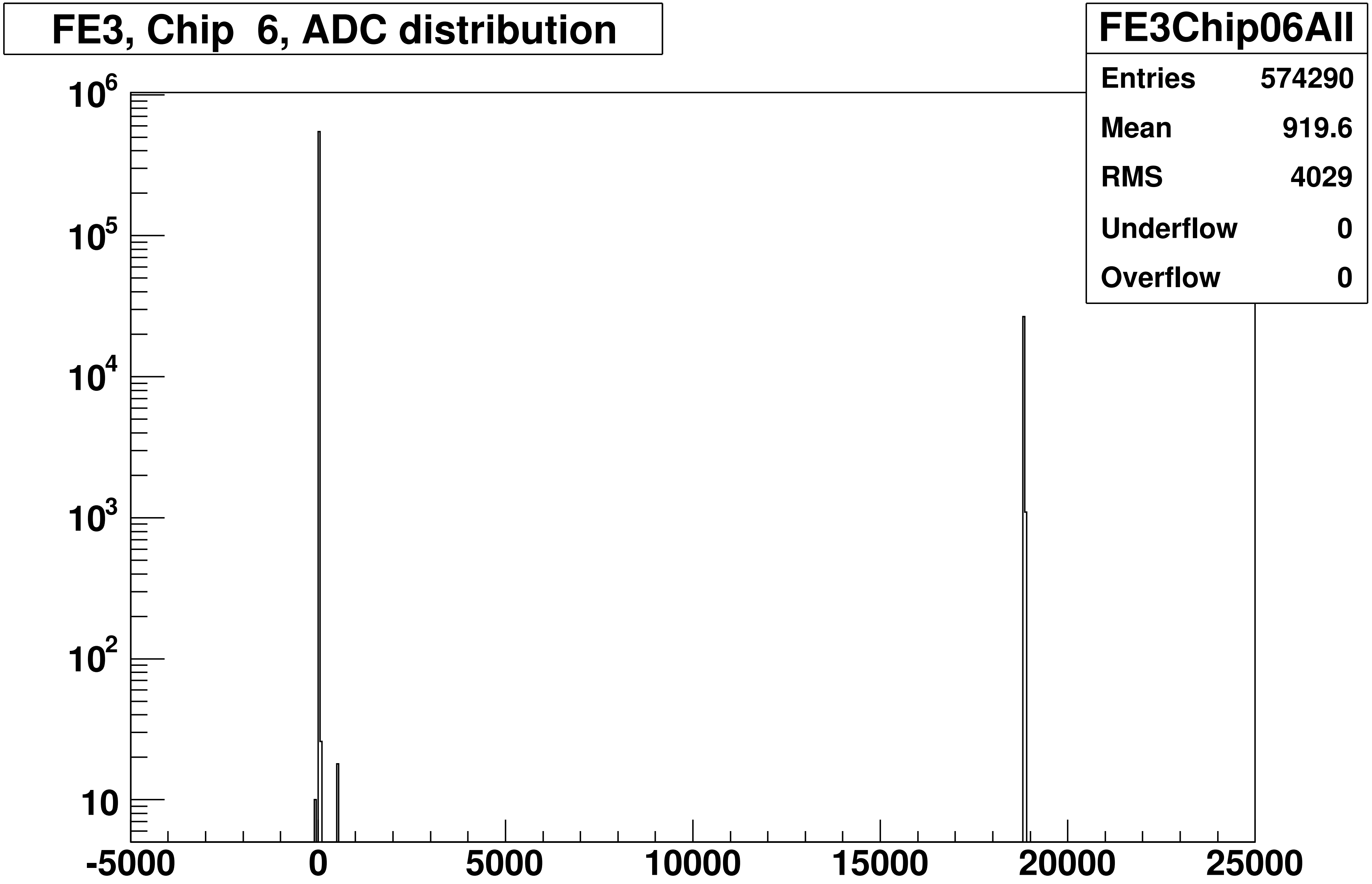


FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 574290
Mean 1.881e+04
RMS 4.078
Underflow 5.507e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 574290
Mean 919.6
RMS 4029
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
0
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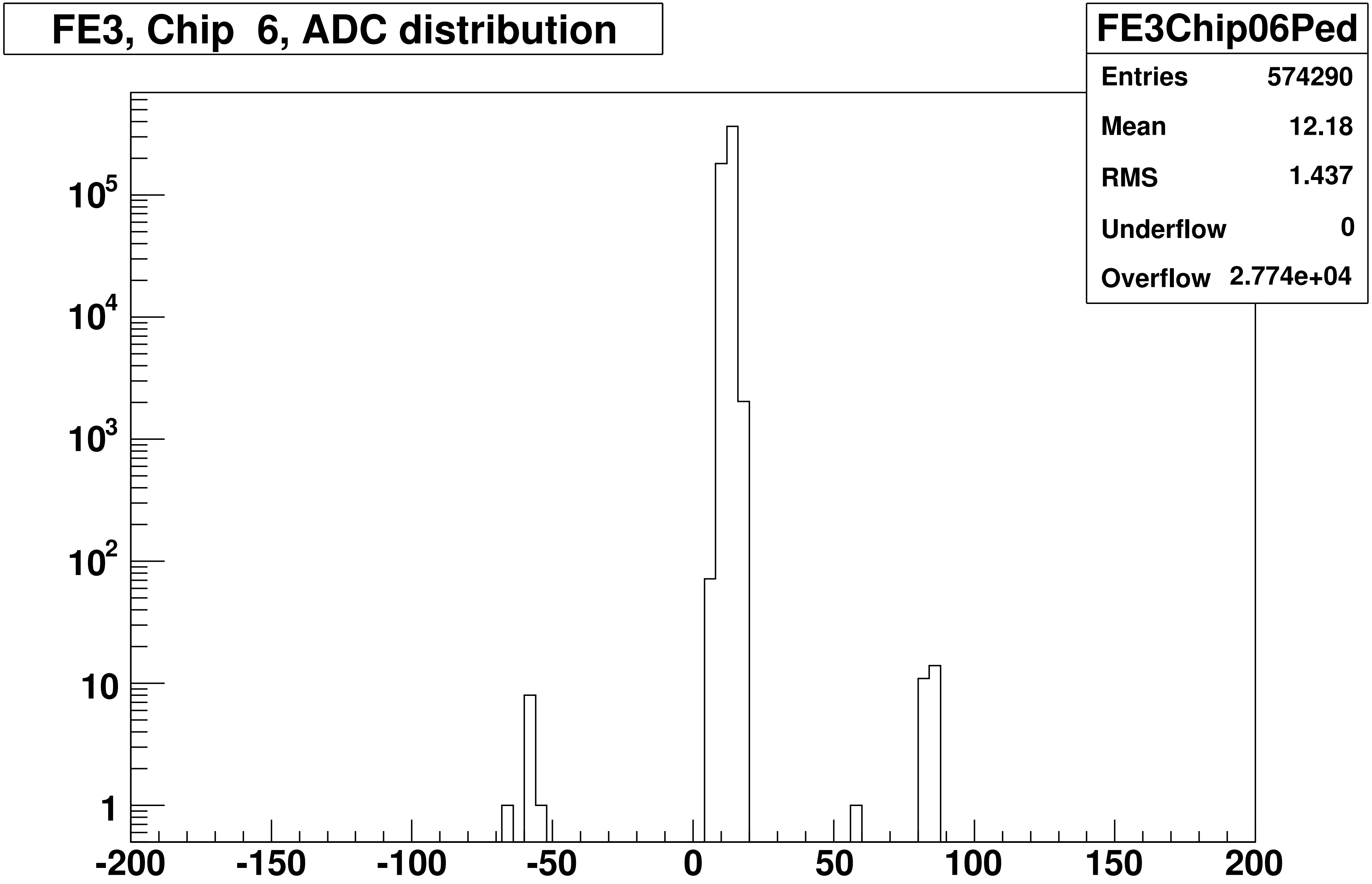
10000
15000
20000
25000


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 574290
Mean 12.18
RMS 1.437
Underflow 0
Overflow 2.774e+04
10^5
10^4
10^3
10^2
10
1
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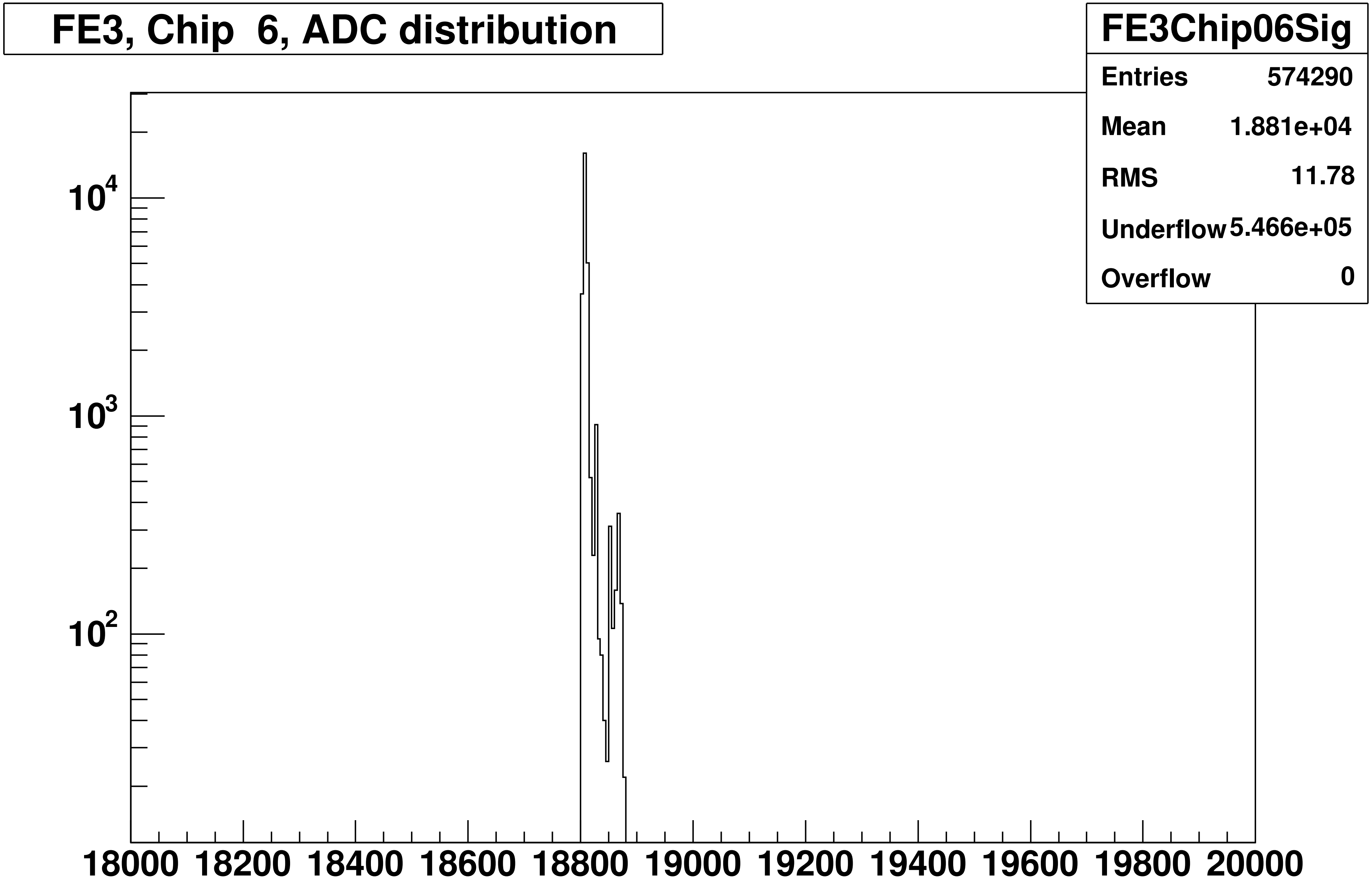
100
150
200


FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 574290
Mean 1.881e+04
RMS 11.78
Underflow 5.466e+05
Overflow 0
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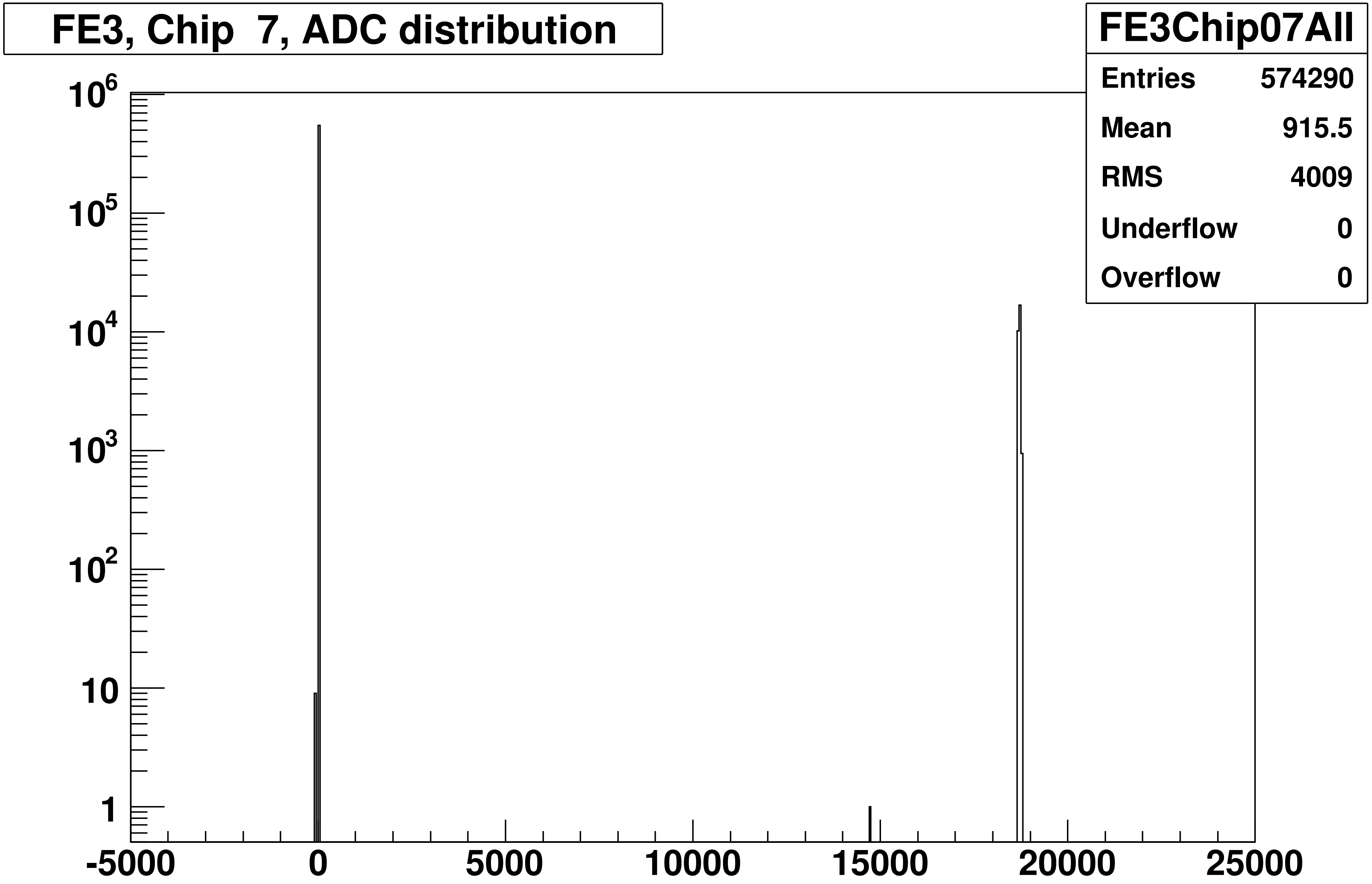
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 574290
Mean 915.5
RMS 4009
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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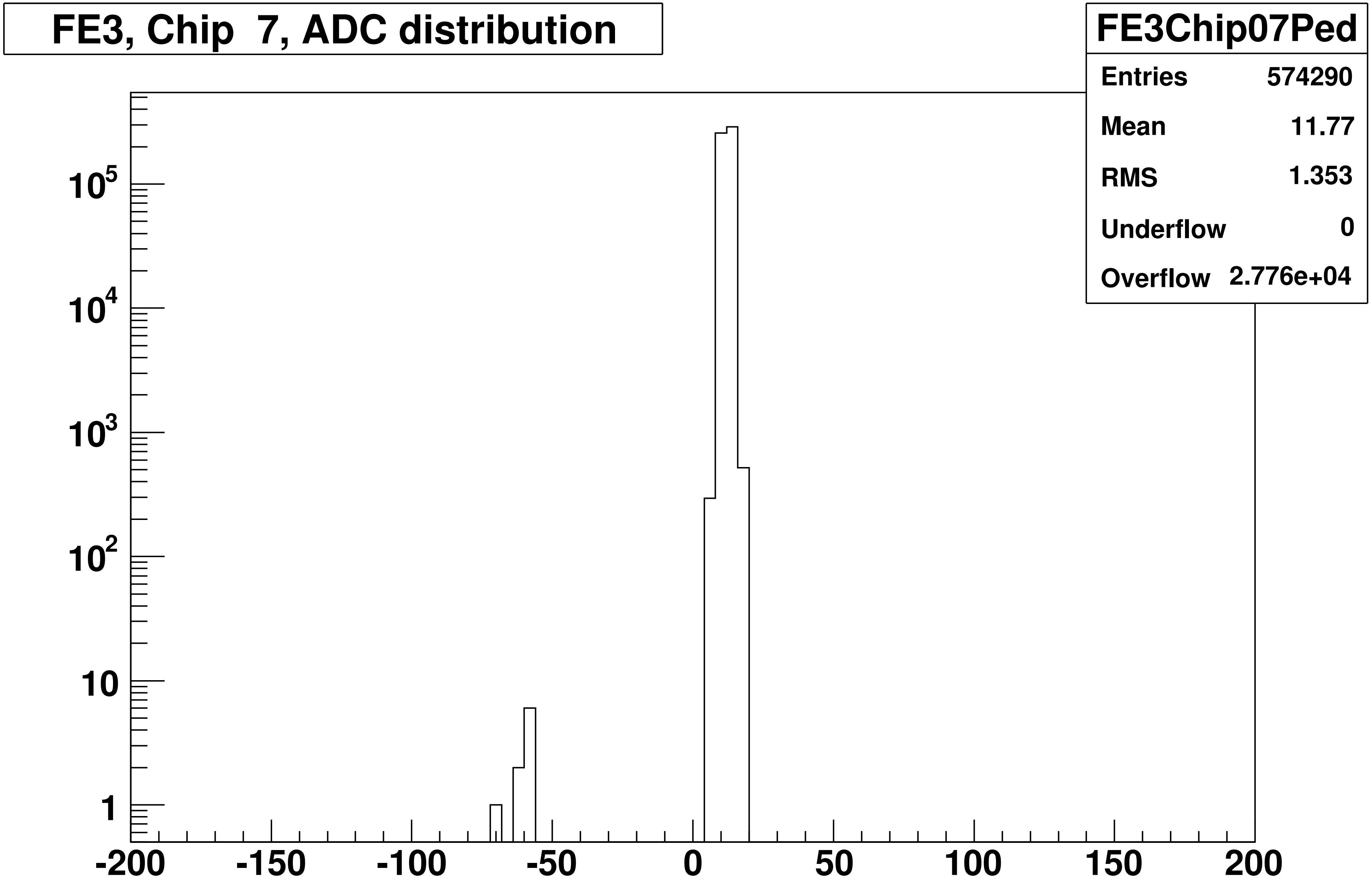
10000
15000
20000
25000


FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 574290
Mean 11.77
RMS 1.353
Underflow 0
Overflow 2.776e+04
10^5
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10^3
10^2
10
1
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-100
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150
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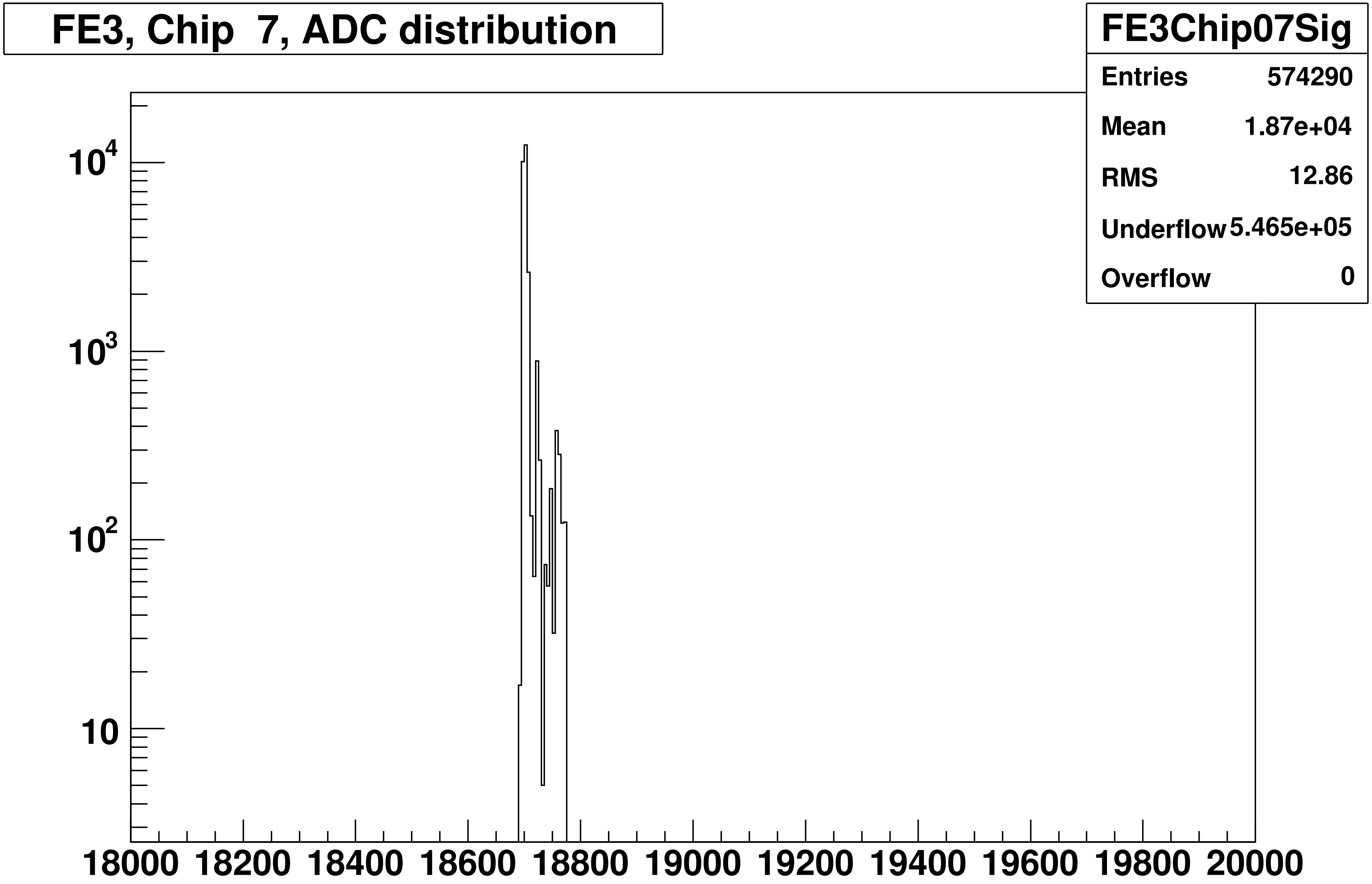
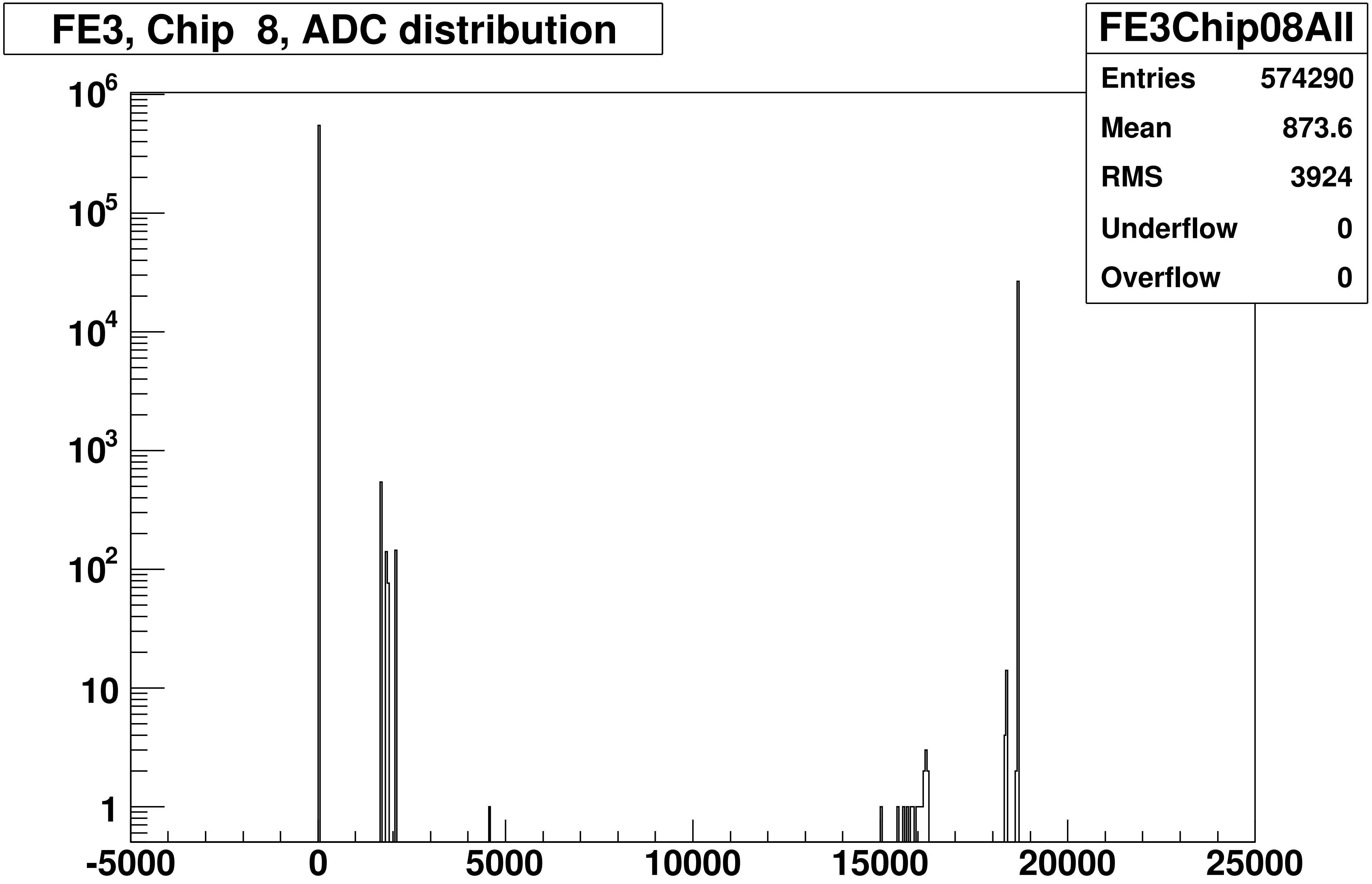

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 574290
Mean 1.87e+04
RMS 12.86
Underflow 5.465e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 574290
Mean 873.6
RMS 3924
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
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15000
20000
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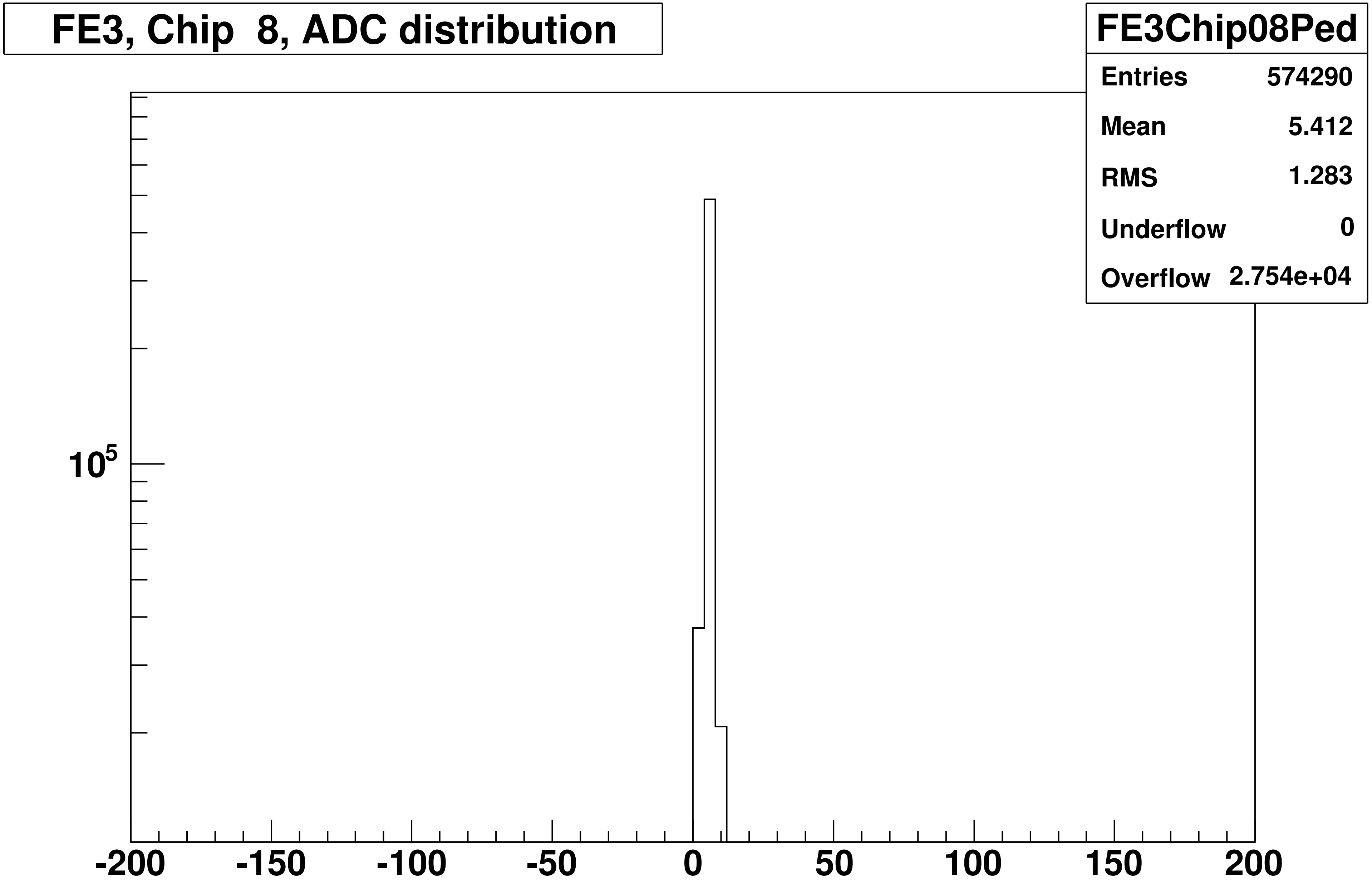
FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 574290
Mean 5.412
RMS 1.283
Underflow 0
Overflow 2.754e+04
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-100
-50
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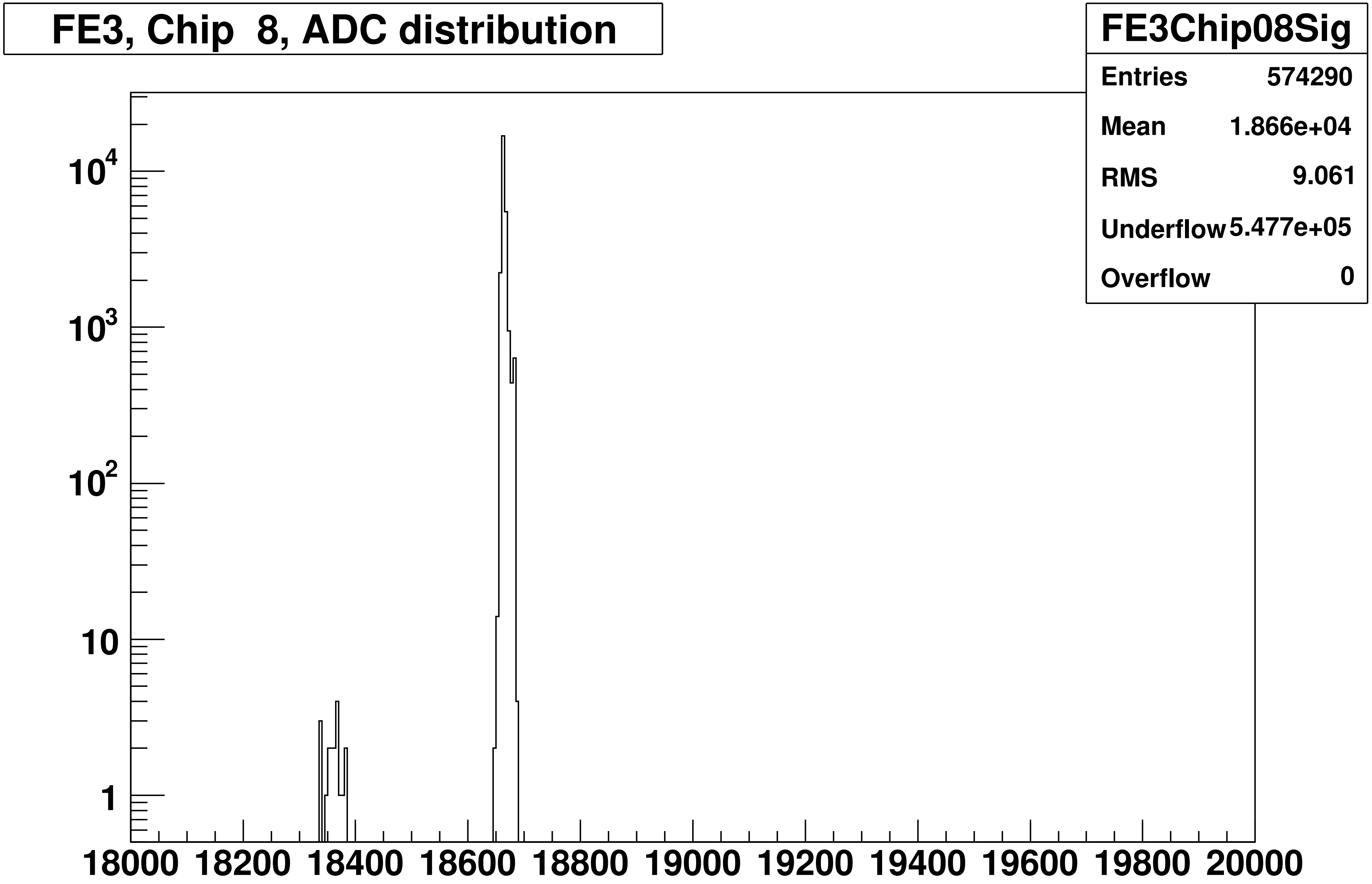
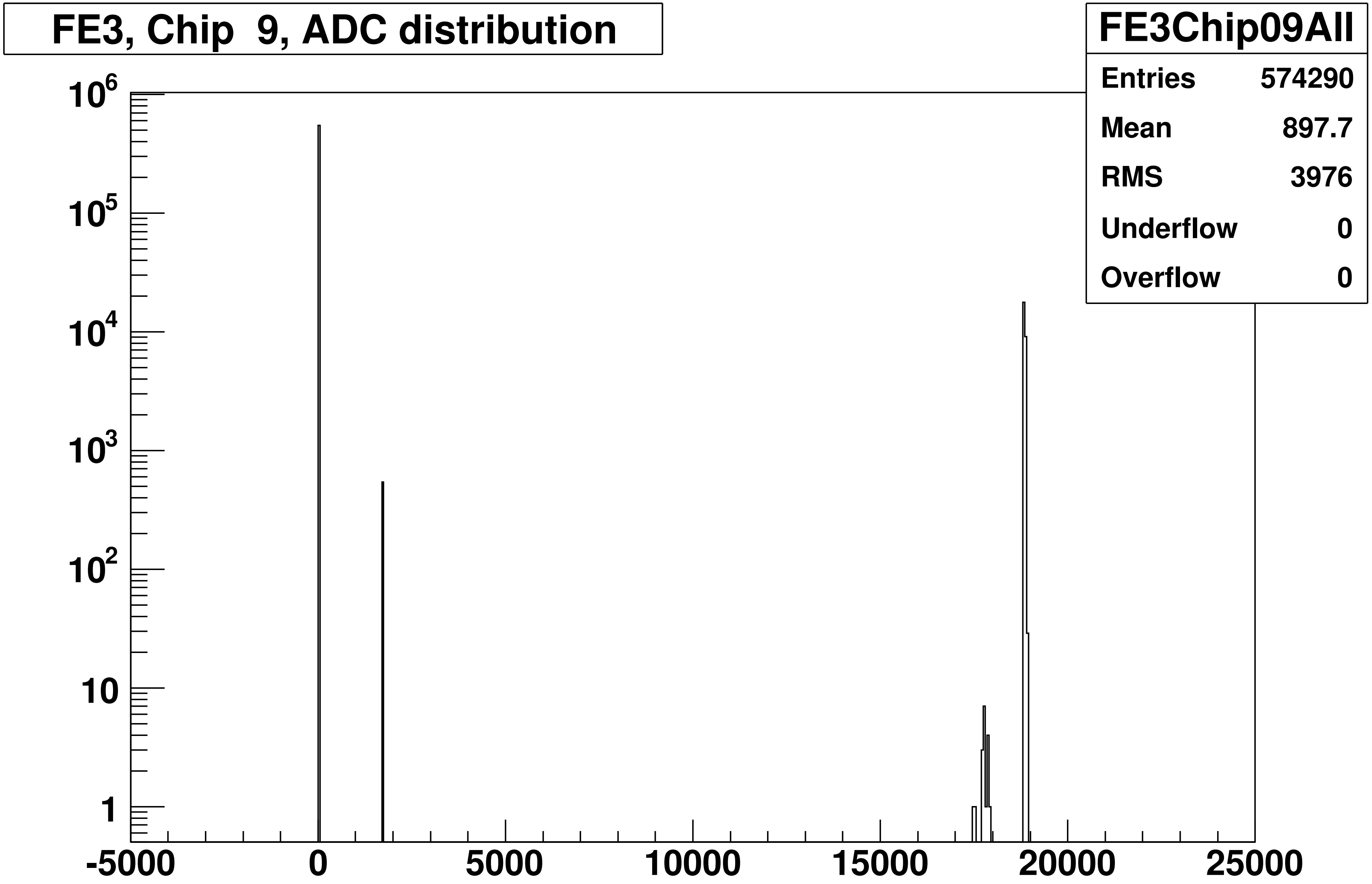
100
150
200


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 574290
Mean 1.866e+04
RMS 9.061
Underflow 5.477e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 574290
Mean 897.7
RMS 3976
Underflow 0
Overflow 0
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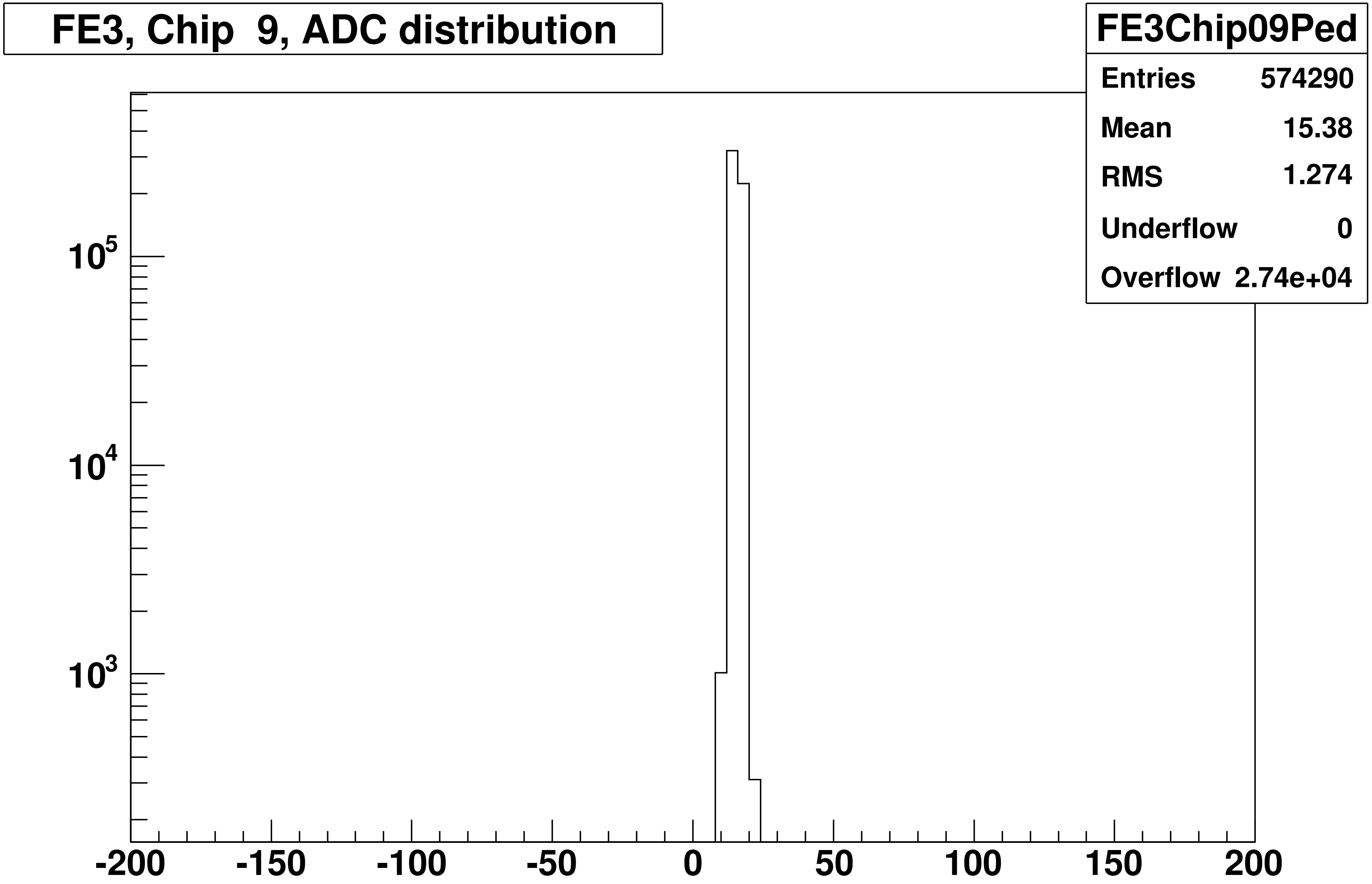
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 574290
Mean 15.38
RMS 1.274
Underflow 0
Overflow 2.74e+04
10^5
10^4
10^3
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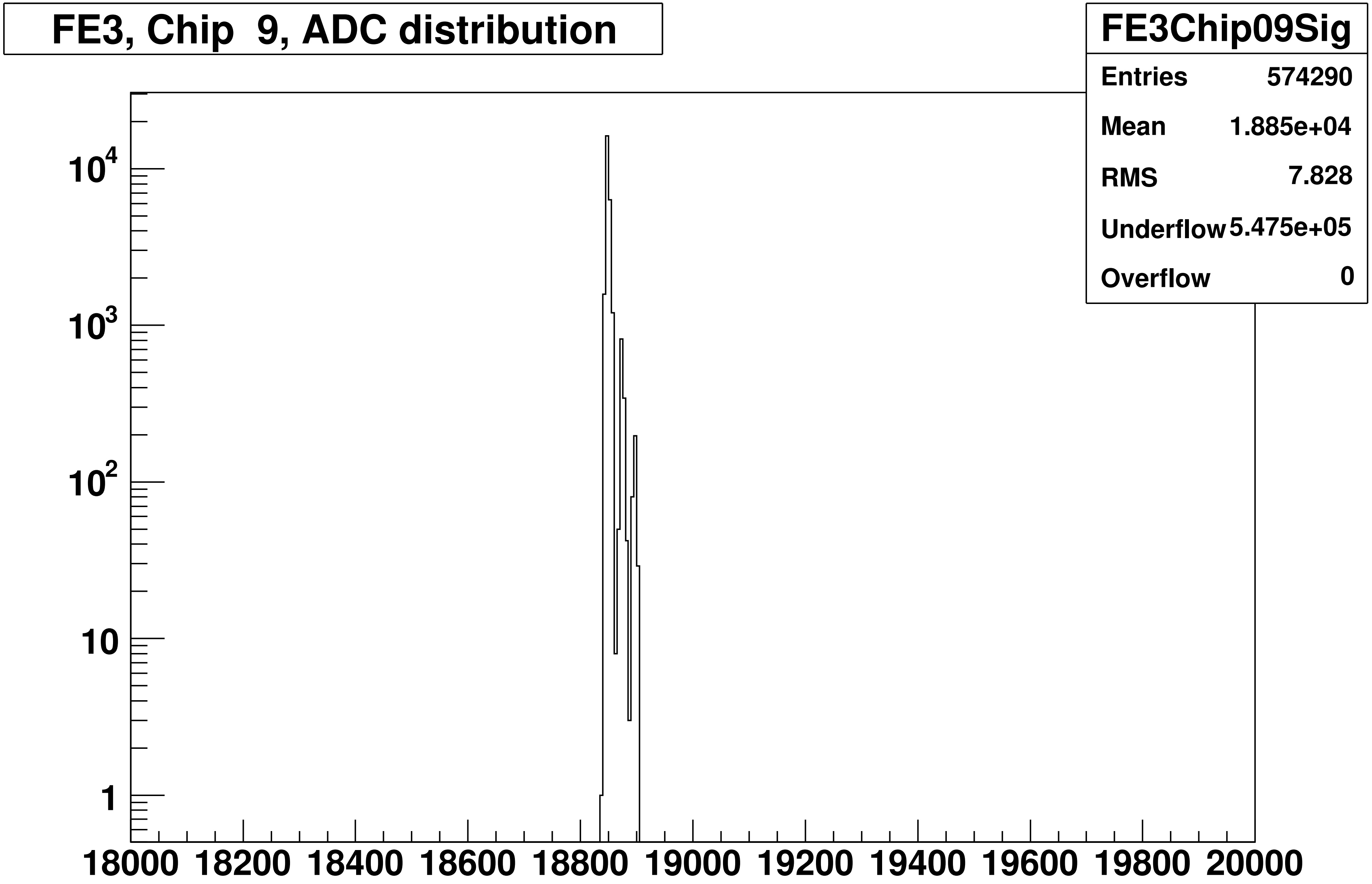
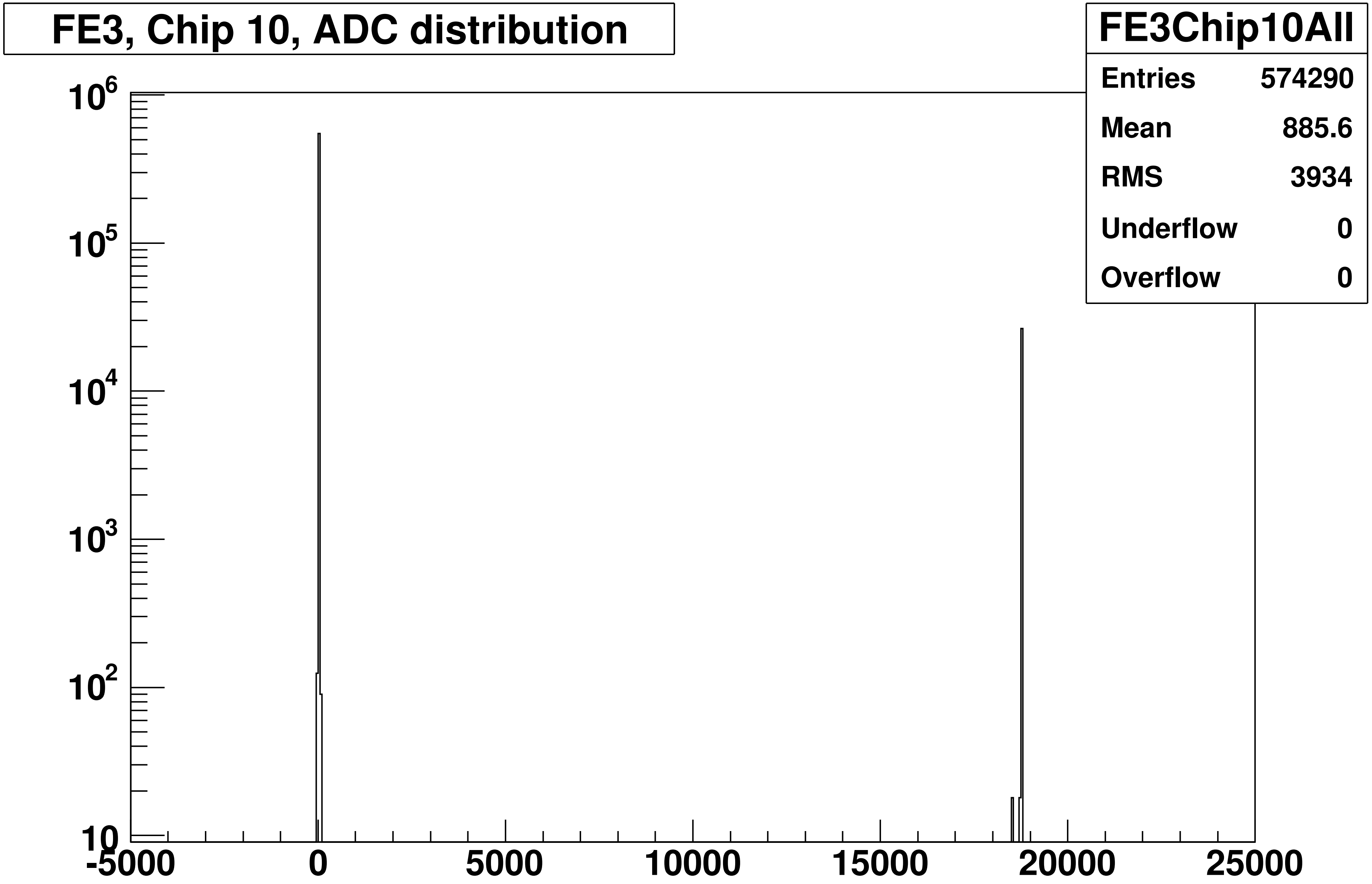
-100
-50
0
50
100
150
200


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 574290
Mean 1.885e+04
RMS 7.828
Underflow 5.475e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 574290
Mean 885.6
RMS 3934
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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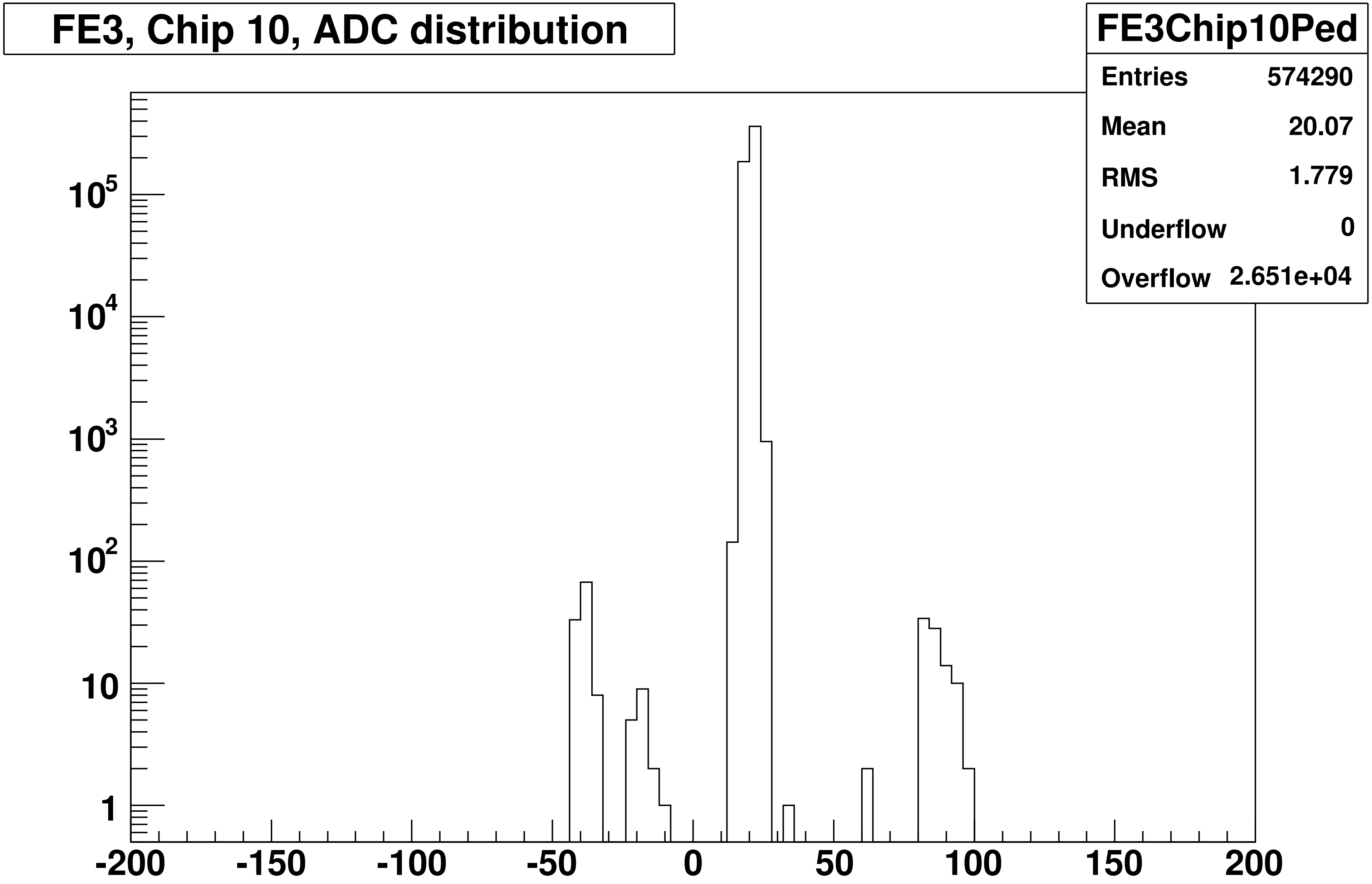
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 574290
Mean 20.07
RMS 1.779
Underflow 0
Overflow 2.651e+04
10^5
10^4
10^3
10^2
10
1
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-100
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0
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100
150
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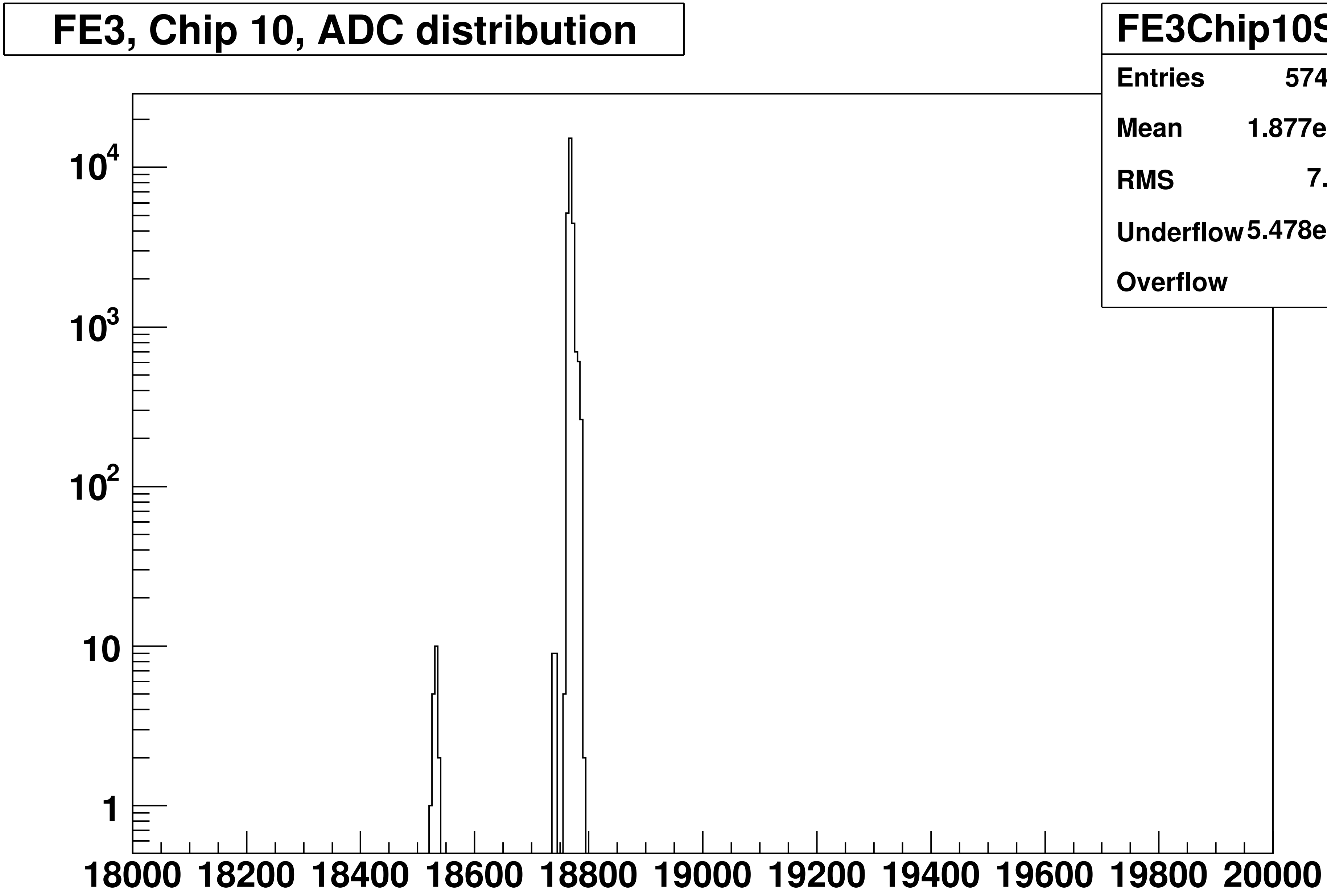
FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 574290
Mean 1.877e+04
RMS 7.538
Underflow 5.478e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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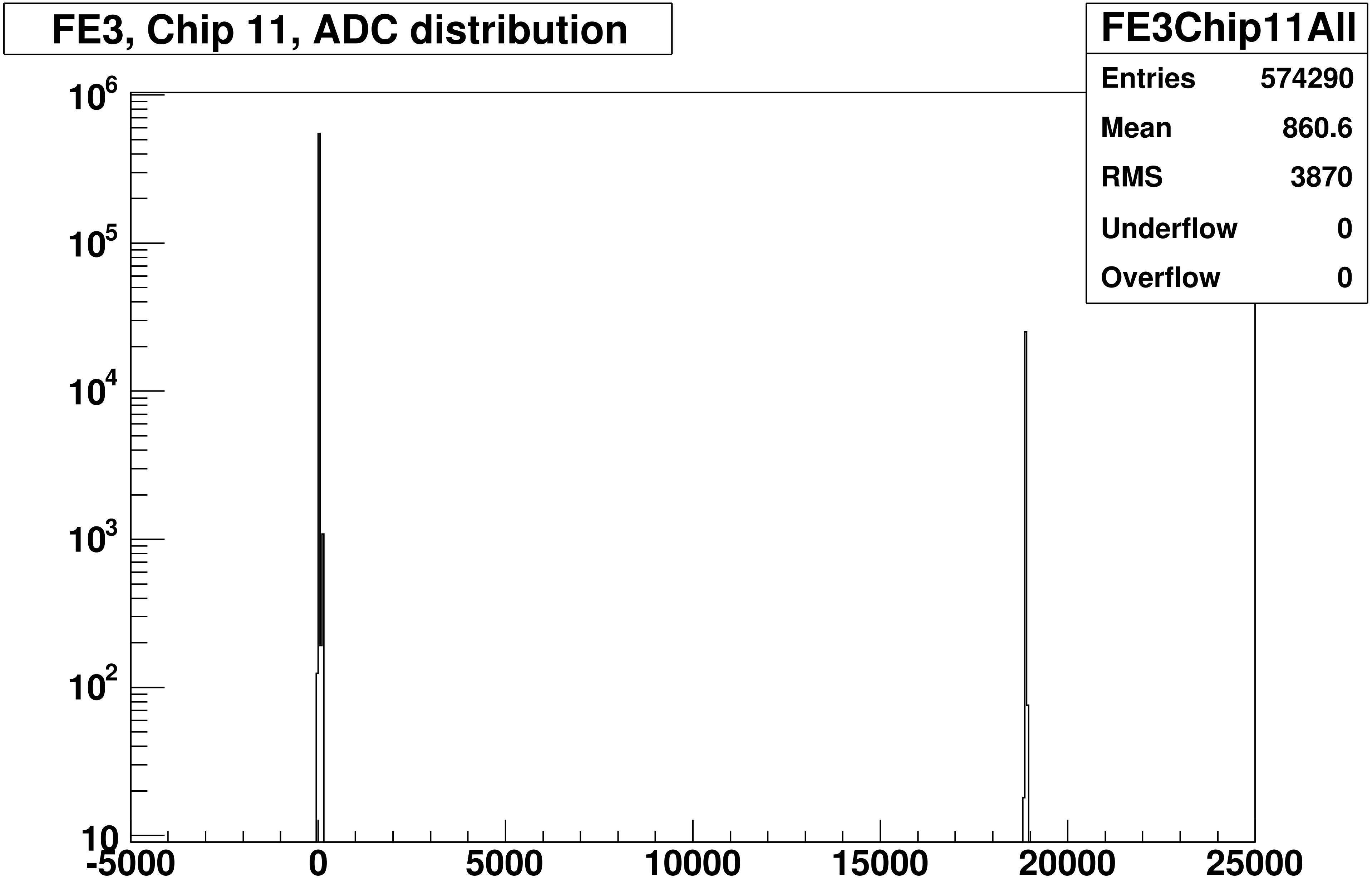
19600
19800
20000


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 574290
Mean 860.6
RMS 3870
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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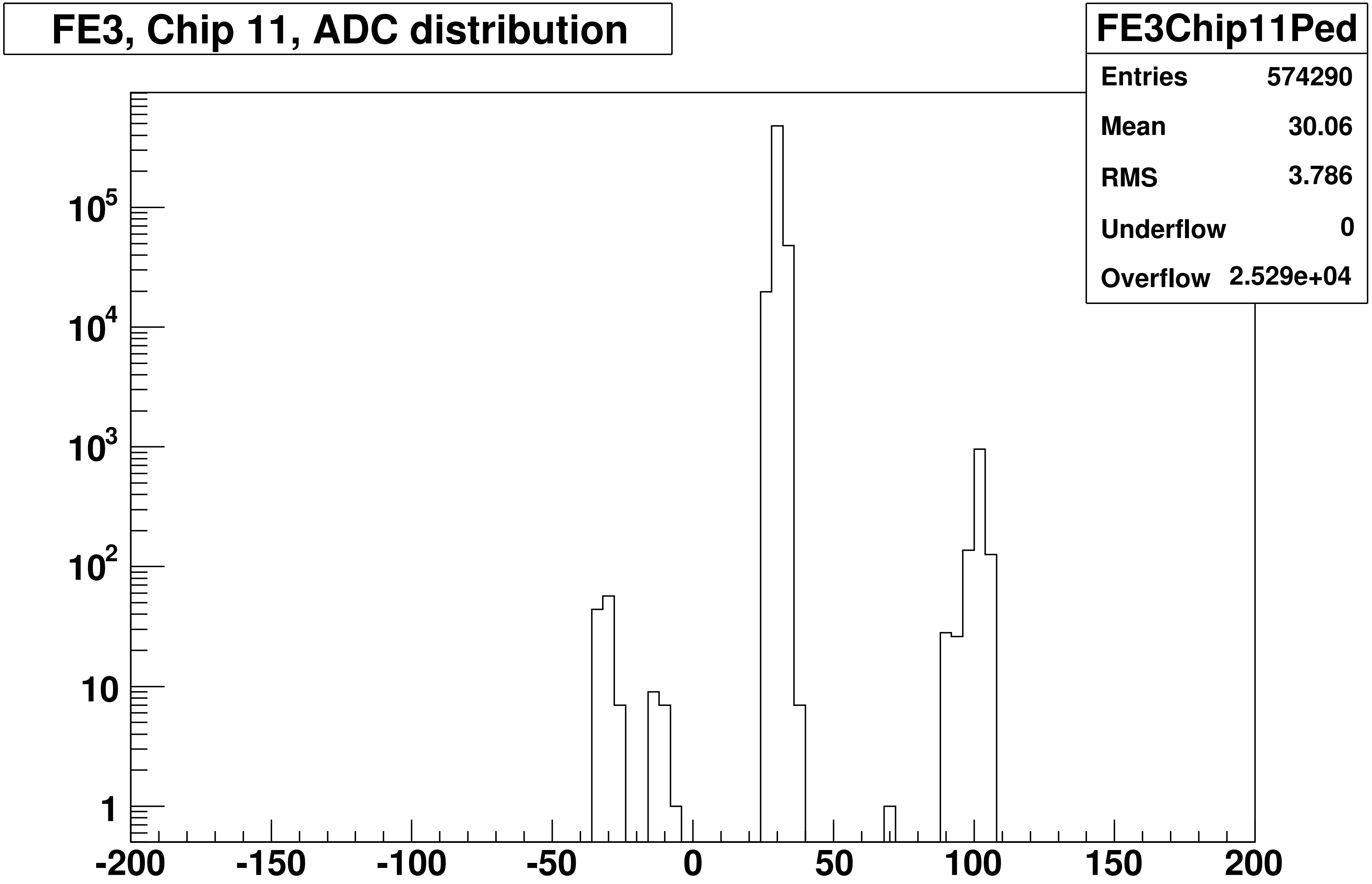
10000
15000
20000
25000



FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 574290
Mean 30.06
RMS 3.786
Underflow 0
Overflow 2.529e+04
10^5
10^4
10^3
10^2
10
1
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-100
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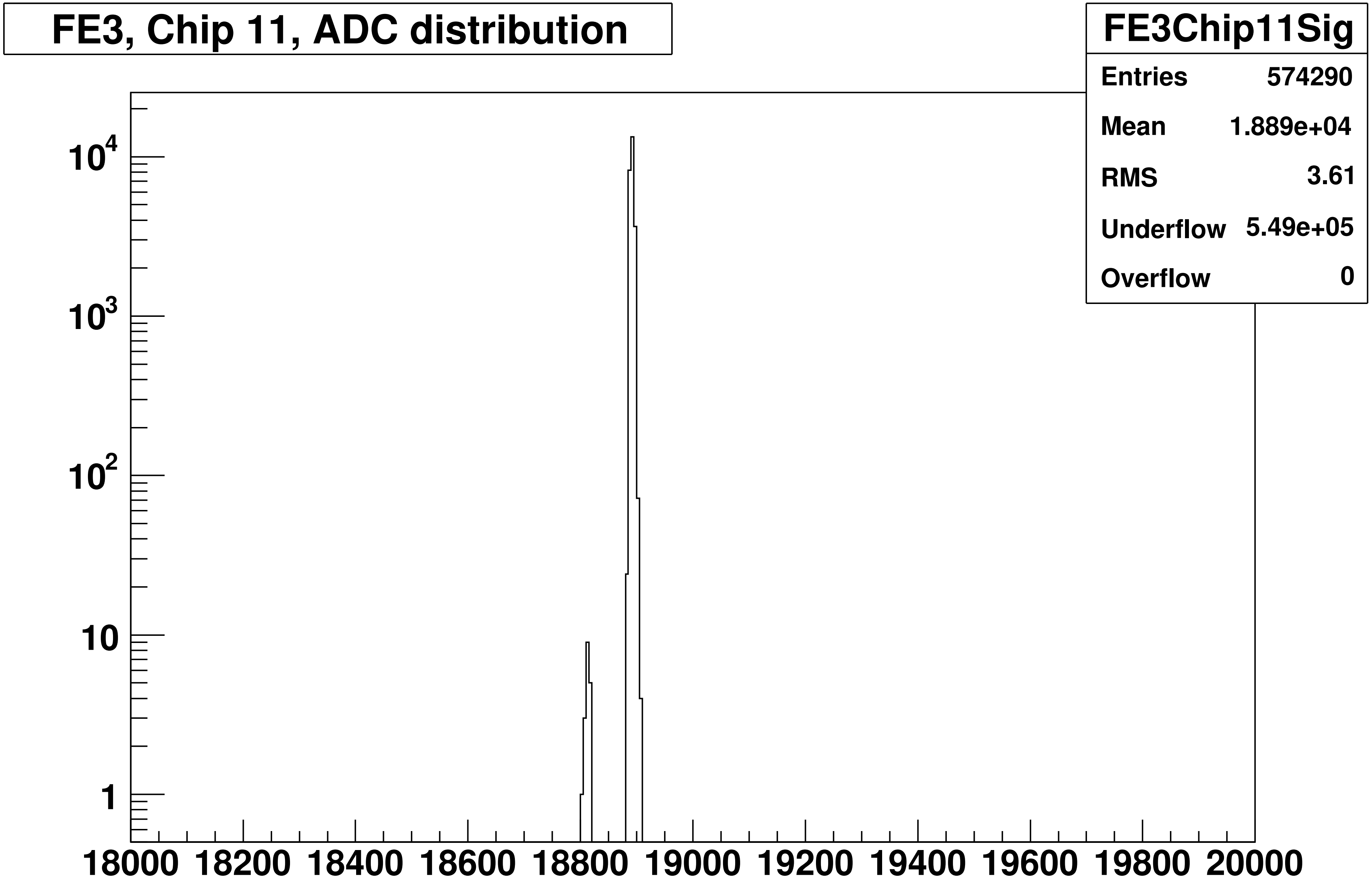
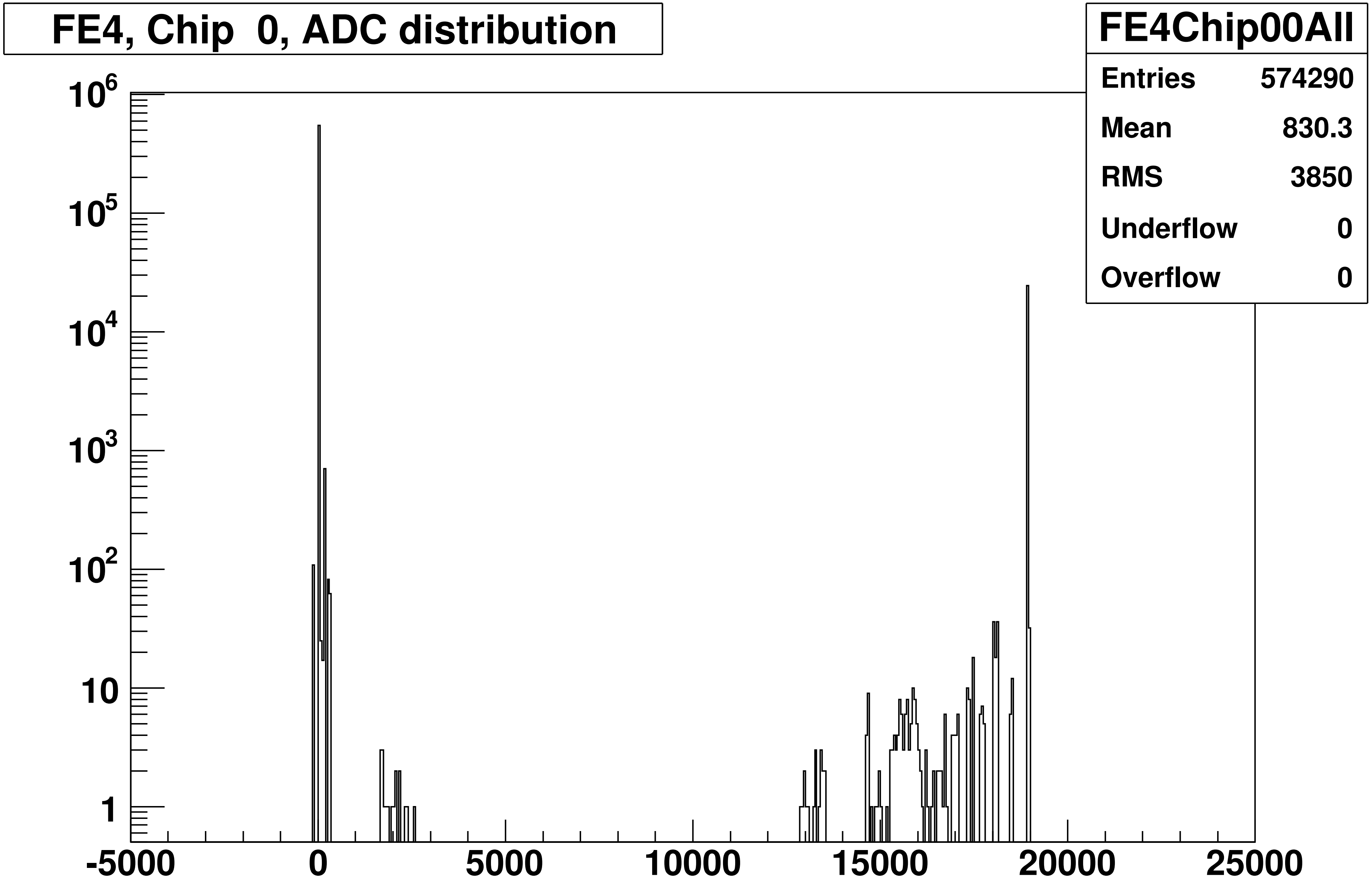
0
50
100
150
200


FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 574290
Mean 1.889e+04
RMS 3.61
Underflow 5.49e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 574290
Mean 830.3
RMS 3850
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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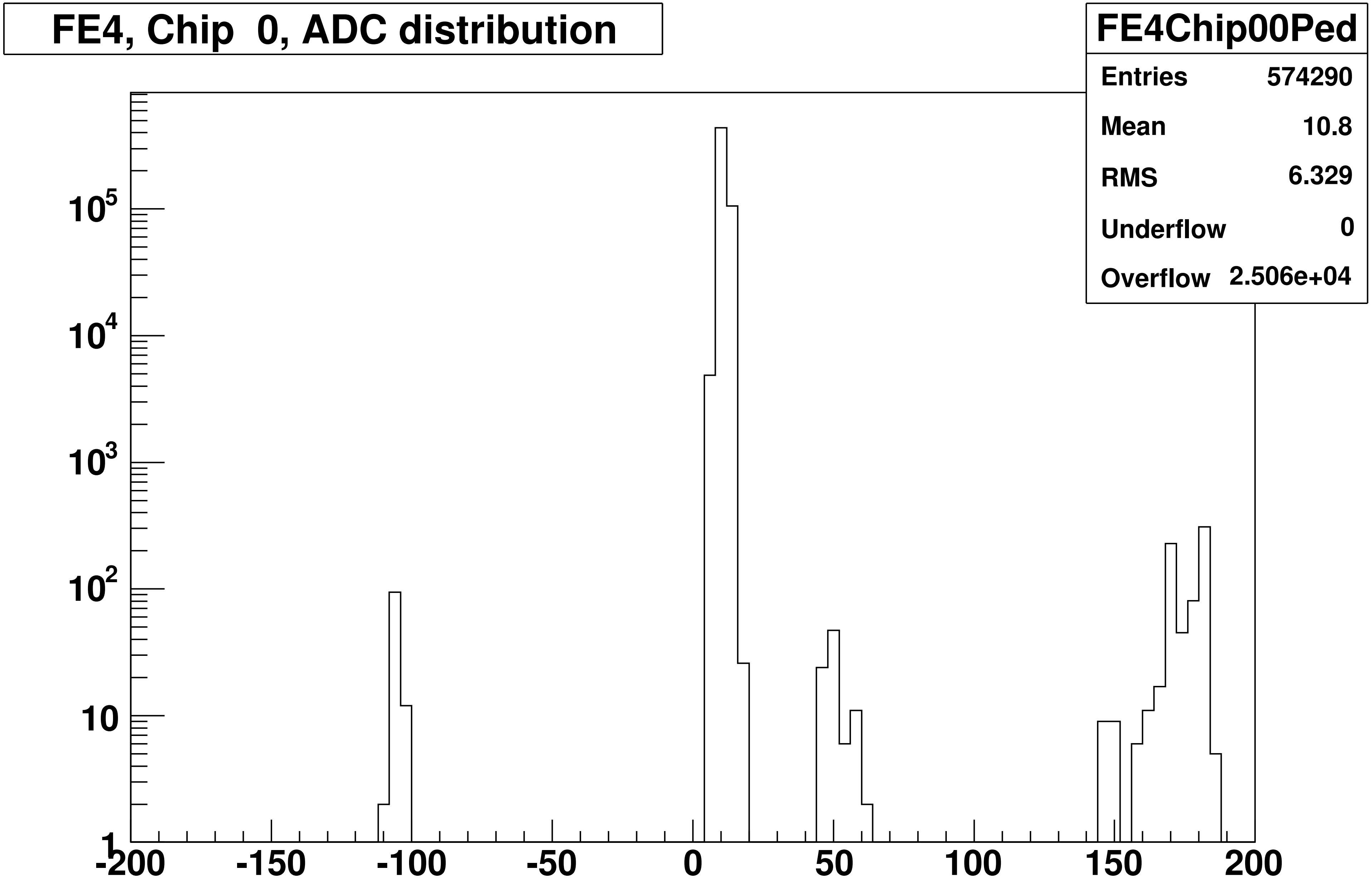
10000
15000
20000
25000



FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 574290
Mean 10.8
RMS 6.329
Underflow 0
Overflow 2.506e+04
10^5
10^4
10^3
10^2
10
1
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-100
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100
150
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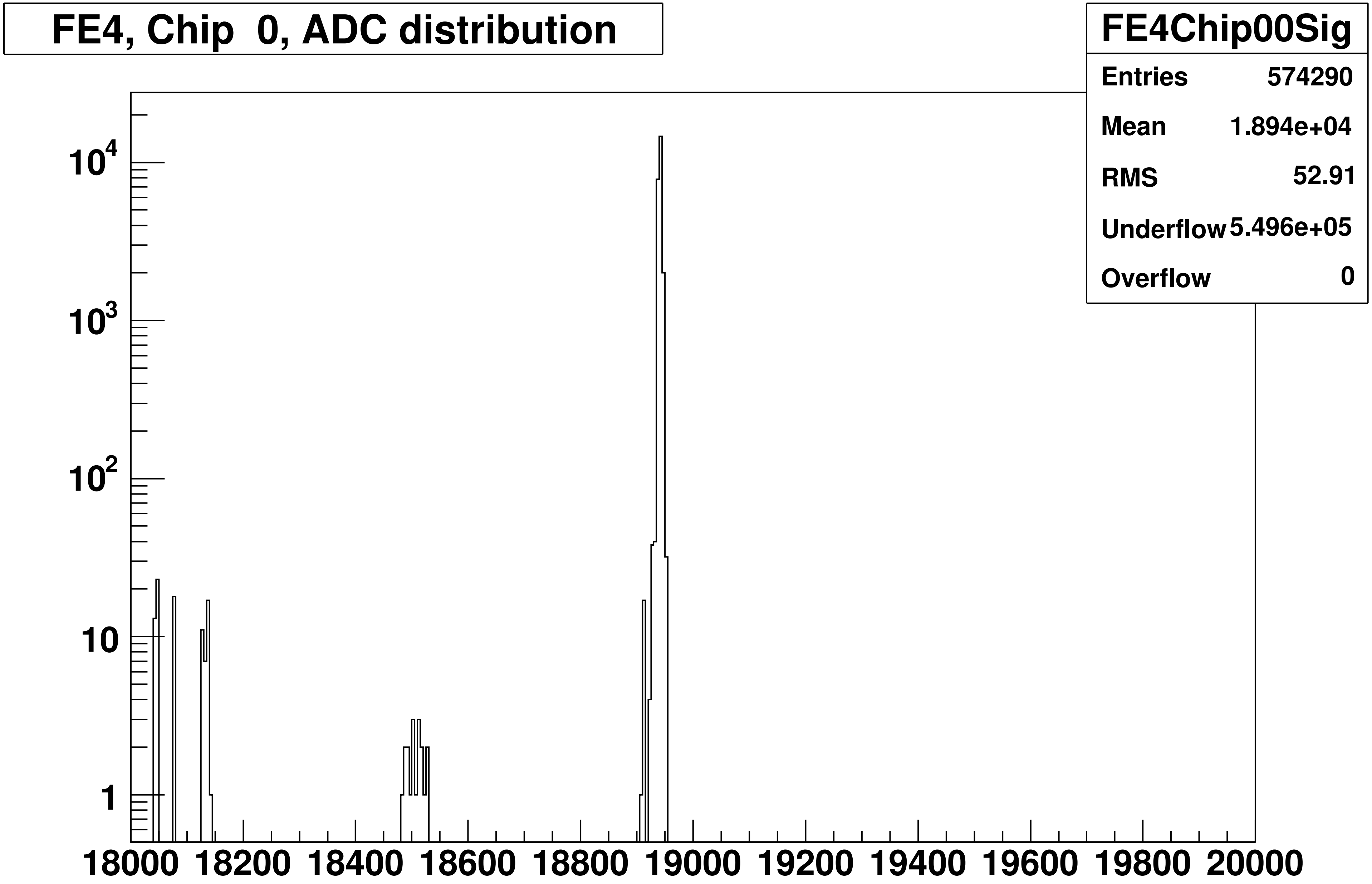
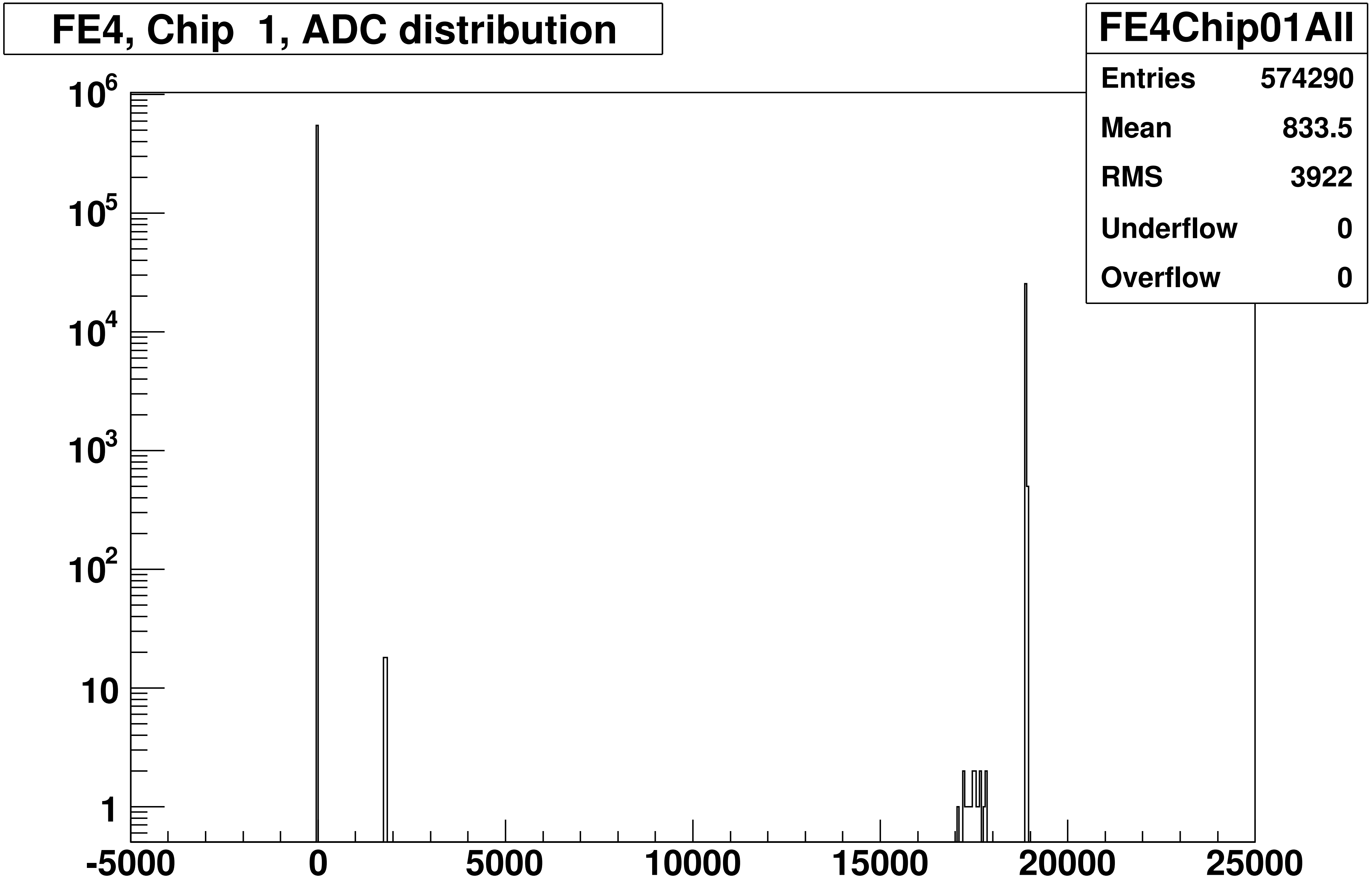


FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 574290
Mean 1.894e+04
RMS 52.91
Underflow 5.496e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 574290
Mean 833.5
RMS 3922
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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0
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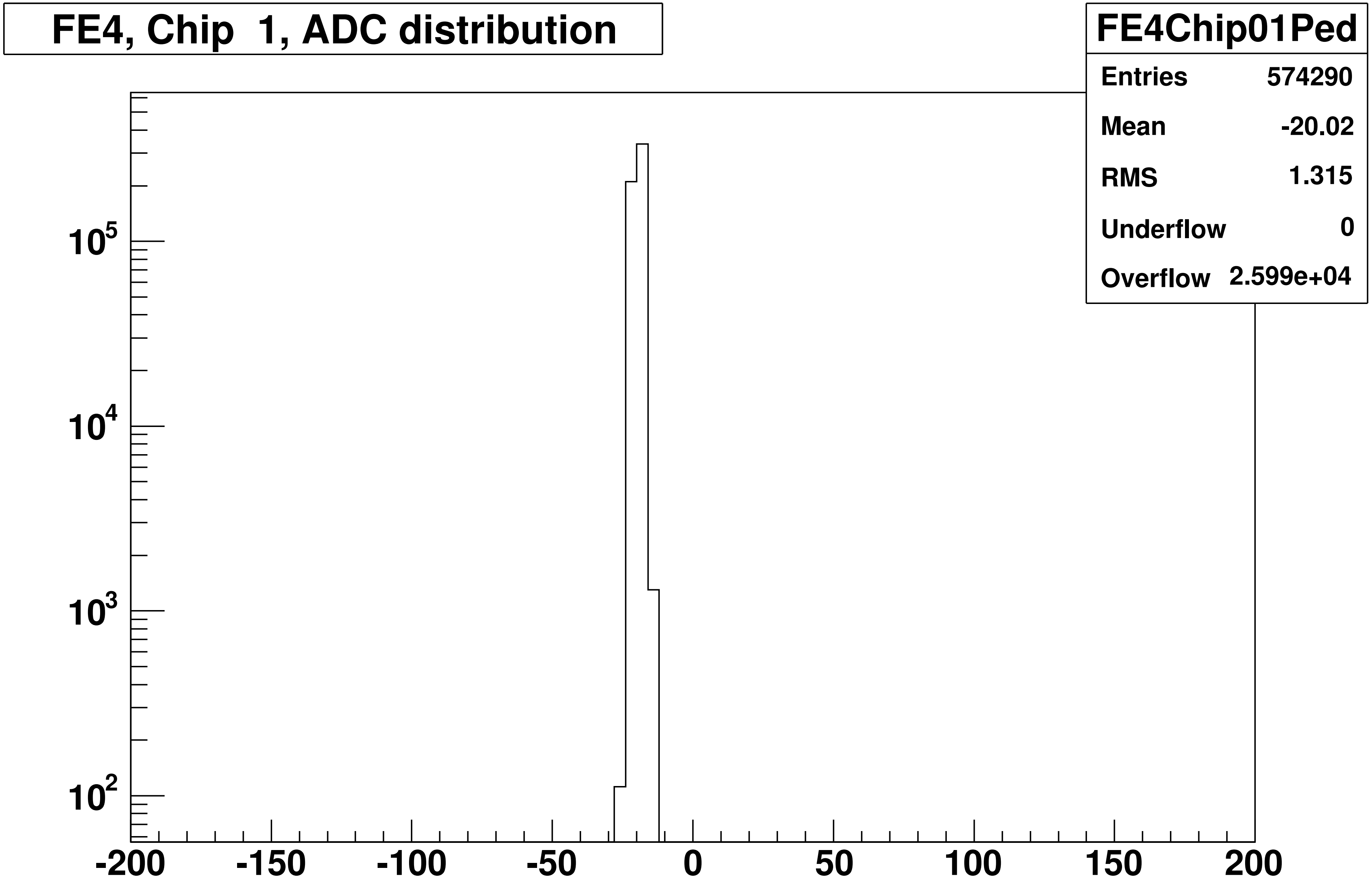
FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 574290
Mean -20.02
RMS 1.315
Underflow 0
Overflow 2.599e+04
10^5
10^4
10^3
10^2
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-100
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0
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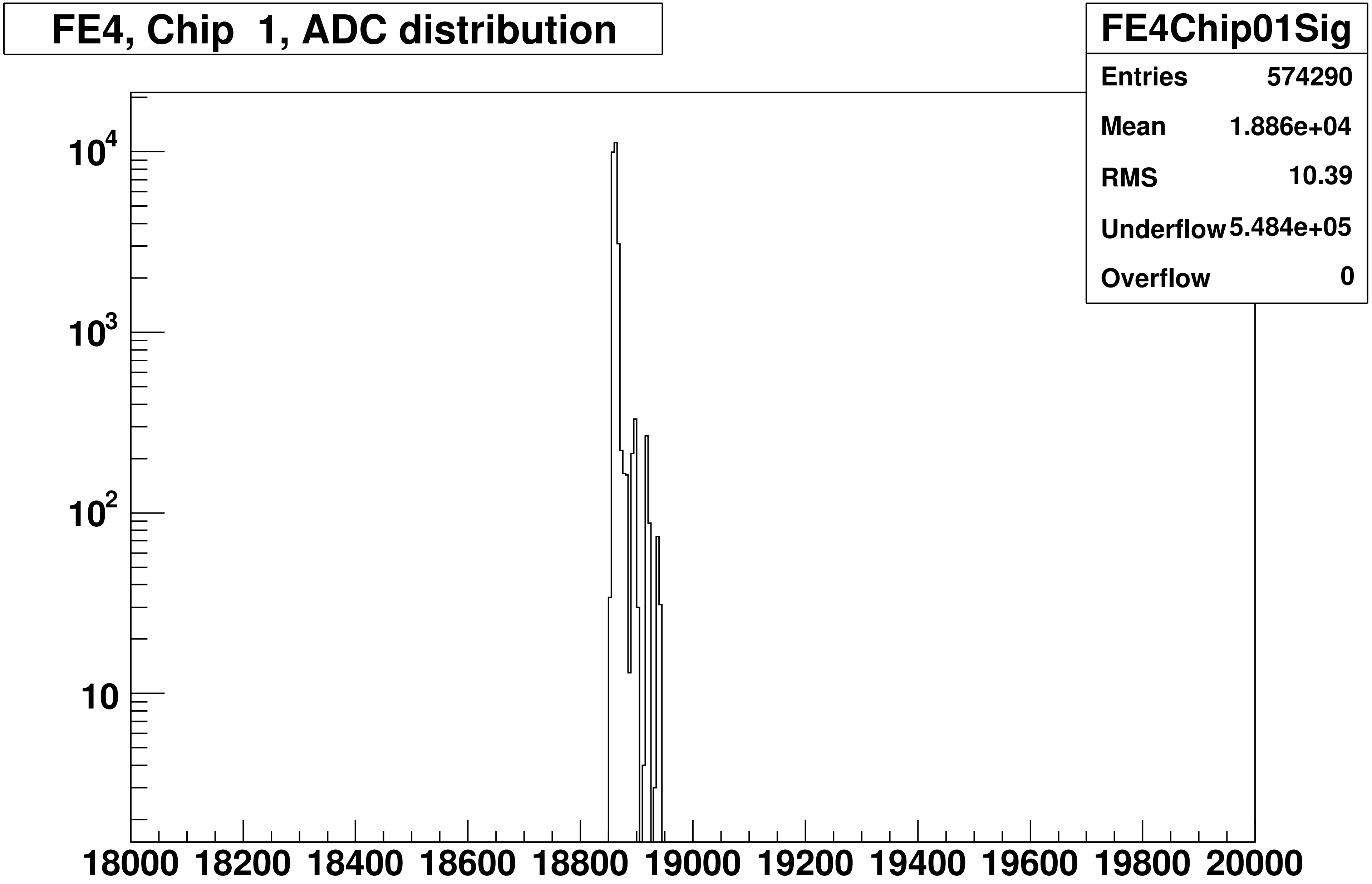
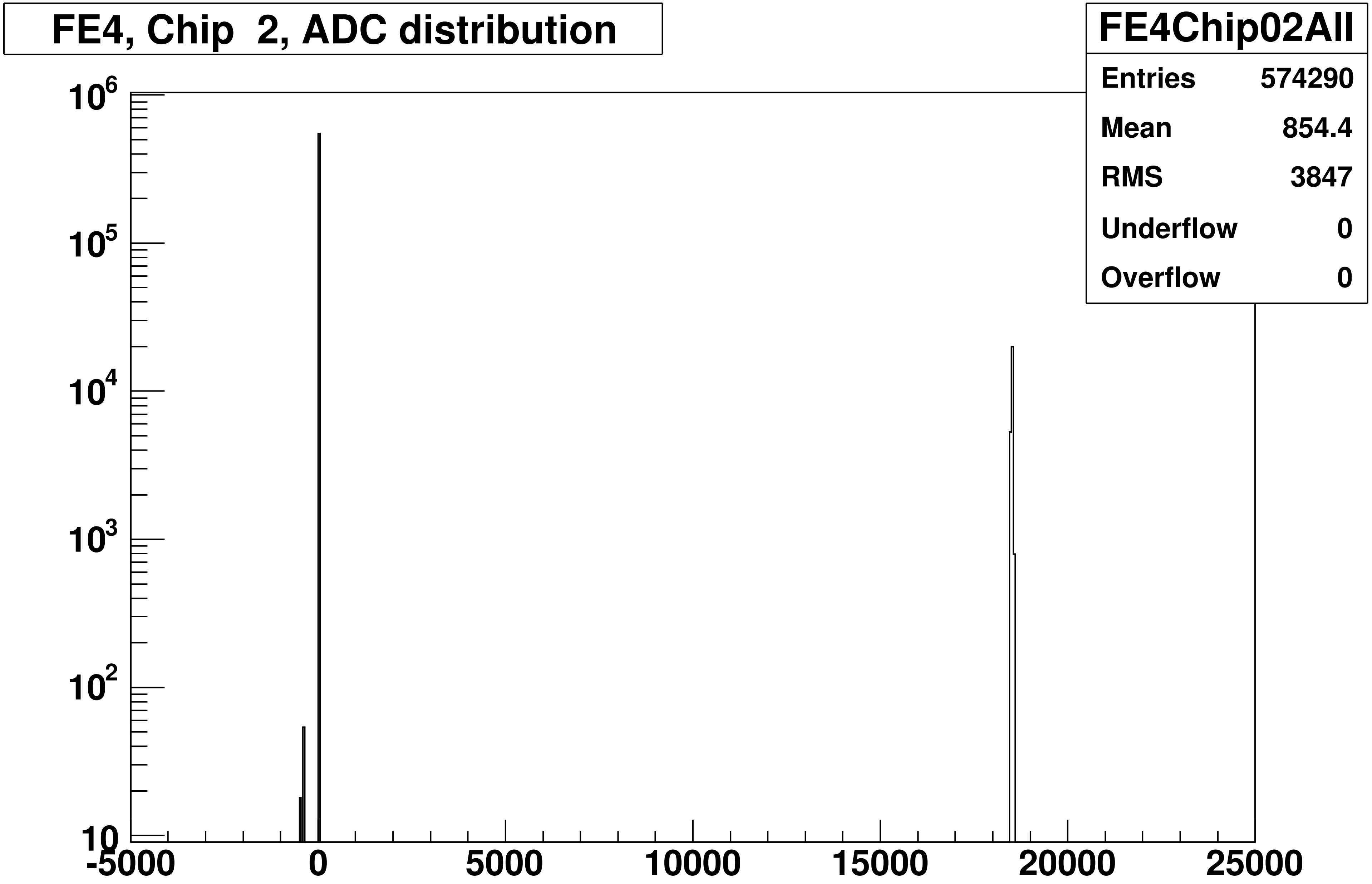
100
150
200



FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 574290
Mean 1.886e+04
RMS 10.39
Underflow 5.484e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 574290
Mean 854.4
RMS 3847
Underflow 0
Overflow 0
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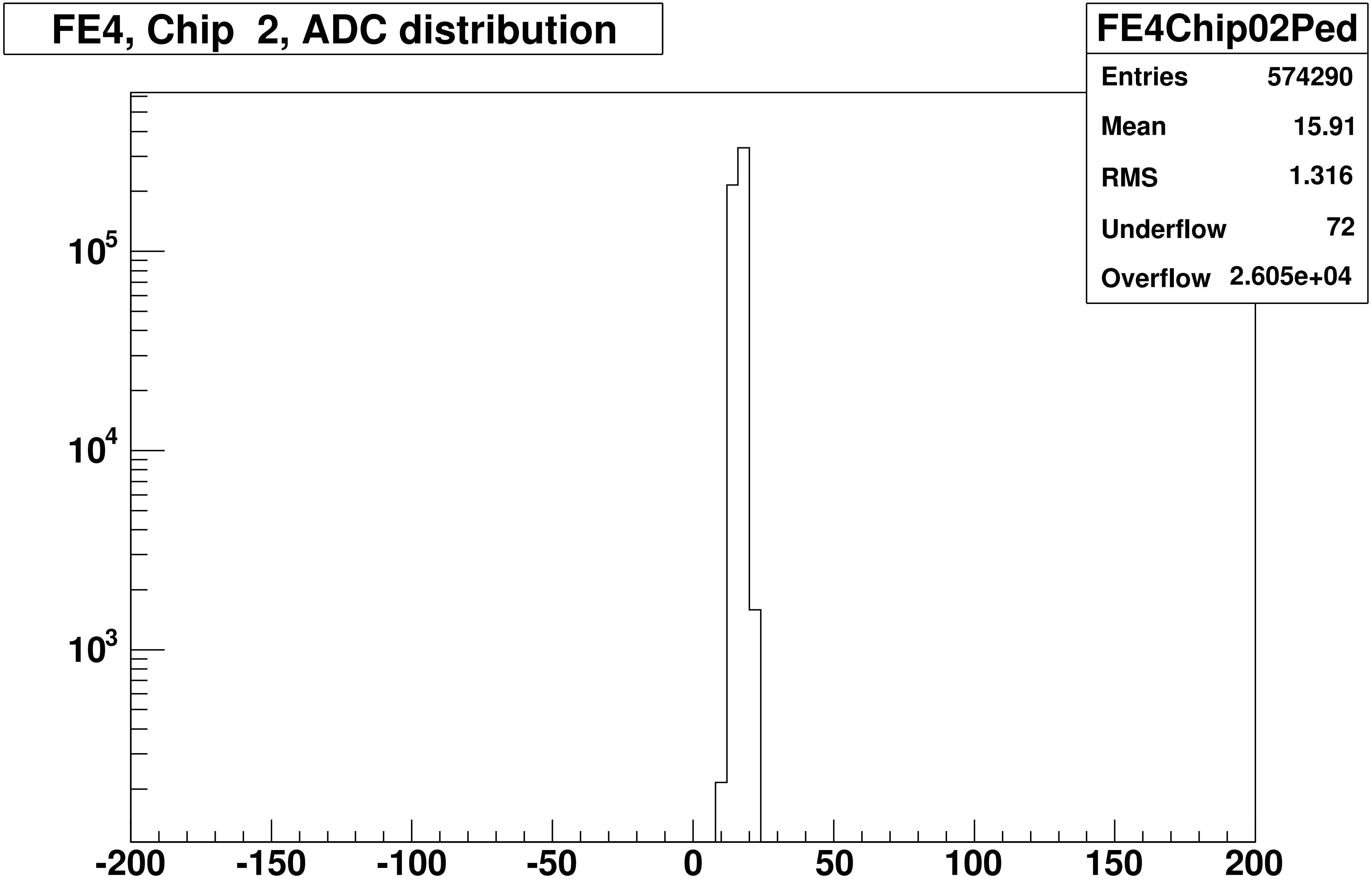
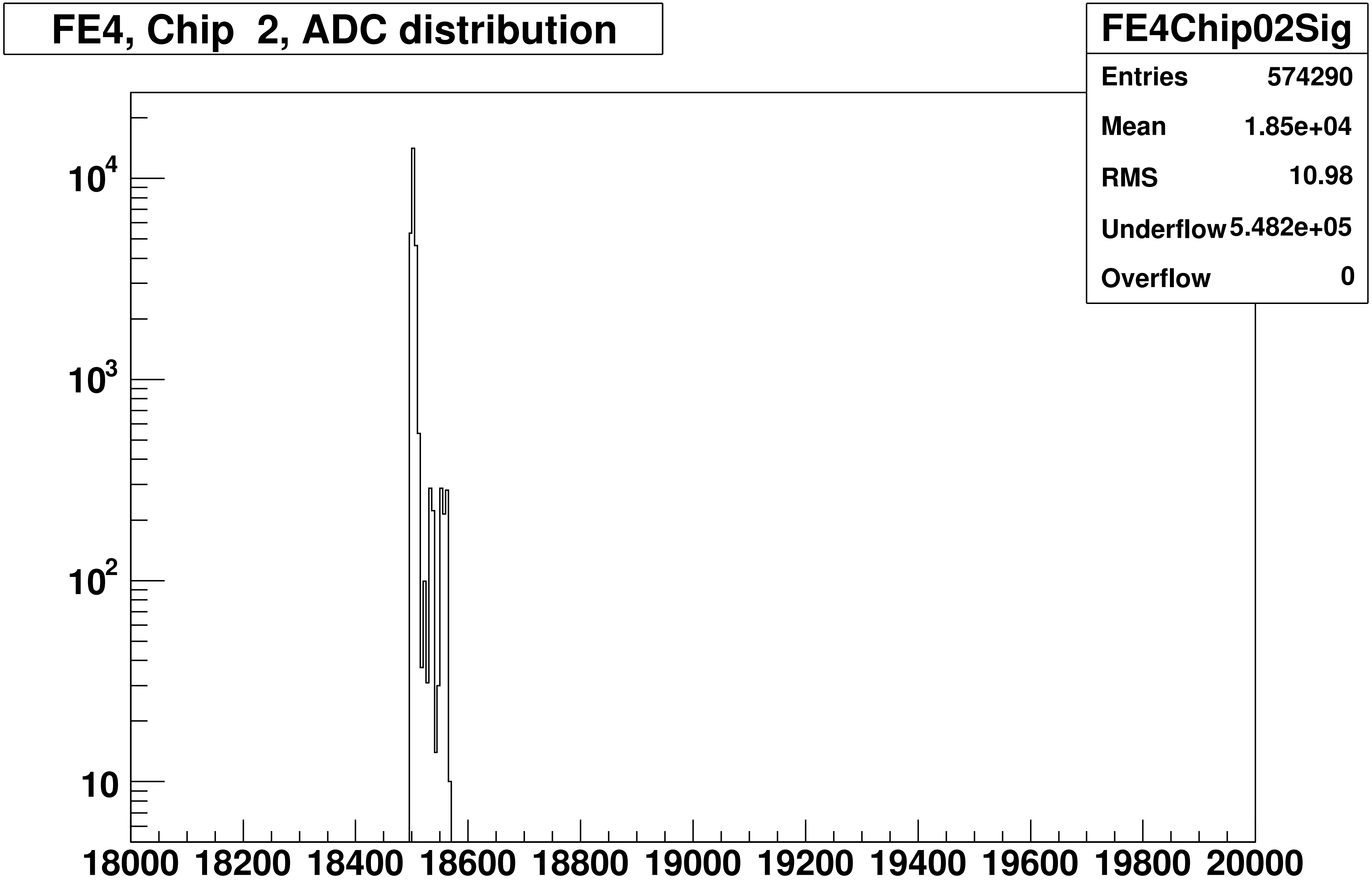
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 574290
Mean 15.91
RMS 1.316
Underflow 72
Overflow 2.605e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 574290
Mean 1.85e+04
RMS 10.98
Underflow 5.482e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
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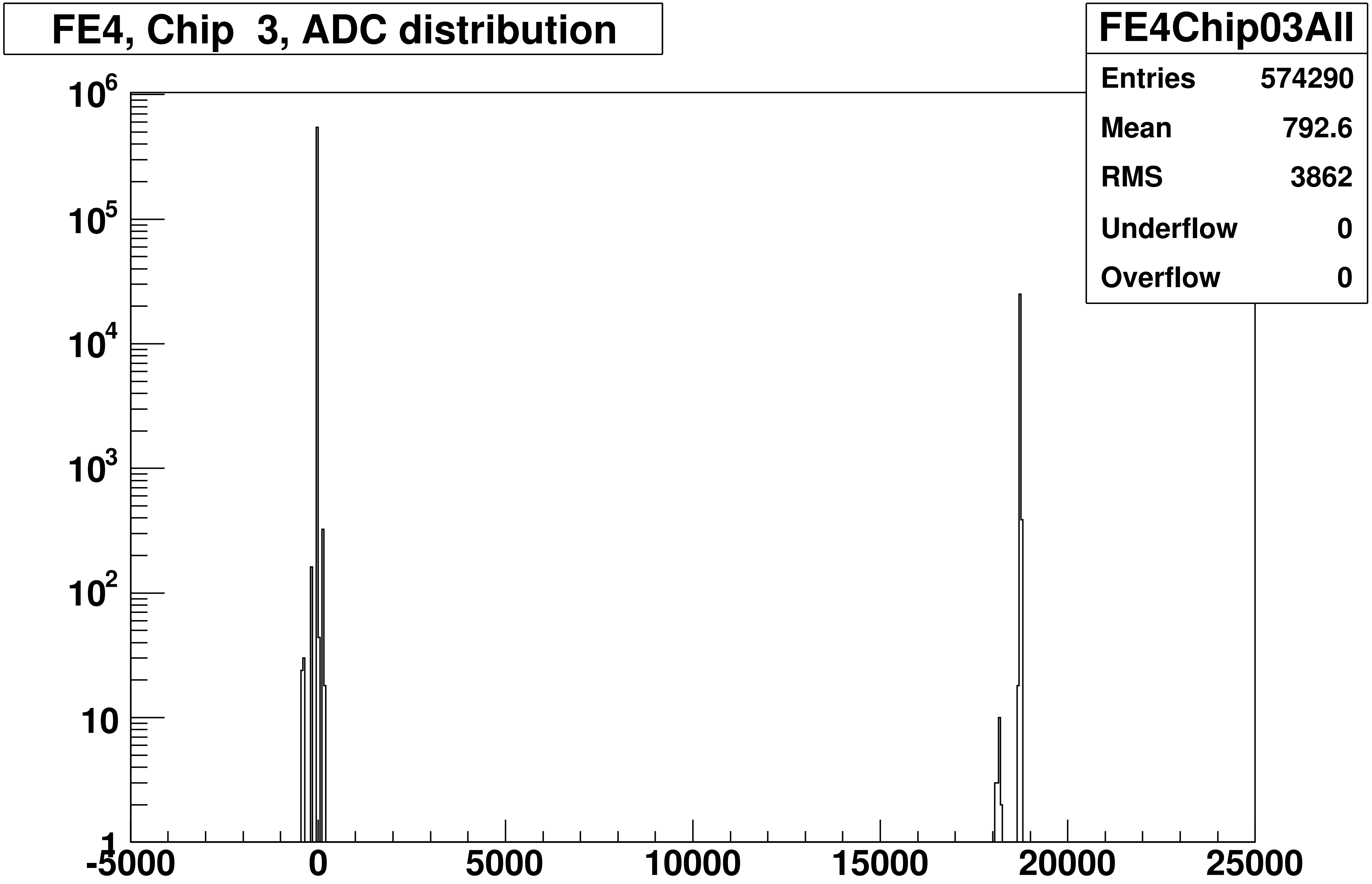
19600
19800
20000


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 574290
Mean 792.6
RMS 3862
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
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-5000
0
5000
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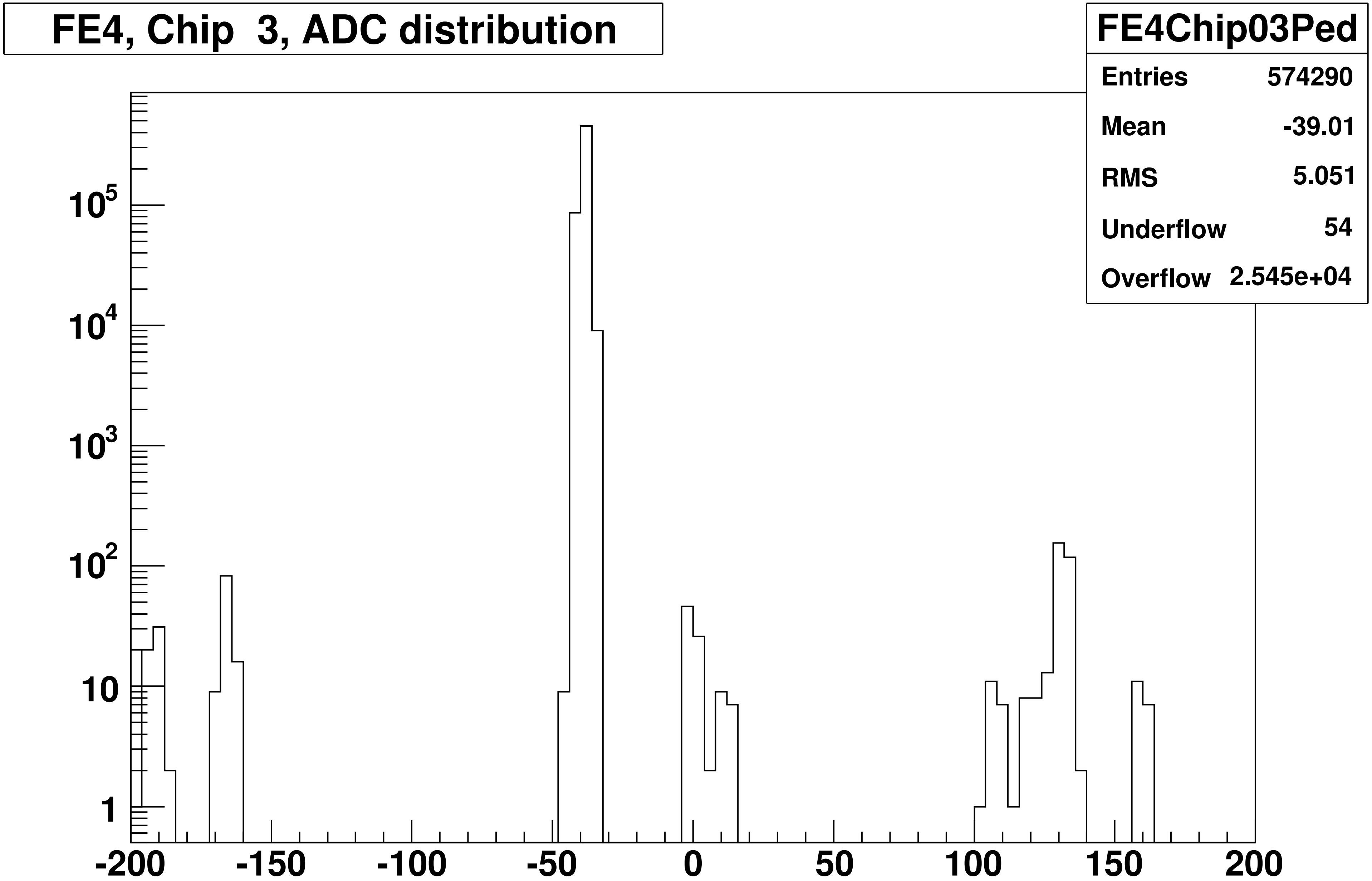
15000
20000
25000



FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 574290
Mean -39.01
RMS 5.051
Underflow 54
Overflow 2.545e+04
10^5
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10^3
10^2
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1
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-100
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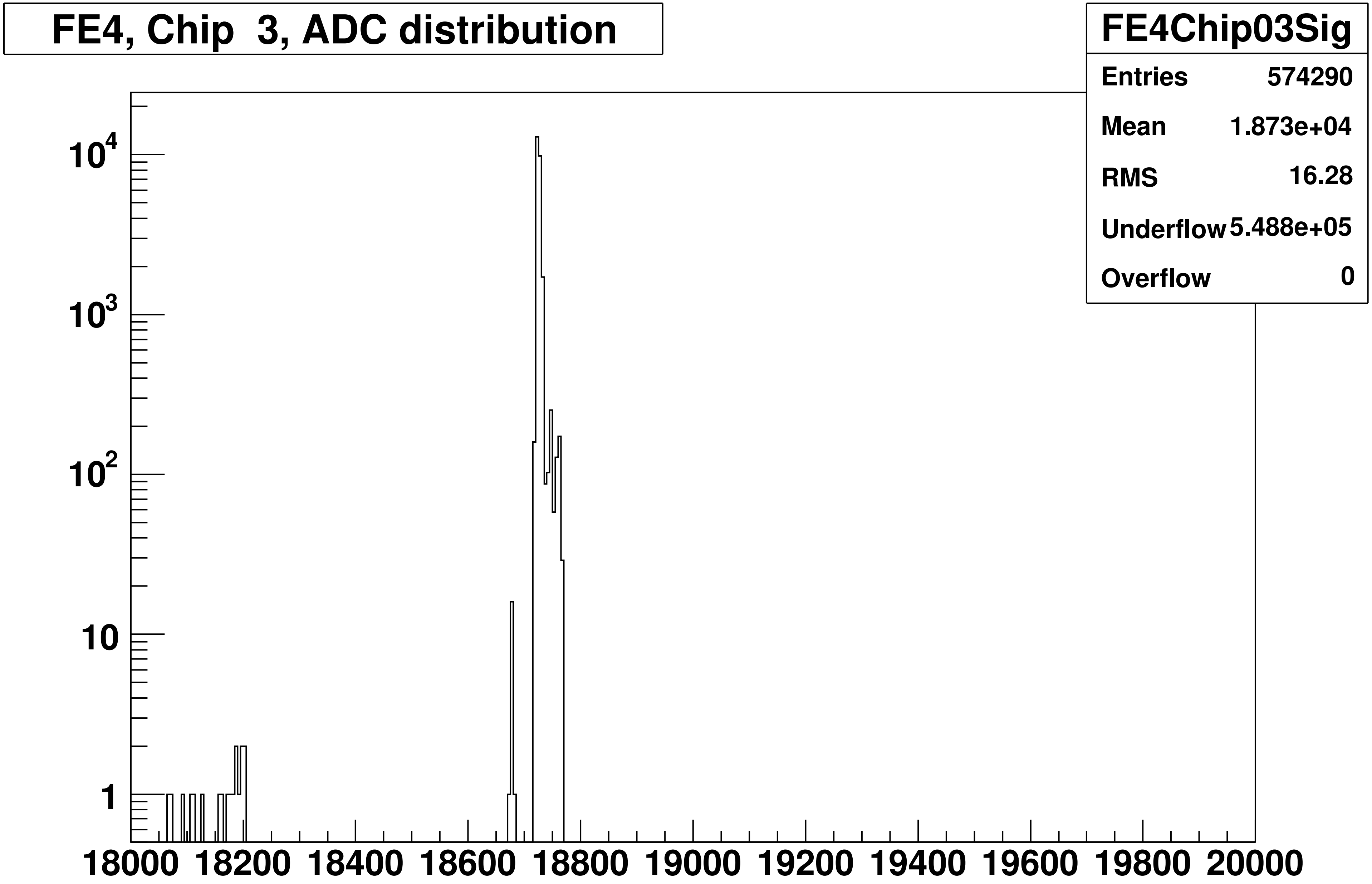
0
50
100
150
200


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 574290
Mean 1.873e+04
RMS 16.28
Underflow 5.488e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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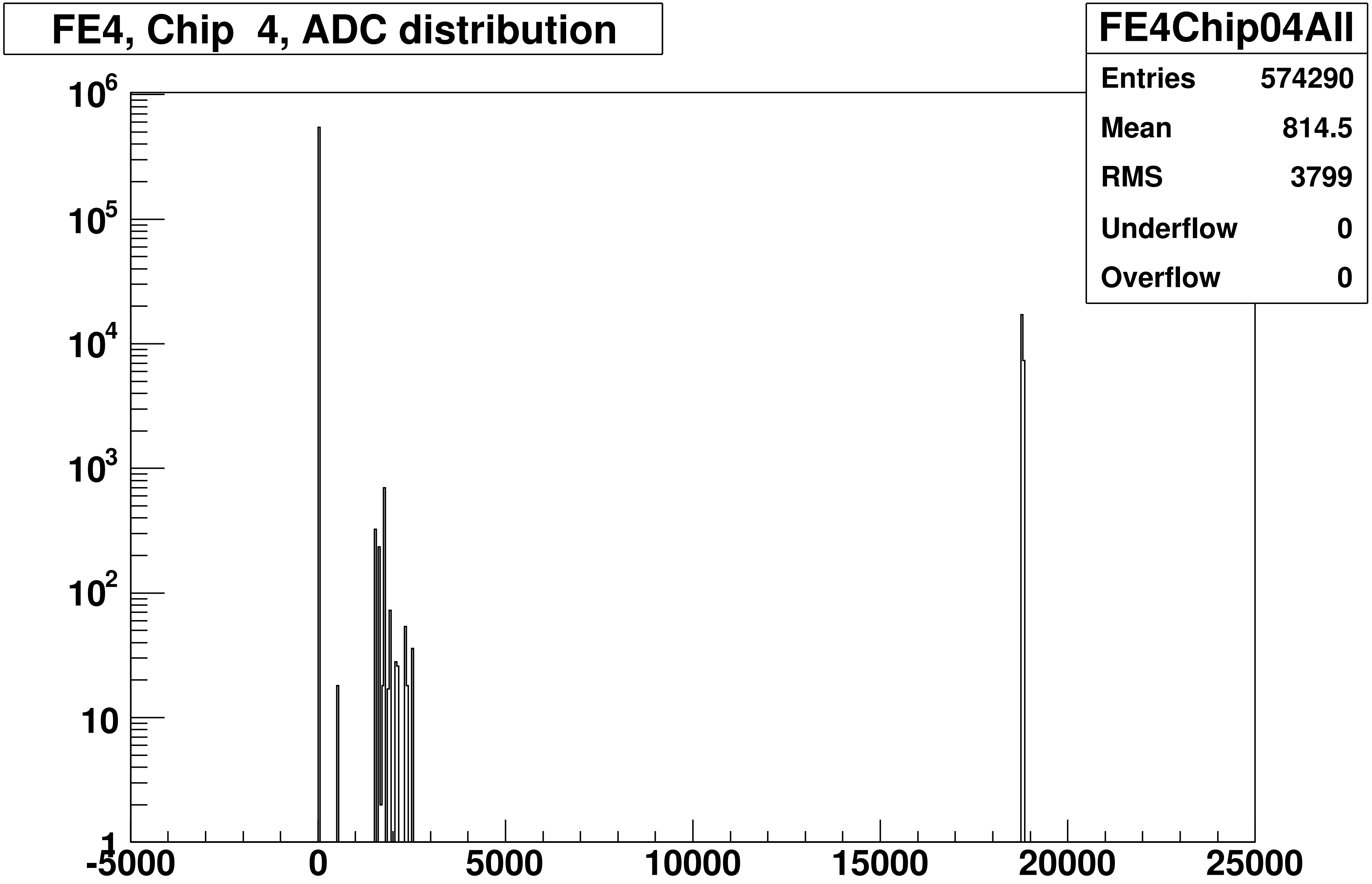

FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 574290
Mean 814.5
RMS 3799
Underflow 0
Overflow 0
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10^3
10^2
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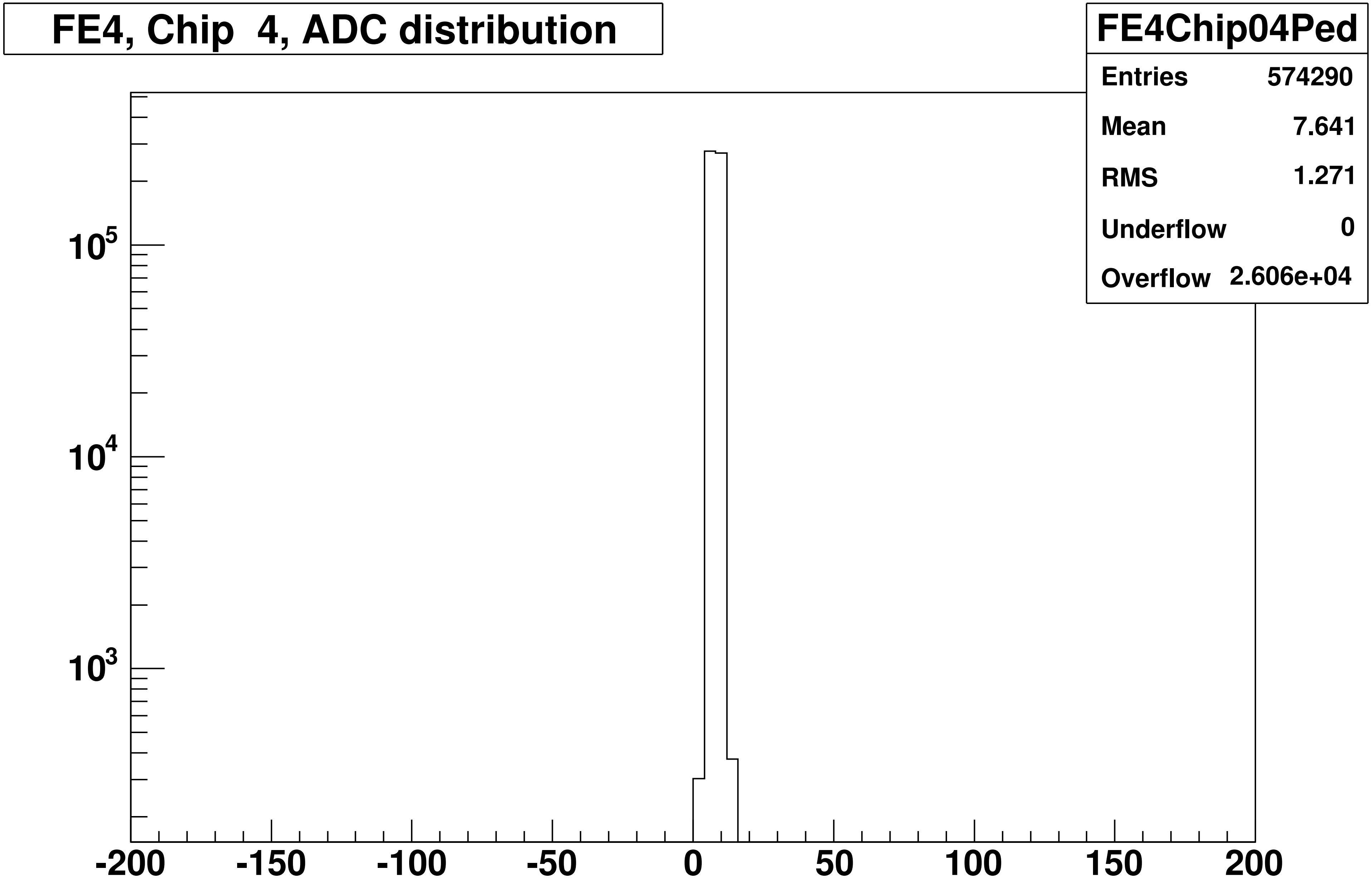

FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 574290
Mean 7.641
RMS 1.271
Underflow 0
Overflow 2.606e+04
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10^4
10^3
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-100
-50
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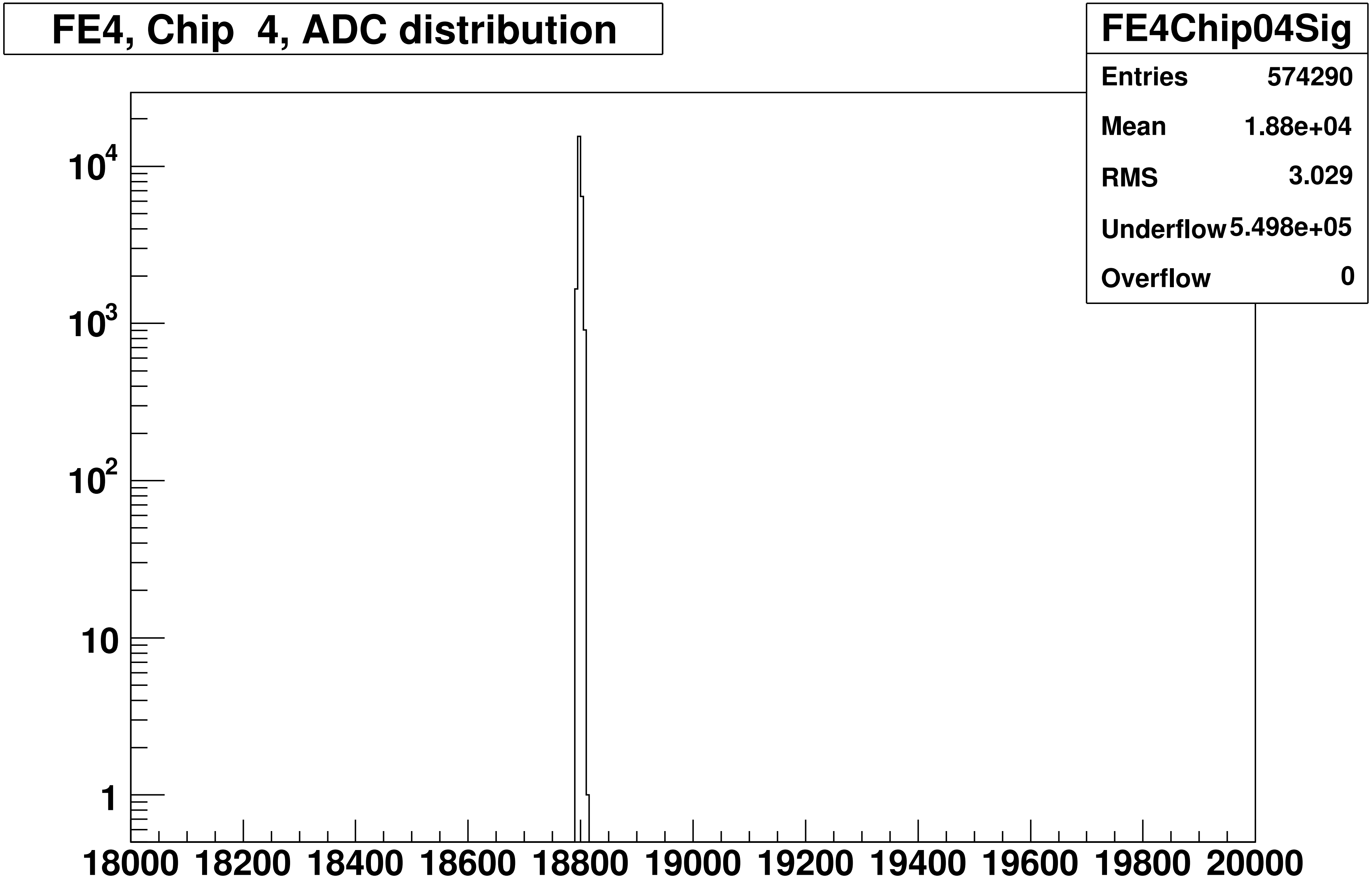
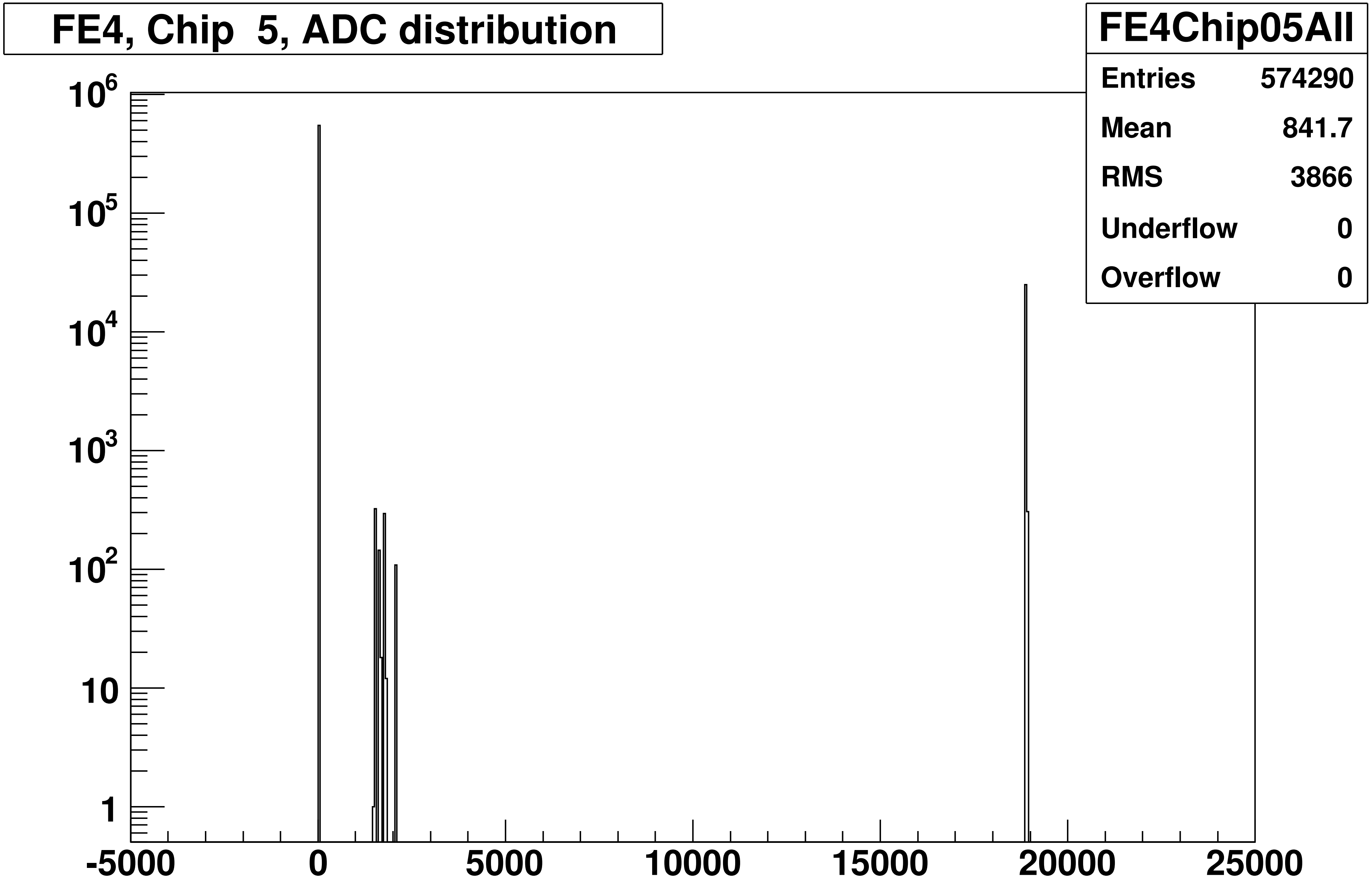
100
150
200


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 574290
Mean 1.88e+04
RMS 3.029
Underflow 5.498e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 574290
Mean 841.7
RMS 3866
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
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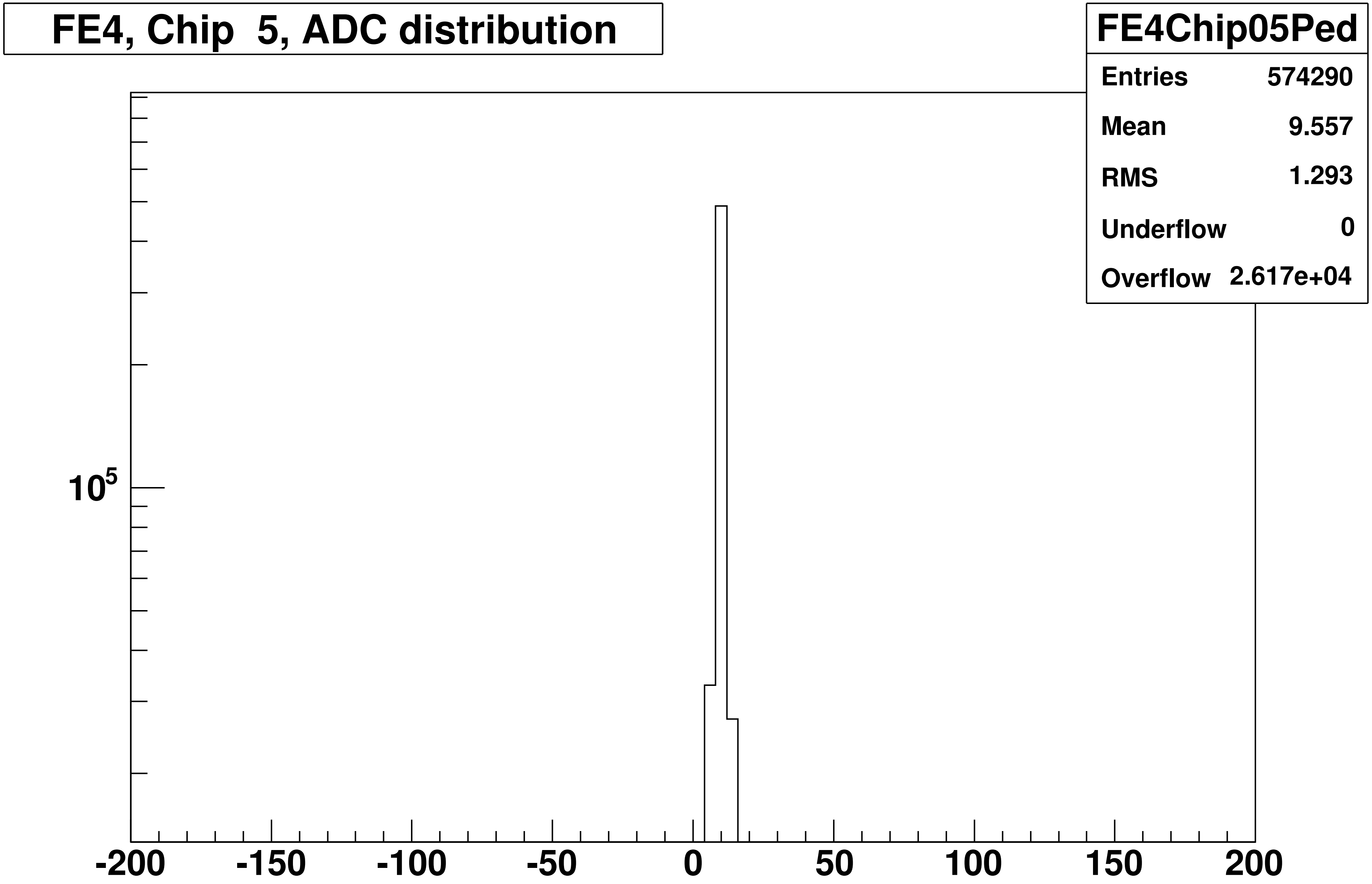
FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 574290
Mean 9.557
RMS 1.293
Underflow 0
Overflow 2.617e+04
10^5
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-150
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100
150
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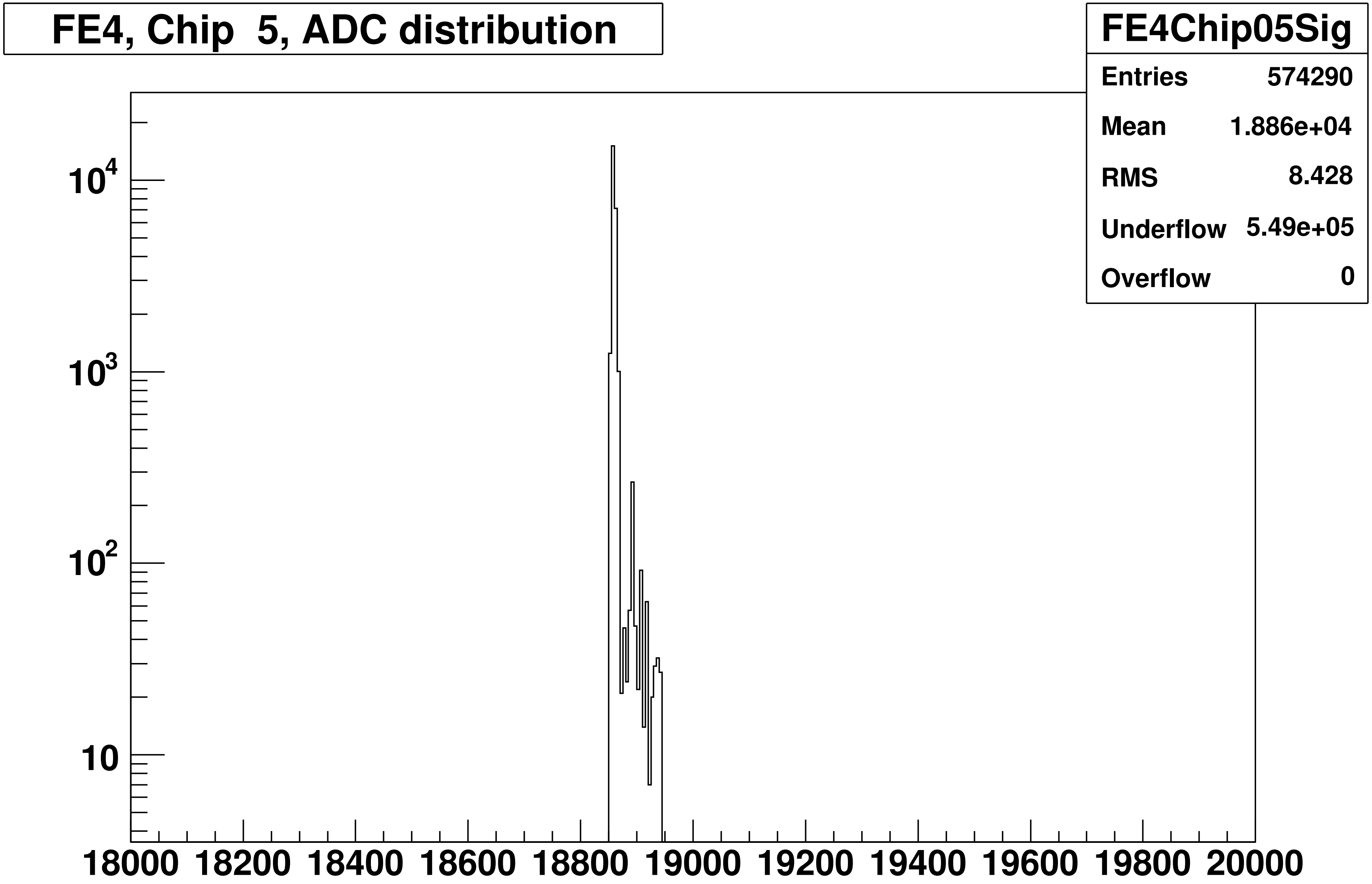

FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 574290
Mean 1.886e+04
RMS 8.428
Underflow 5.49e+05
Overflow 0
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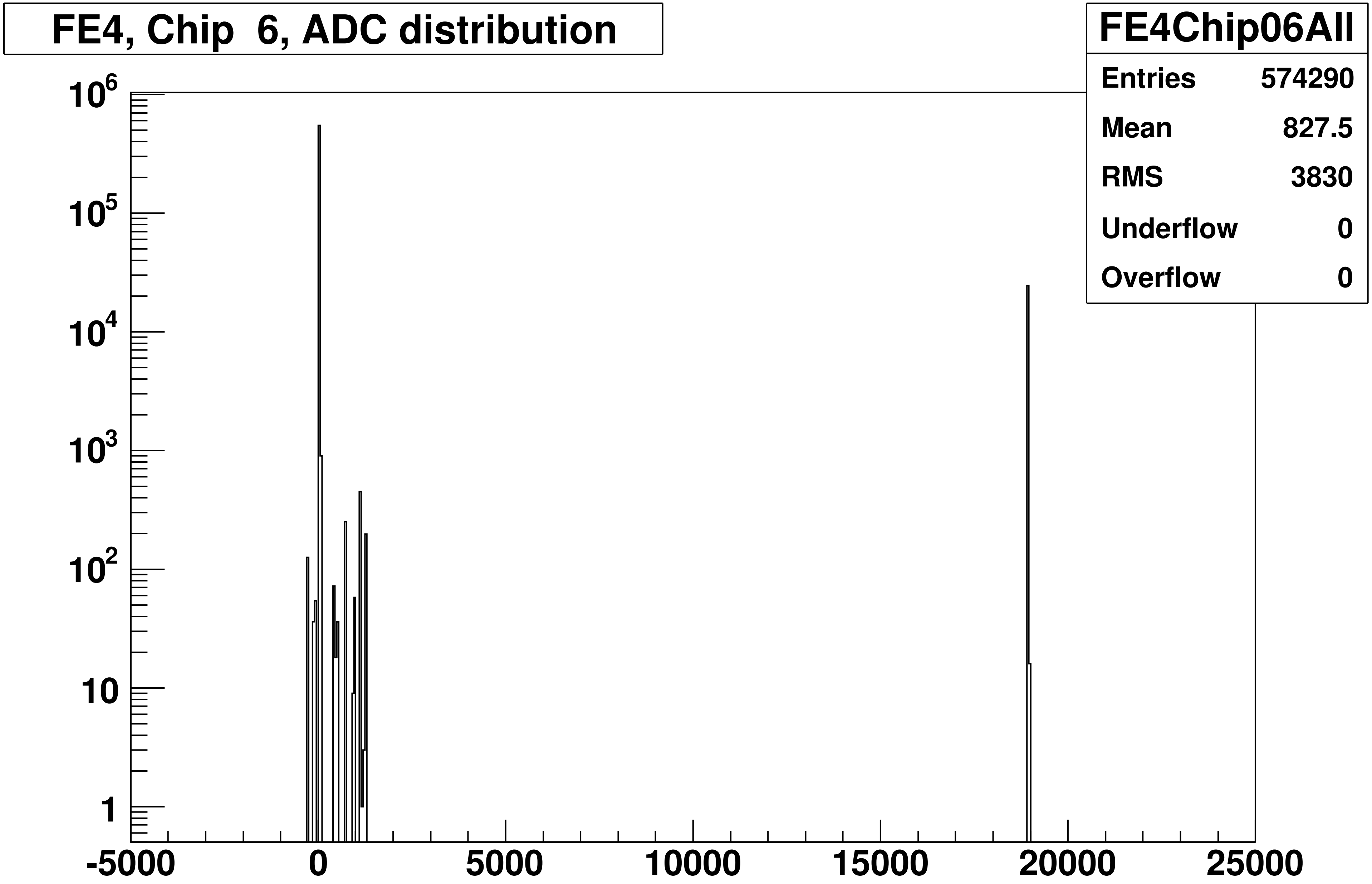
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 574290
Mean 827.5
RMS 3830
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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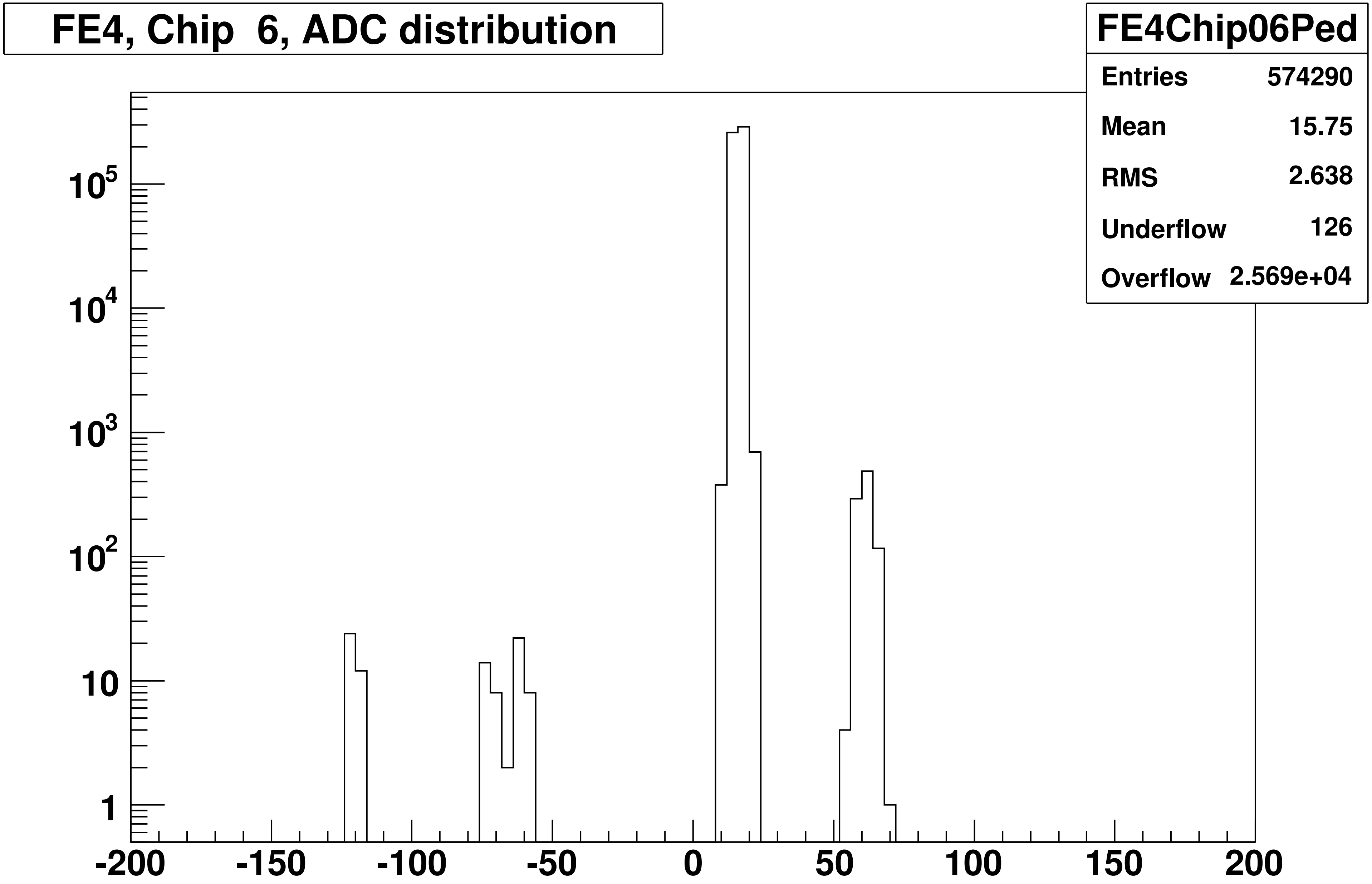
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 574290
Mean 15.75
RMS 2.638
Underflow 126
Overflow 2.569e+04
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10^3
10^2
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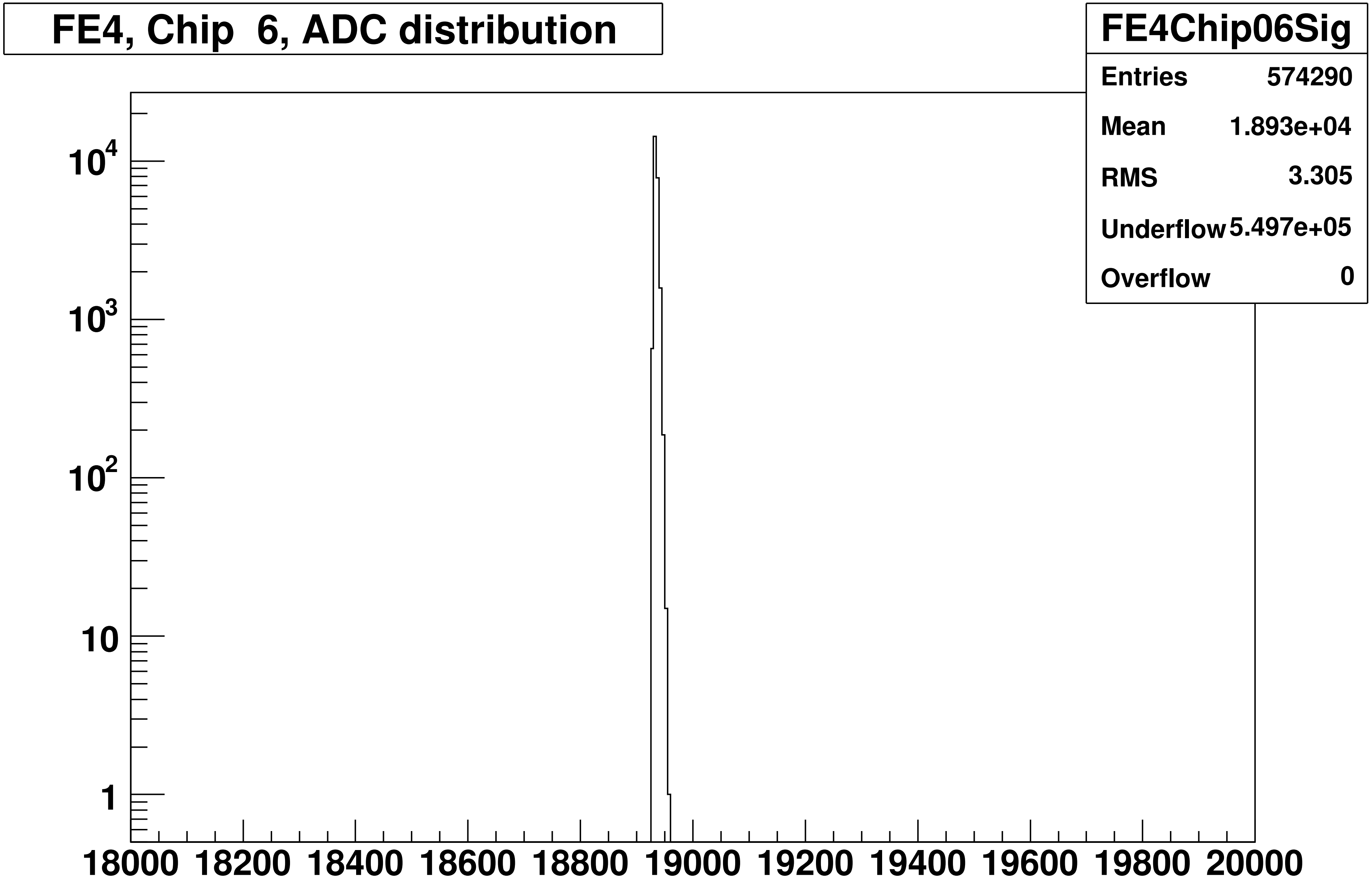
100
150
200


FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 574290
Mean 1.893e+04
RMS 3.305
Underflow 5.497e+05
Overflow 0
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10^3
10^2
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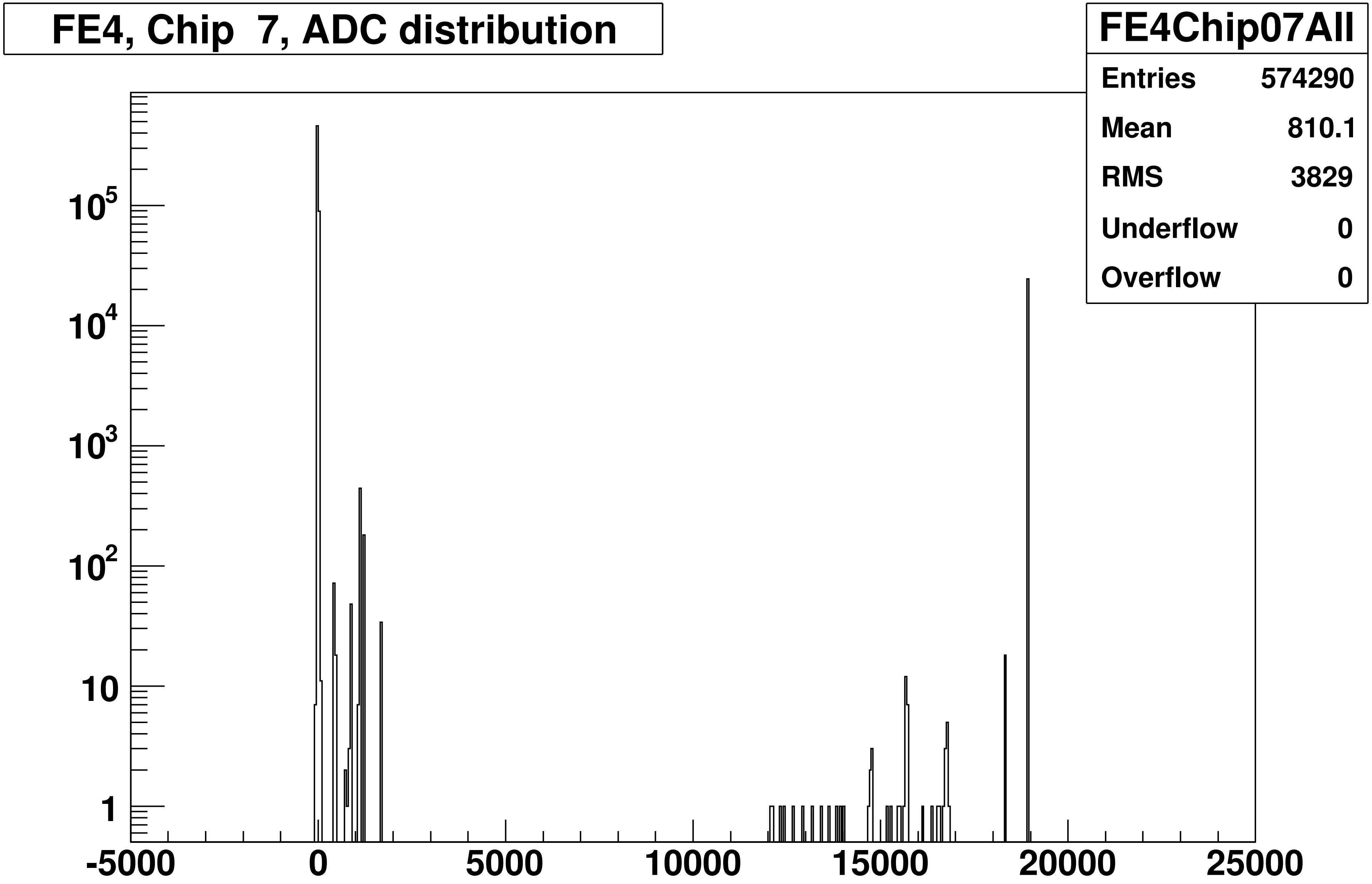
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 574290
Mean 810.1
RMS 3829
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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-5000
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10000
15000
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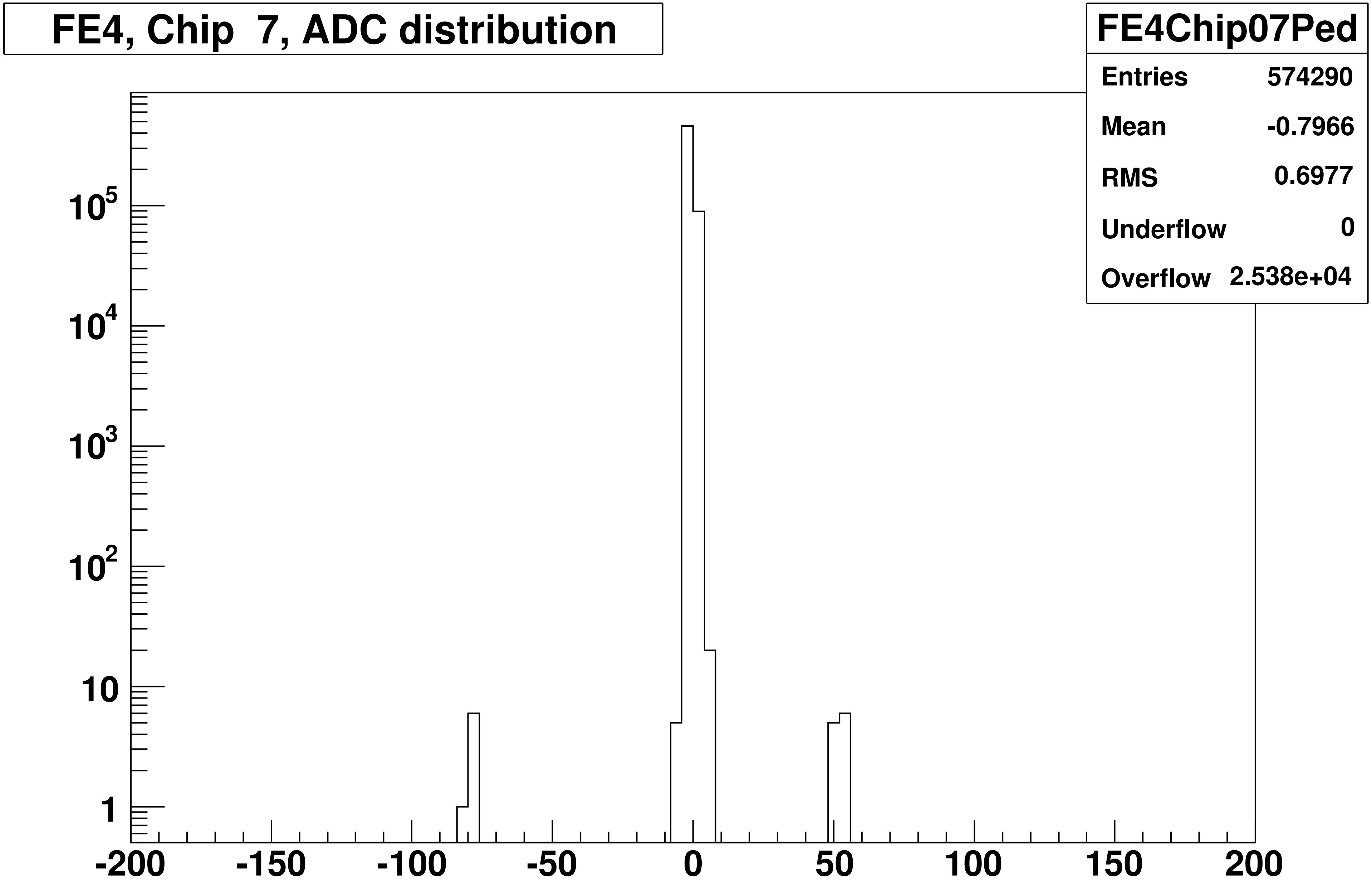
FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 574290
Mean -0.7966
RMS 0.6977
Underflow 0
Overflow 2.538e+04
10^5
10^4
10^3
10^2
10
1
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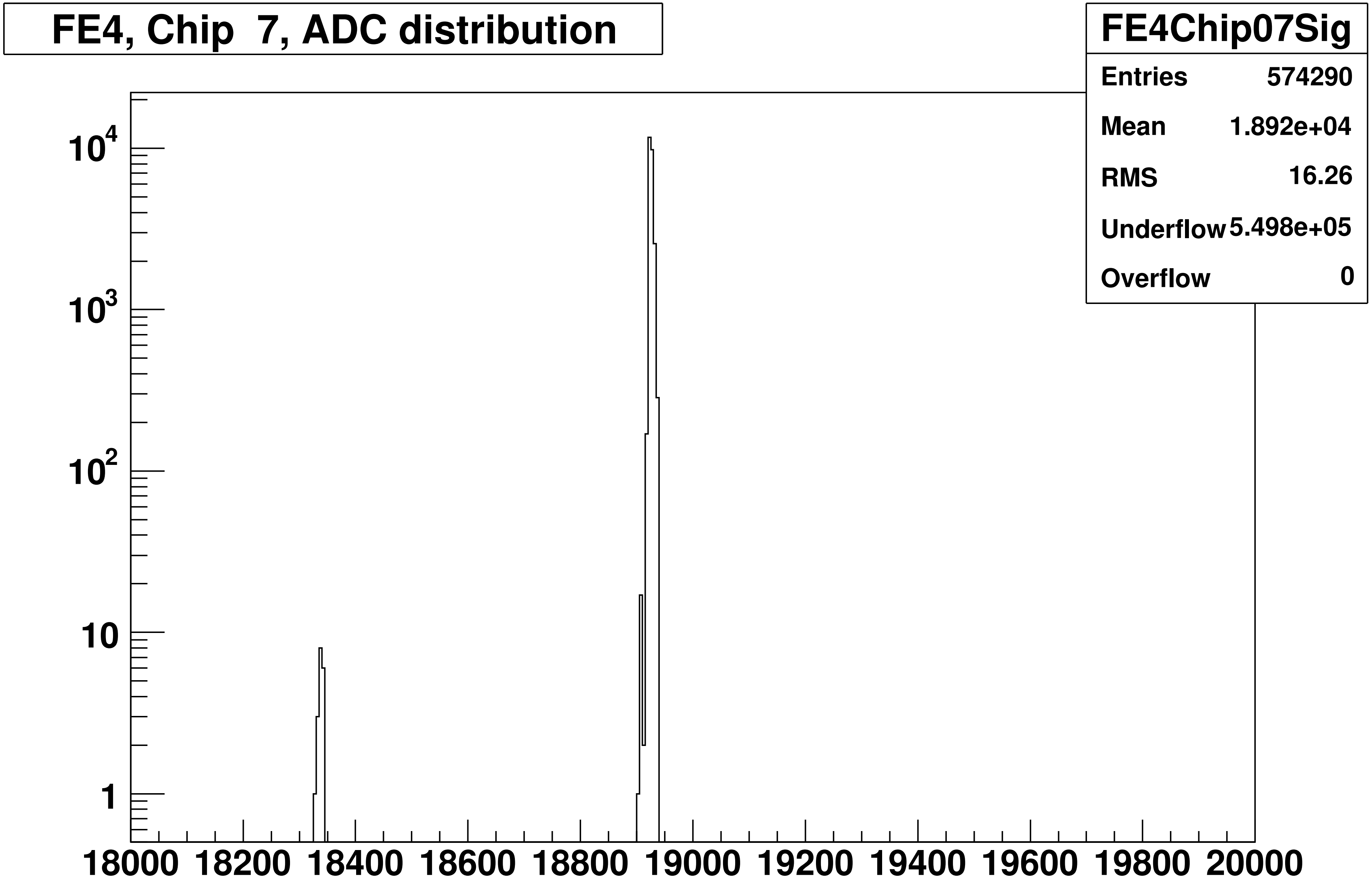
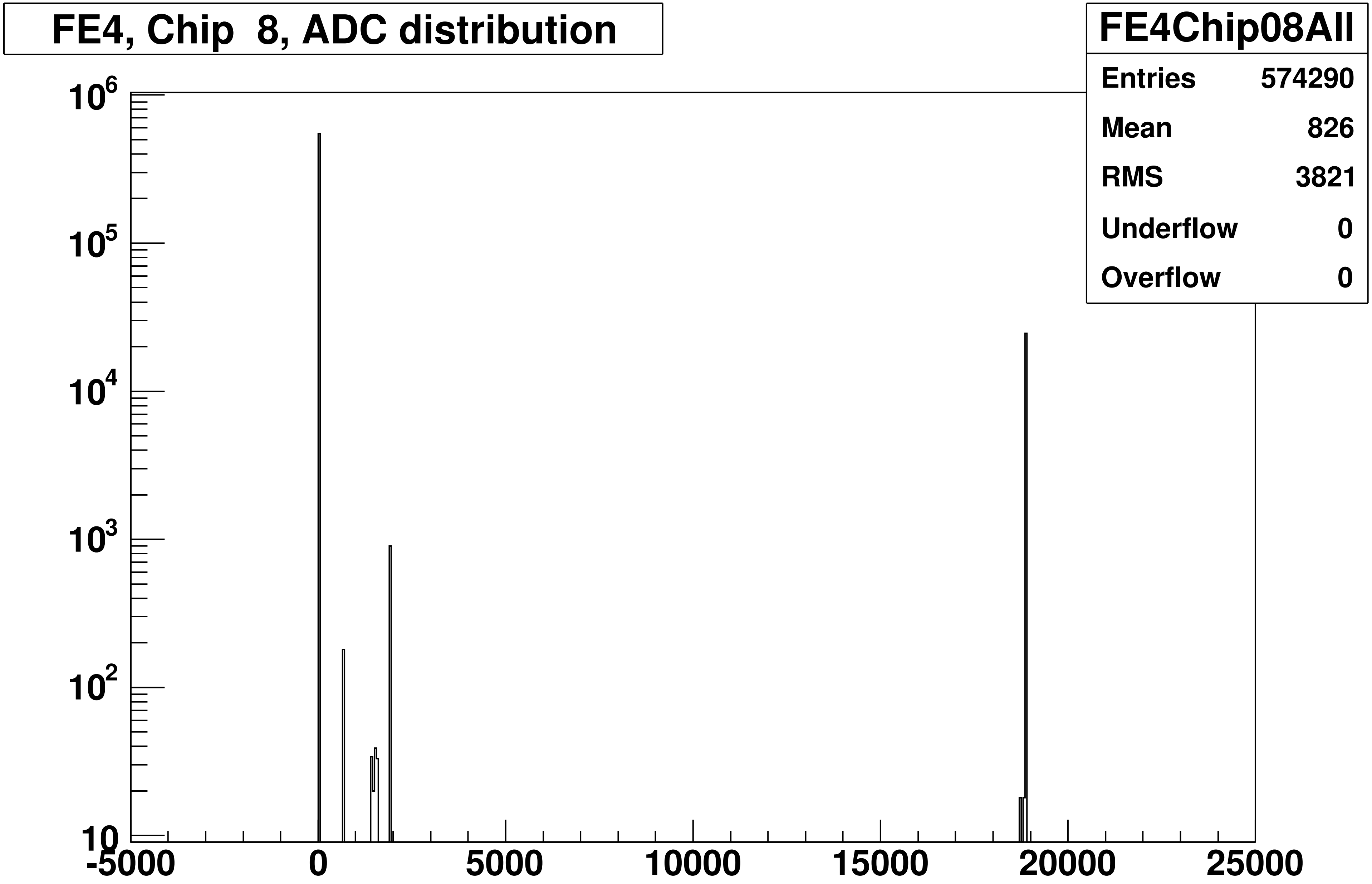
0
50
100
150
200



FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 574290
Mean 1.892e+04
RMS 16.26
Underflow 5.498e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 574290
Mean 826
RMS 3821
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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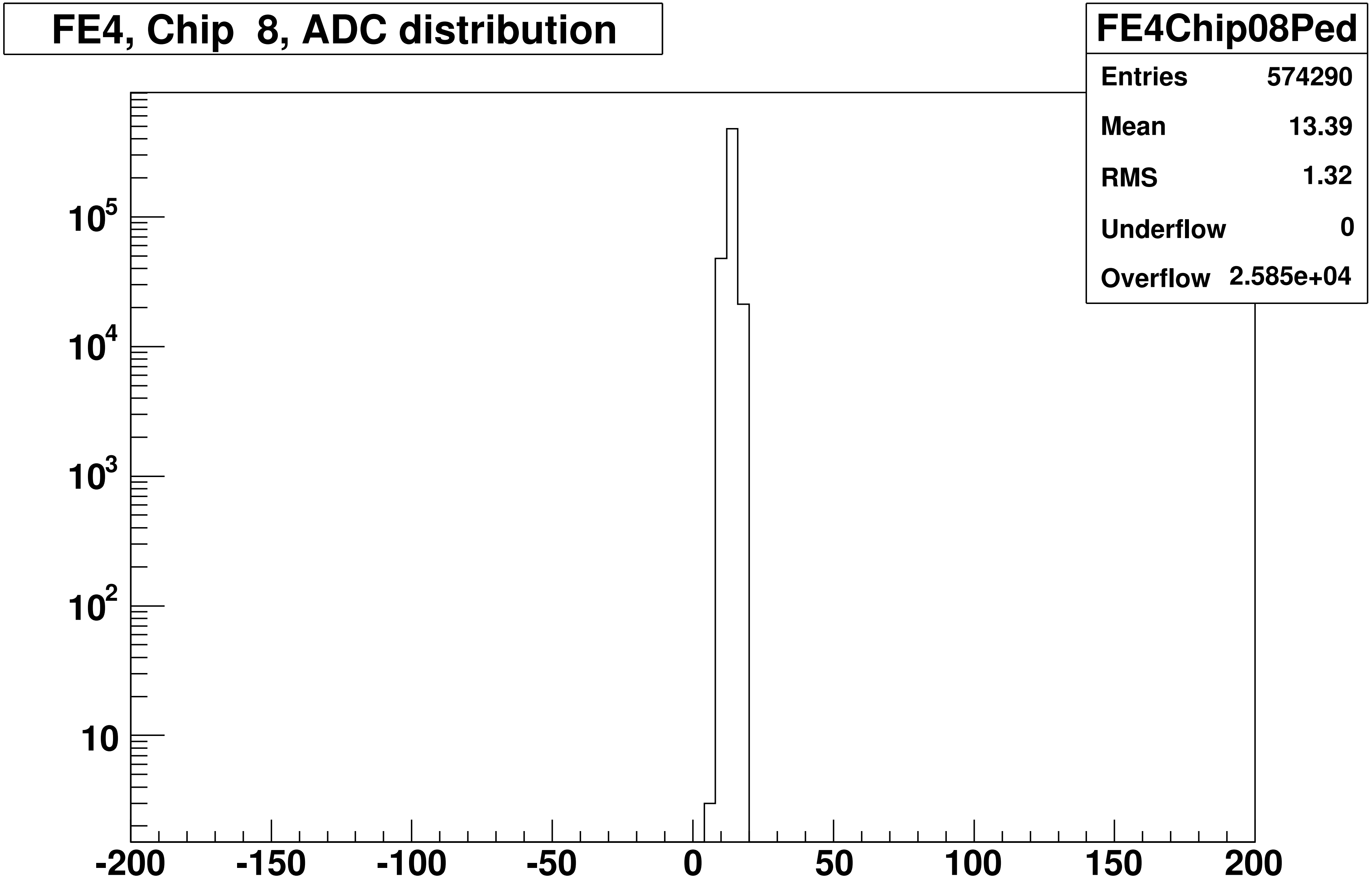
15000
20000
25000


FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 574290
Mean 13.39
RMS 1.32
Underflow 0
Overflow 2.585e+04
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10^4
10^3
10^2
10
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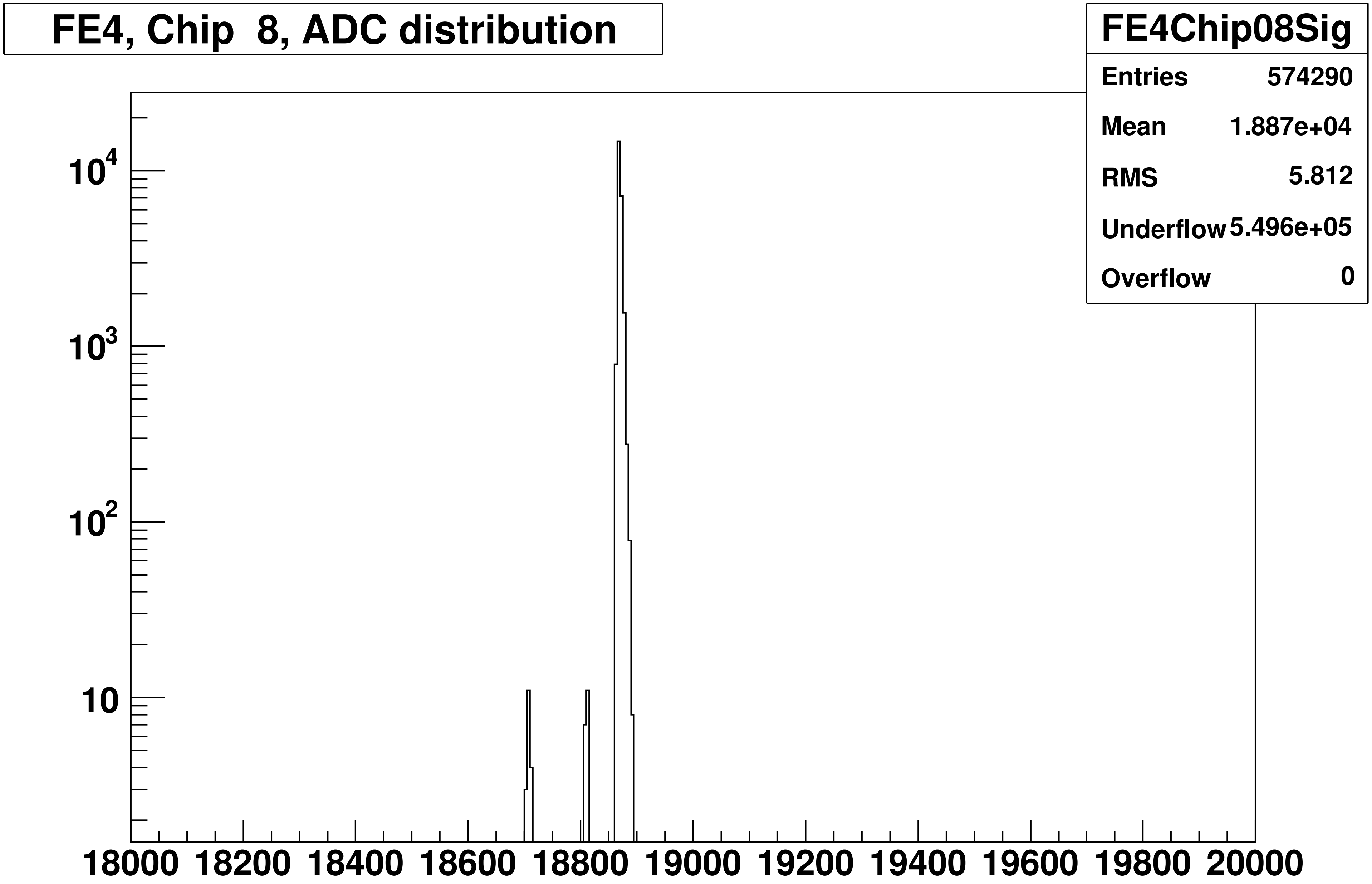
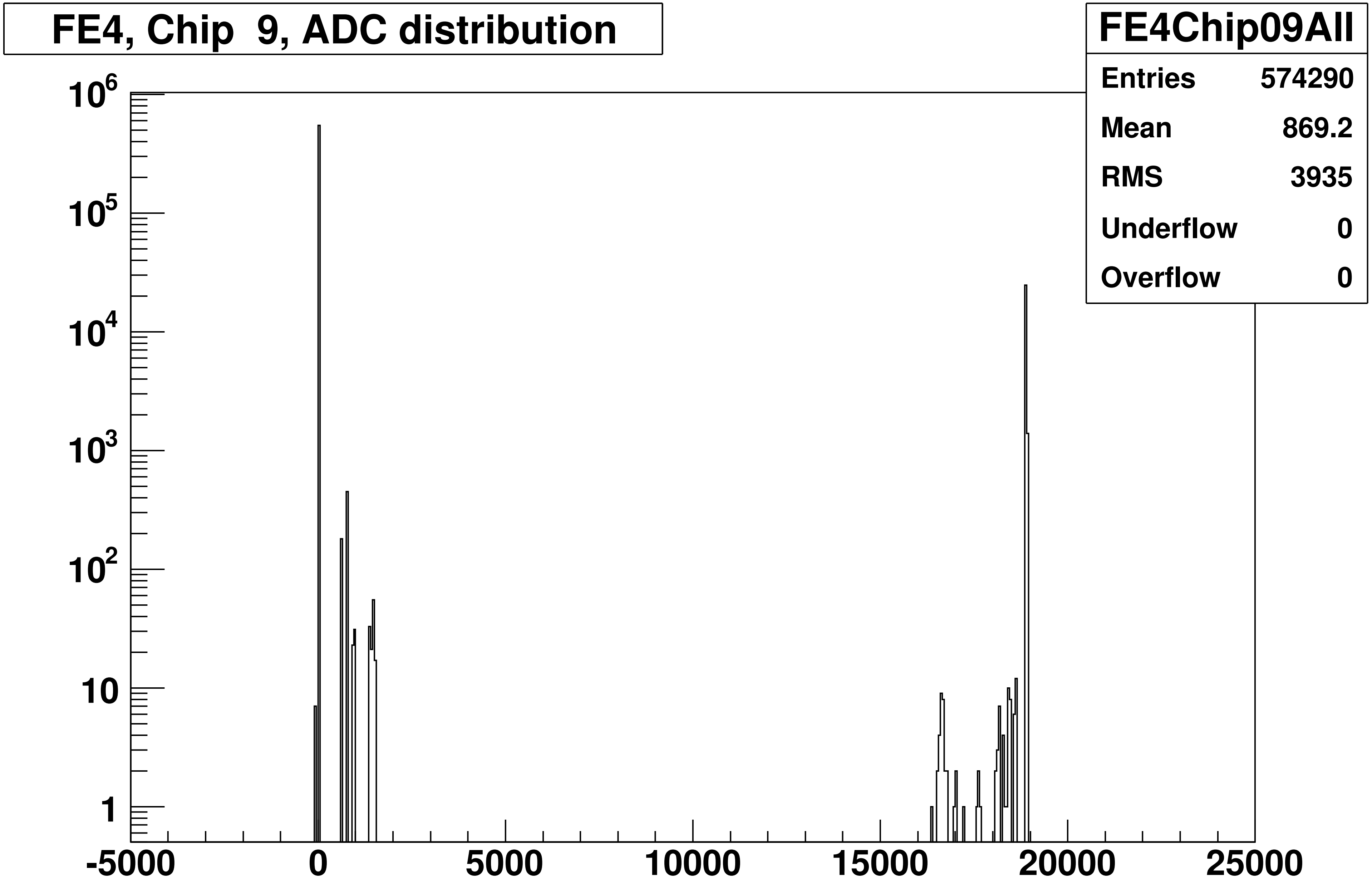
-150
-100
-50
0
50
100
150
200


FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 574290
Mean 1.887e+04
RMS 5.812
Underflow 5.496e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 574290
Mean 869.2
RMS 3935
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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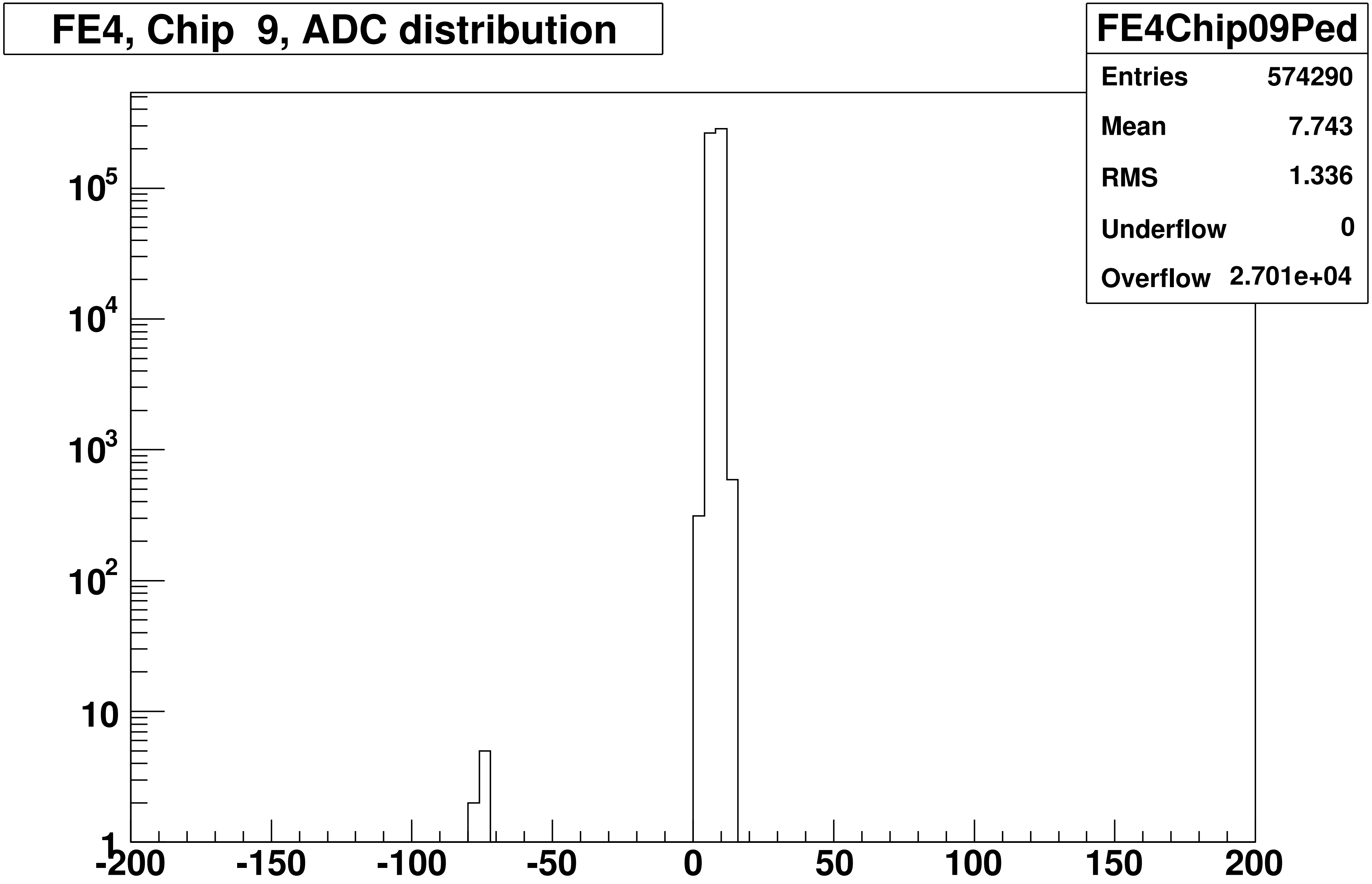
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 574290
Mean 7.743
RMS 1.336
Underflow 0
Overflow 2.701e+04
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10^3
10^2
10
1
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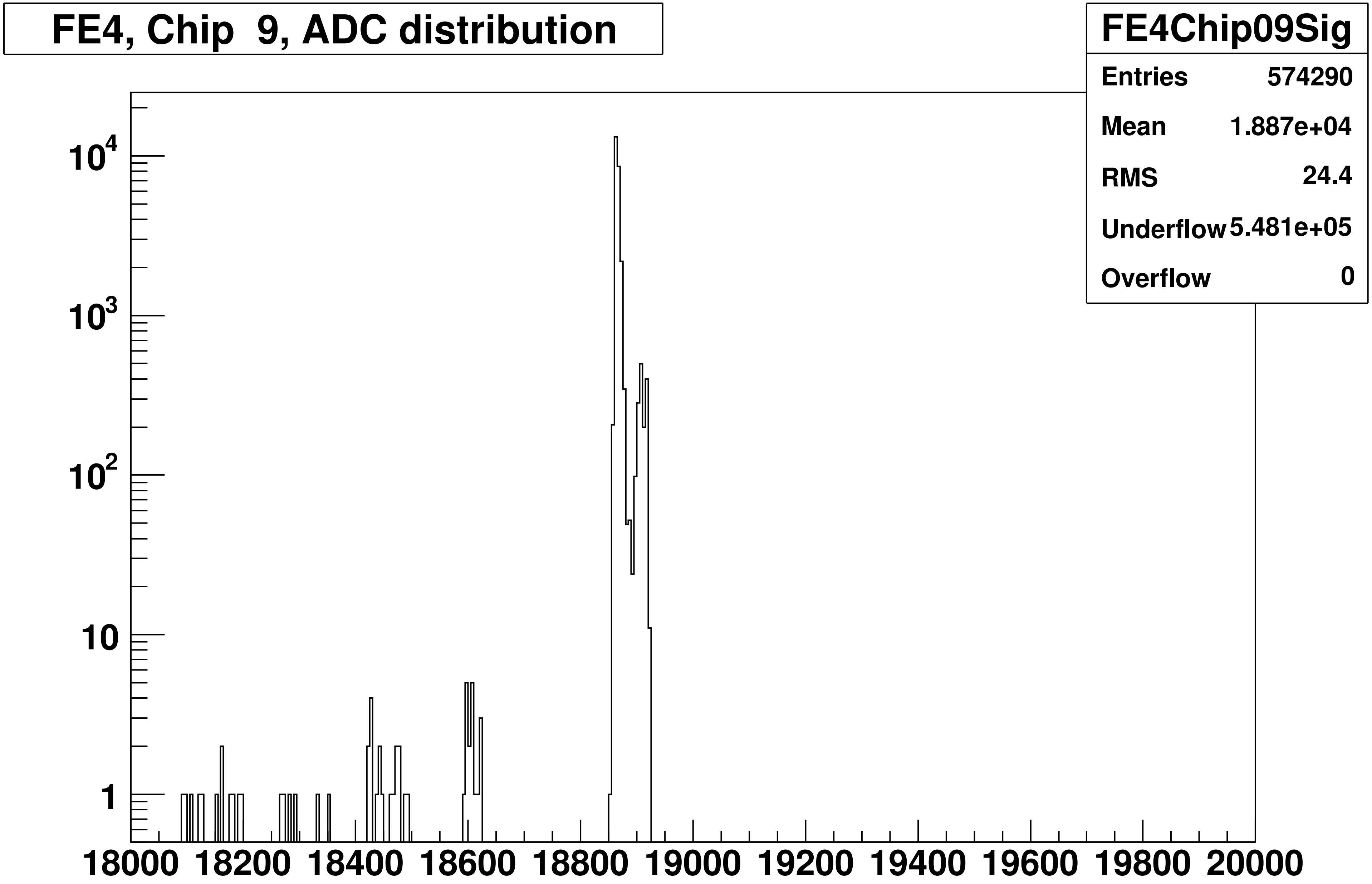
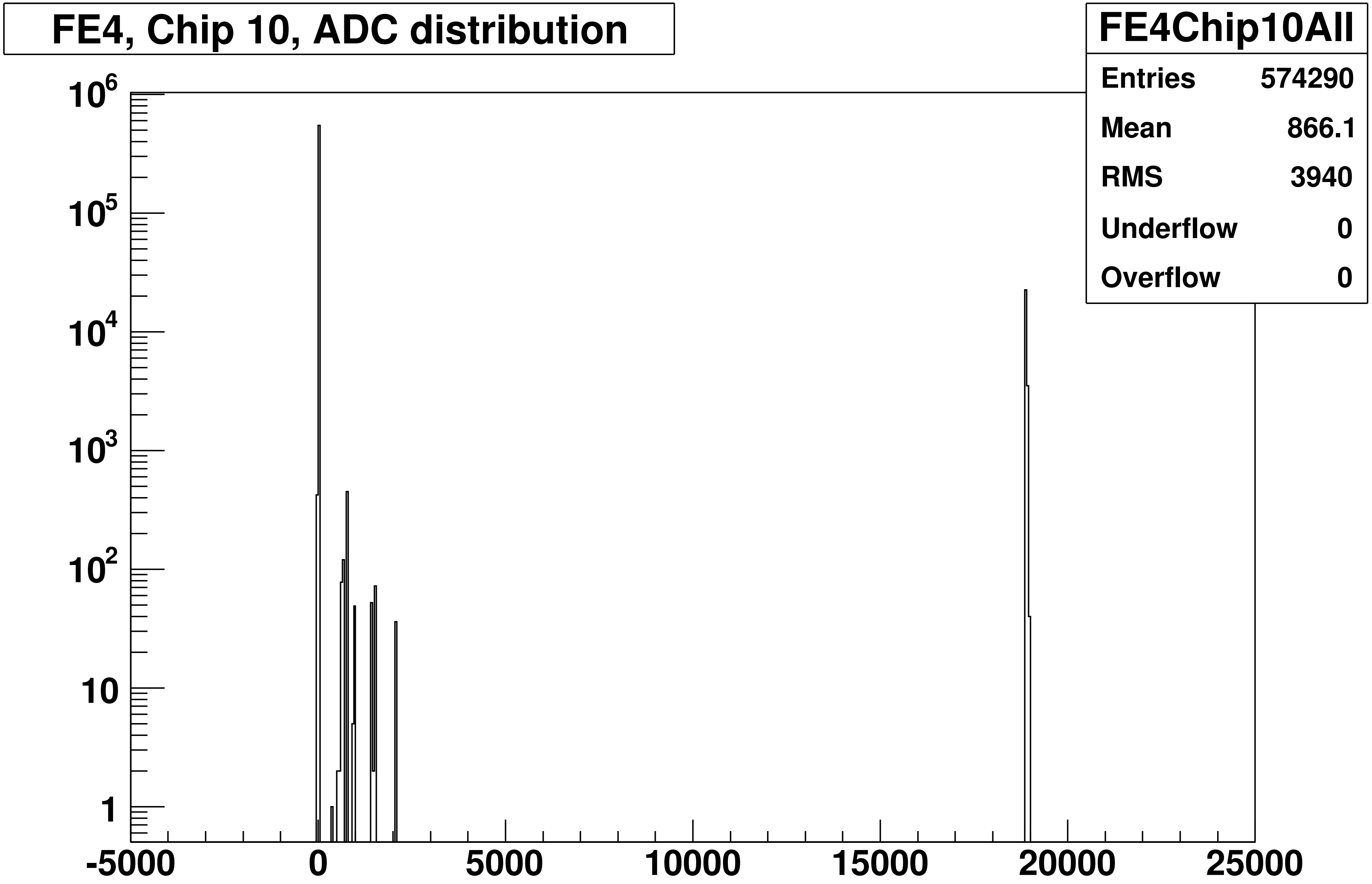
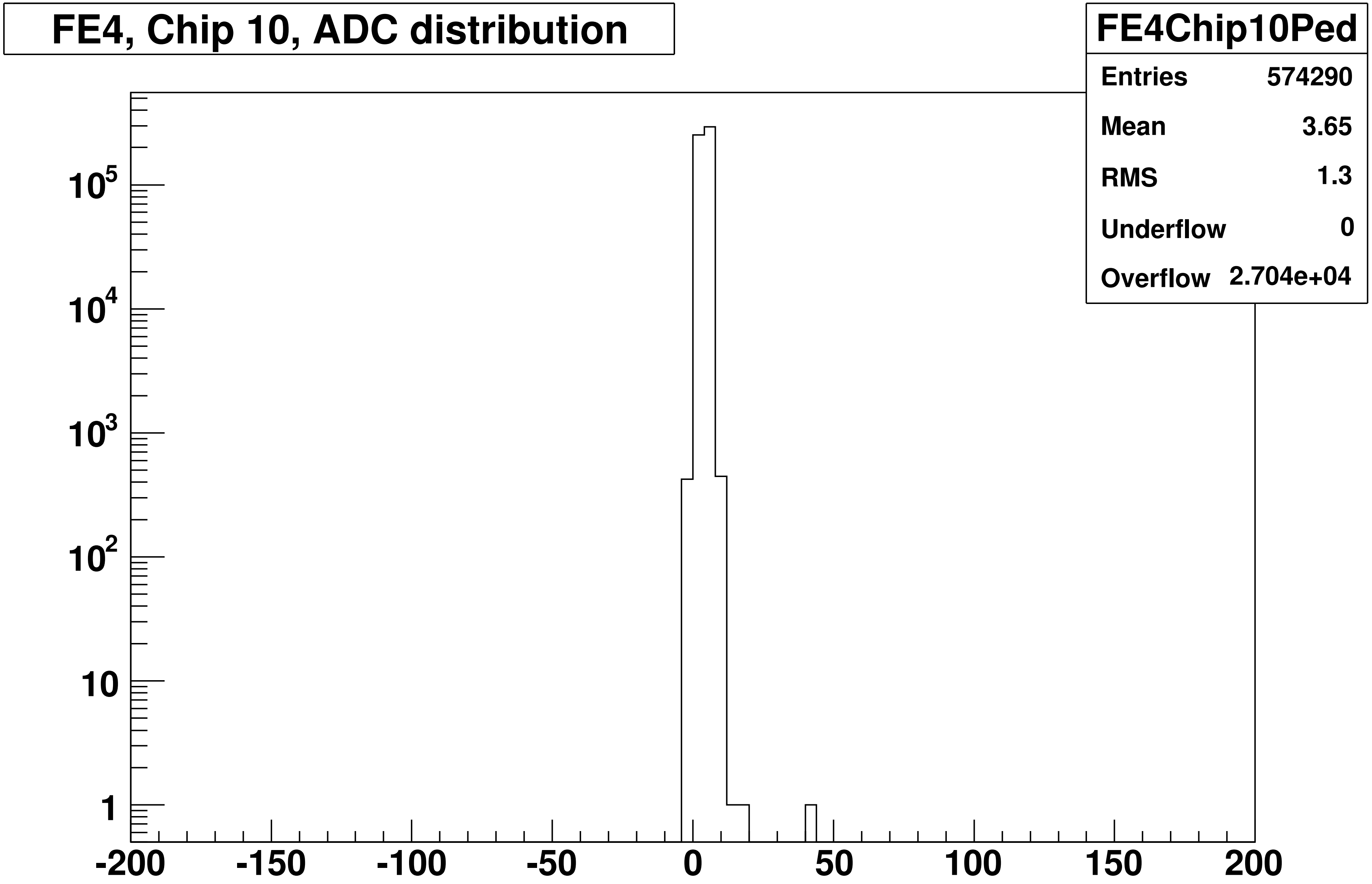
100
150
200


FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 574290
Mean 1.887e+04
RMS 24.4
Underflow 5.481e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 574290
Mean 866.1
RMS 3940
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 574290
Mean 3.65
RMS 1.3
Underflow 0
Overflow 2.704e+04
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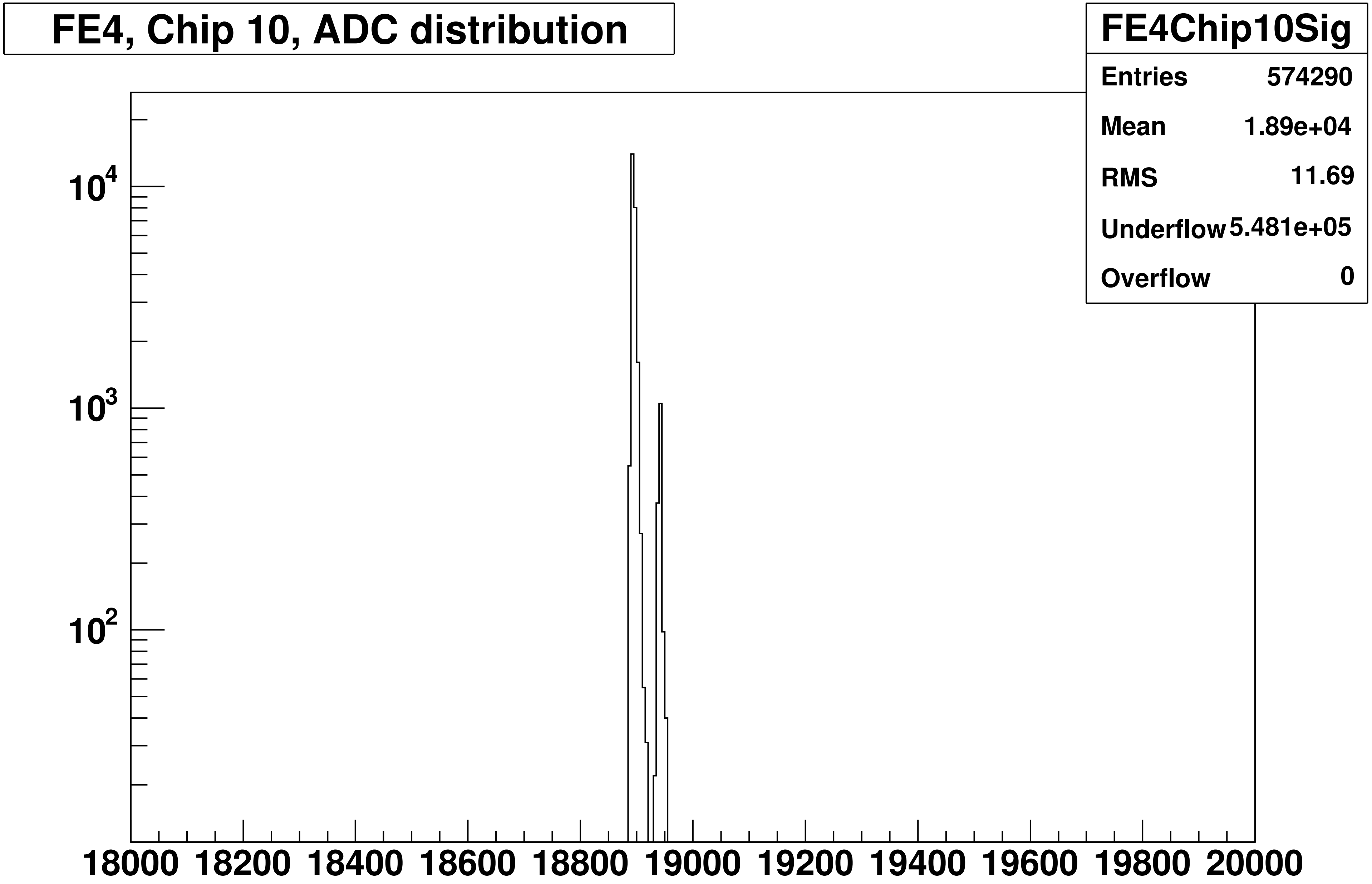
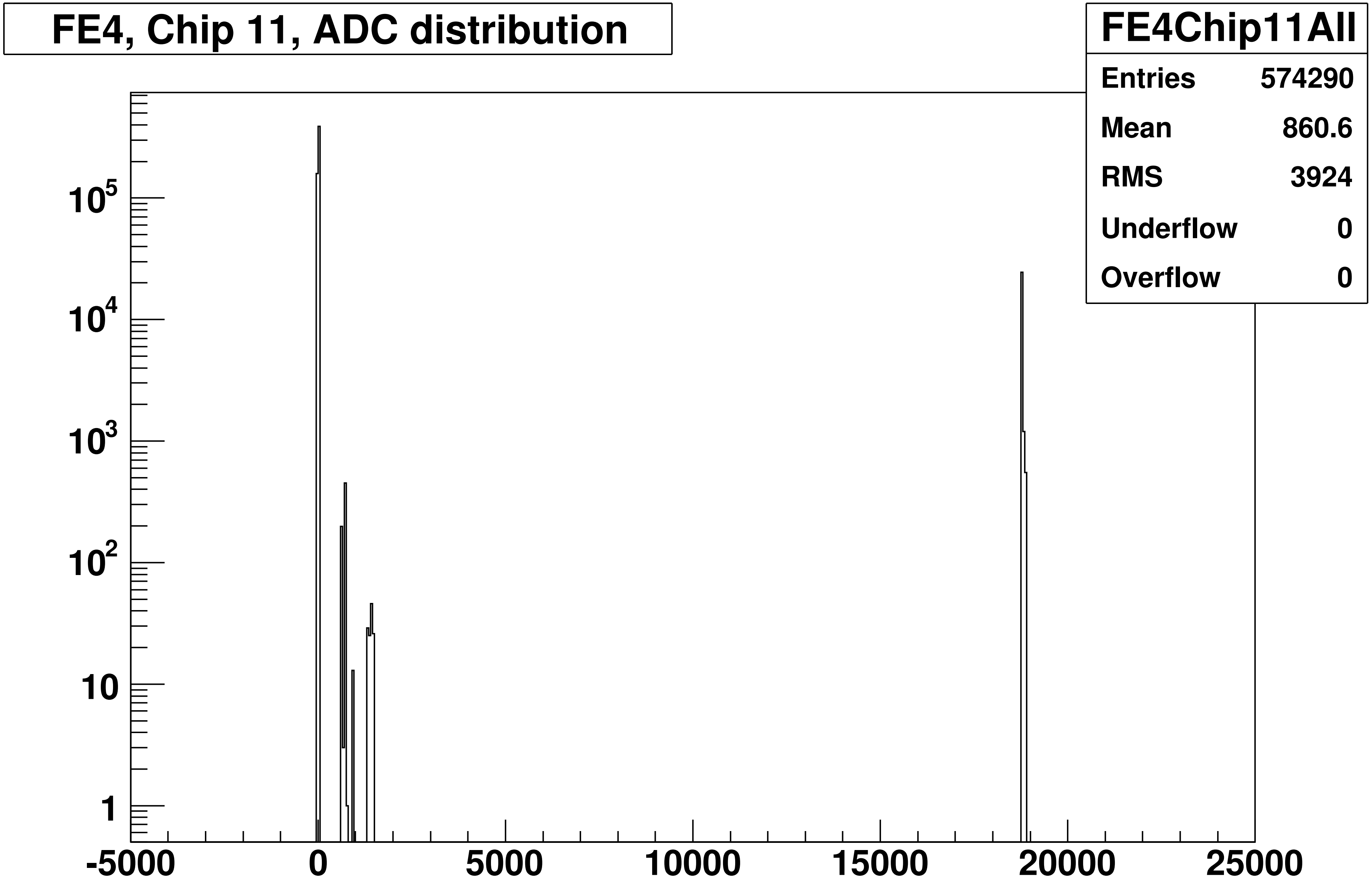
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 574290
Mean 1.89e+04
RMS 11.69
Underflow 5.481e+05
Overflow 0
10^4
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 574290
Mean 860.6
RMS 3924
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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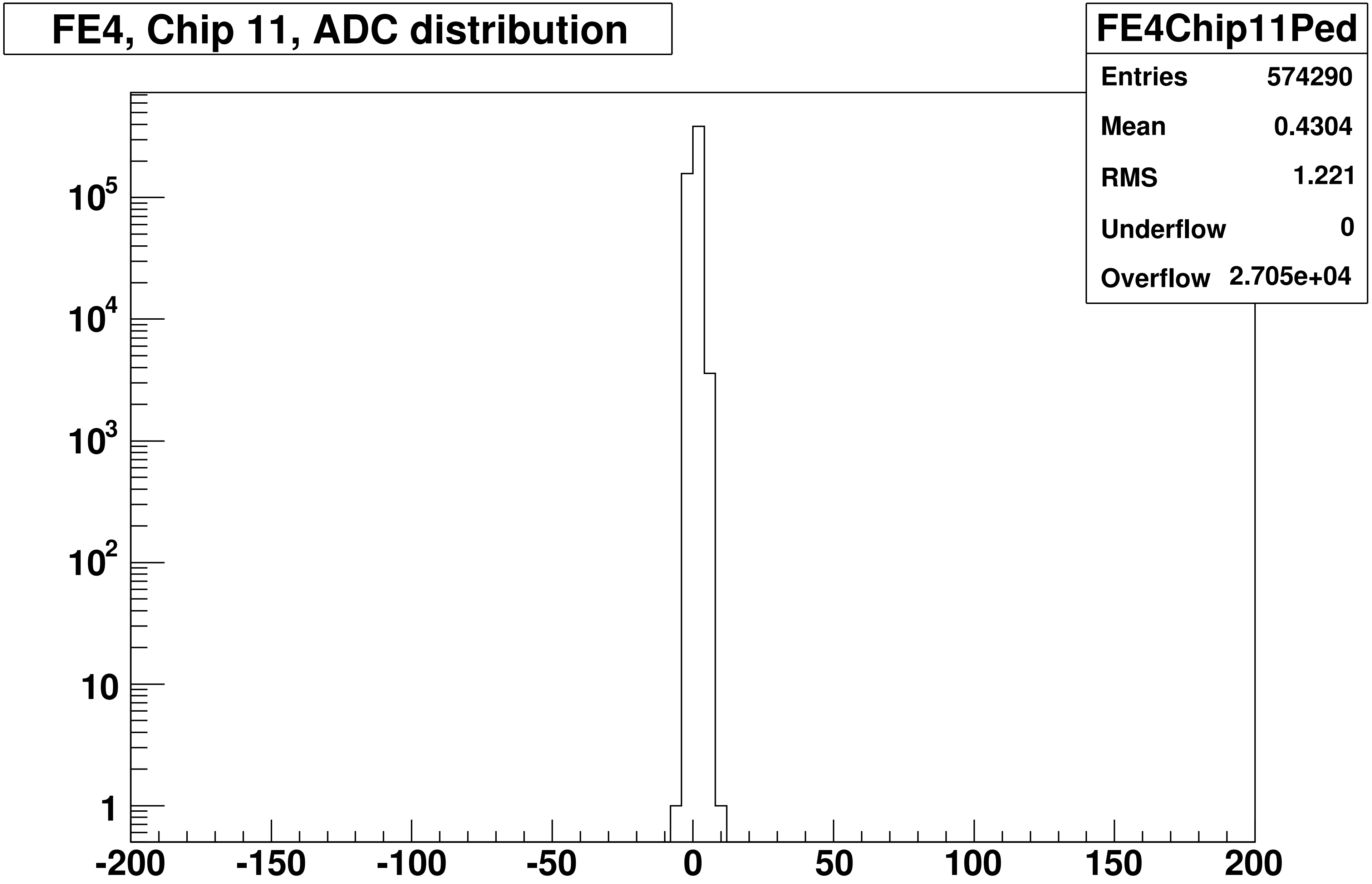
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 574290
Mean 0.4304
RMS 1.221
Underflow 0
Overflow 2.705e+04
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10^4
10^3
10^2
10
1
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-150
-100
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0
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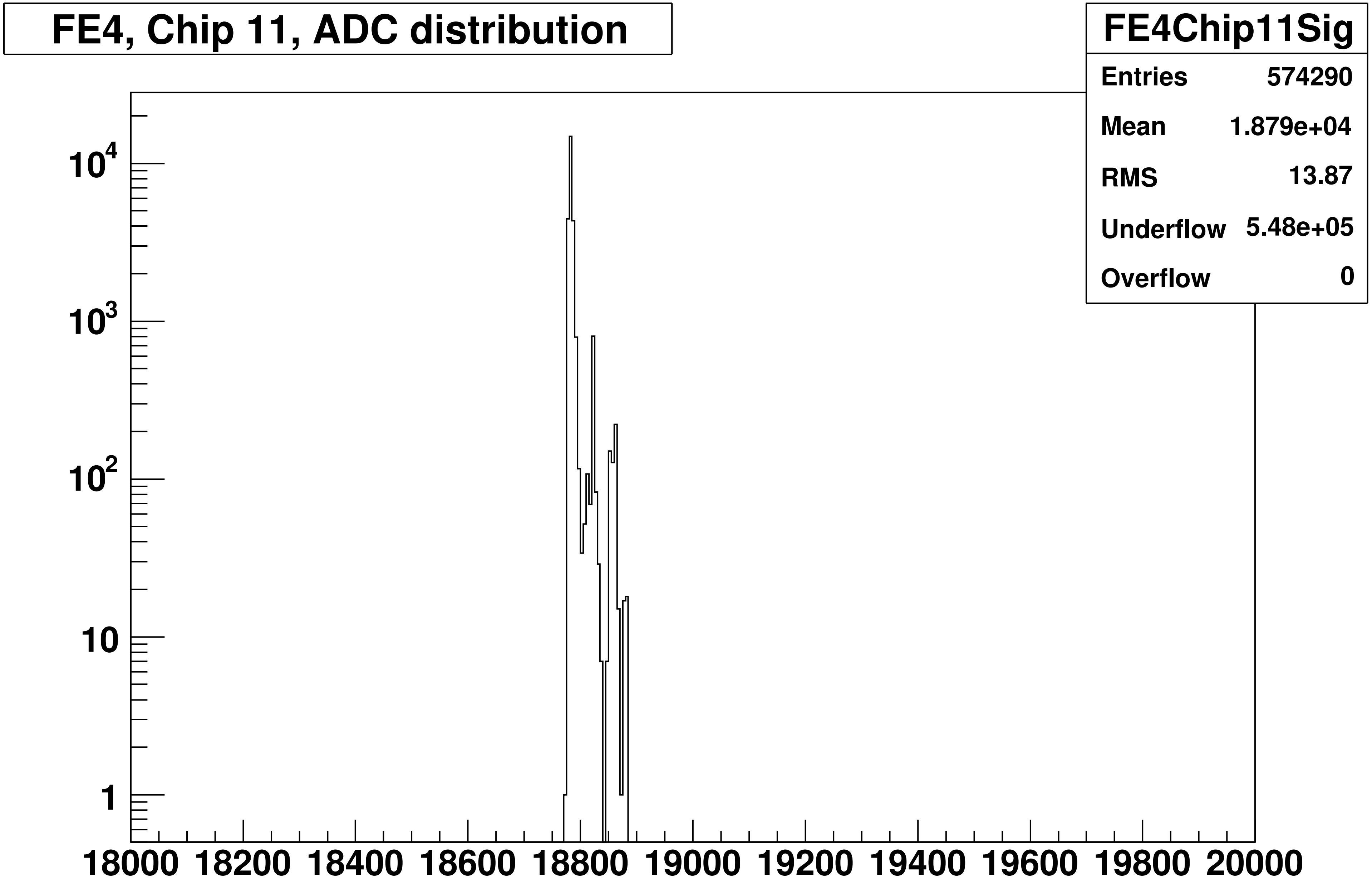
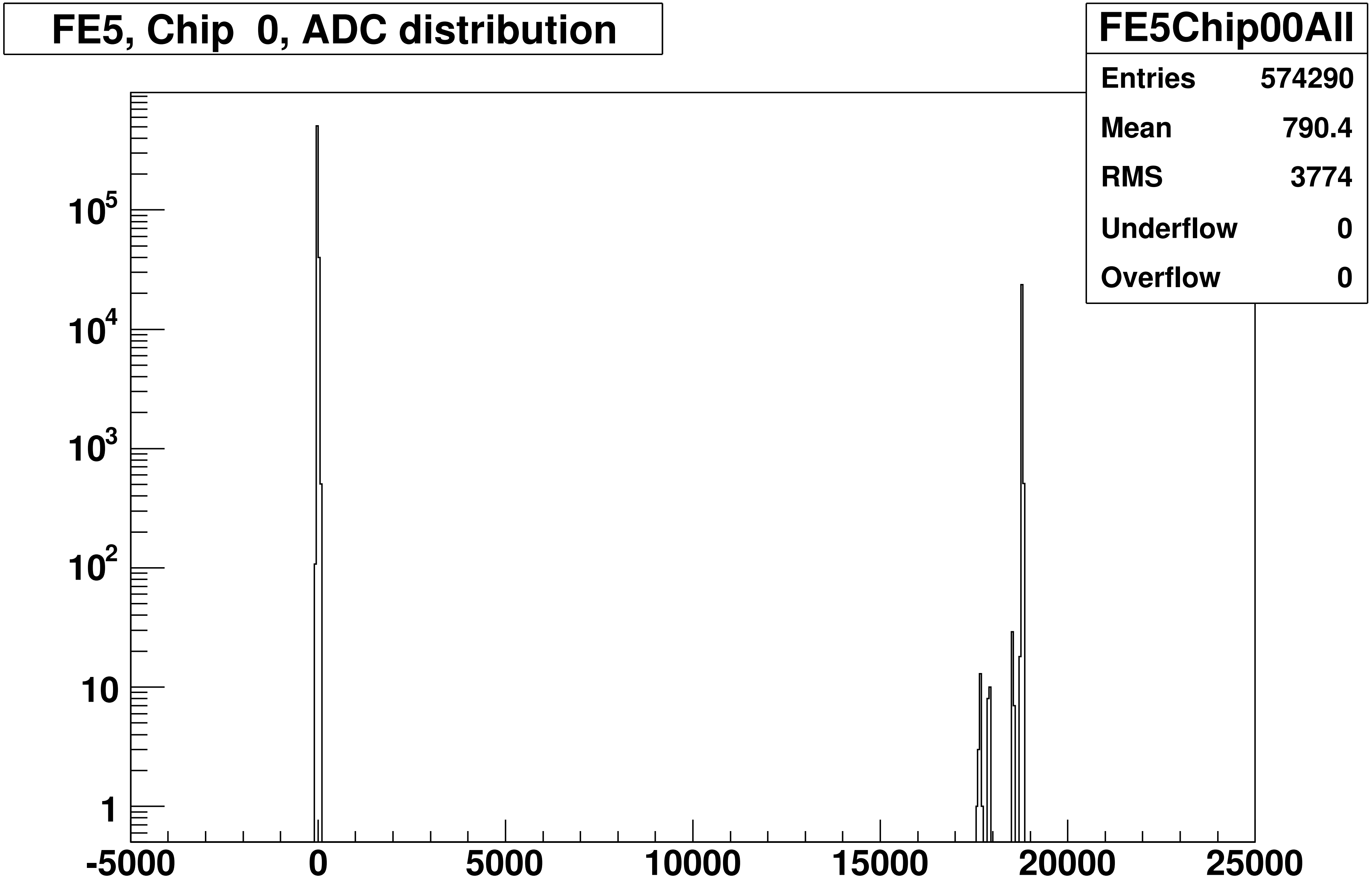
100
150
200



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 574290
Mean 1.879e+04
RMS 13.87
Underflow 5.48e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 574290
Mean 790.4
RMS 3774
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
0
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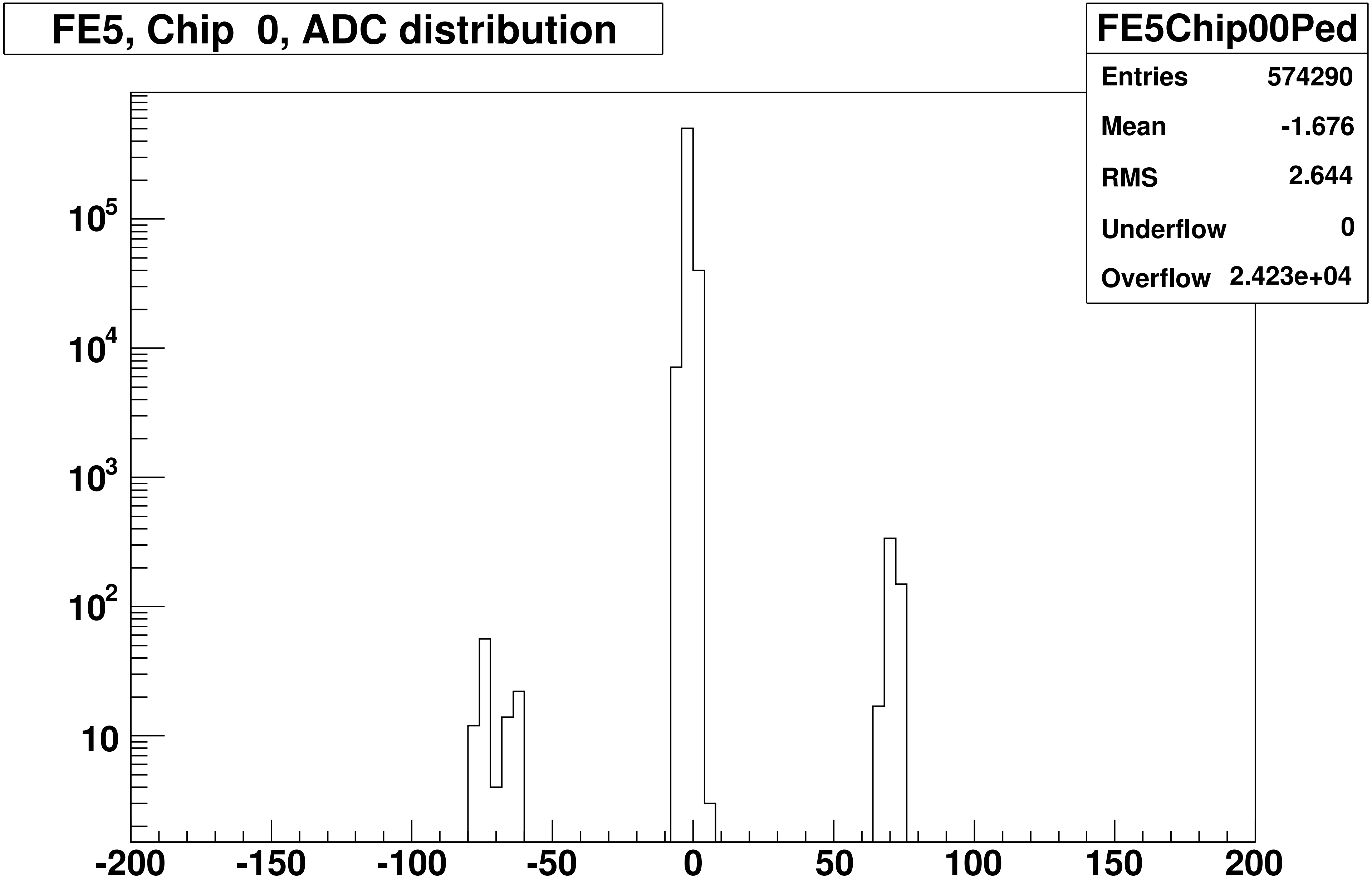
10000
15000
20000
25000


FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 574290
Mean -1.676
RMS 2.644
Underflow 0
Overflow 2.423e+04
10
10^2
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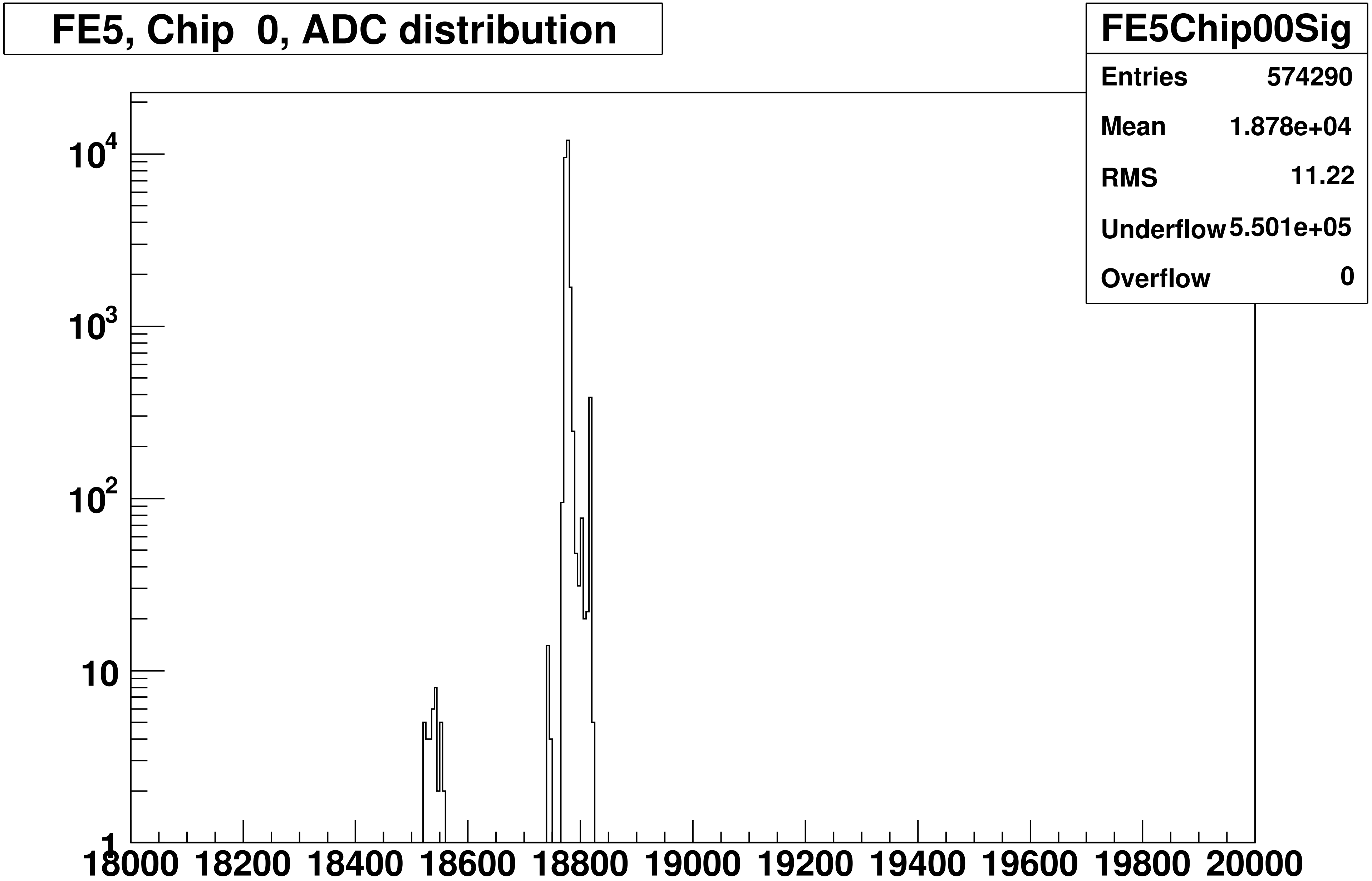
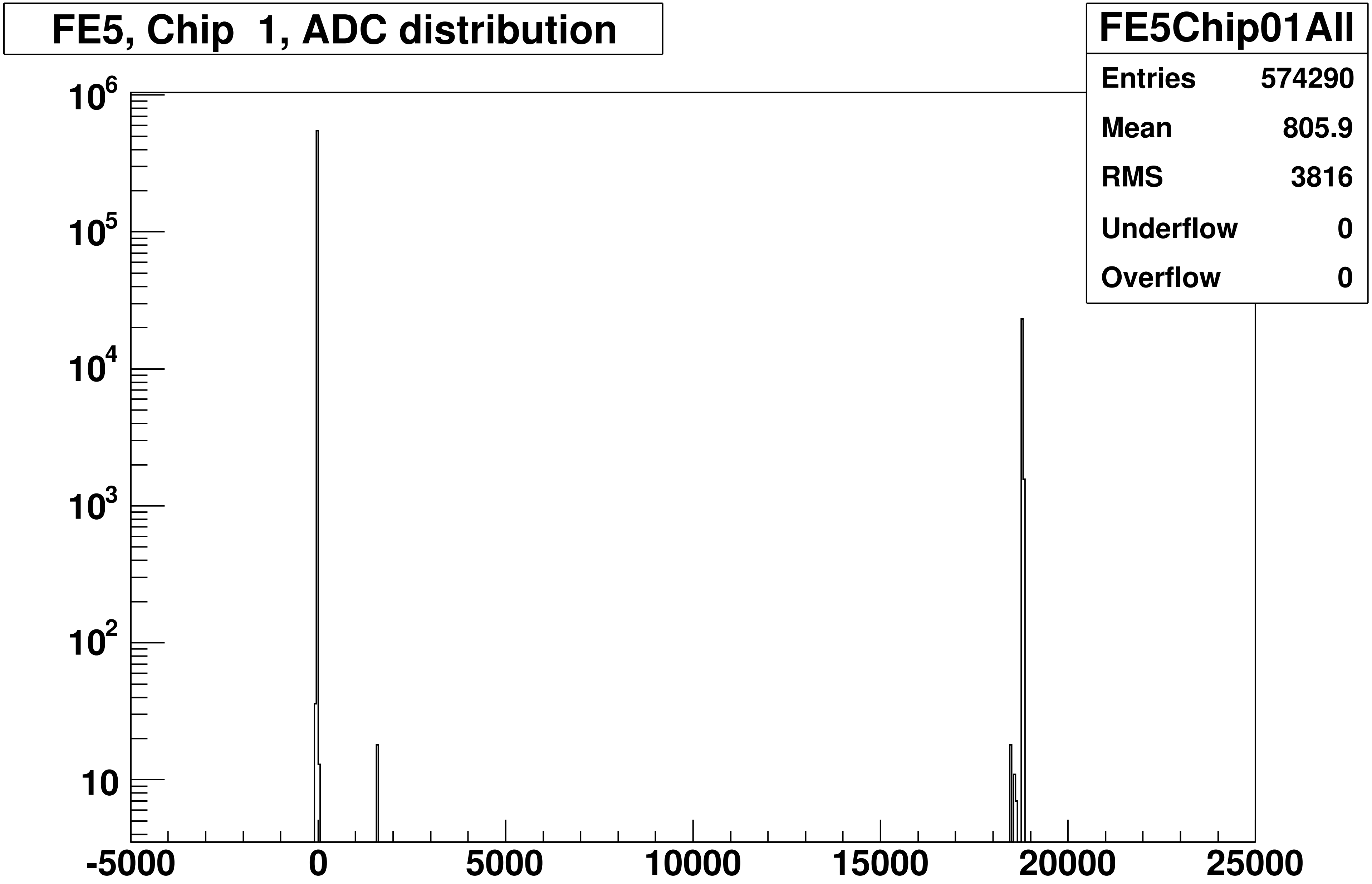
10^4
10^5
-200 -150 -100 -50 0 50 100 150 200


FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 574290
Mean 1.878e+04
RMS 11.22
Underflow 5.501e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 574290
Mean 805.9
RMS 3816
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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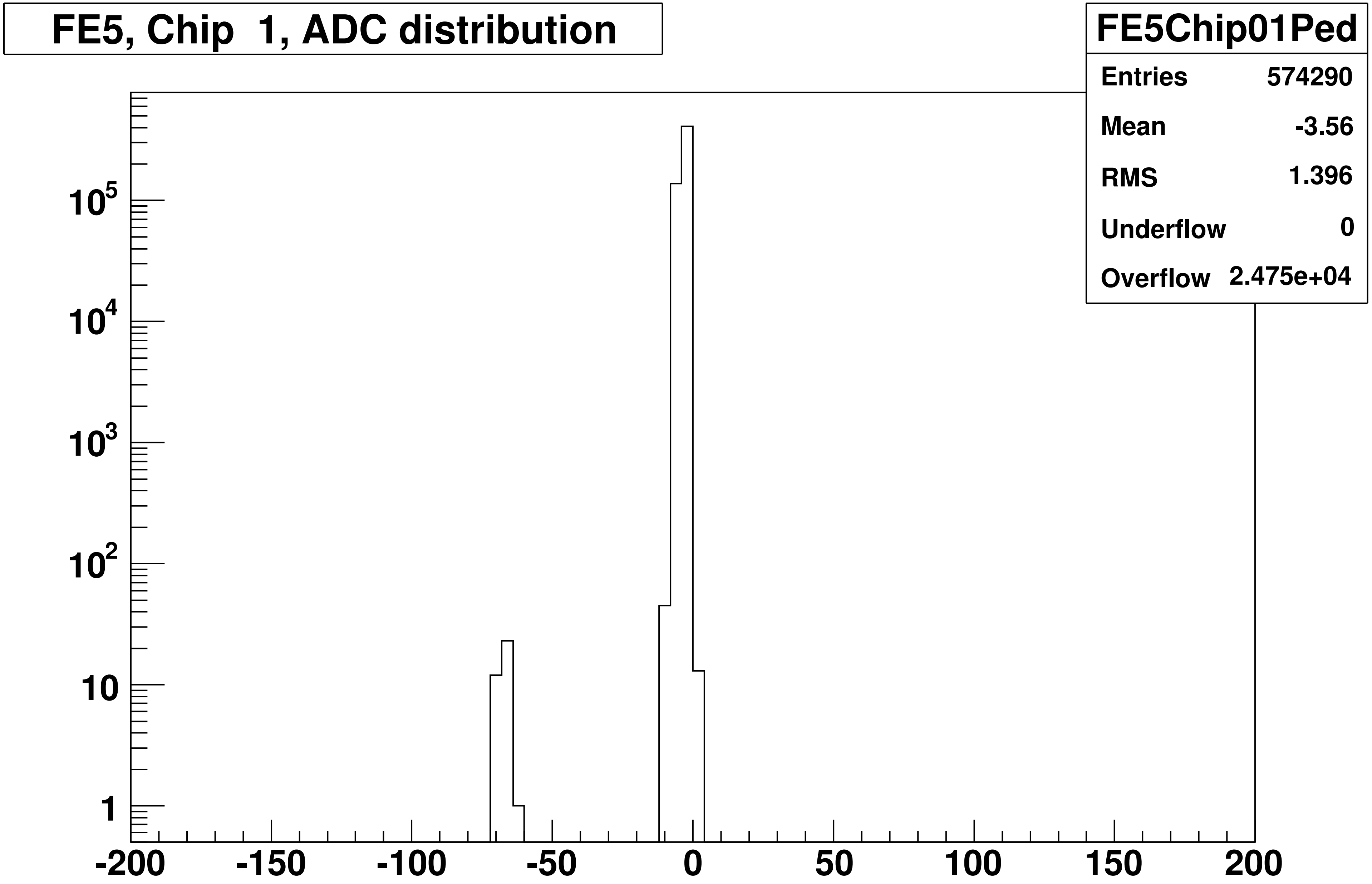
10000
15000
20000
25000



FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 574290
Mean -3.56
RMS 1.396
Underflow 0
Overflow 2.475e+04
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10^3
10^2
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-100
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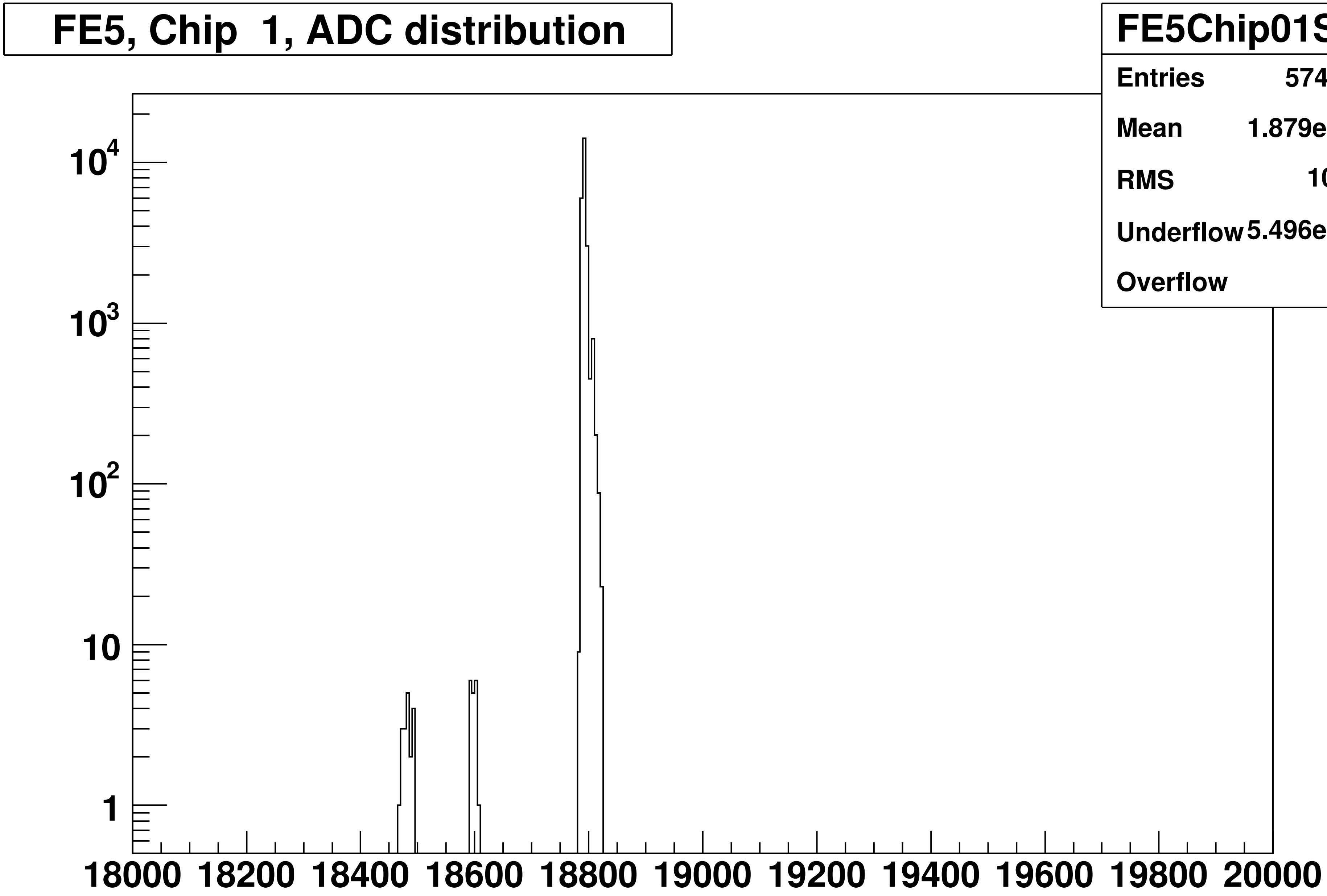
100
150
200


FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 574290
Mean 1.879e+04
RMS 10.96
Underflow 5.496e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
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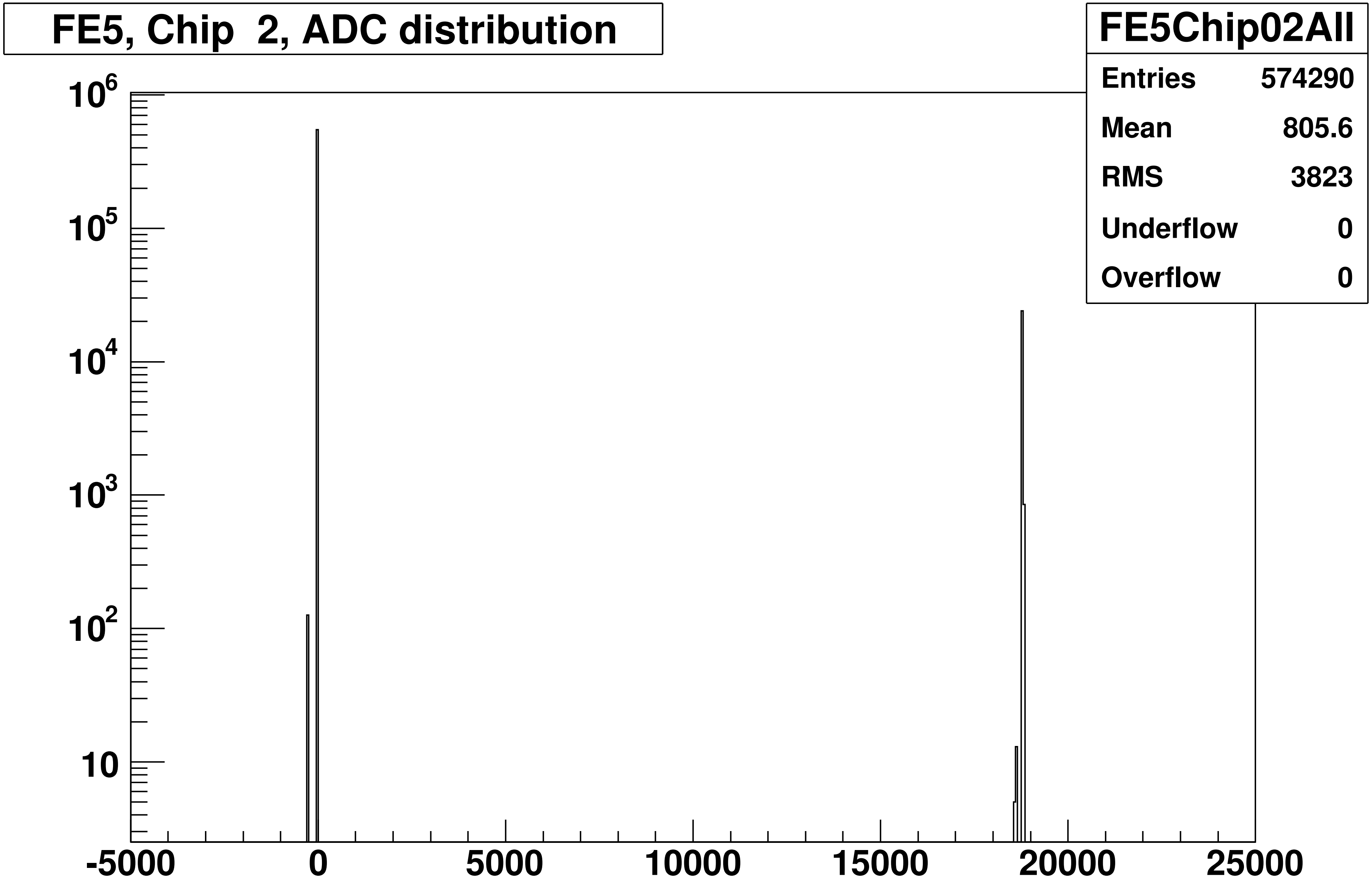
19000
19200
19400
19600
19800
20000



FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 574290
Mean 805.6
RMS 3823
Underflow 0
Overflow 0
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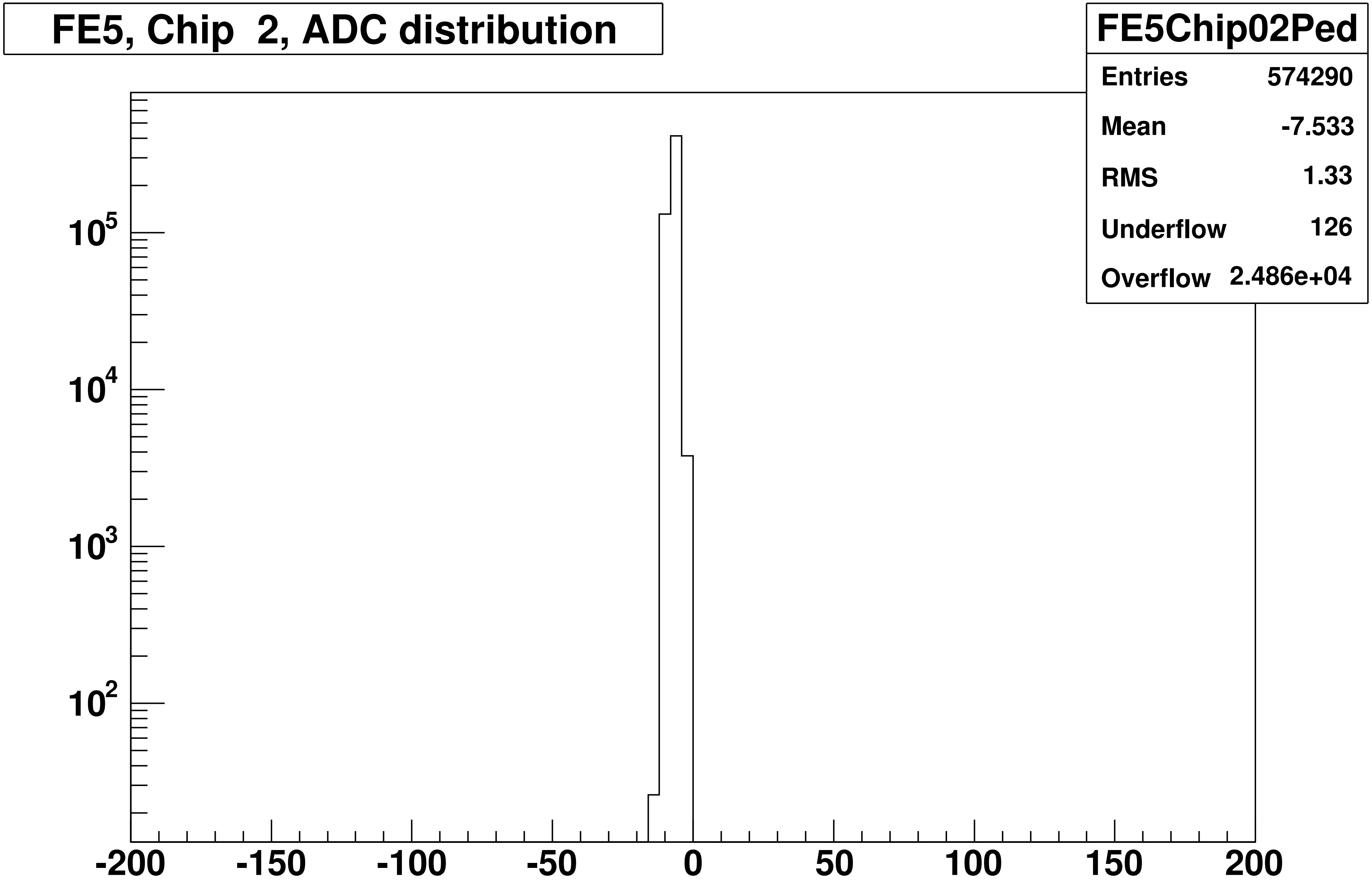
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 574290
Mean -7.533
RMS 1.33
Underflow 126
Overflow 2.486e+04
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10^4
10^3
10^2
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-100
-50
0
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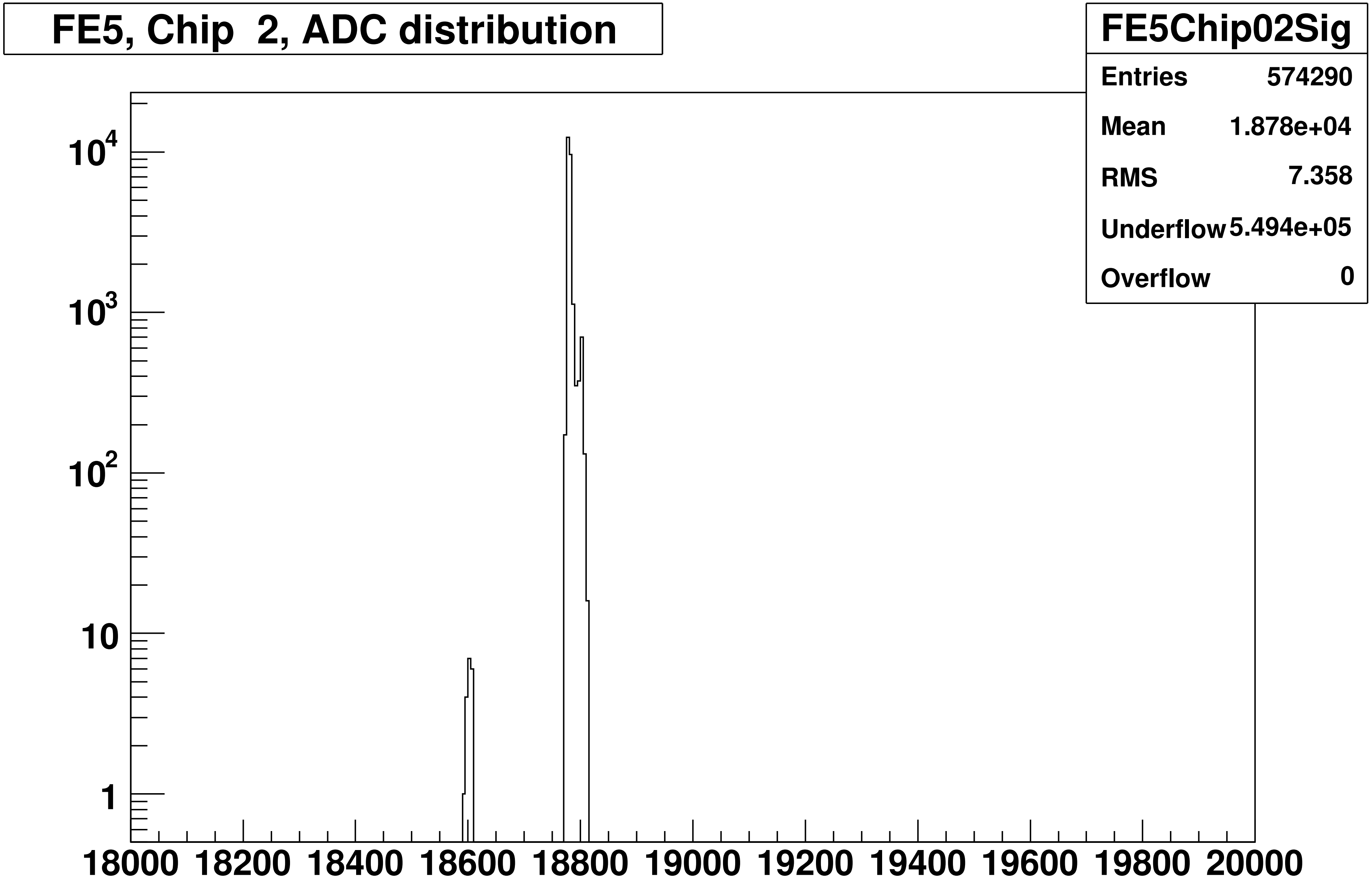
100
150
200


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 574290
Mean 1.878e+04
RMS 7.358
Underflow 5.494e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
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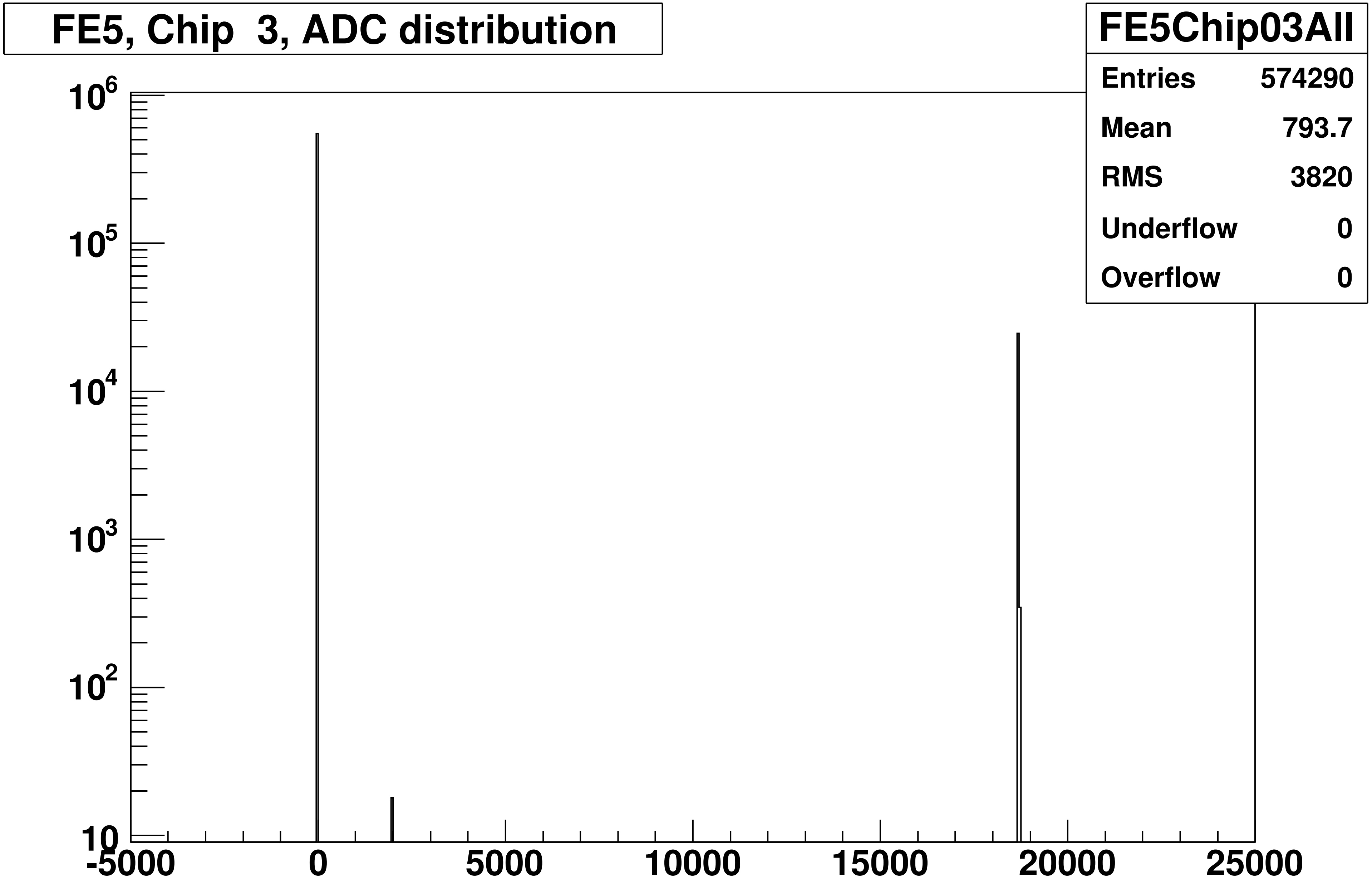
19600
19800
20000



FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 574290
Mean 793.7
RMS 3820
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
0
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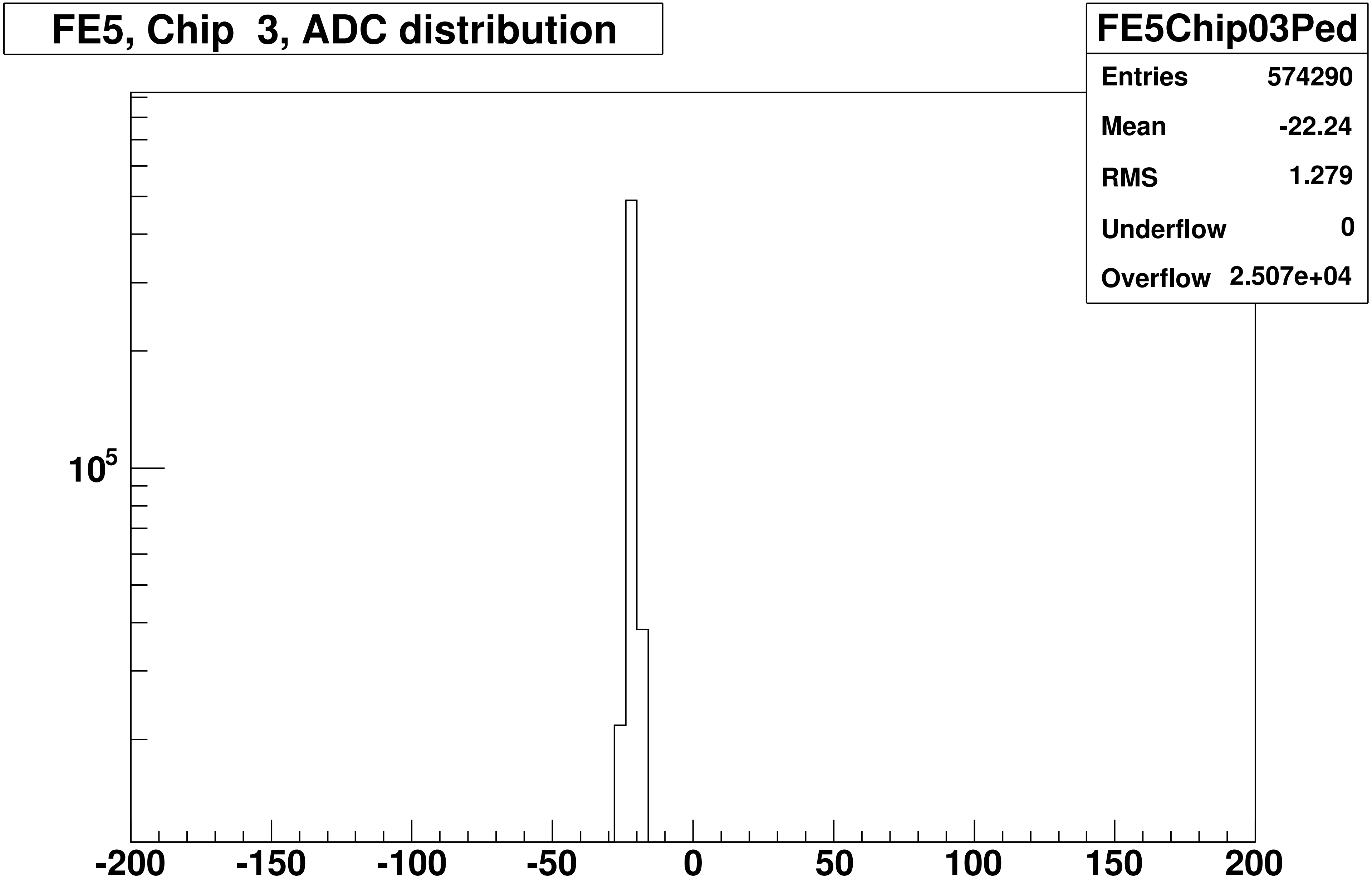
10000
15000
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 574290
Mean -22.24
RMS 1.279
Underflow 0
Overflow 2.507e+04
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-150
-100
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0
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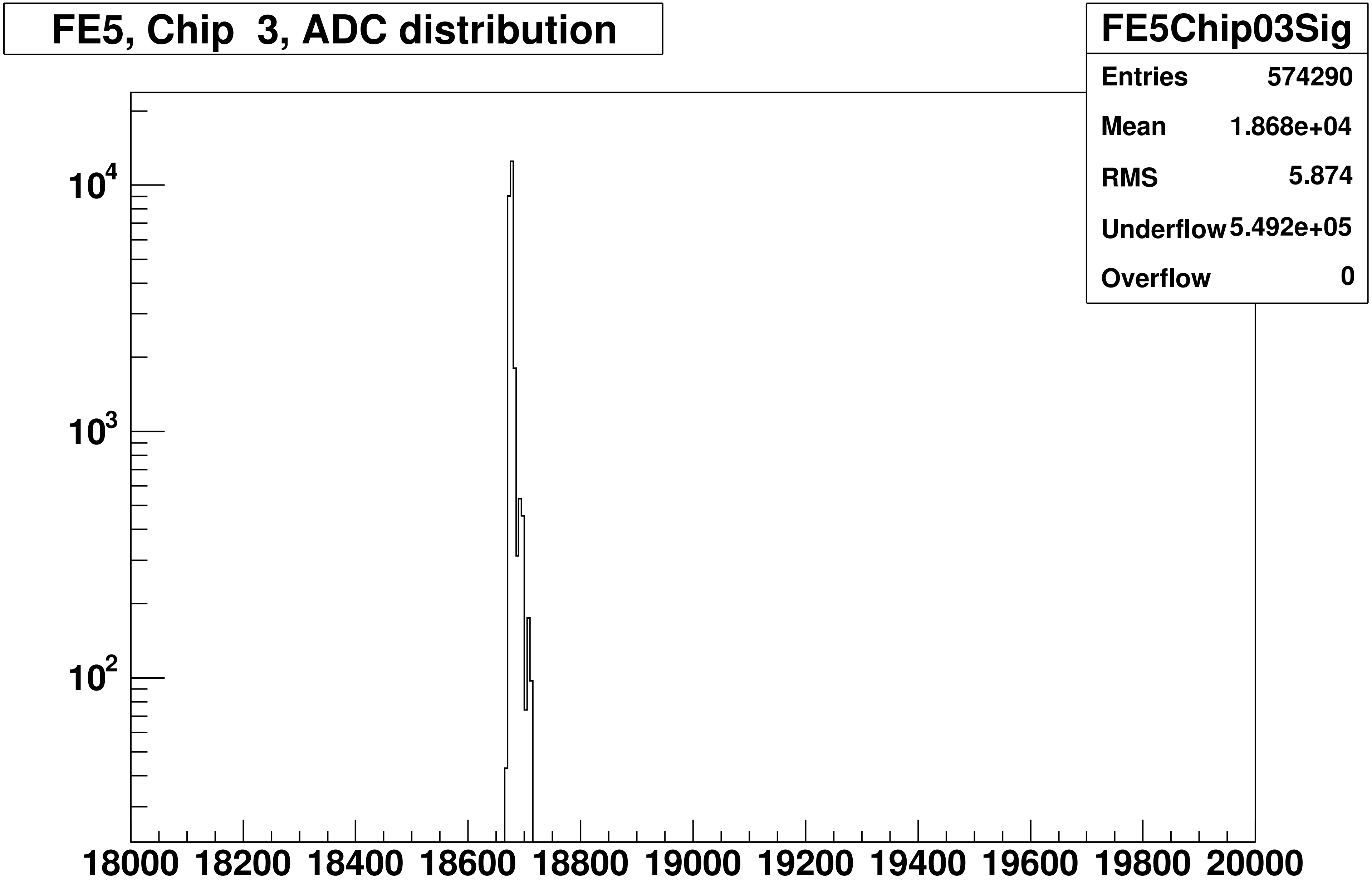
100
150
200


FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 574290
Mean 1.868e+04
RMS 5.874
Underflow 5.492e+05
Overflow 0
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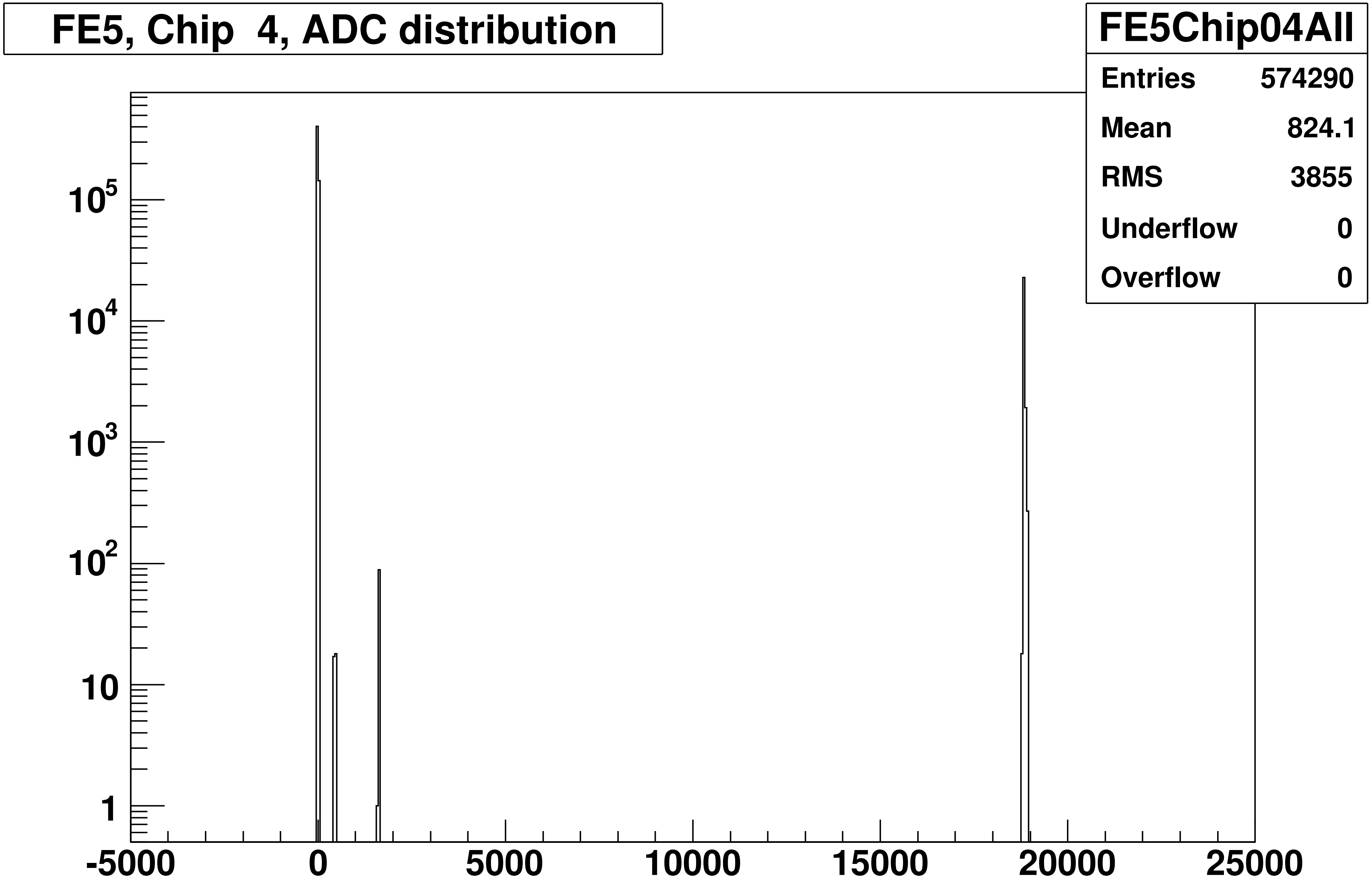
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 574290
Mean 824.1
RMS 3855
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
1
-5000
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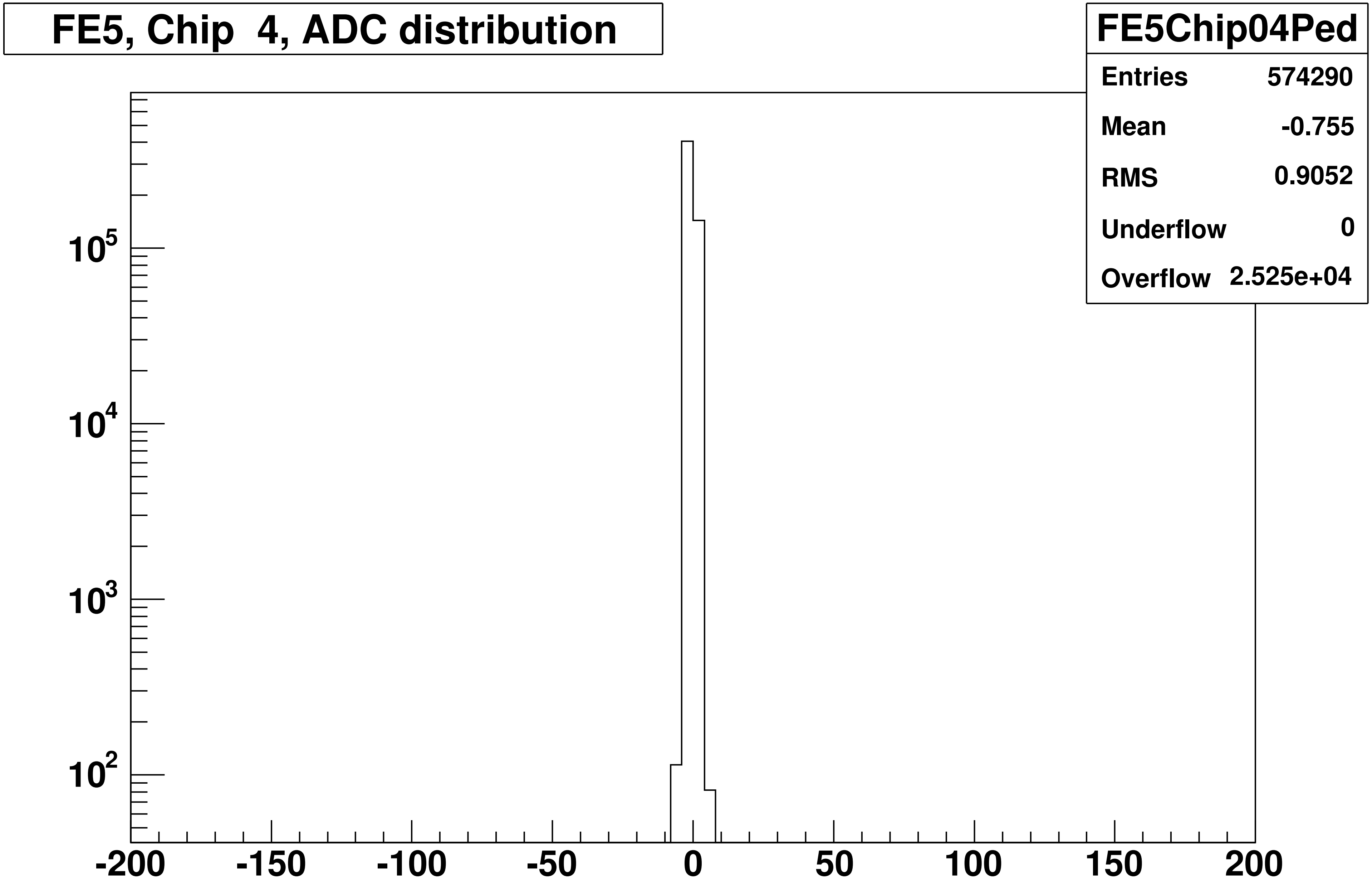
25000


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 574290
Mean -0.755
RMS 0.9052
Underflow 0
Overflow 2.525e+04
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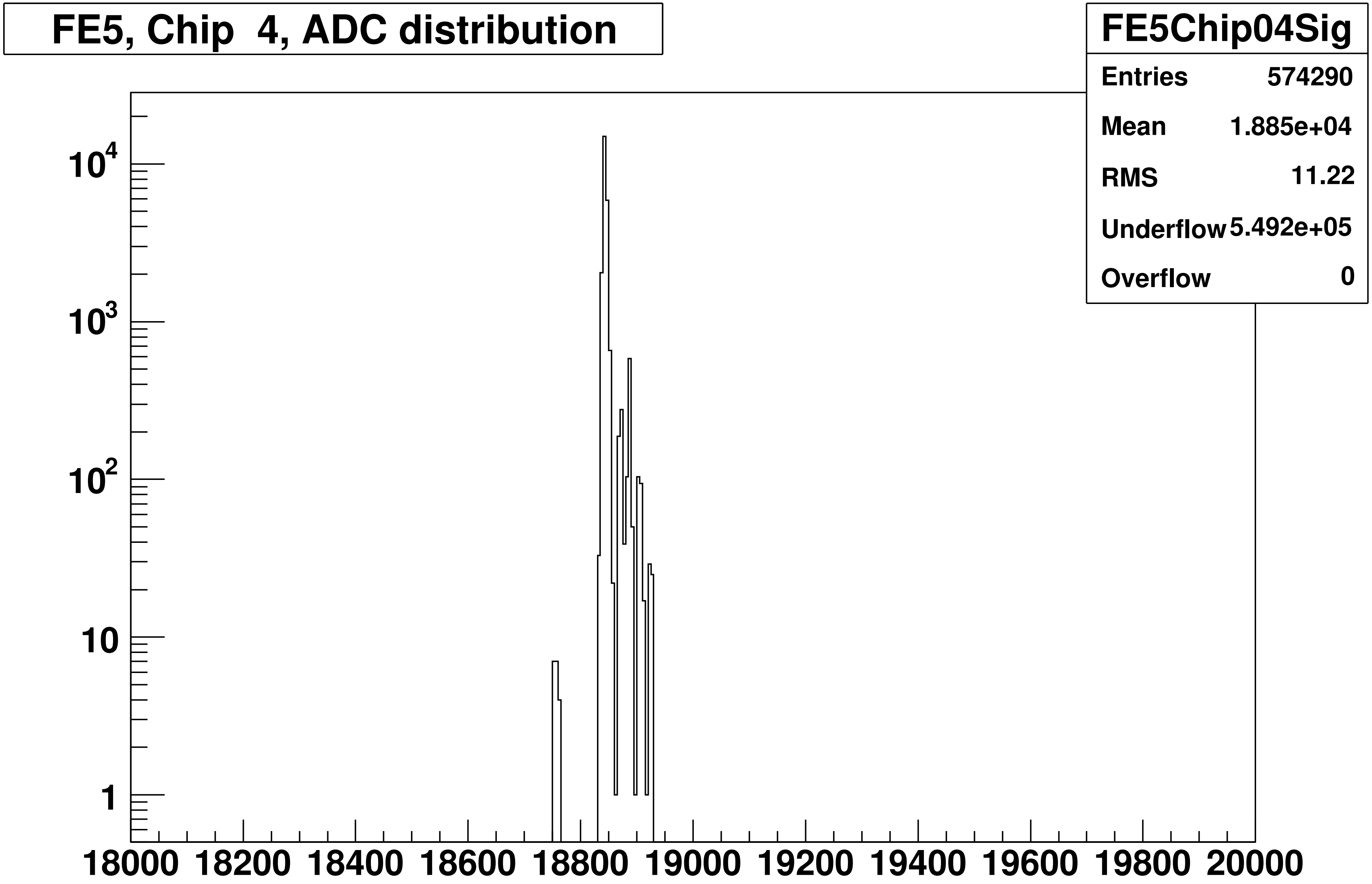
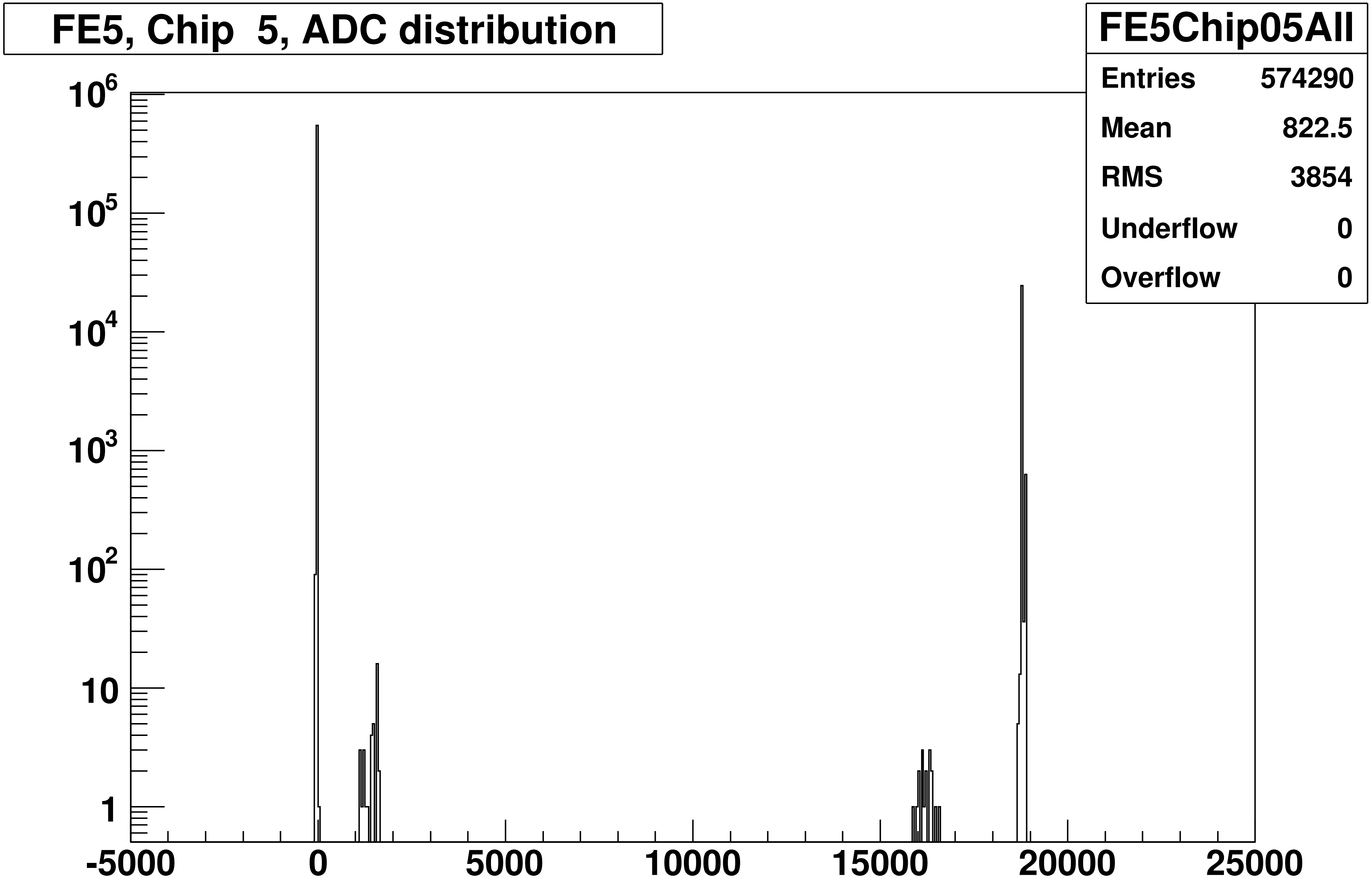
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 574290
Mean 1.885e+04
RMS 11.22
Underflow 5.492e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 574290
Mean 822.5
RMS 3854
Underflow 0
Overflow 0
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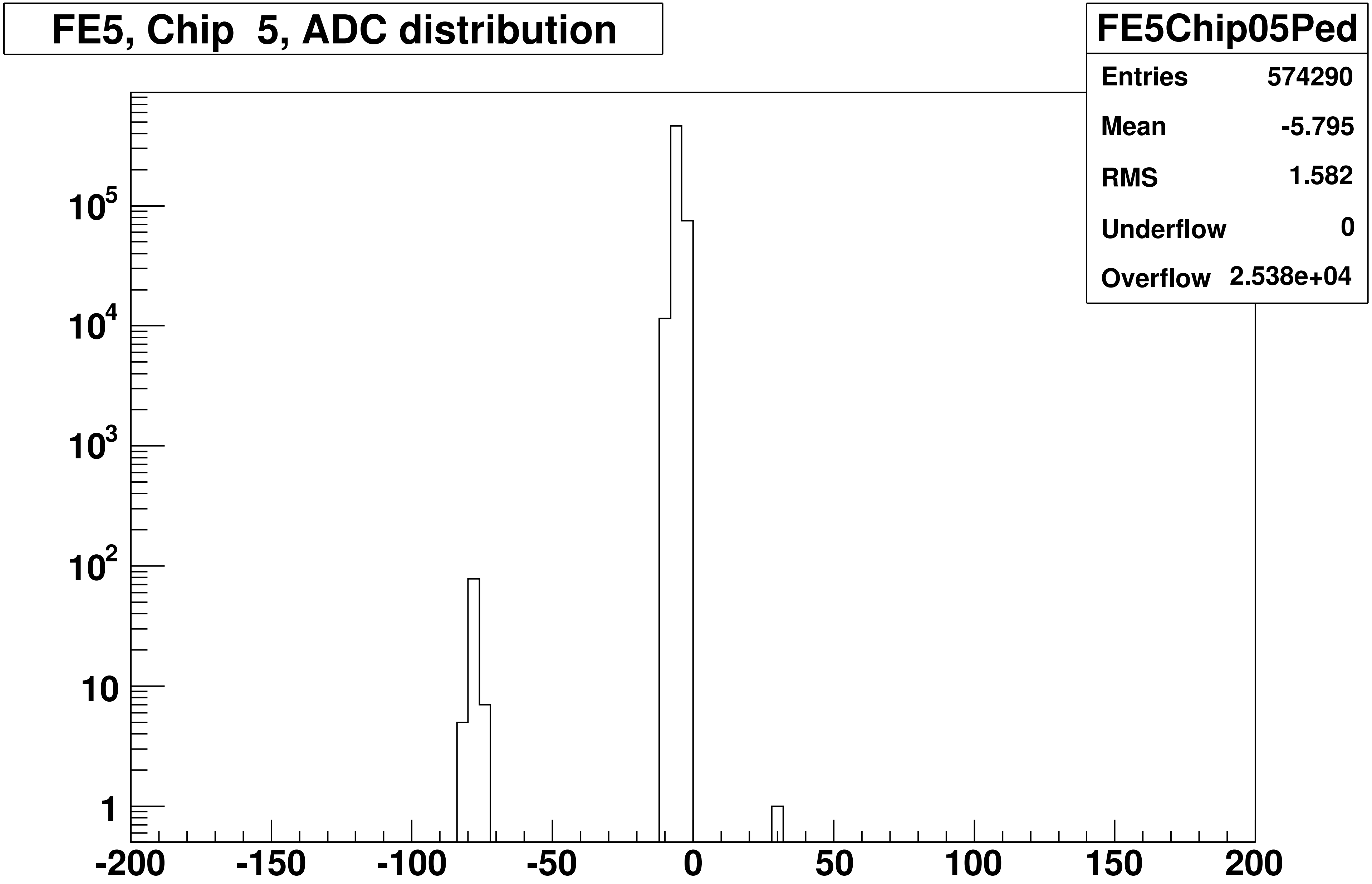
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 574290
Mean -5.795
RMS 1.582
Underflow 0
Overflow 2.538e+04
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10^4
10^3
10^2
10
1
-200
-150
-100
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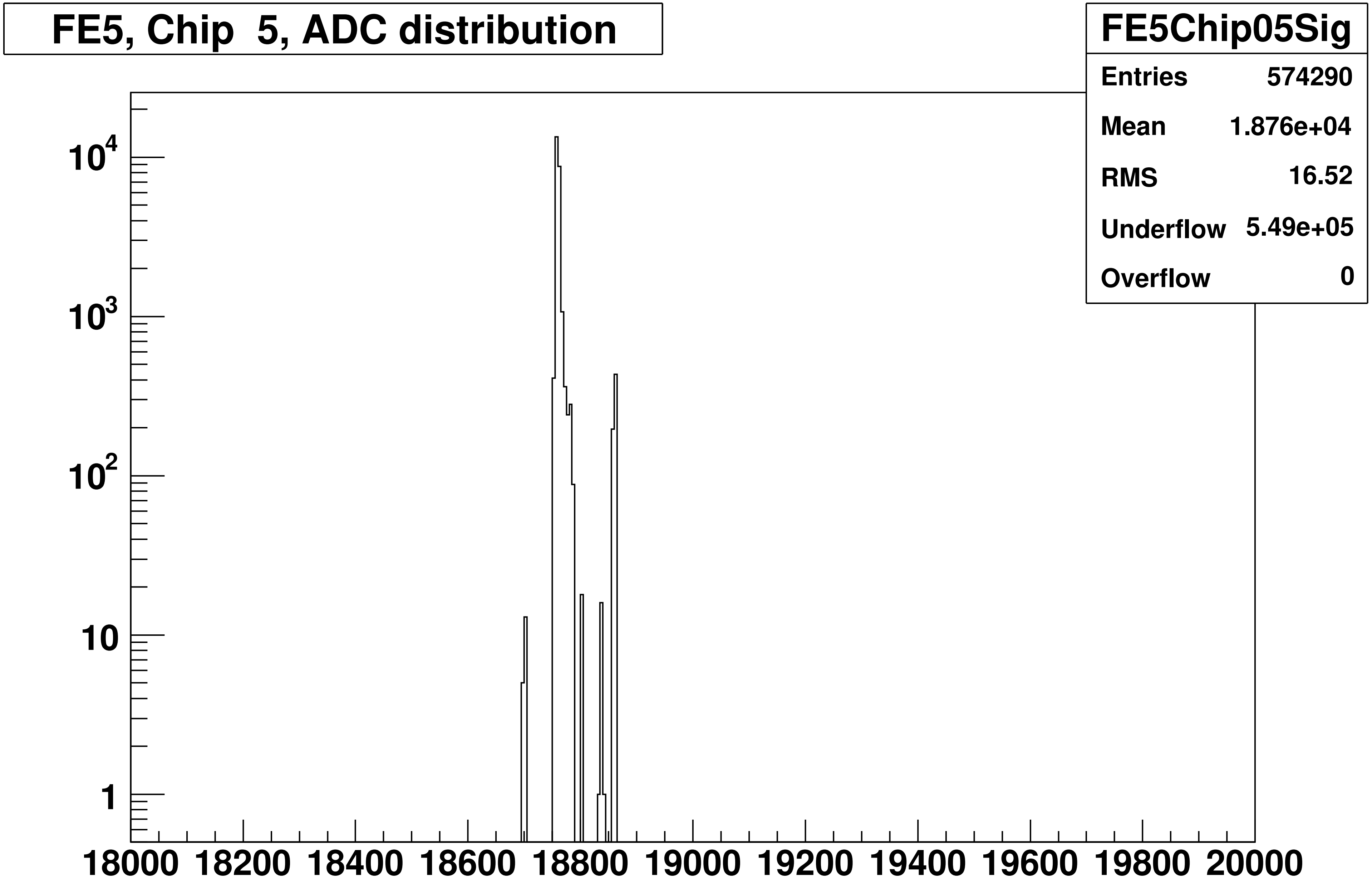
50
100
150
200


FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 574290
Mean 1.876e+04
RMS 16.52
Underflow 5.49e+05
Overflow 0
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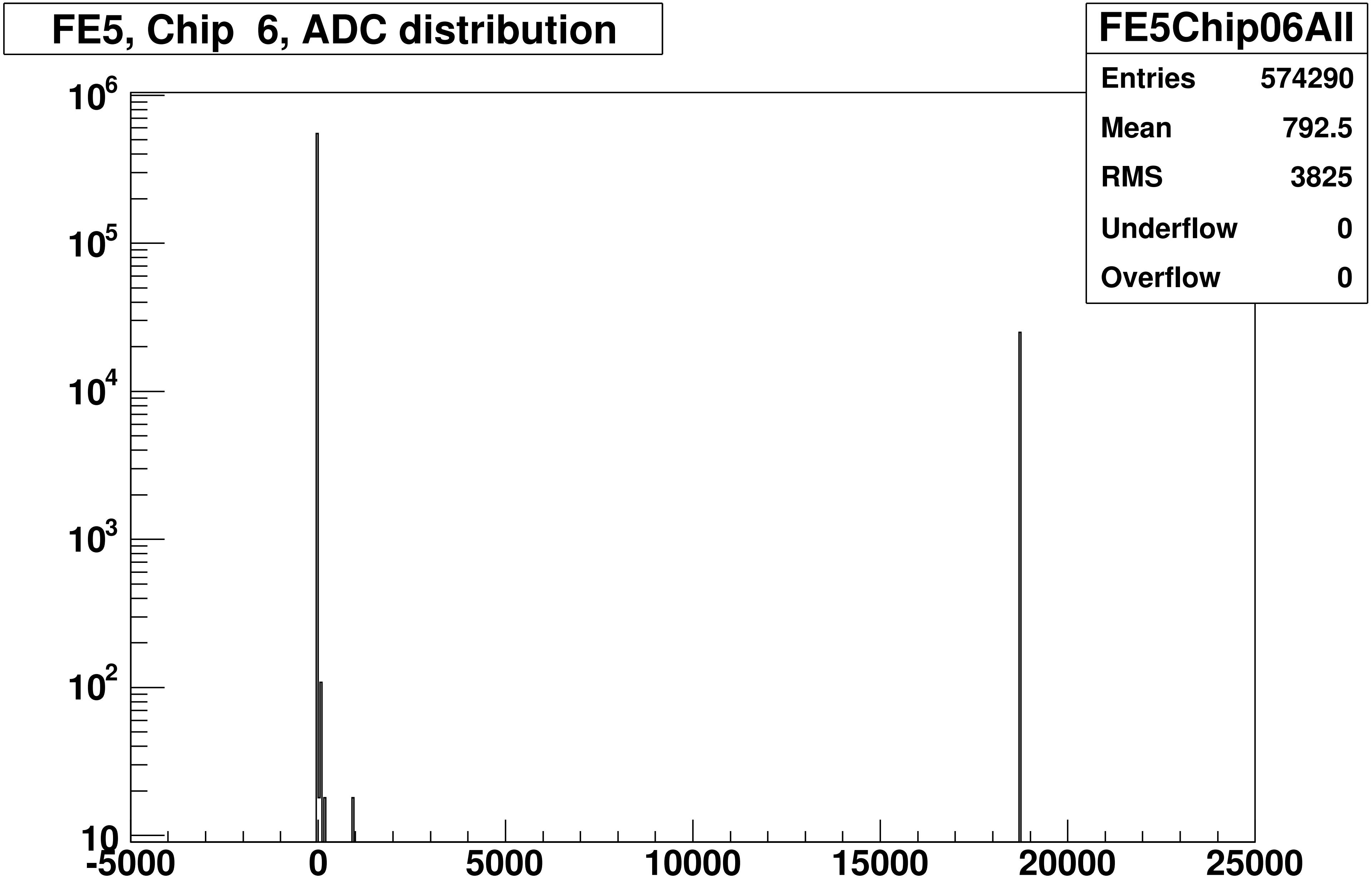
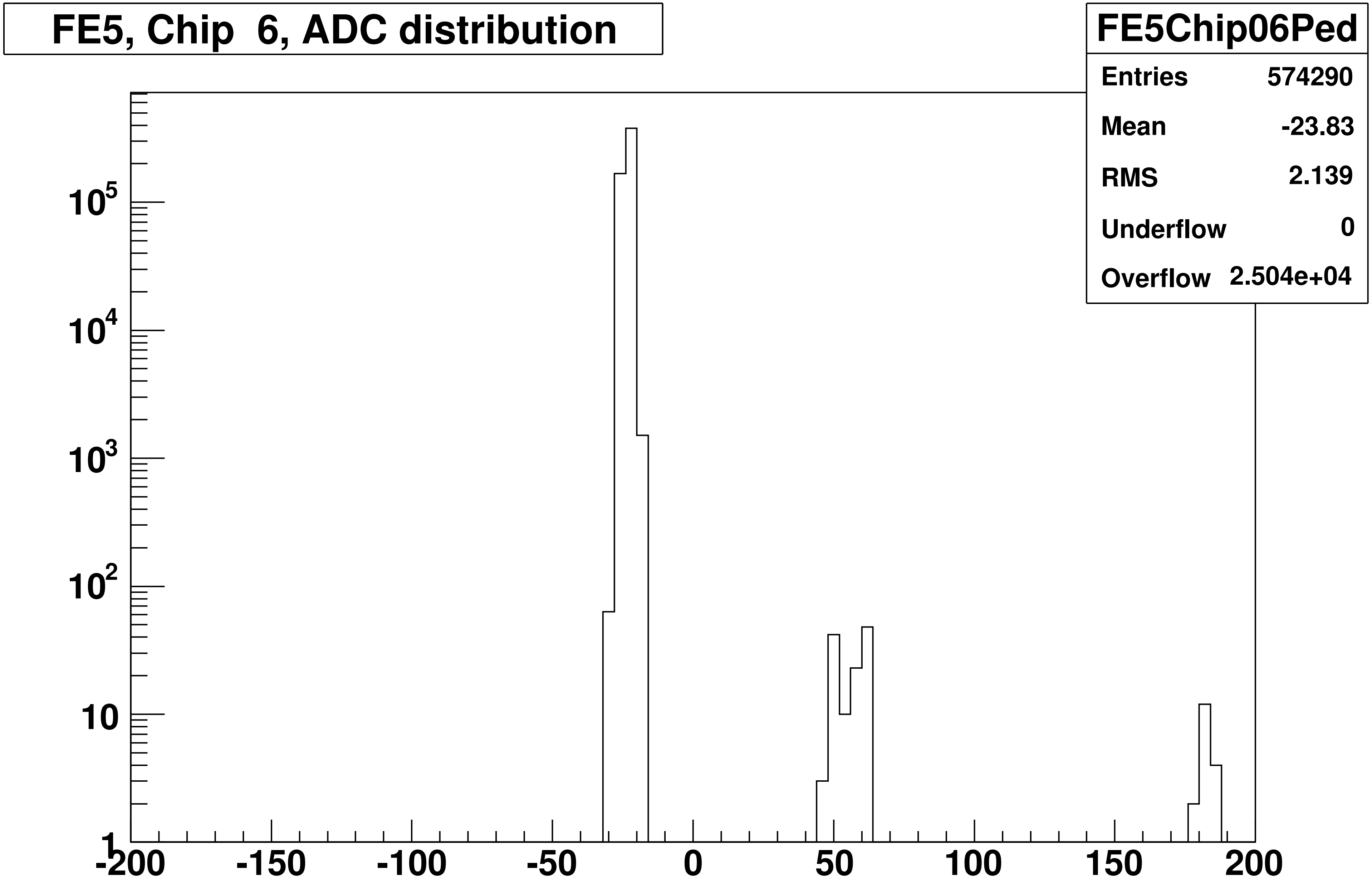
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 574290
Mean 792.5
RMS 3825
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 574290
Mean -23.83
RMS 2.139
Underflow 0
Overflow 2.504e+04
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10^3
10^2
10
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-150
-100
-50
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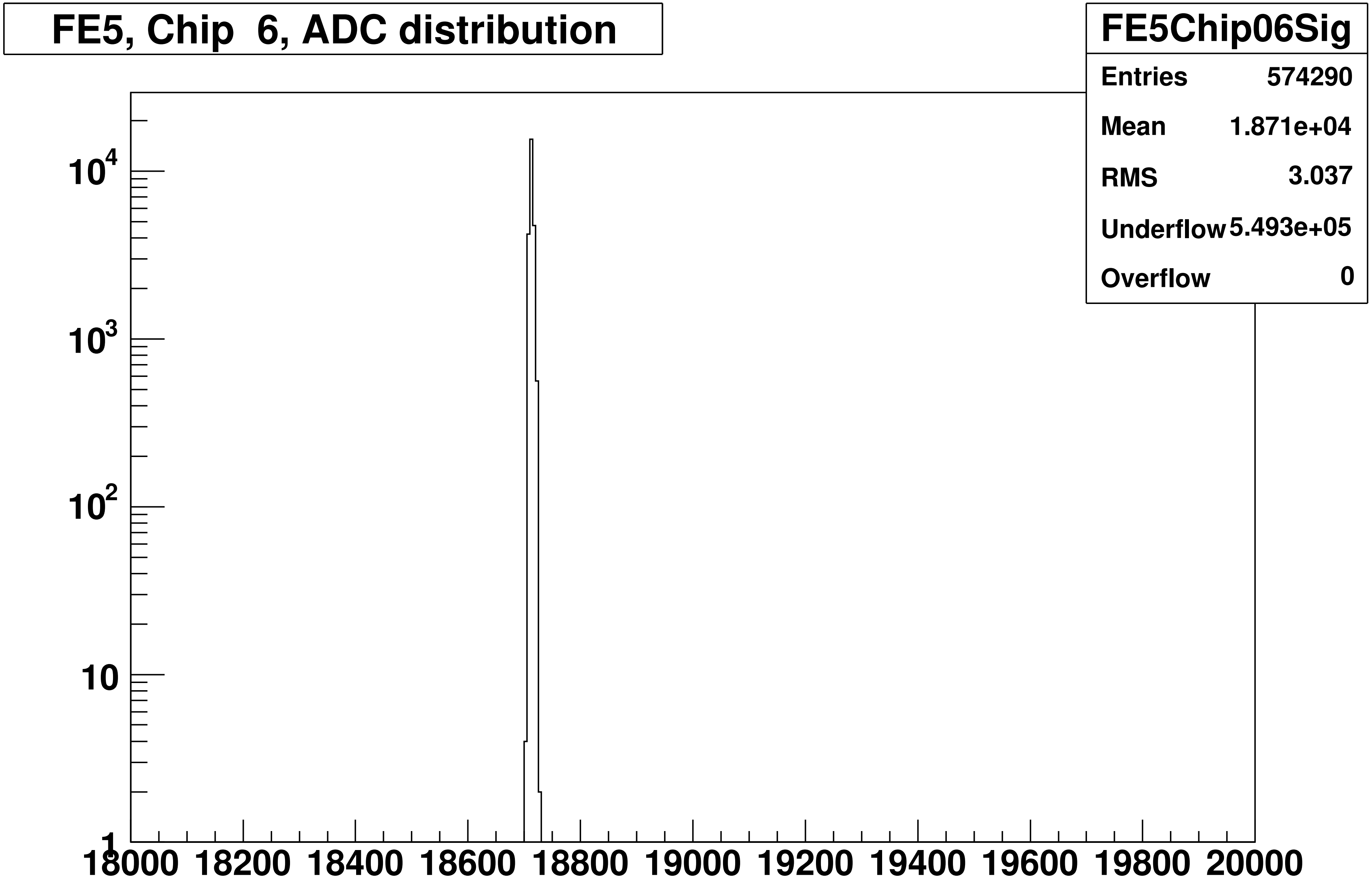
100
150
200


FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 574290
Mean 1.871e+04
RMS 3.037
Underflow 5.493e+05
Overflow 0
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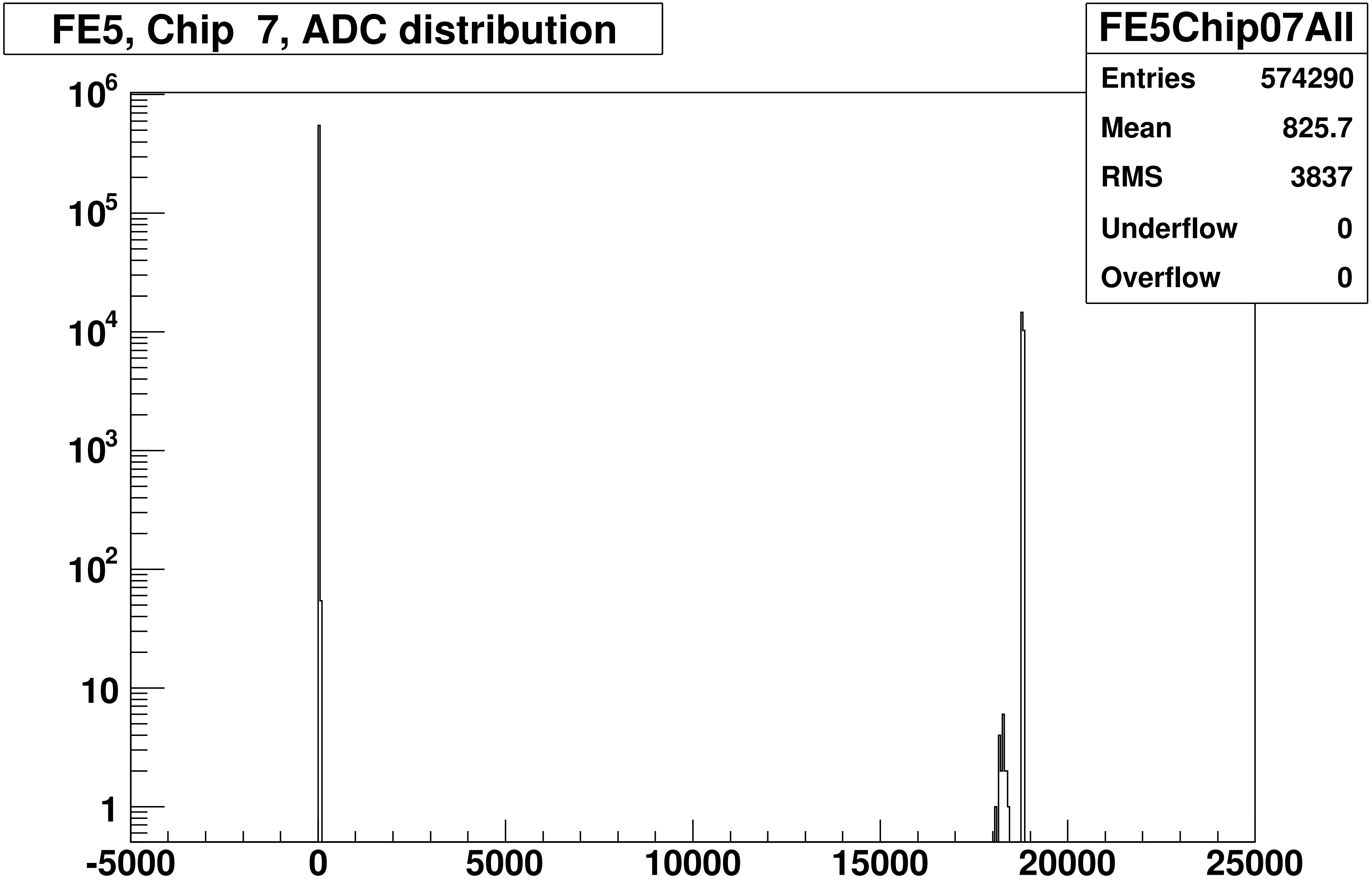
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 574290
Mean 825.7
RMS 3837
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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1
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10000
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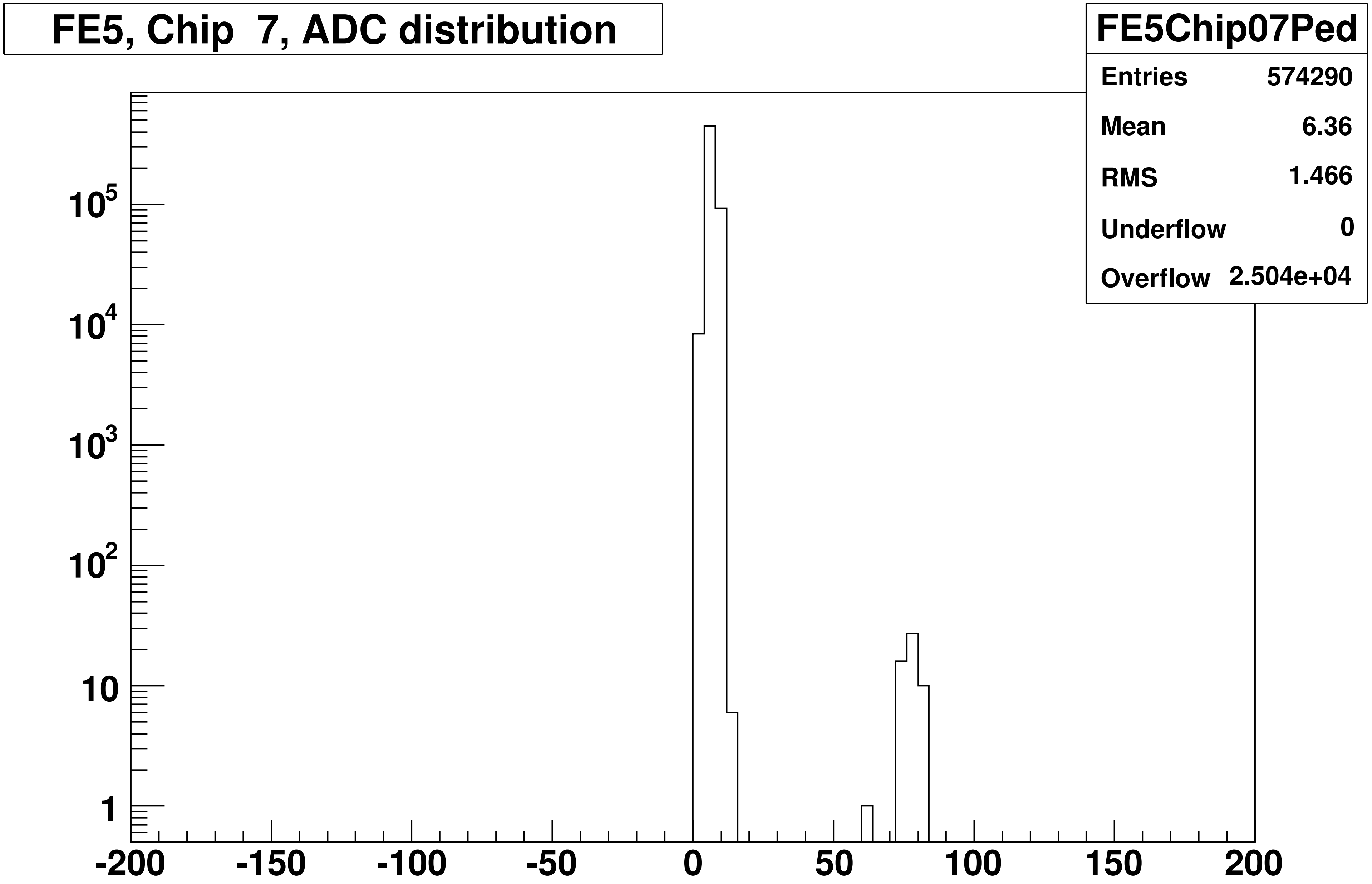
20000
25000


FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 574290
Mean 6.36
RMS 1.466
Underflow 0
Overflow 2.504e+04
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10^3
10^2
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150
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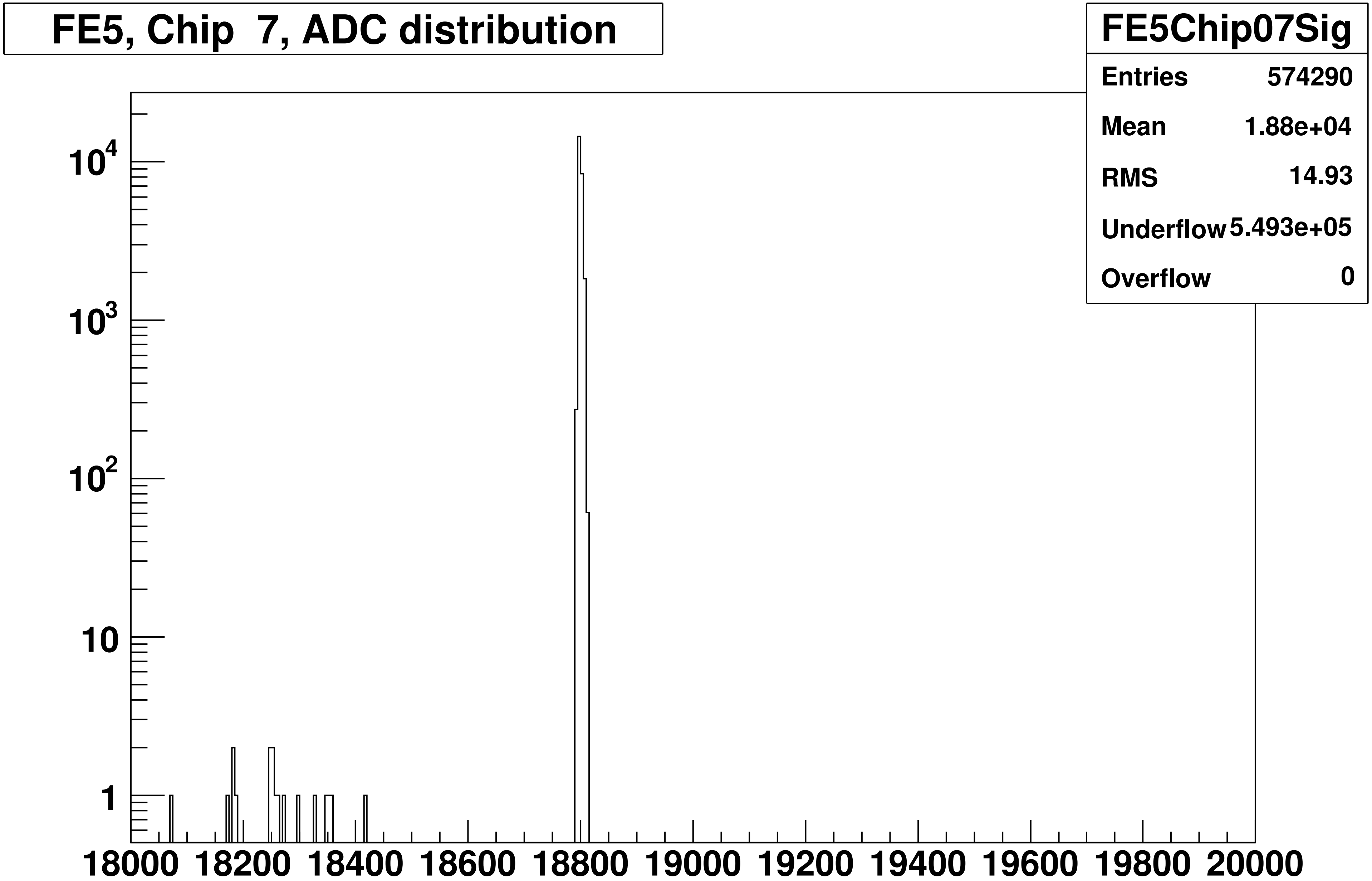
FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 574290
Mean 1.88e+04
RMS 14.93
Underflow 5.493e+05
Overflow 0
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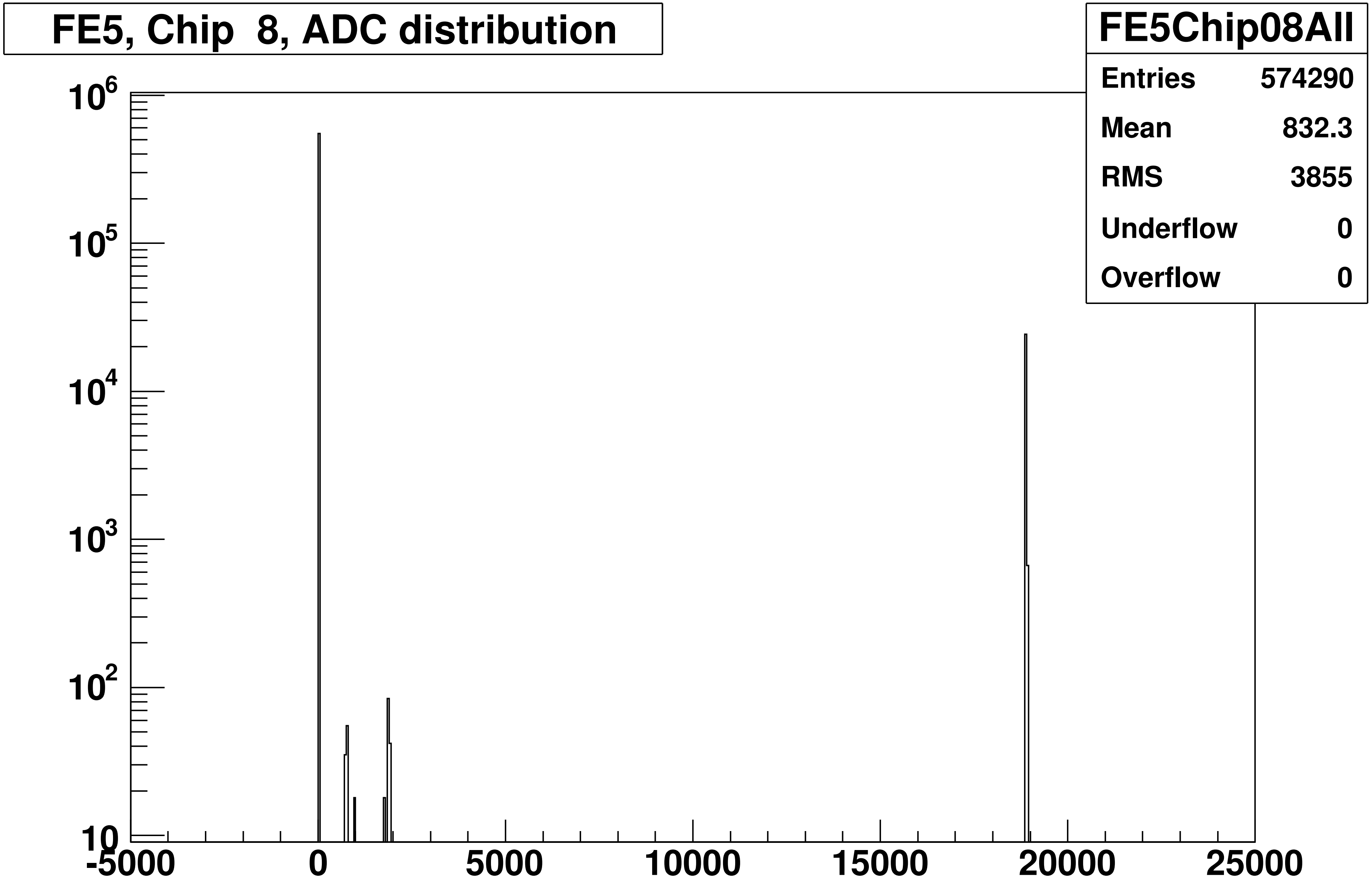
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 574290
Mean 832.3
RMS 3855
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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10000
15000
20000
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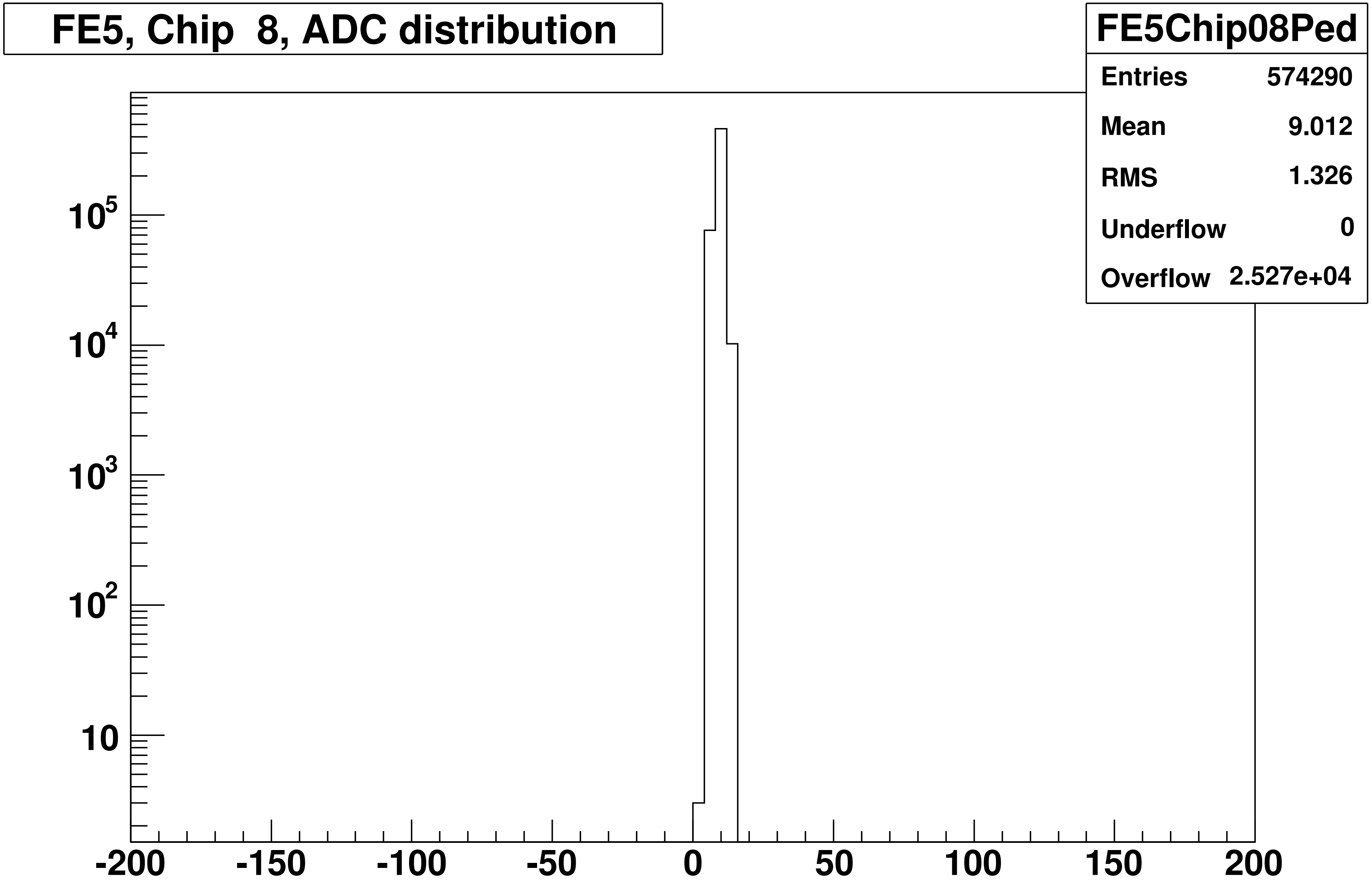
FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 574290
Mean 9.012
RMS 1.326
Underflow 0
Overflow 2.527e+04
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10^2
10^3
10^4
10^5
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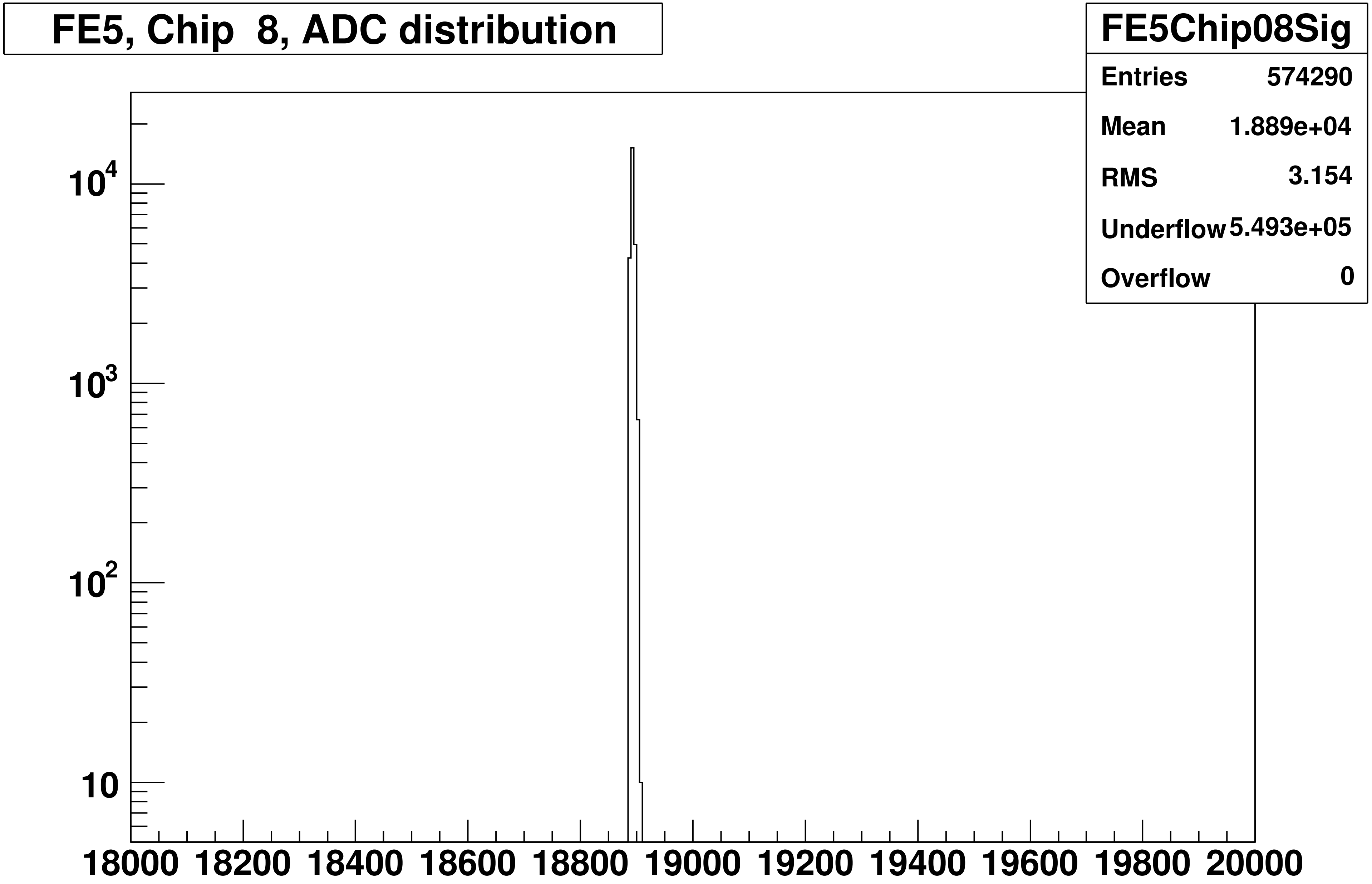
FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 574290
Mean 1.889e+04
RMS 3.154
Underflow 5.493e+05
Overflow 0
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10^3
10^2
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18000
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19000
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19800
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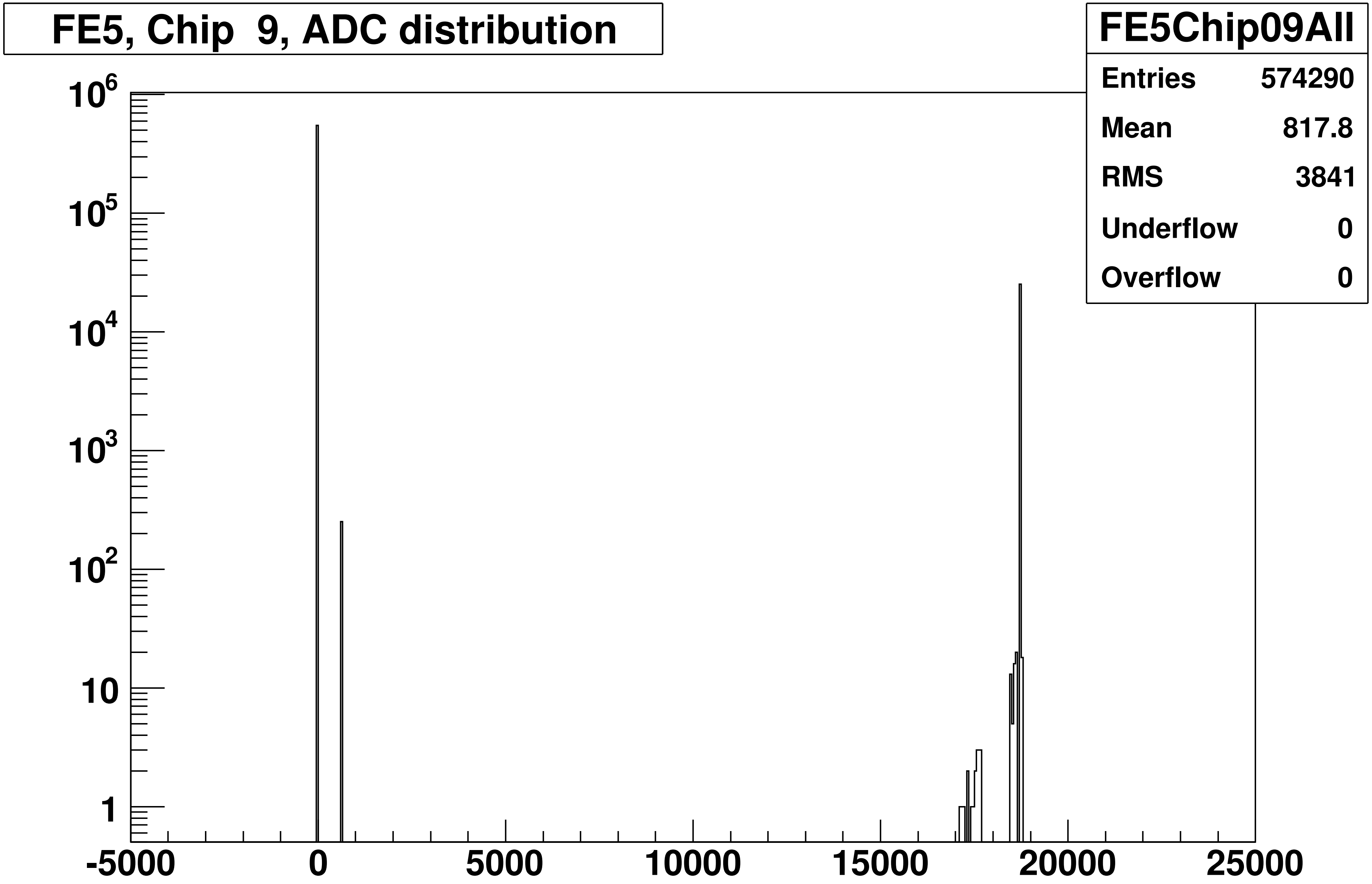

FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 574290
Mean 817.8
RMS 3841
Underflow 0
Overflow 0
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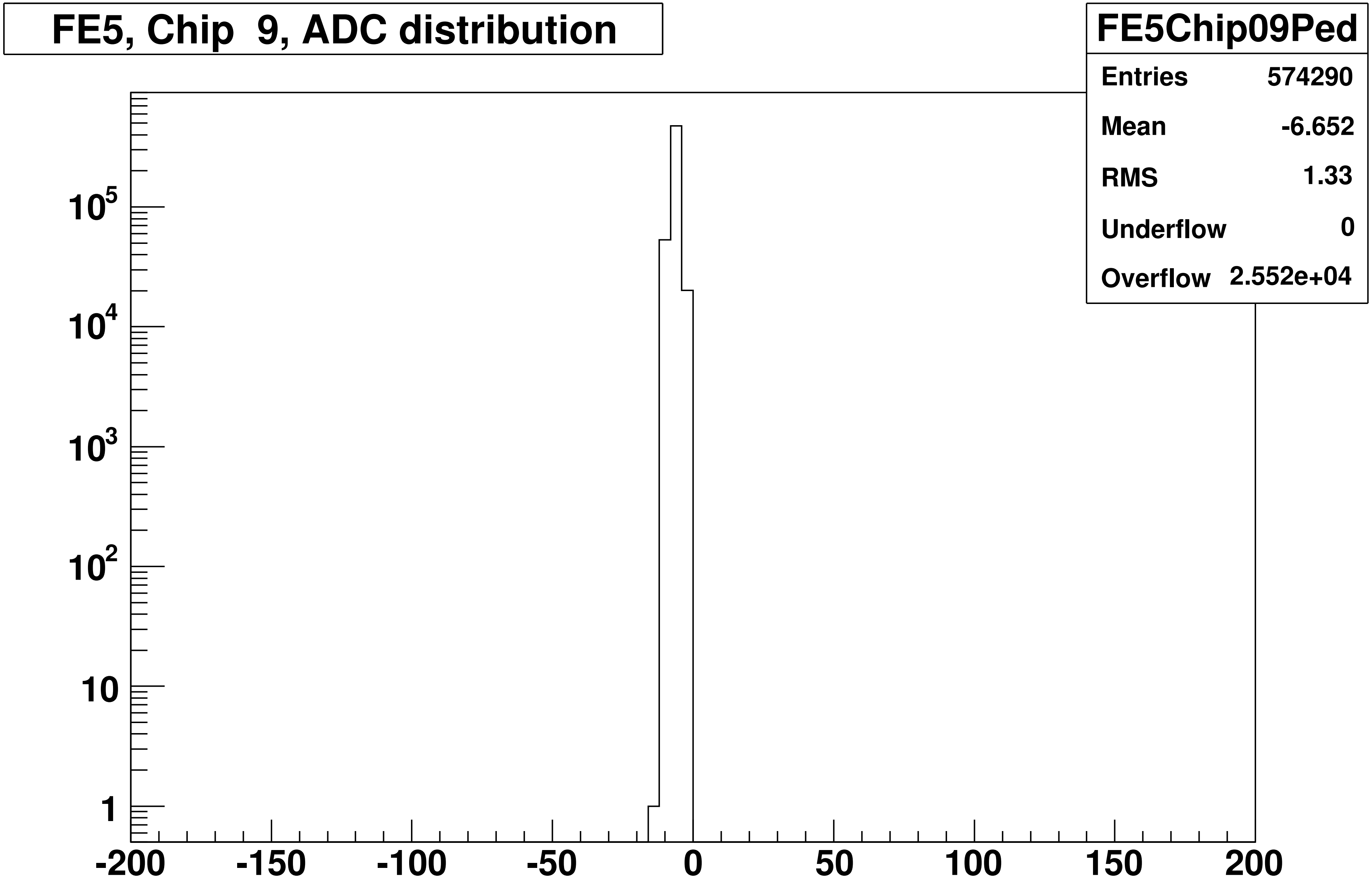
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 574290
Mean -6.652
RMS 1.33
Underflow 0
Overflow 2.552e+04
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10^4
10^3
10^2
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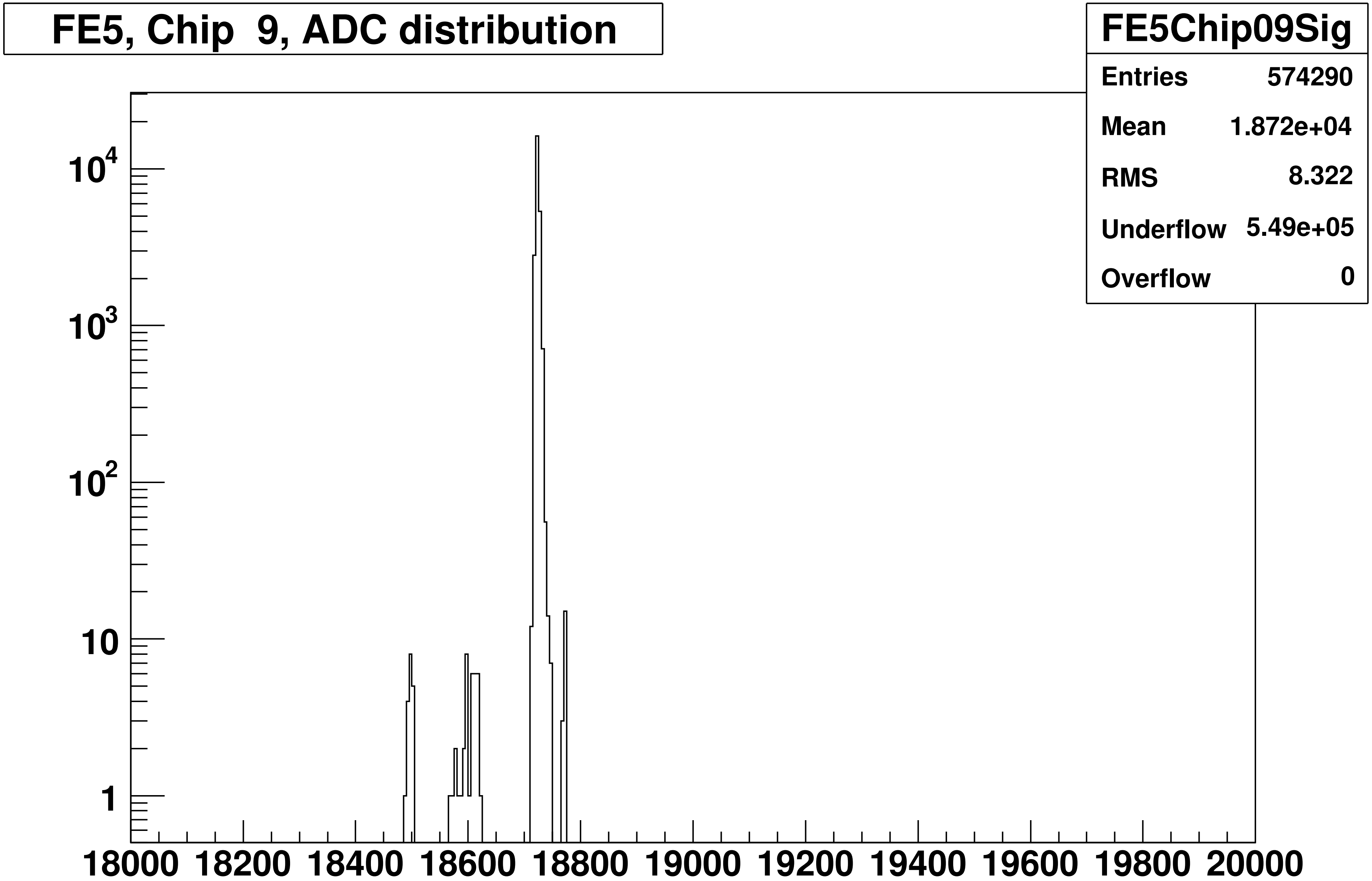
0
50
100
150
200


FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 574290
Mean 1.872e+04
RMS 8.322
Underflow 5.49e+05
Overflow 0
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10^3
10^2
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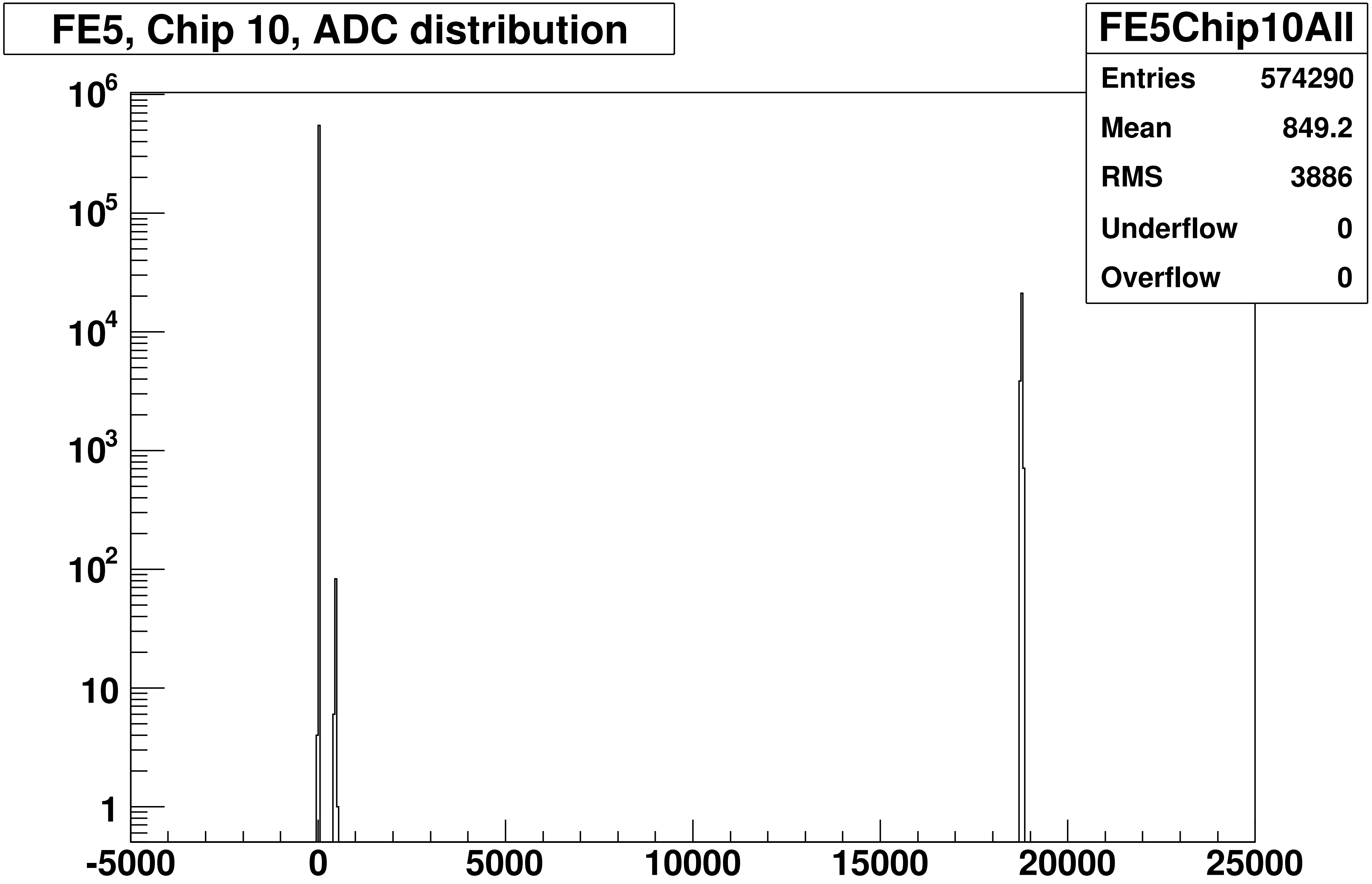

FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 574290
Mean 849.2
RMS 3886
Underflow 0
Overflow 0
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10^4
10^3
10^2
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20000
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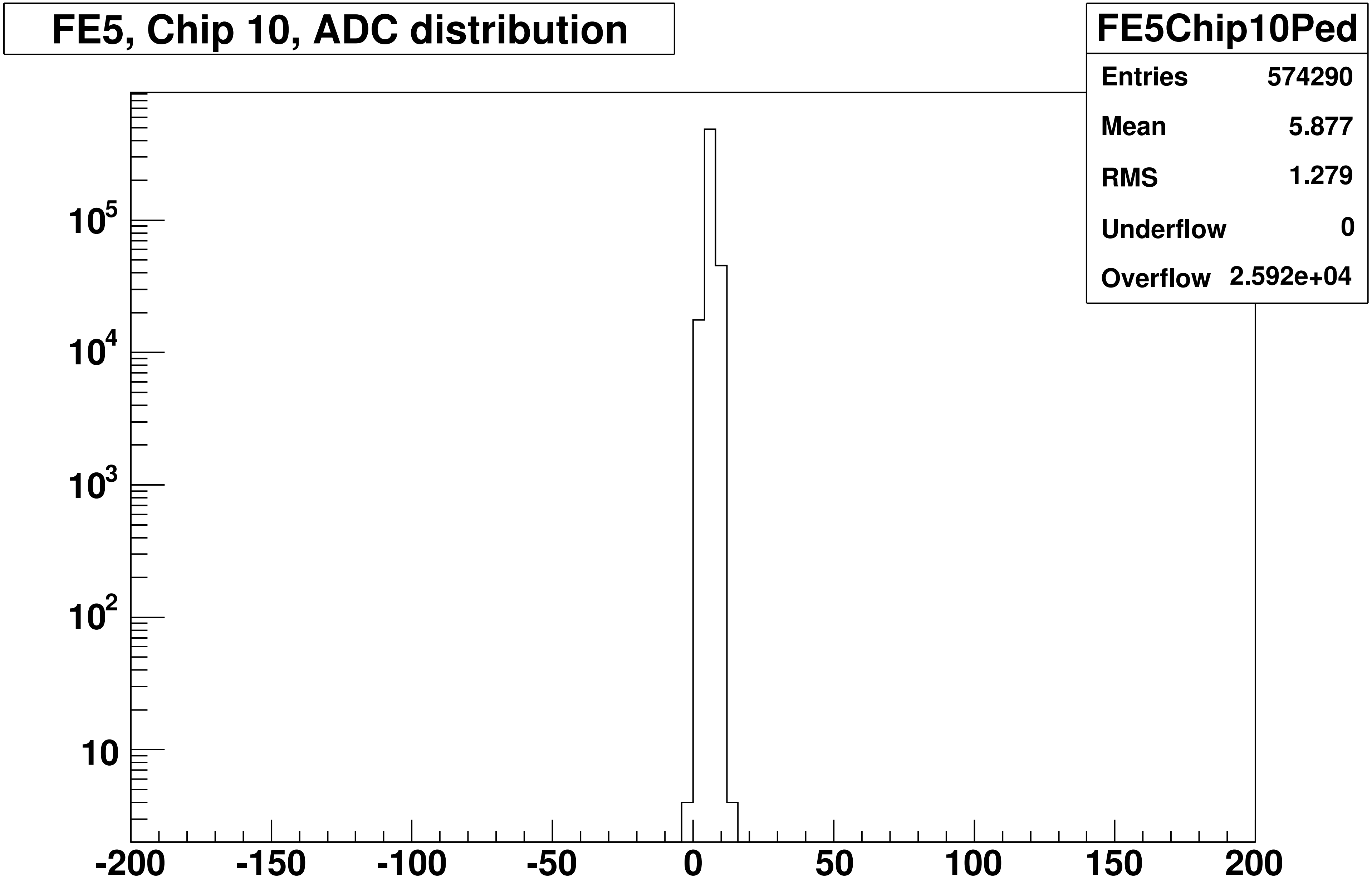
FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 574290
Mean 5.877
RMS 1.279
Underflow 0
Overflow 2.592e+04
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10^2
10^3
10^4
10^5
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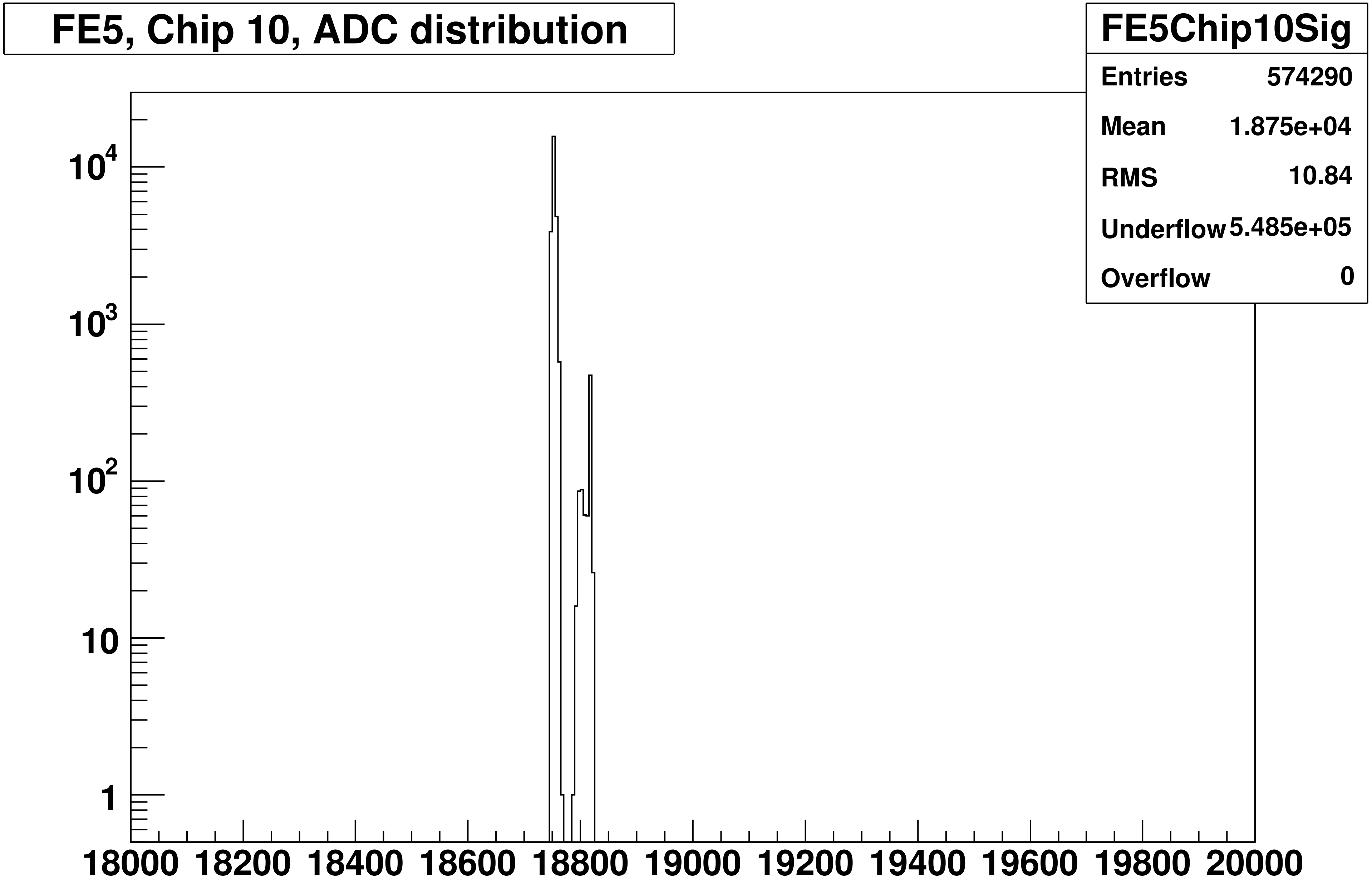
100
150
200



FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 574290
Mean 1.875e+04
RMS 10.84
Underflow 5.485e+05
Overflow 0
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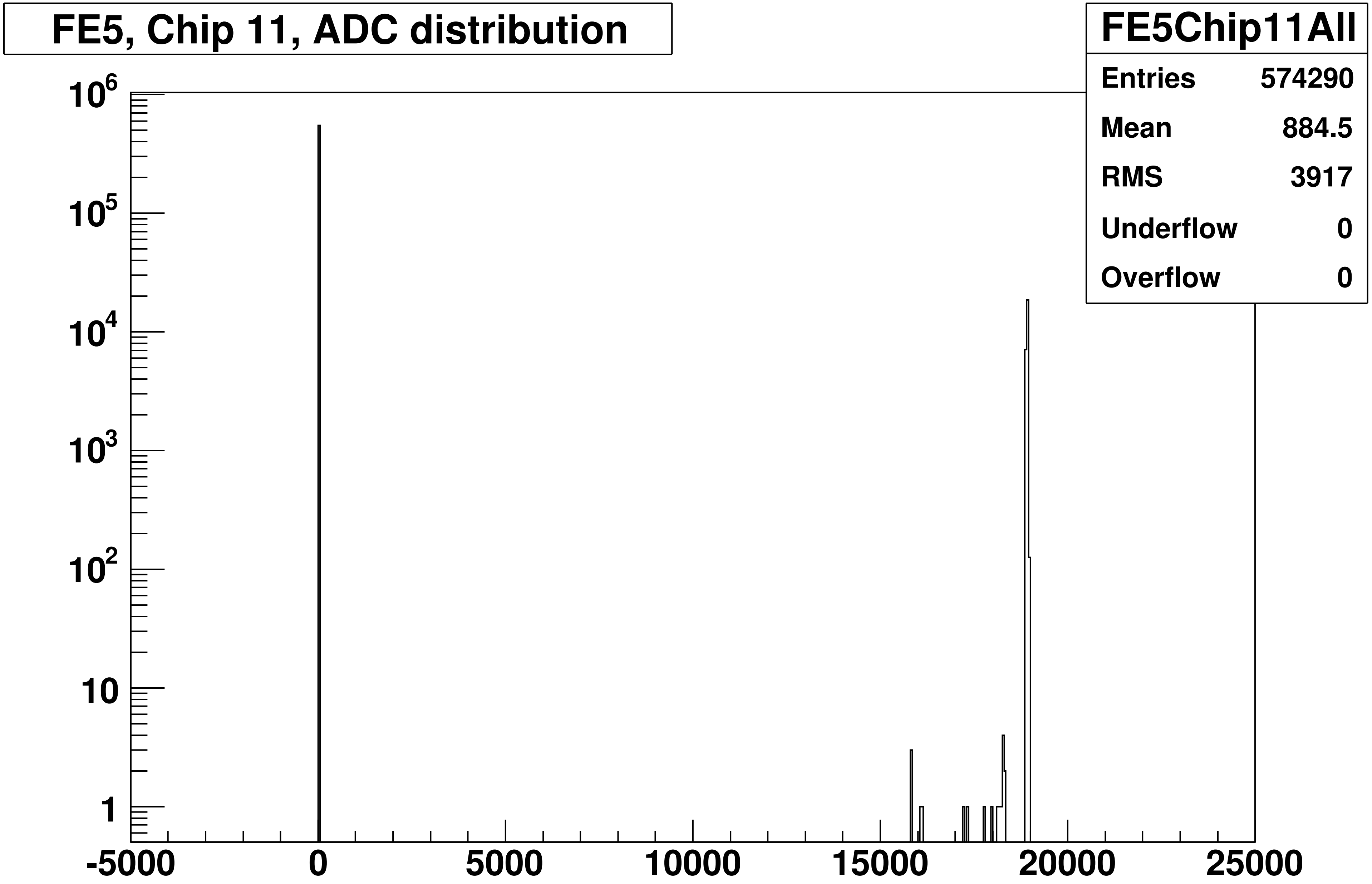
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 574290
Mean 884.5
RMS 3917
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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20000
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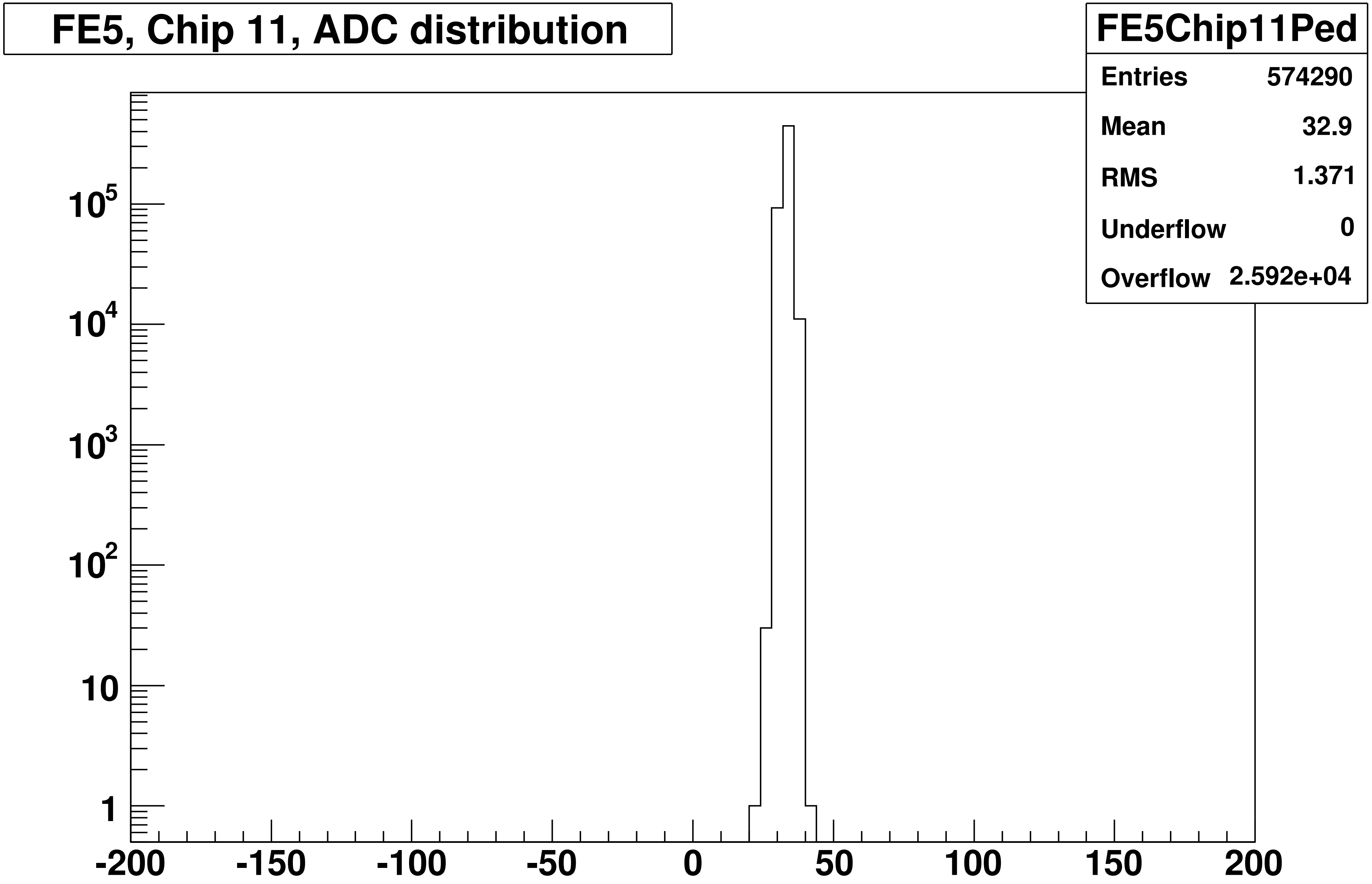
FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 574290
Mean 32.9
RMS 1.371
Underflow 0
Overflow 2.592e+04
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10^4
10^3
10^2
10
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150
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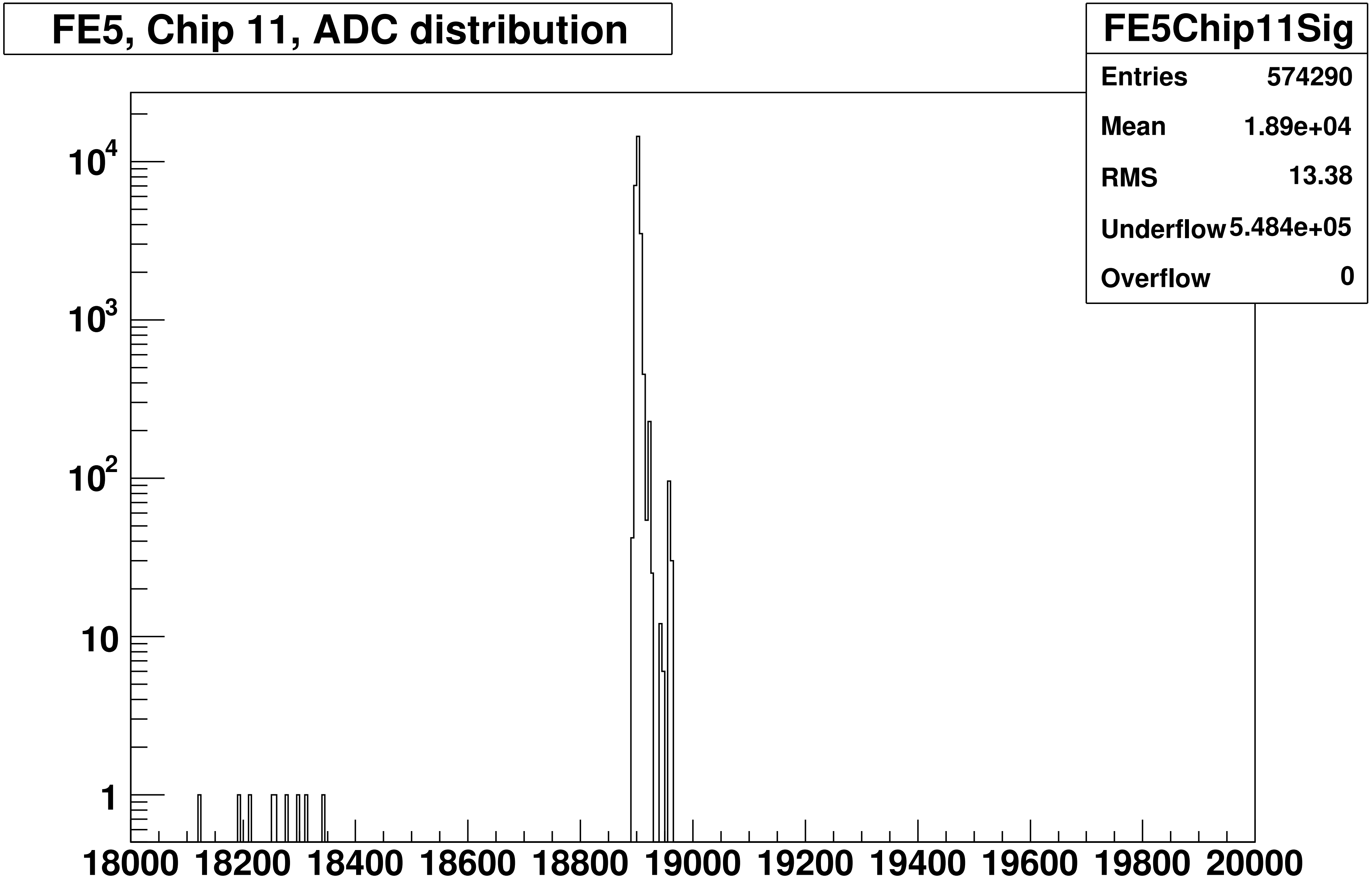

FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 574290
Mean 1.89e+04
RMS 13.38
Underflow 5.484e+05
Overflow 0
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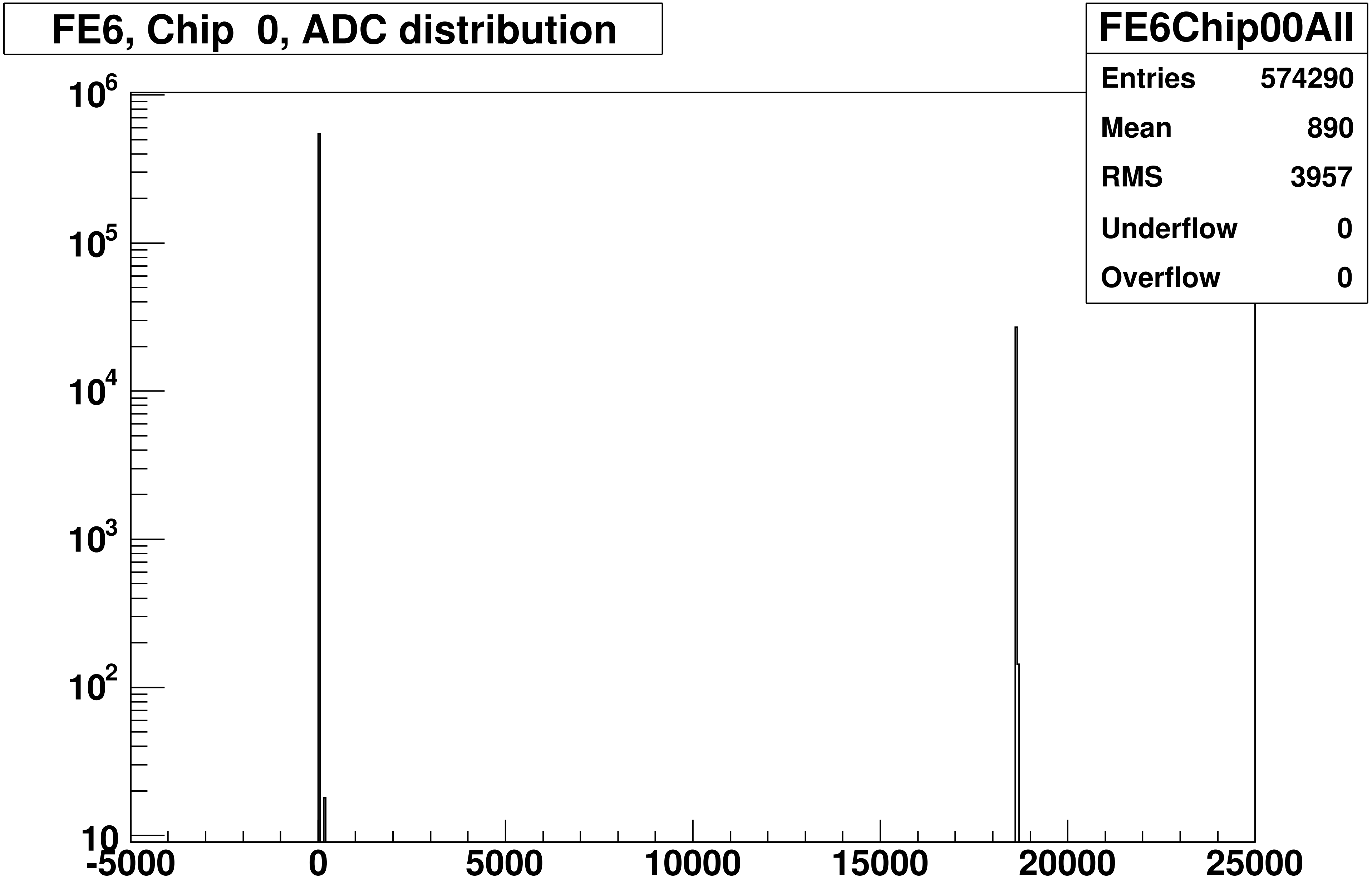
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 574290
Mean 890
RMS 3957
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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10000
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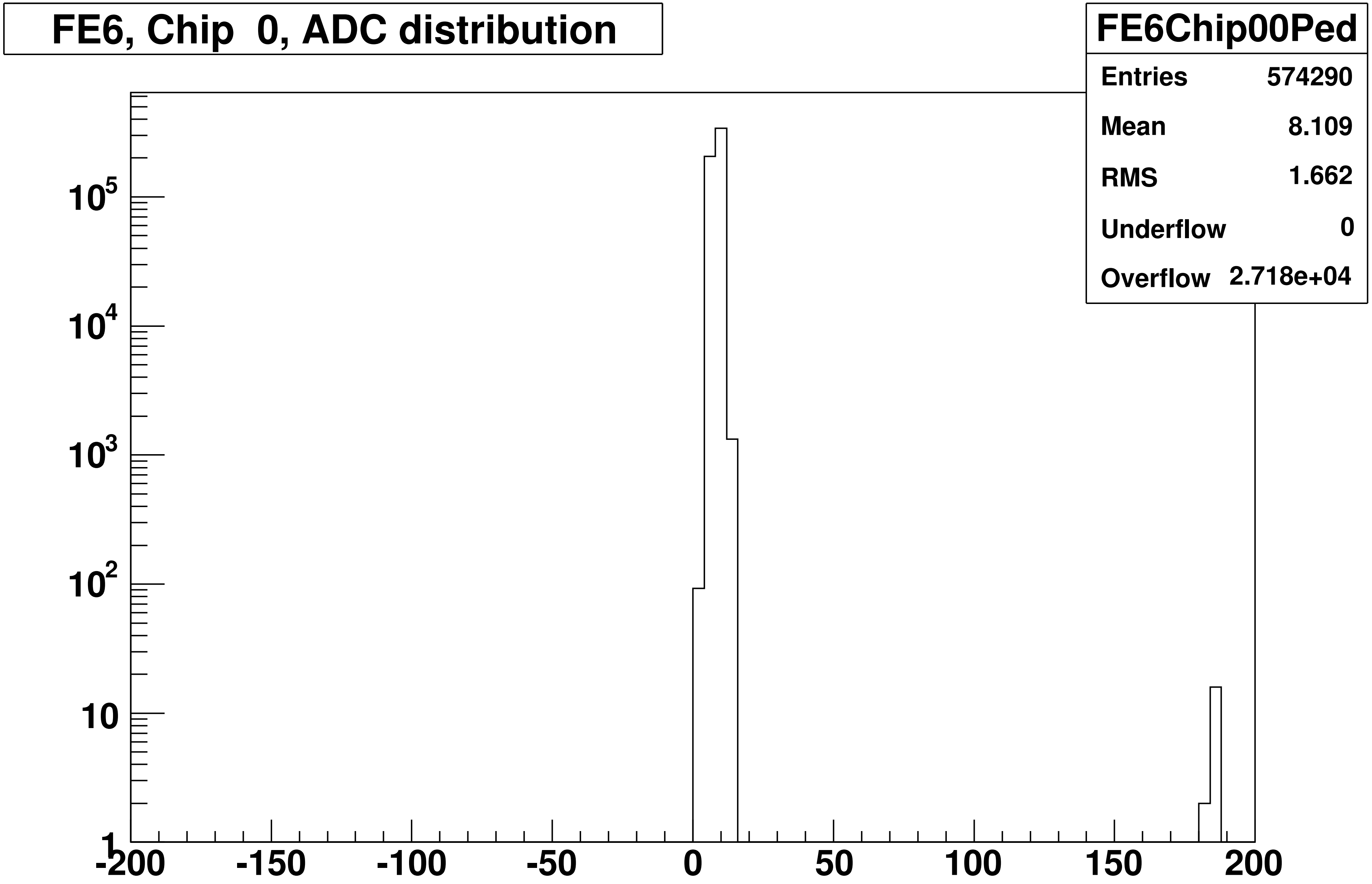
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 574290
Mean 8.109
RMS 1.662
Underflow 0
Overflow 2.718e+04
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10^3
10^2
10
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-100
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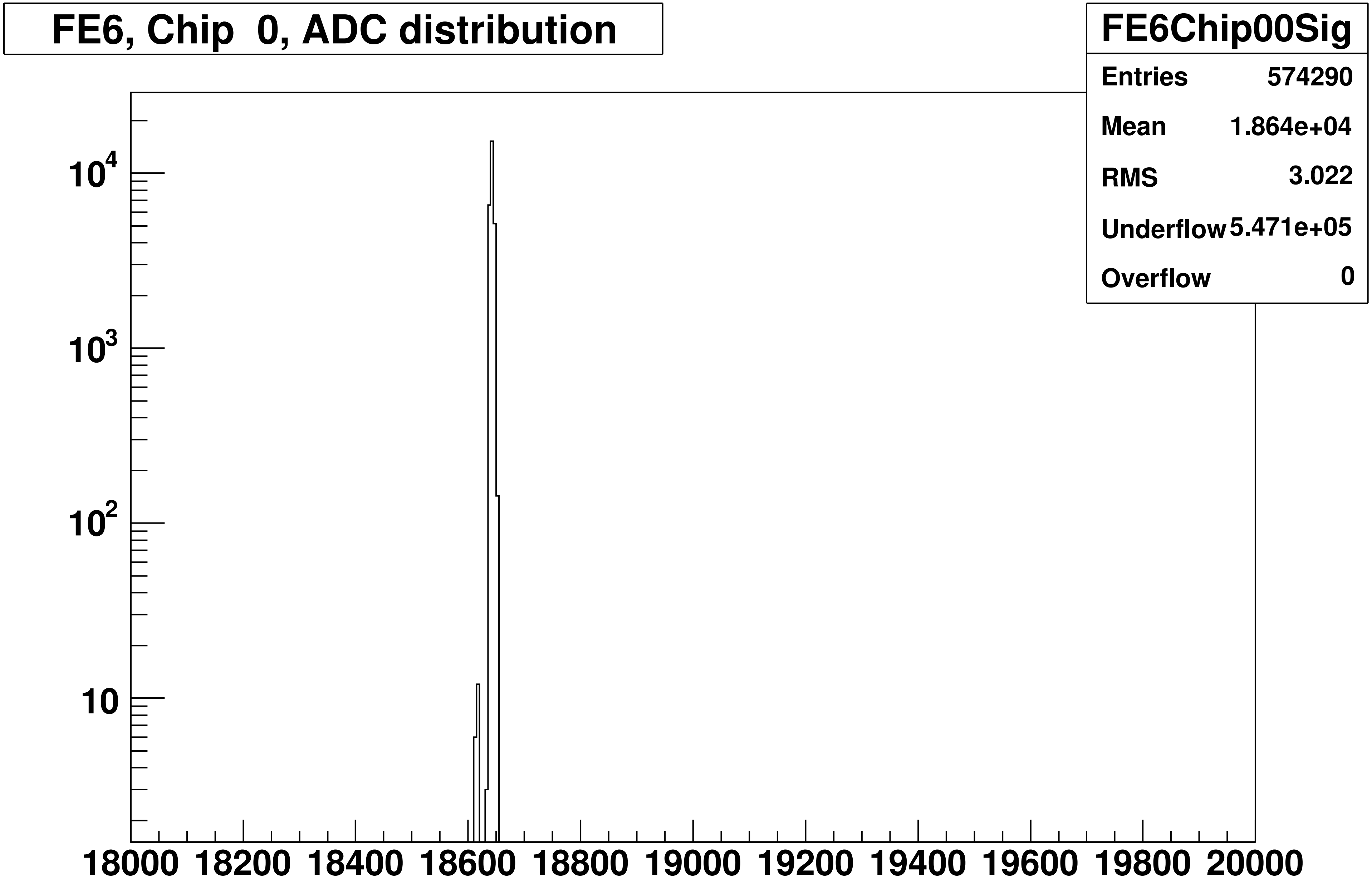
150
200



FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 574290
Mean 1.864e+04
RMS 3.022
Underflow 5.471e+05
Overflow 0
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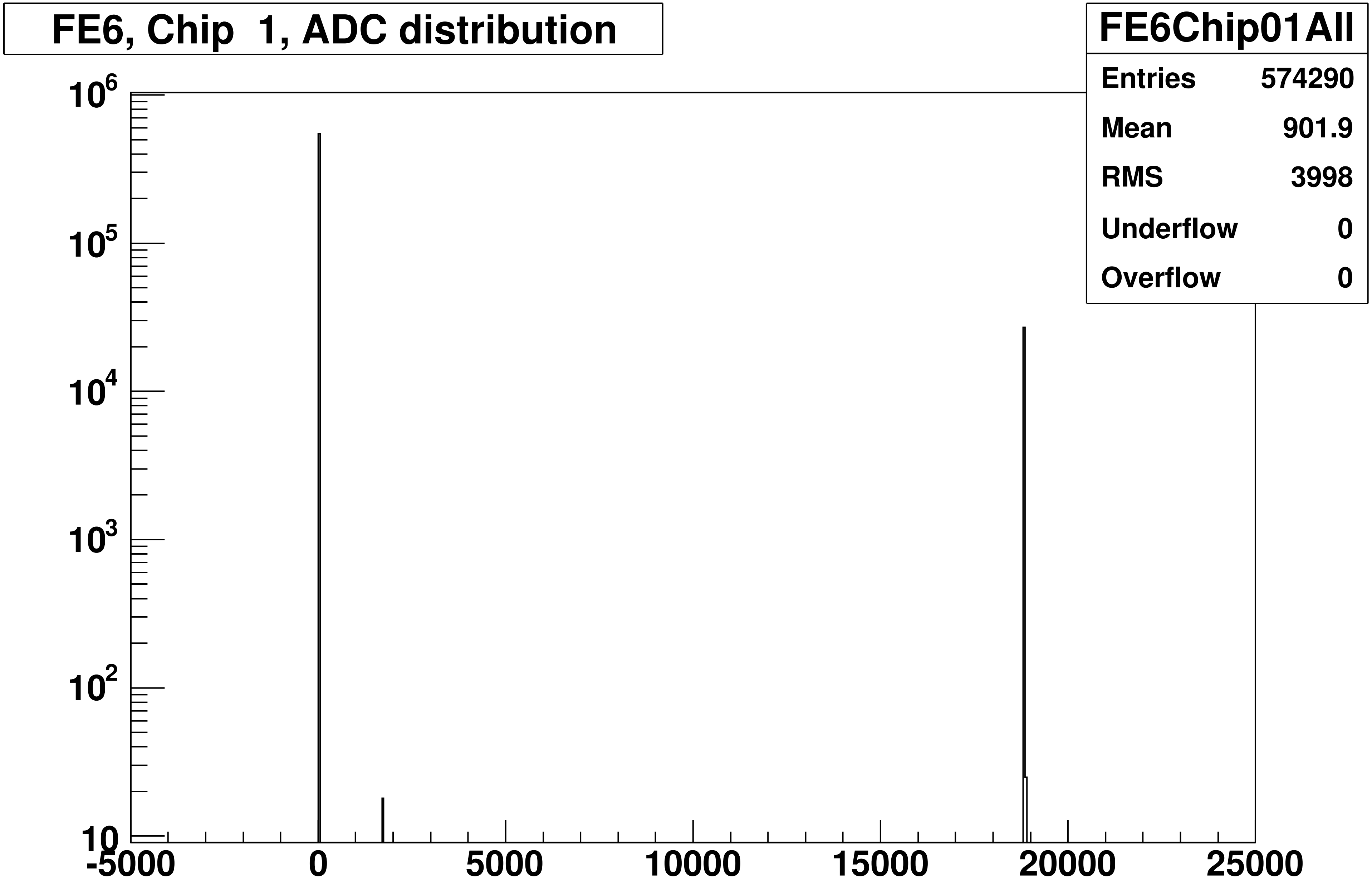
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 574290
Mean 901.9
RMS 3998
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
-5000
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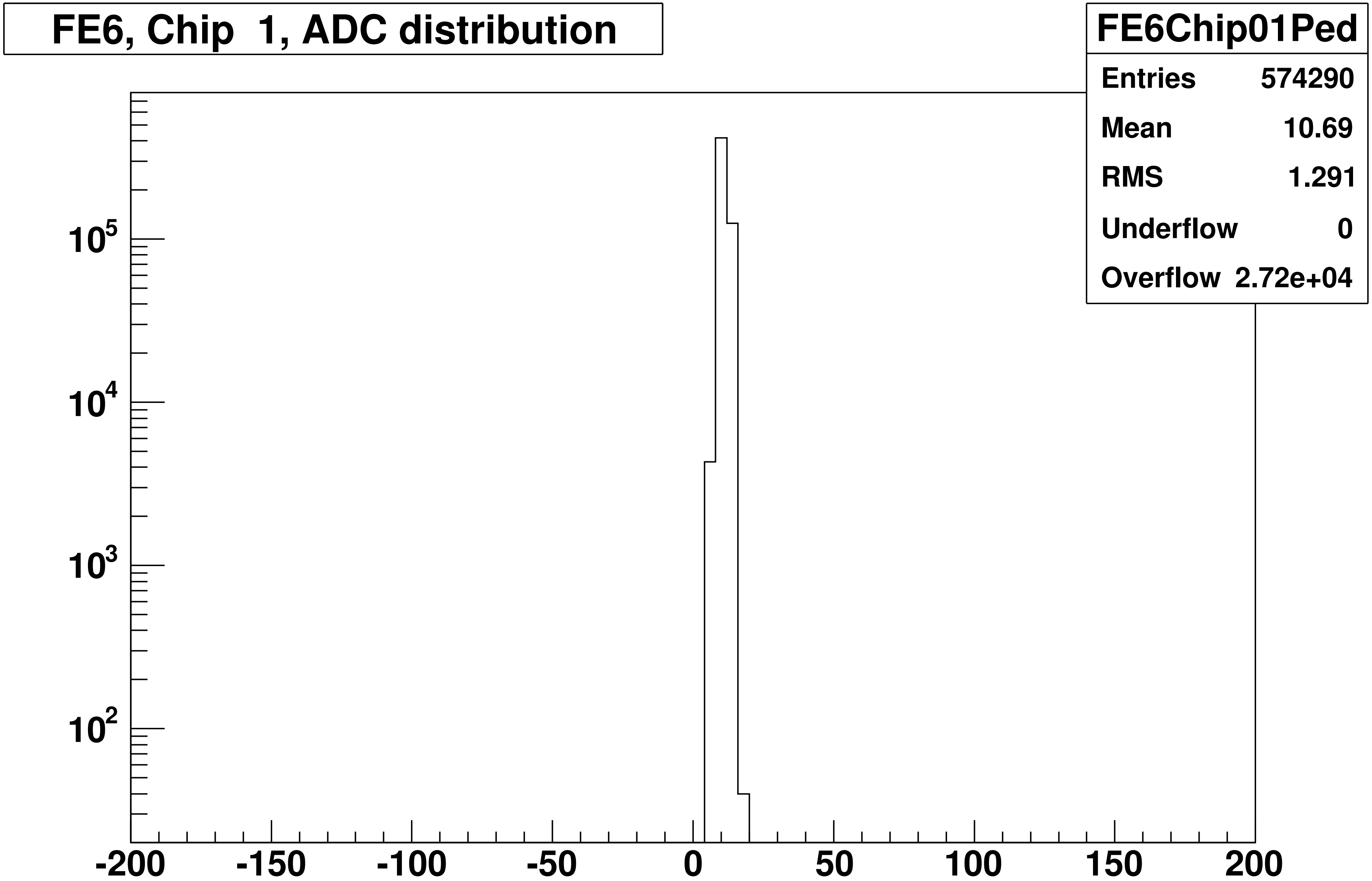
5000
10000
15000
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 574290
Mean 10.69
RMS 1.291
Underflow 0
Overflow 2.72e+04
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10^4
10^3
10^2
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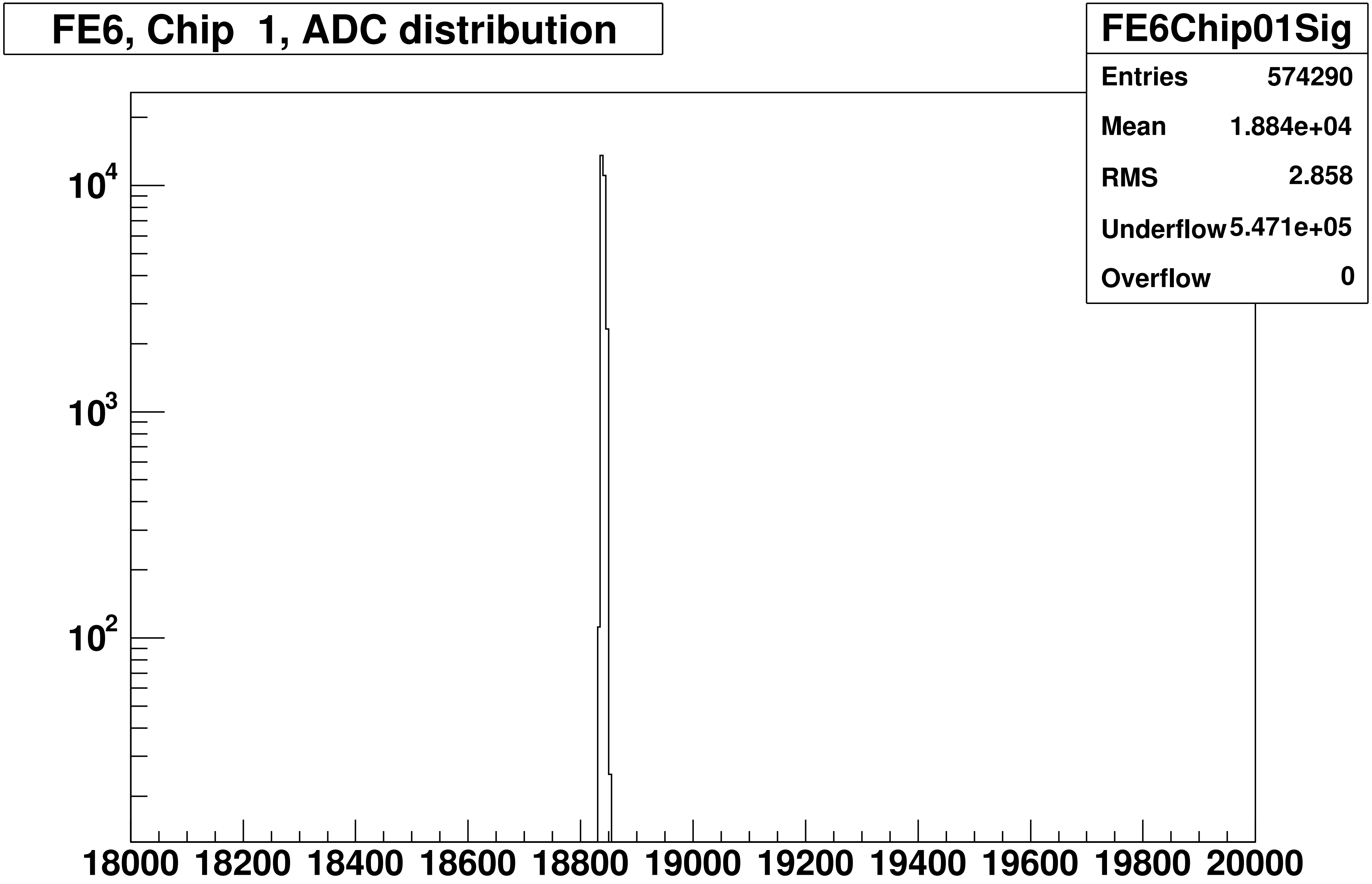
100
150
200



FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 574290
Mean 1.884e+04
RMS 2.858
Underflow 5.471e+05
Overflow 0
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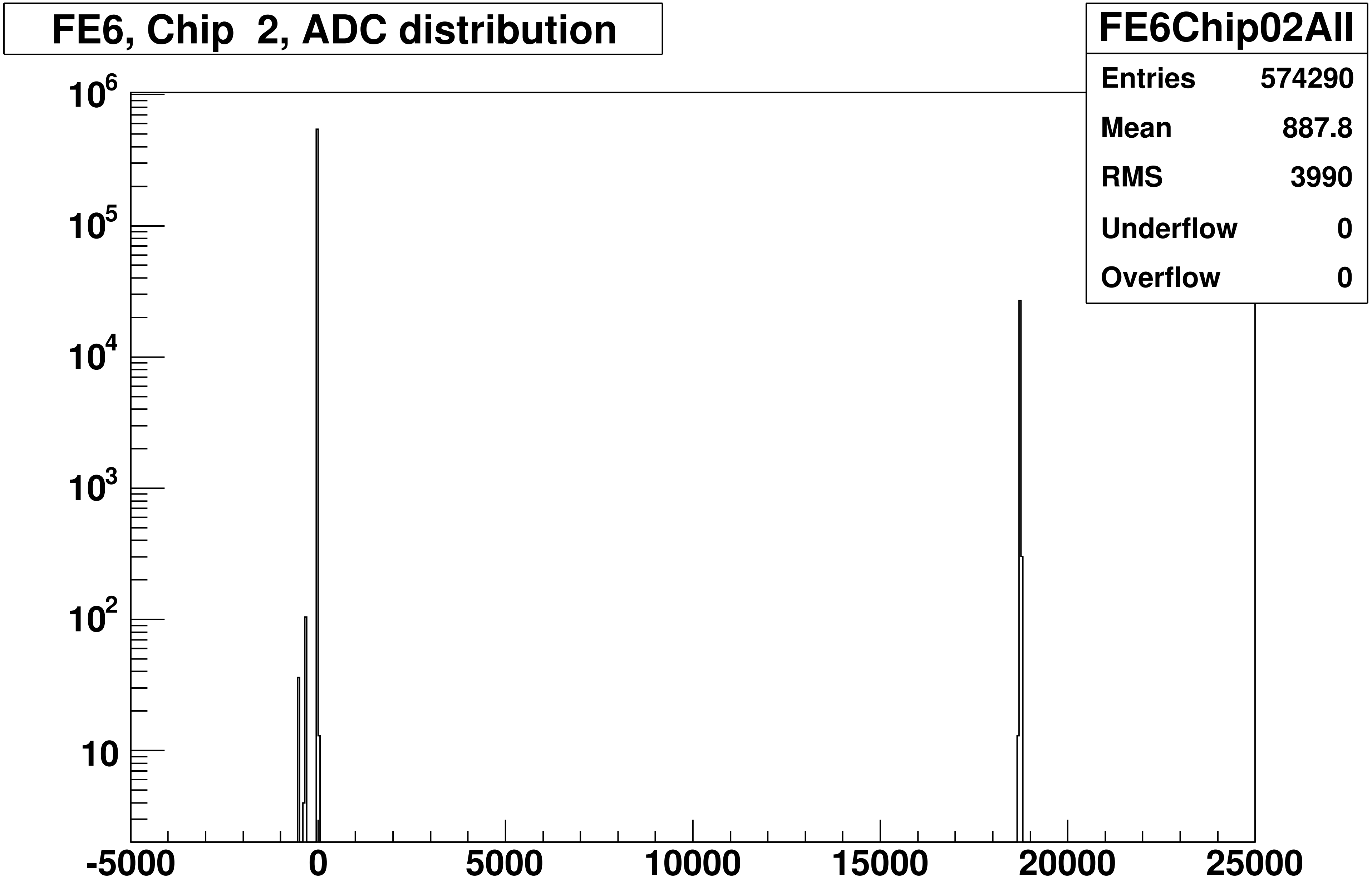
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 574290
Mean 887.8
RMS 3990
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
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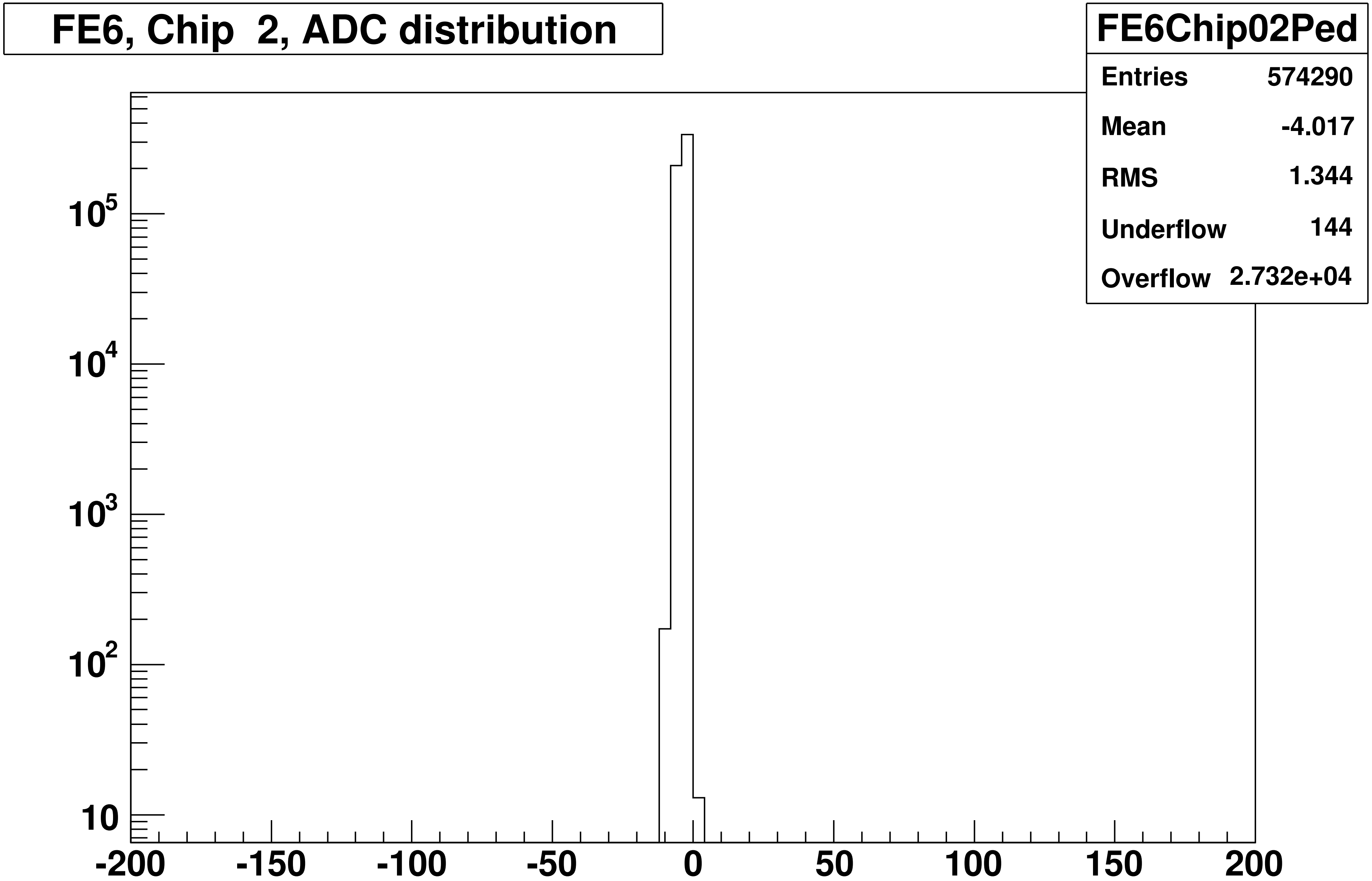
20000
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 574290
Mean -4.017
RMS 1.344
Underflow 144
Overflow 2.732e+04
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10^3
10^4
10^5
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-100
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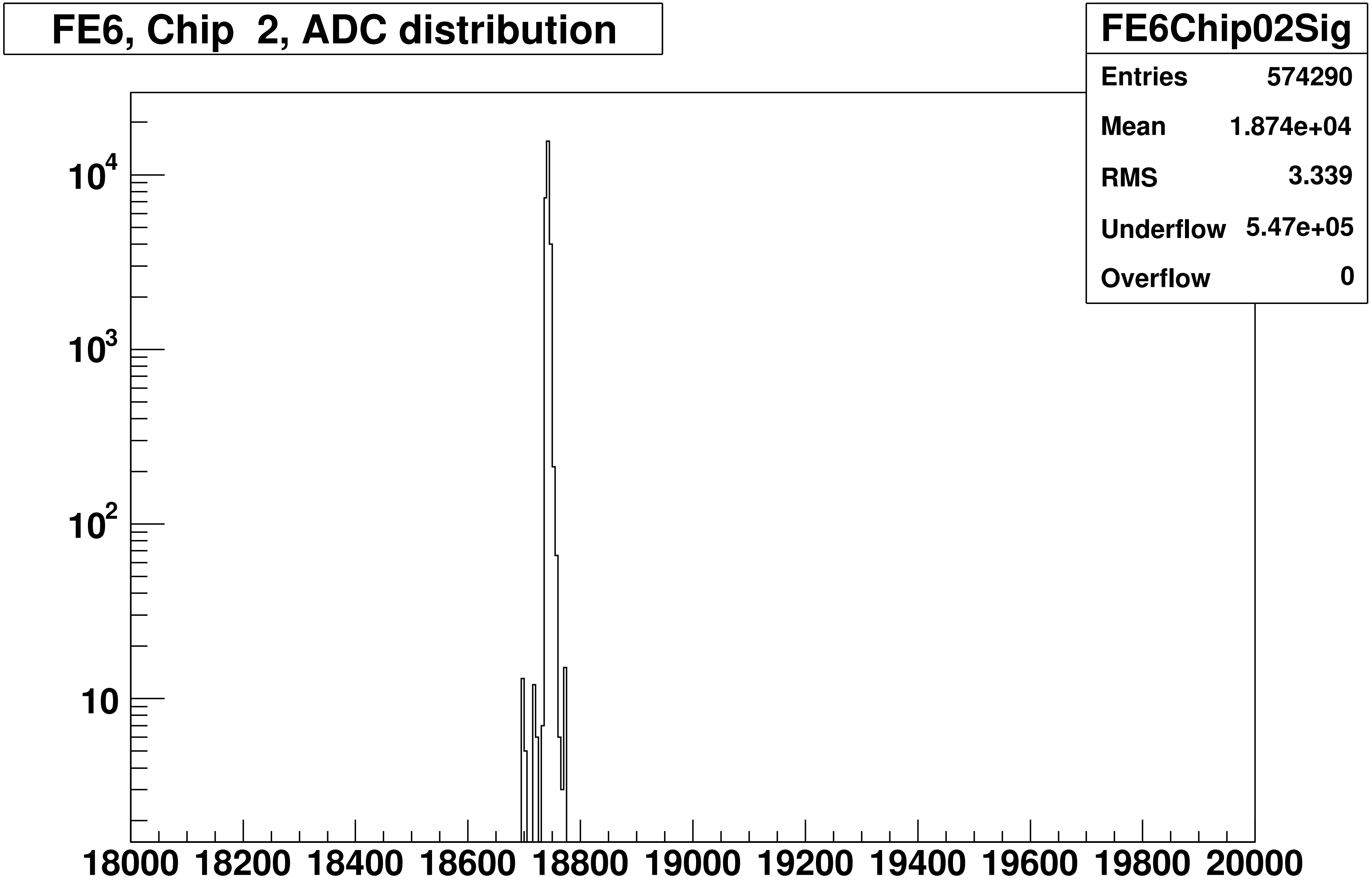
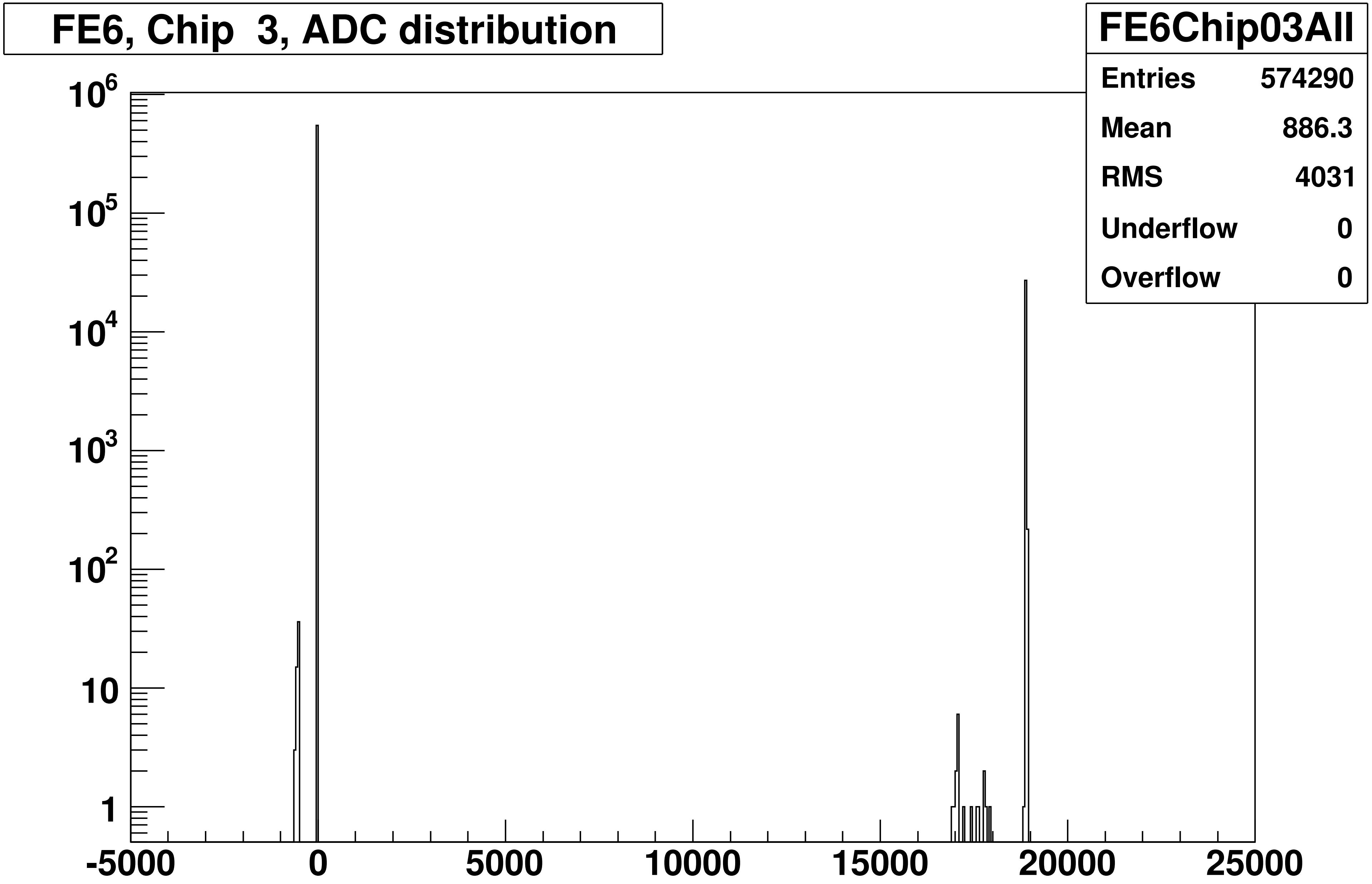
150
200



FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 574290
Mean 1.874e+04
RMS 3.339
Underflow 5.47e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 574290
Mean 886.3
RMS 4031
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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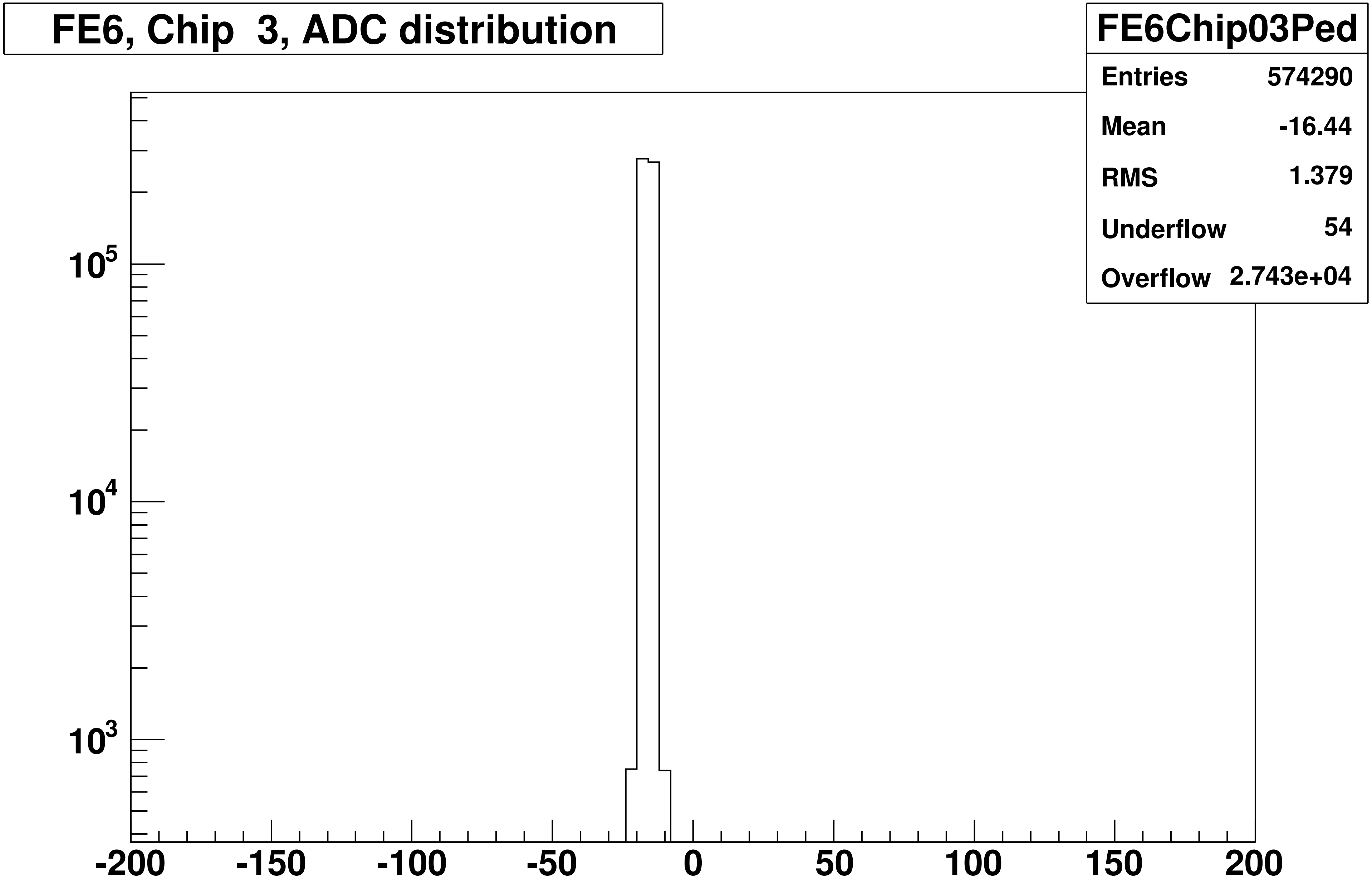
FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 574290
Mean -16.44
RMS 1.379
Underflow 54
Overflow 2.743e+04
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10^4
10^3
-200
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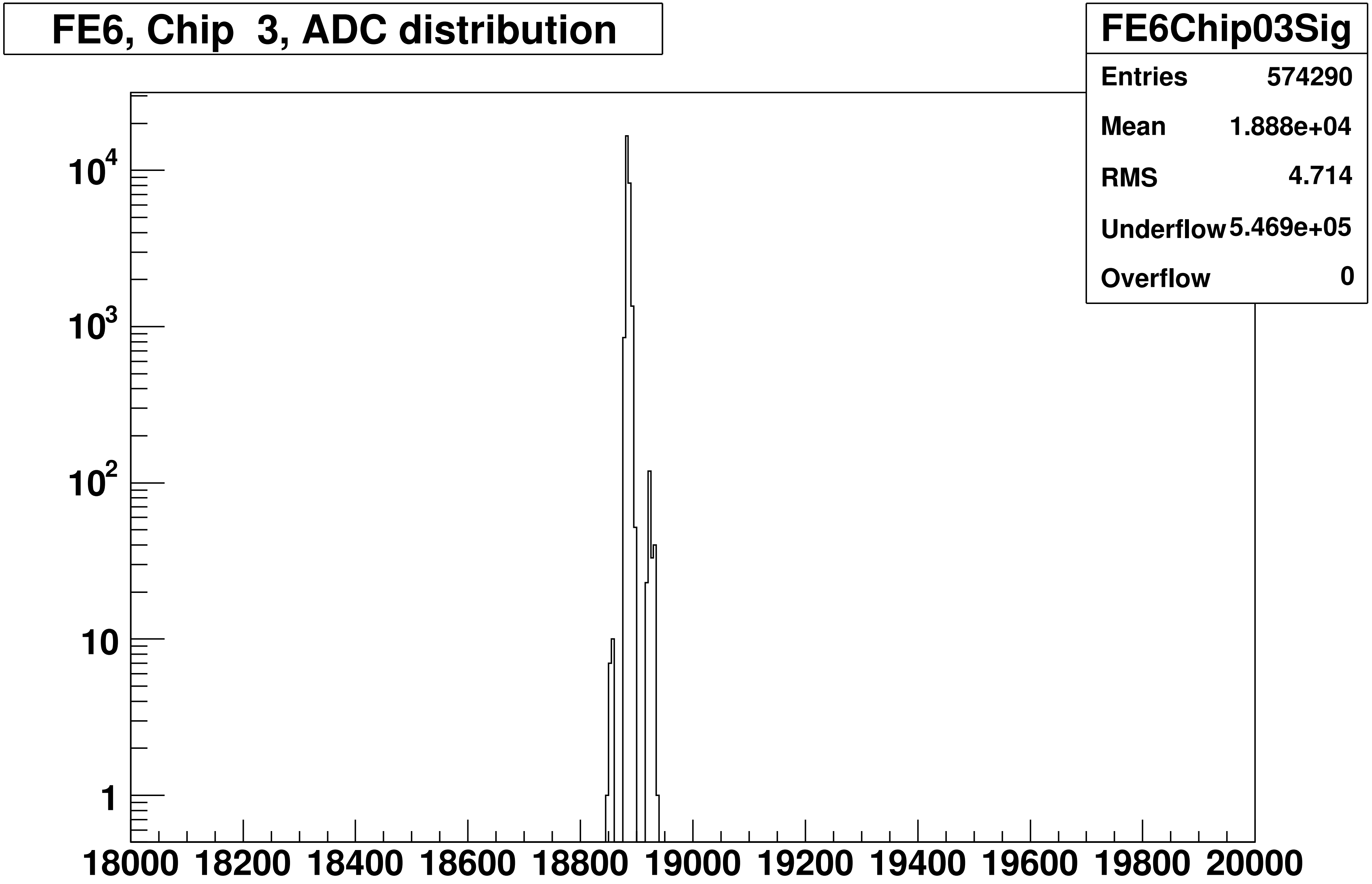
-100
-50
0
50
100
150
200


FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 574290
Mean 1.888e+04
RMS 4.714
Underflow 5.469e+05
Overflow 0
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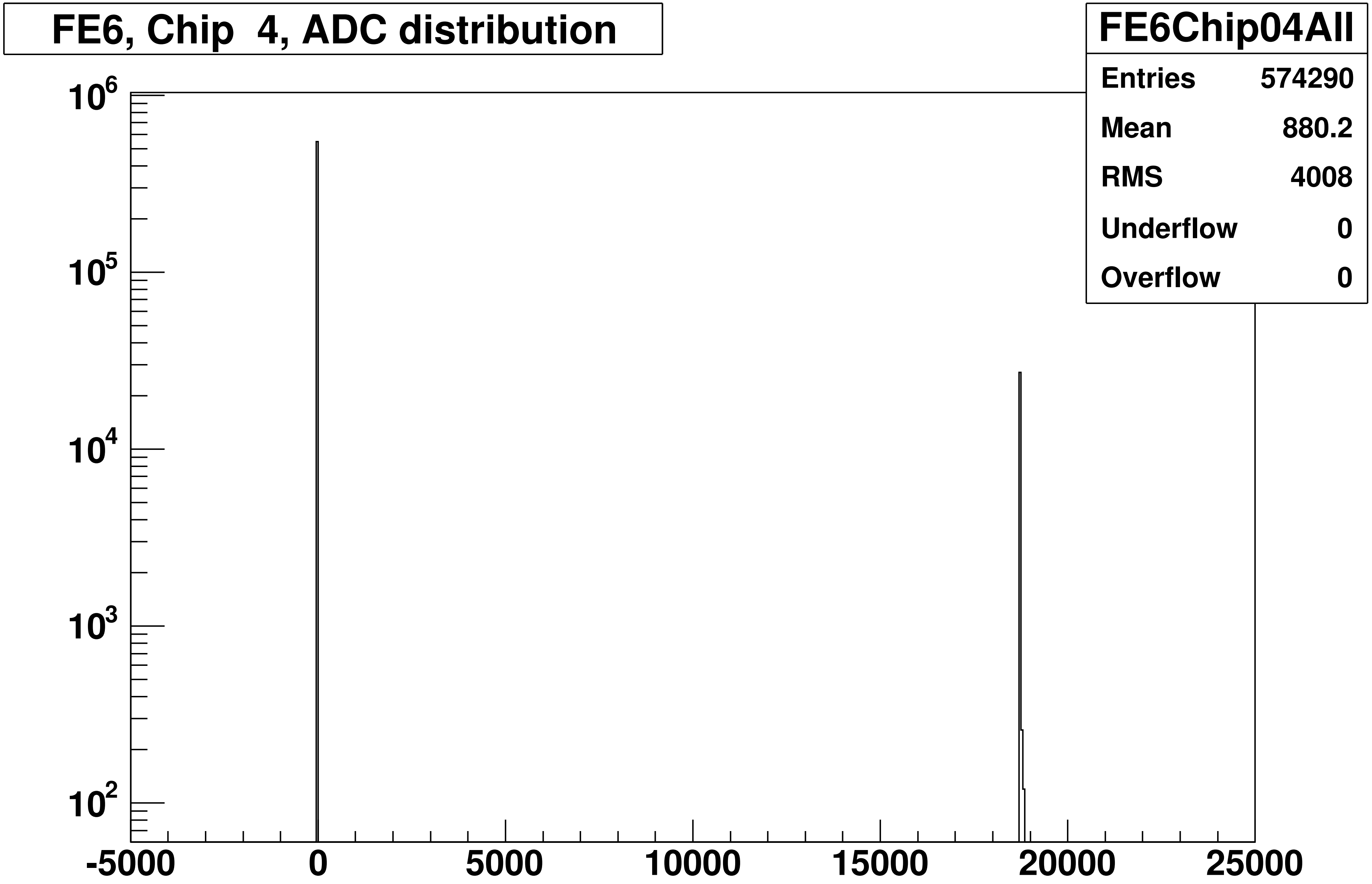
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 574290
Mean 880.2
RMS 4008
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
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10000
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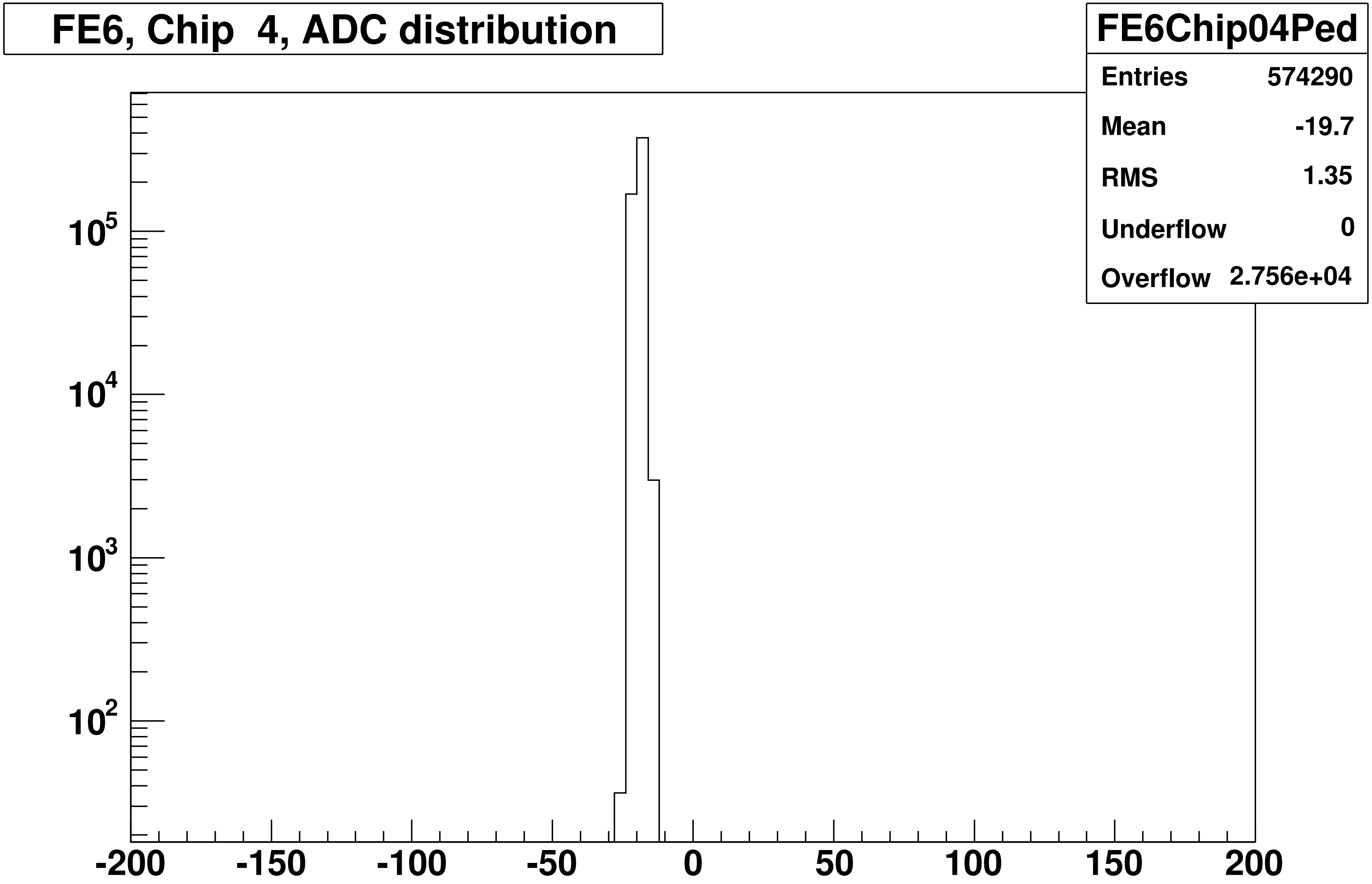
20000
25000



FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 574290
Mean -19.7
RMS 1.35
Underflow 0
Overflow 2.756e+04
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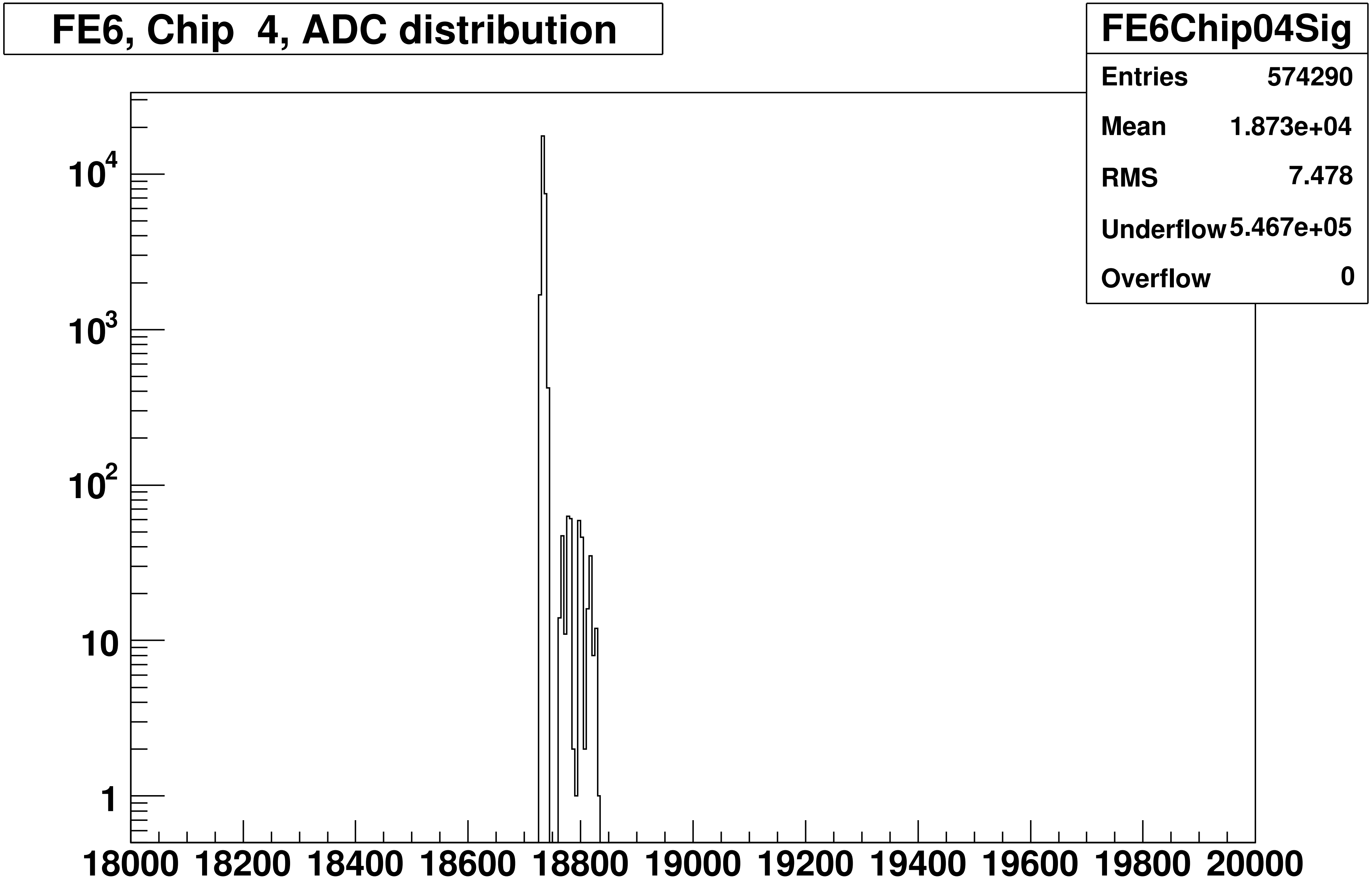
10^4
10^3
10^2
-200 -150 -100 -50 0 50 100 150 200



FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 574290
Mean 1.873e+04
RMS 7.478
Underflow 5.467e+05
Overflow 0
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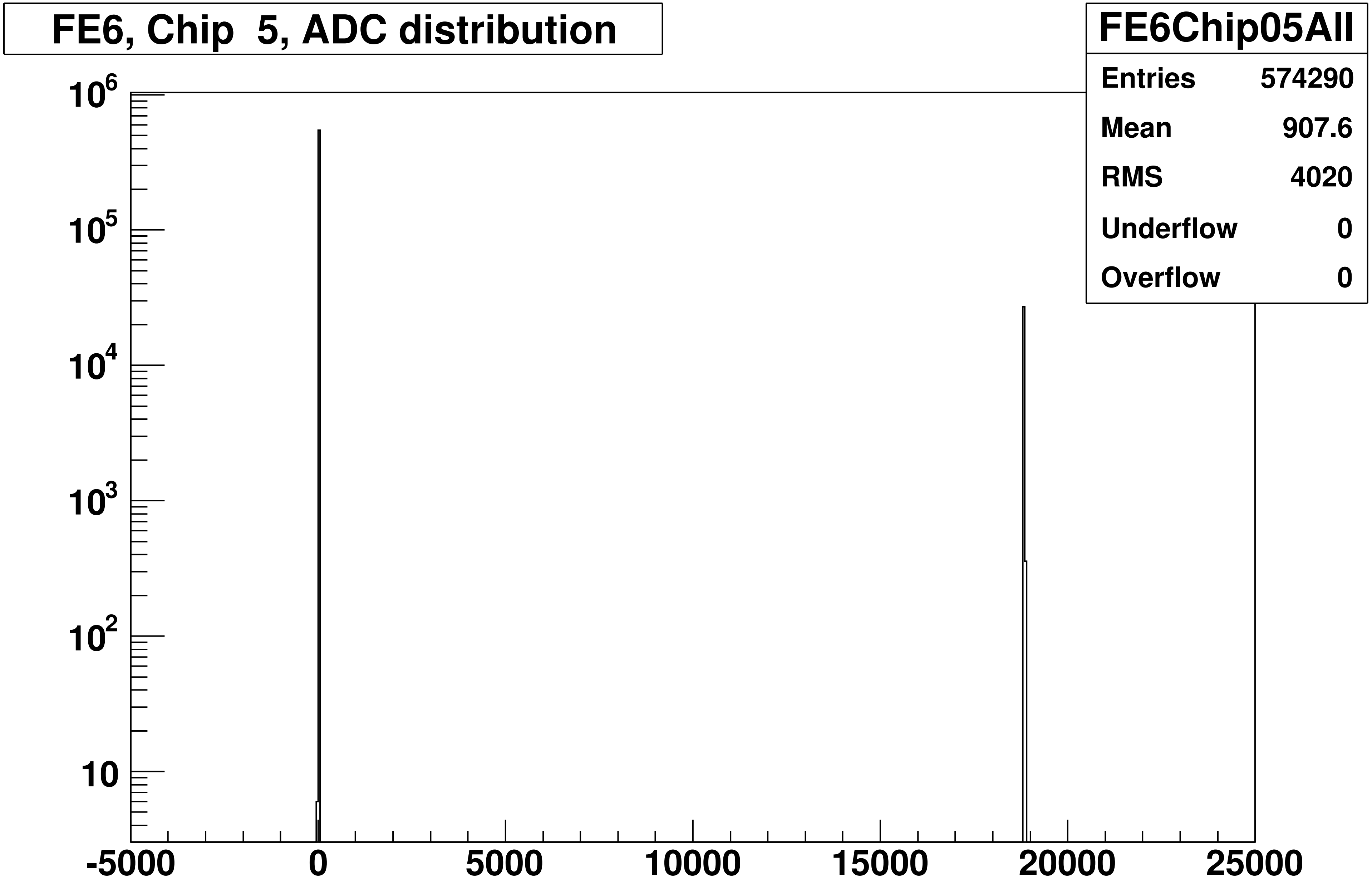
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 574290
Mean 907.6
RMS 4020
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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10000
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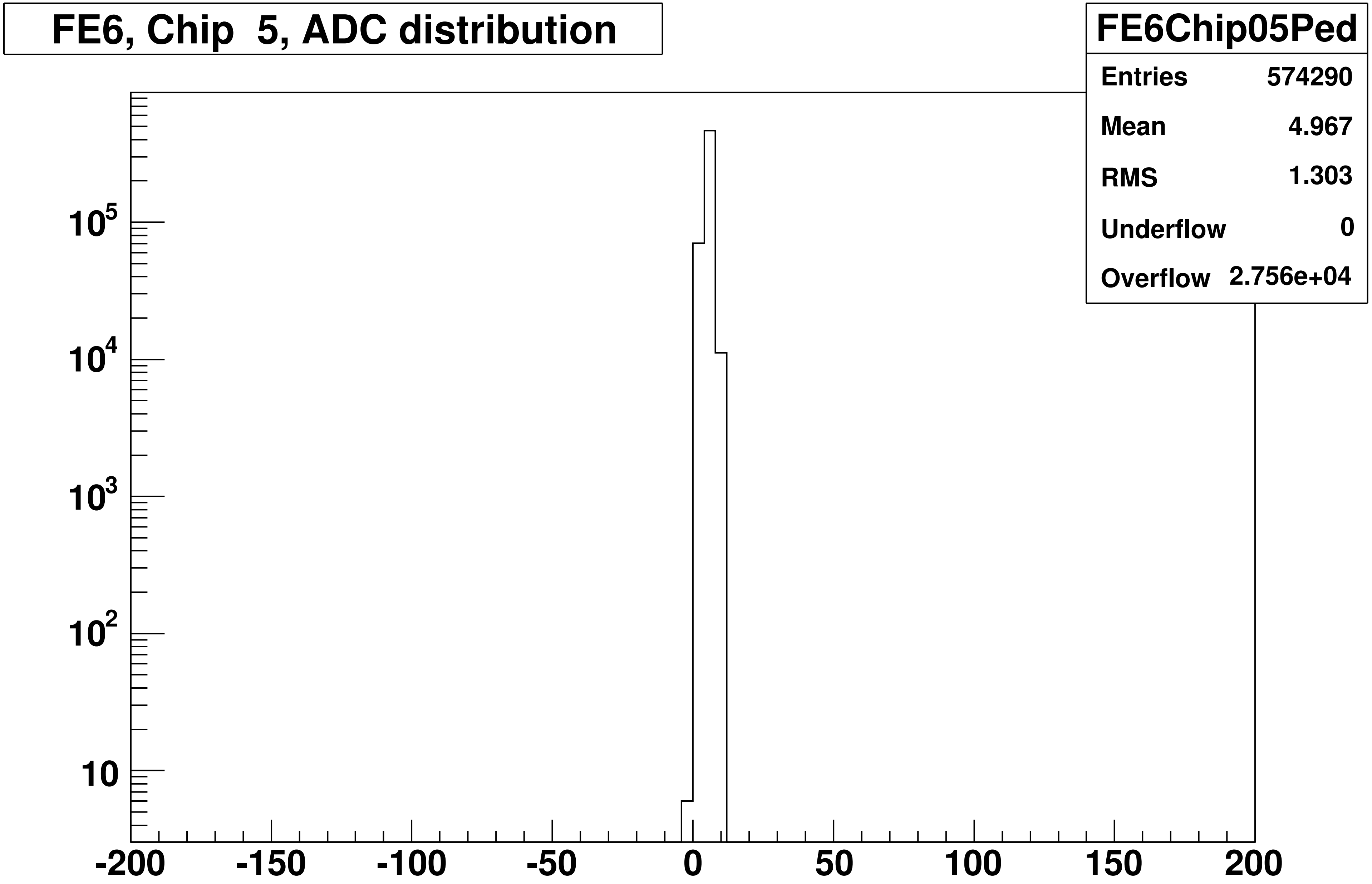
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 574290
Mean 4.967
RMS 1.303
Underflow 0
Overflow 2.756e+04
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10^3
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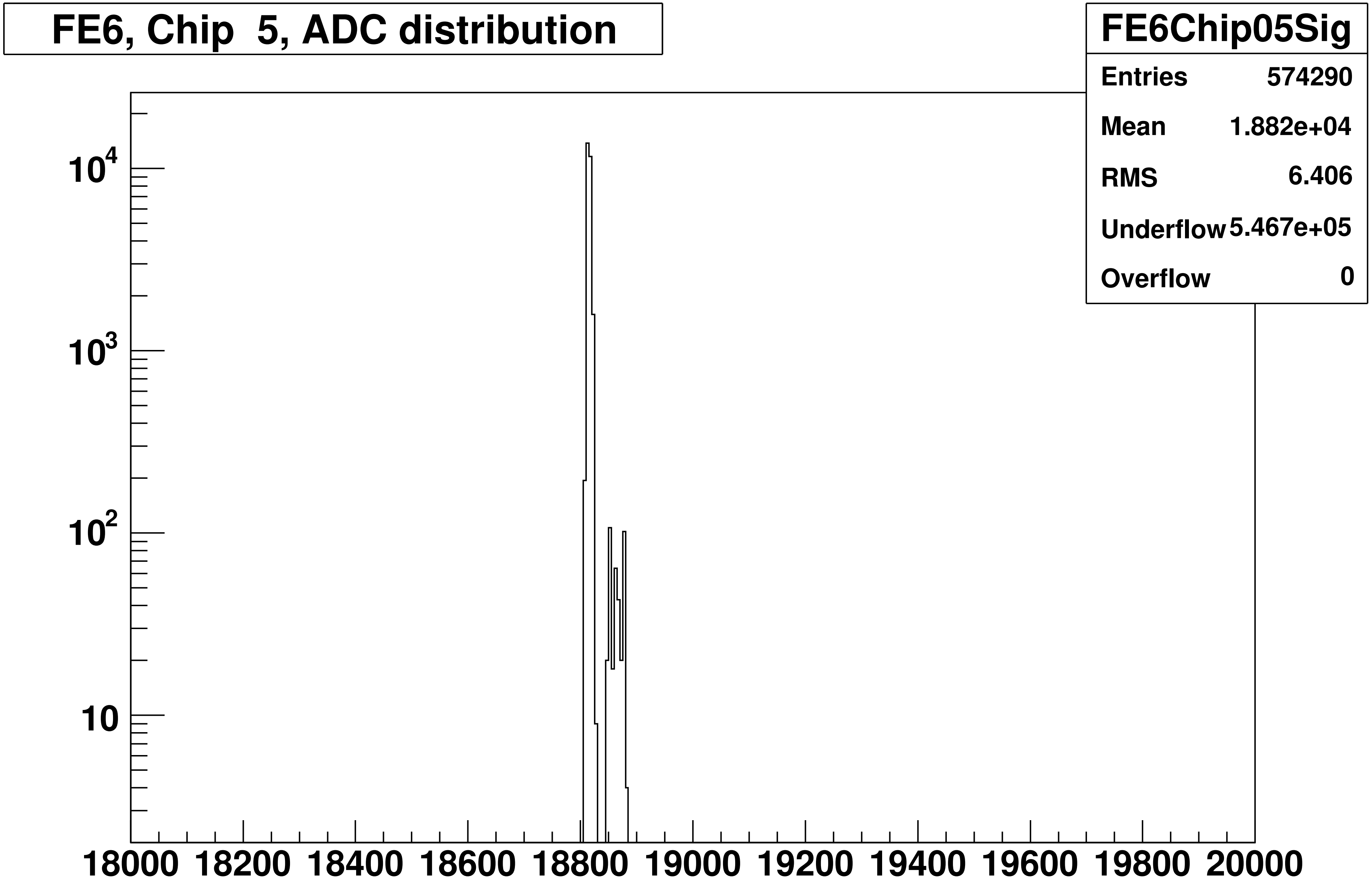
FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 574290
Mean 1.882e+04
RMS 6.406
Underflow 5.467e+05
Overflow 0
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10
18000
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18400
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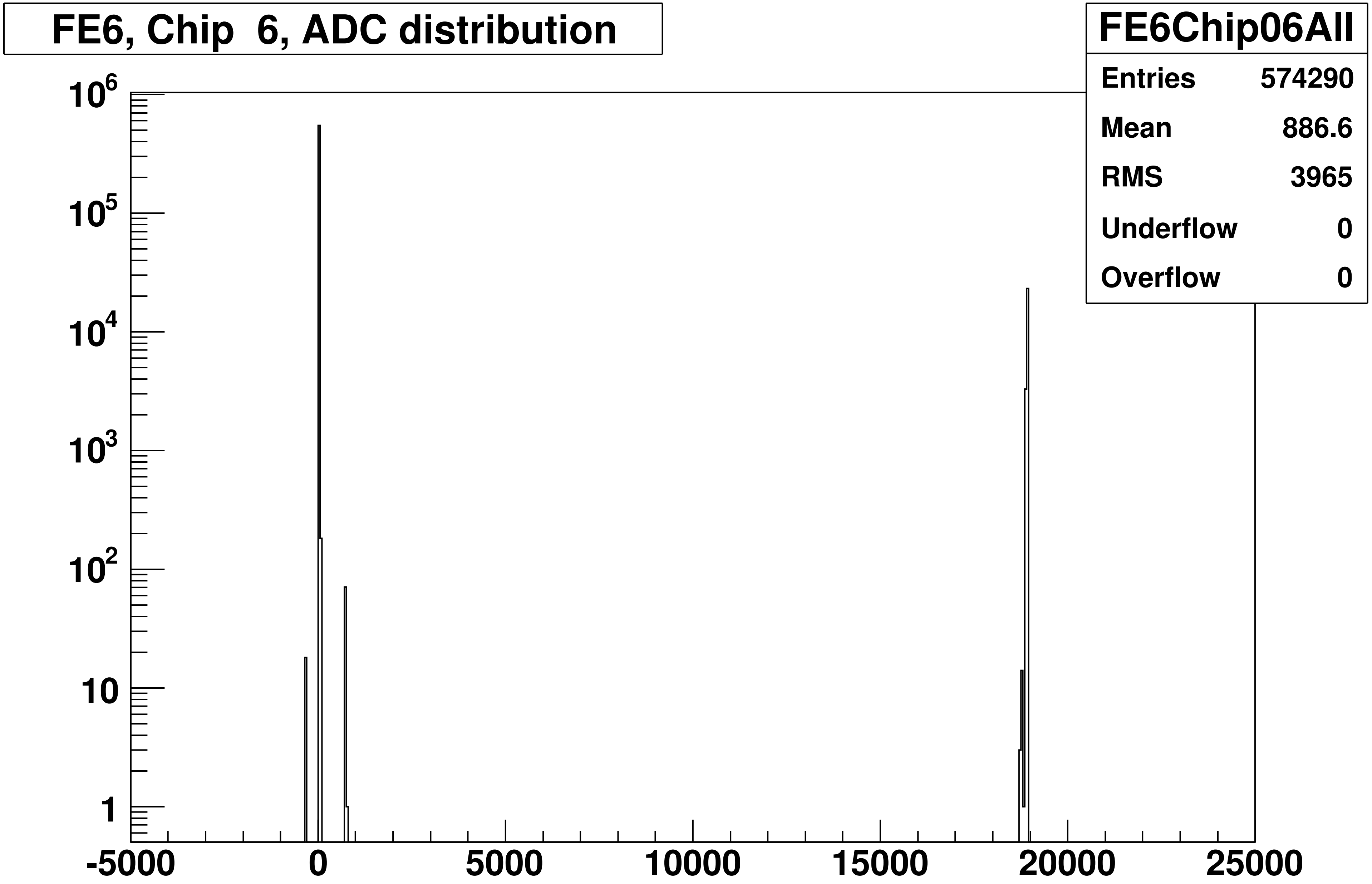

FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 574290
Mean 886.6
RMS 3965
Underflow 0
Overflow 0
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10^3
10^2
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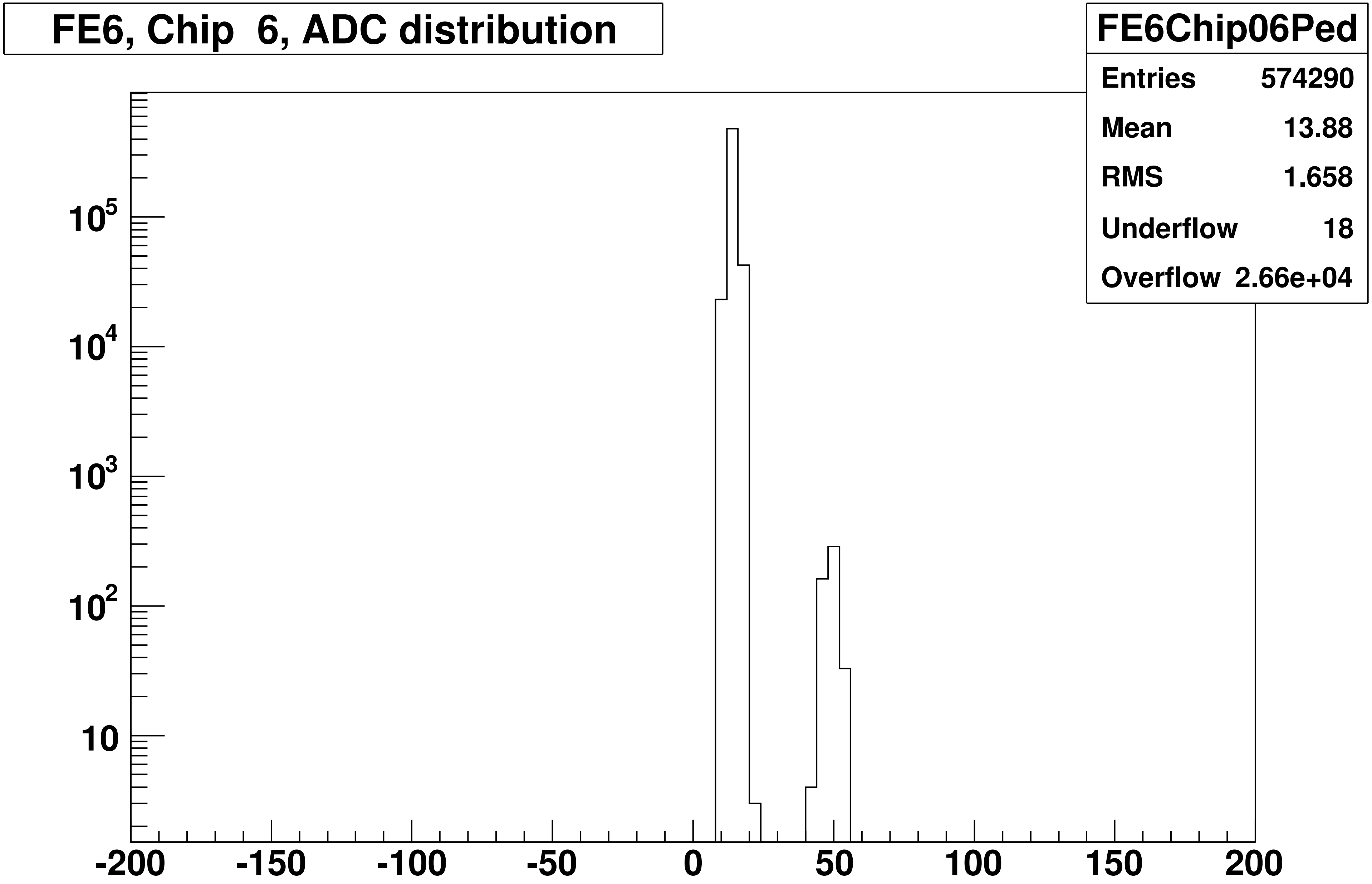

FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 574290
Mean 13.88
RMS 1.658
Underflow 18
Overflow 2.66e+04
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10^4
10^3
10^2
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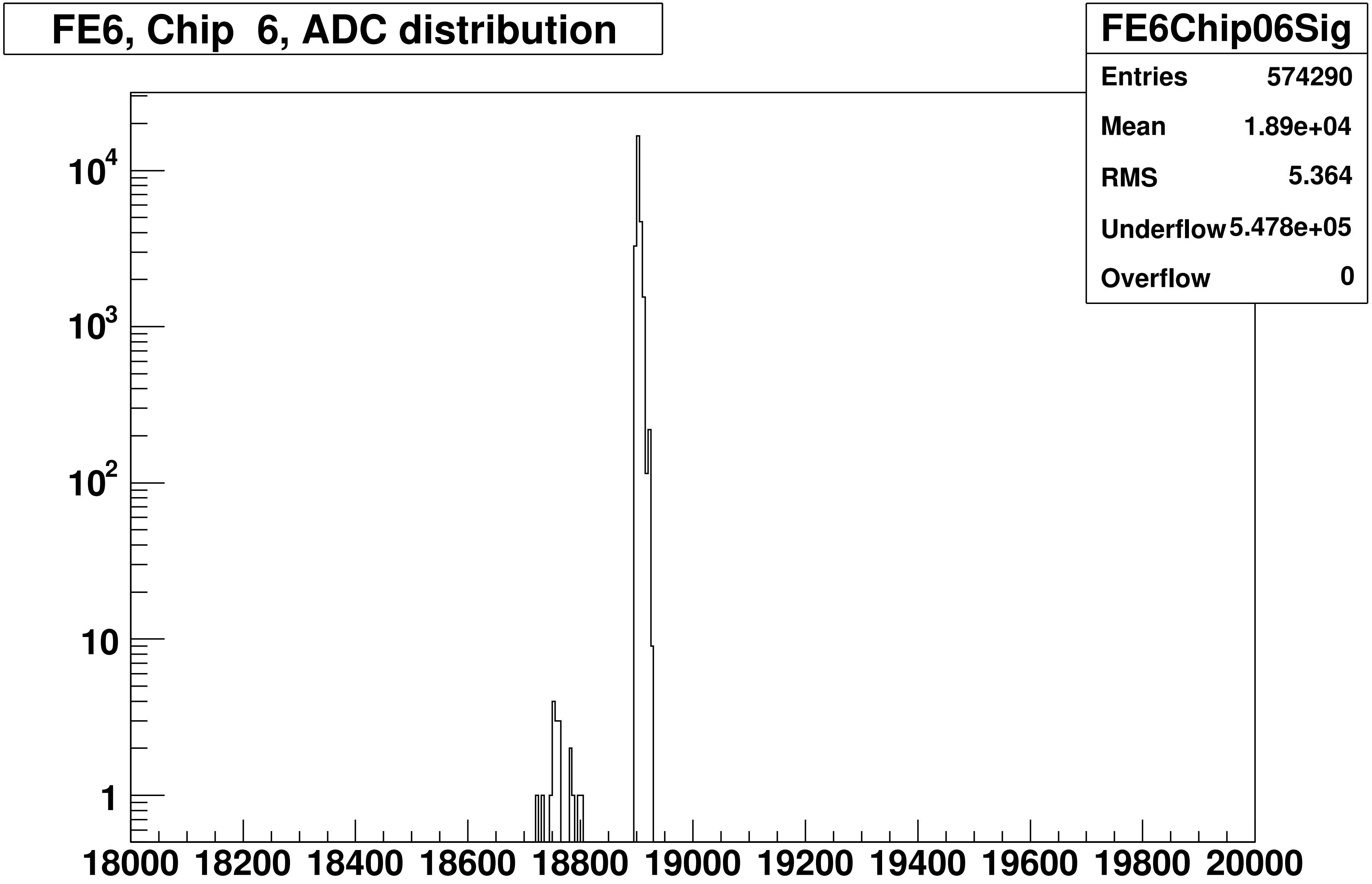
FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 574290
Mean 1.89e+04
RMS 5.364
Underflow 5.478e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
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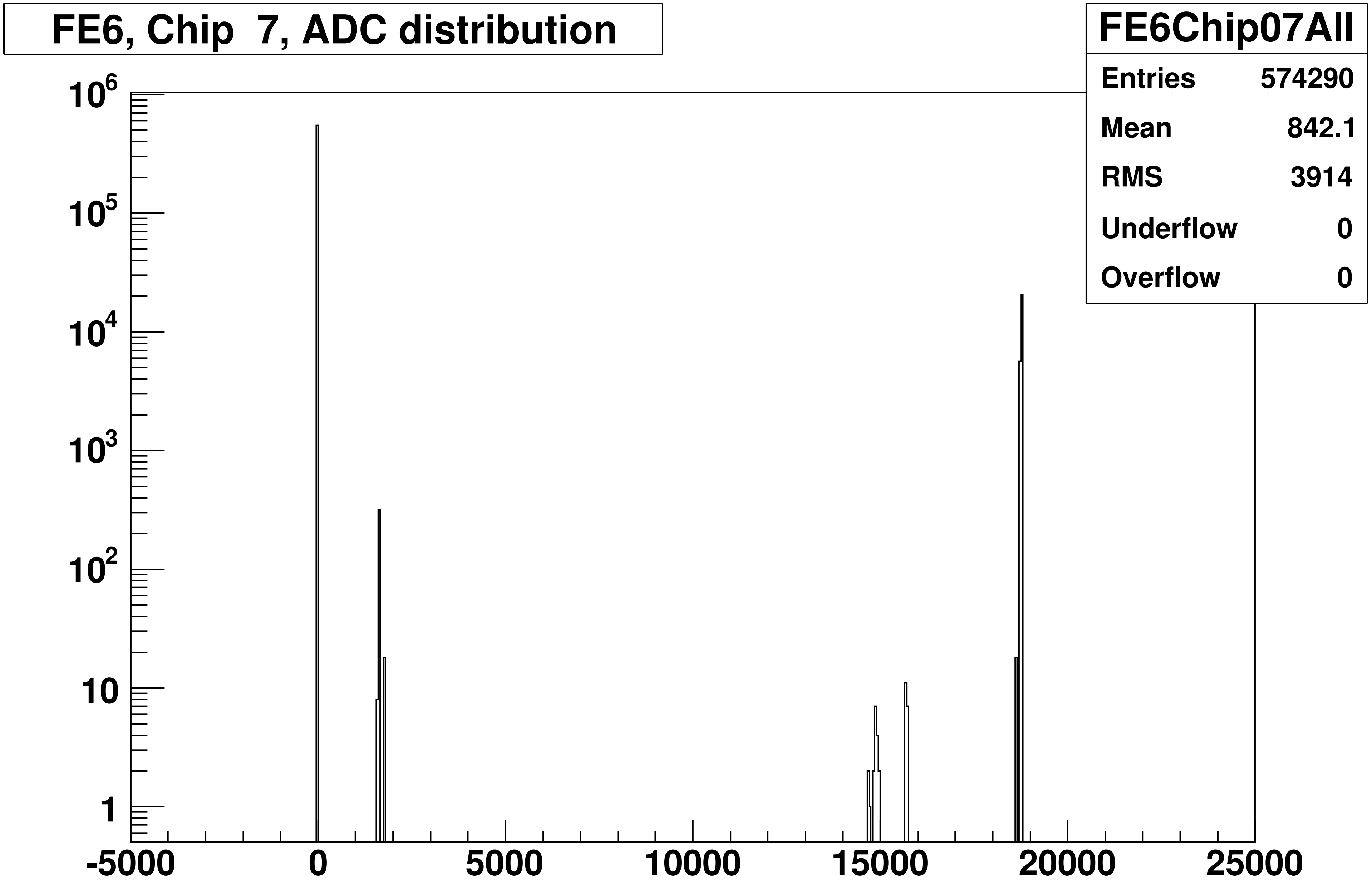
20000


FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 574290
Mean 842.1
RMS 3914
Underflow 0
Overflow 0
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10^3
10^2
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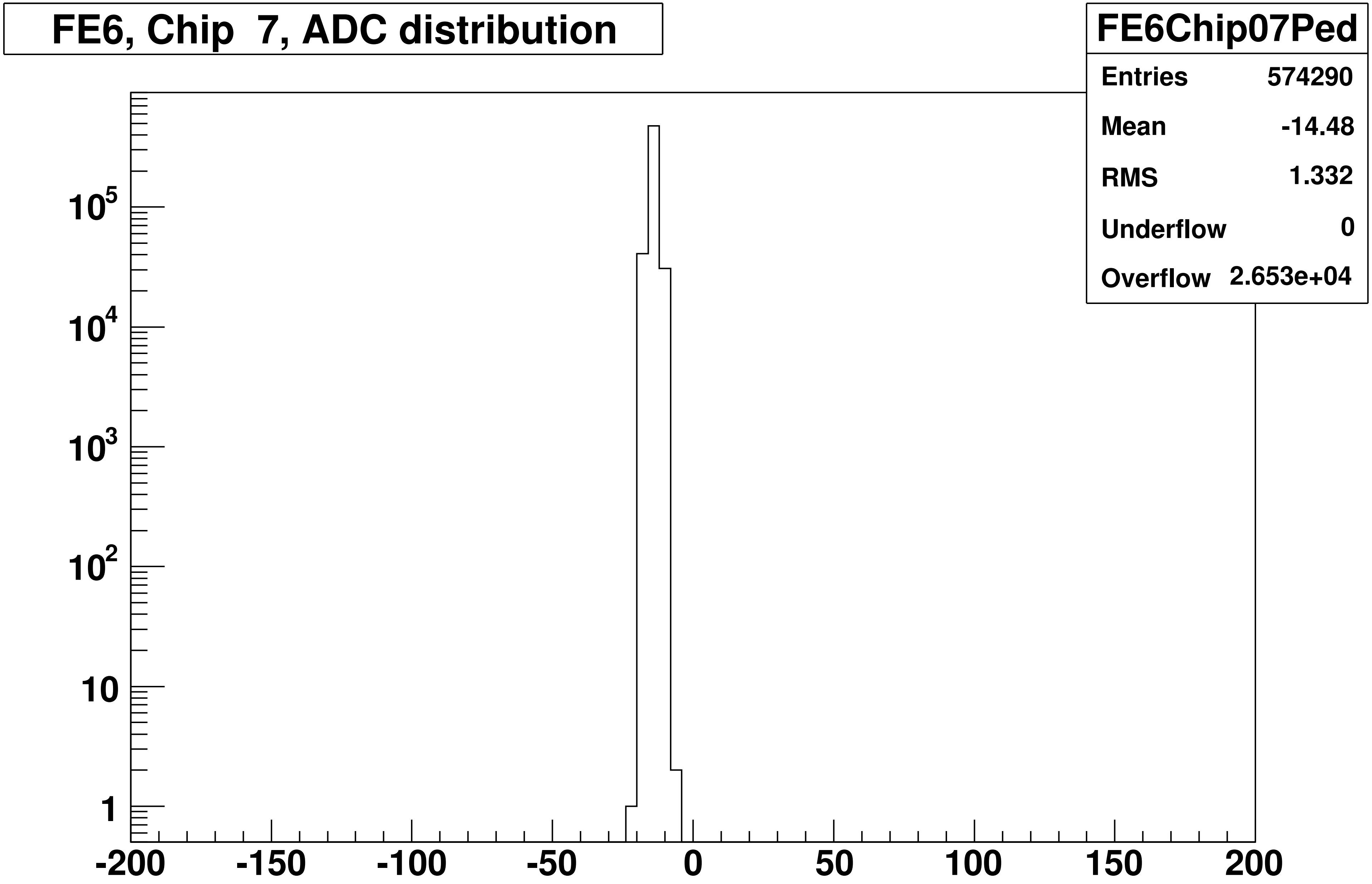
FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 574290
Mean -14.48
RMS 1.332
Underflow 0
Overflow 2.653e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
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50
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150
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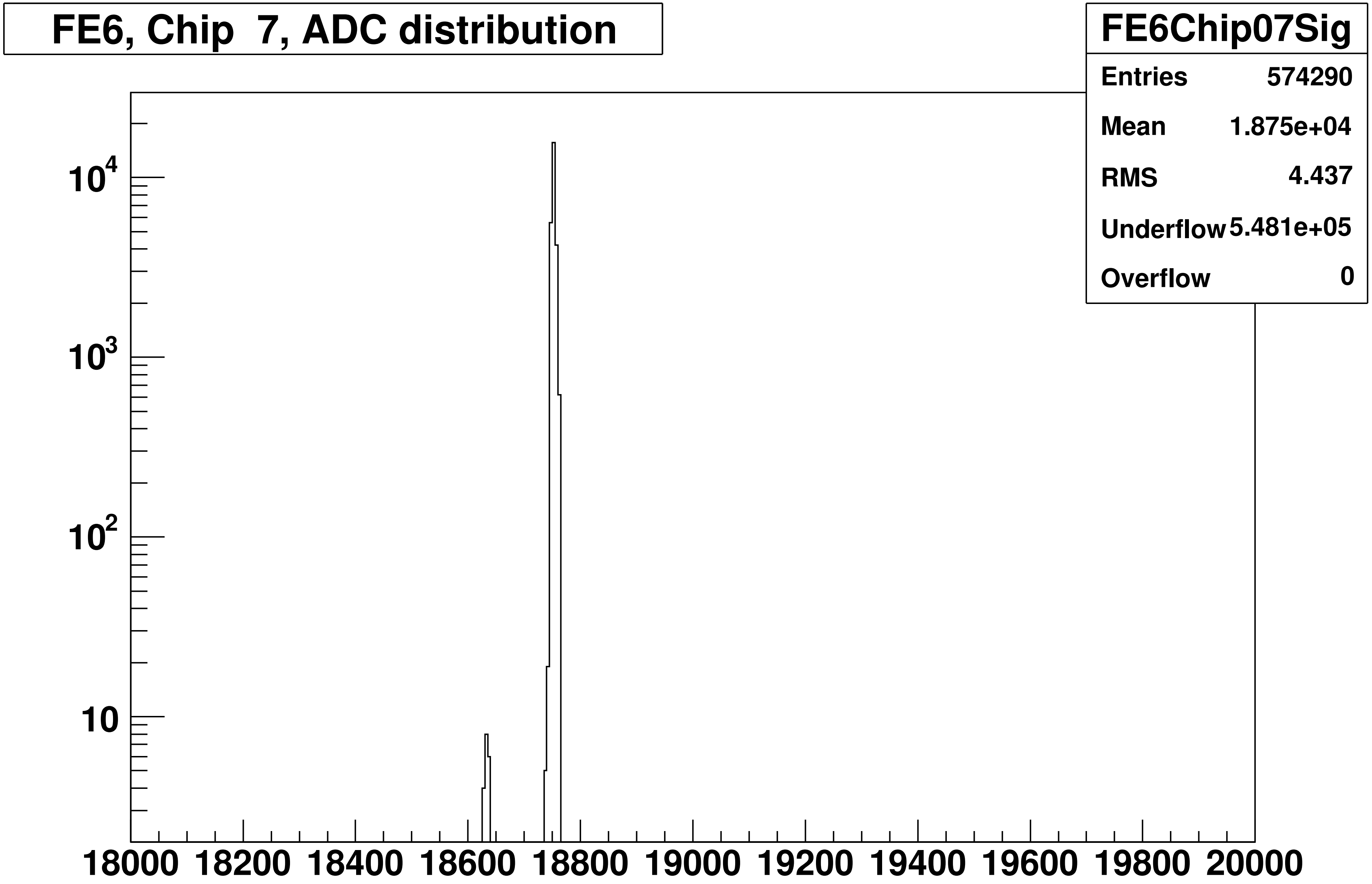
FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 574290
Mean 1.875e+04
RMS 4.437
Underflow 5.481e+05
Overflow 0
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10^3
10^2
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18000
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18400
18600
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19000
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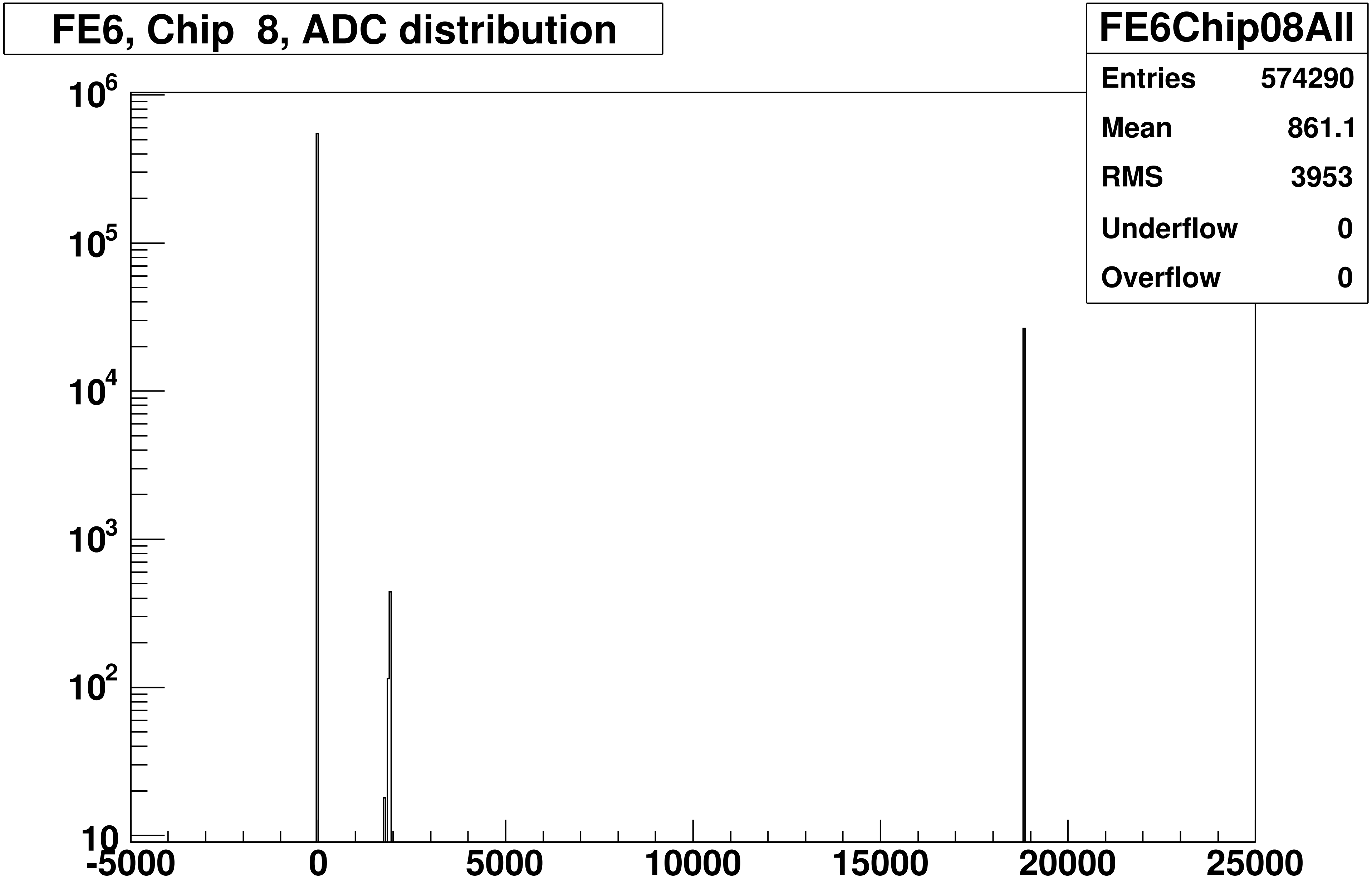
FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 574290
Mean 861.1
RMS 3953
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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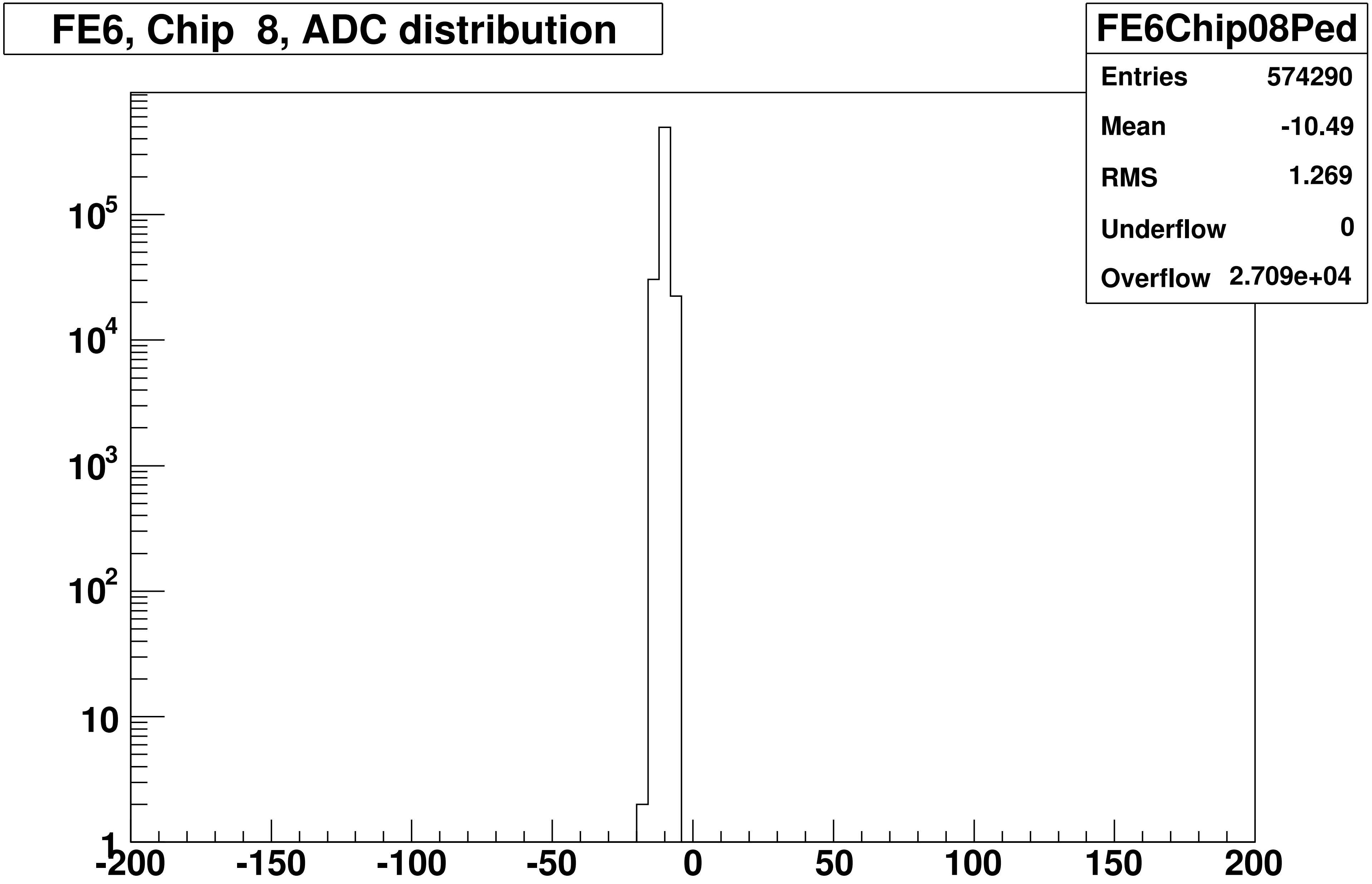
FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 574290
Mean -10.49
RMS 1.269
Underflow 0
Overflow 2.709e+04
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10^3
10^2
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150
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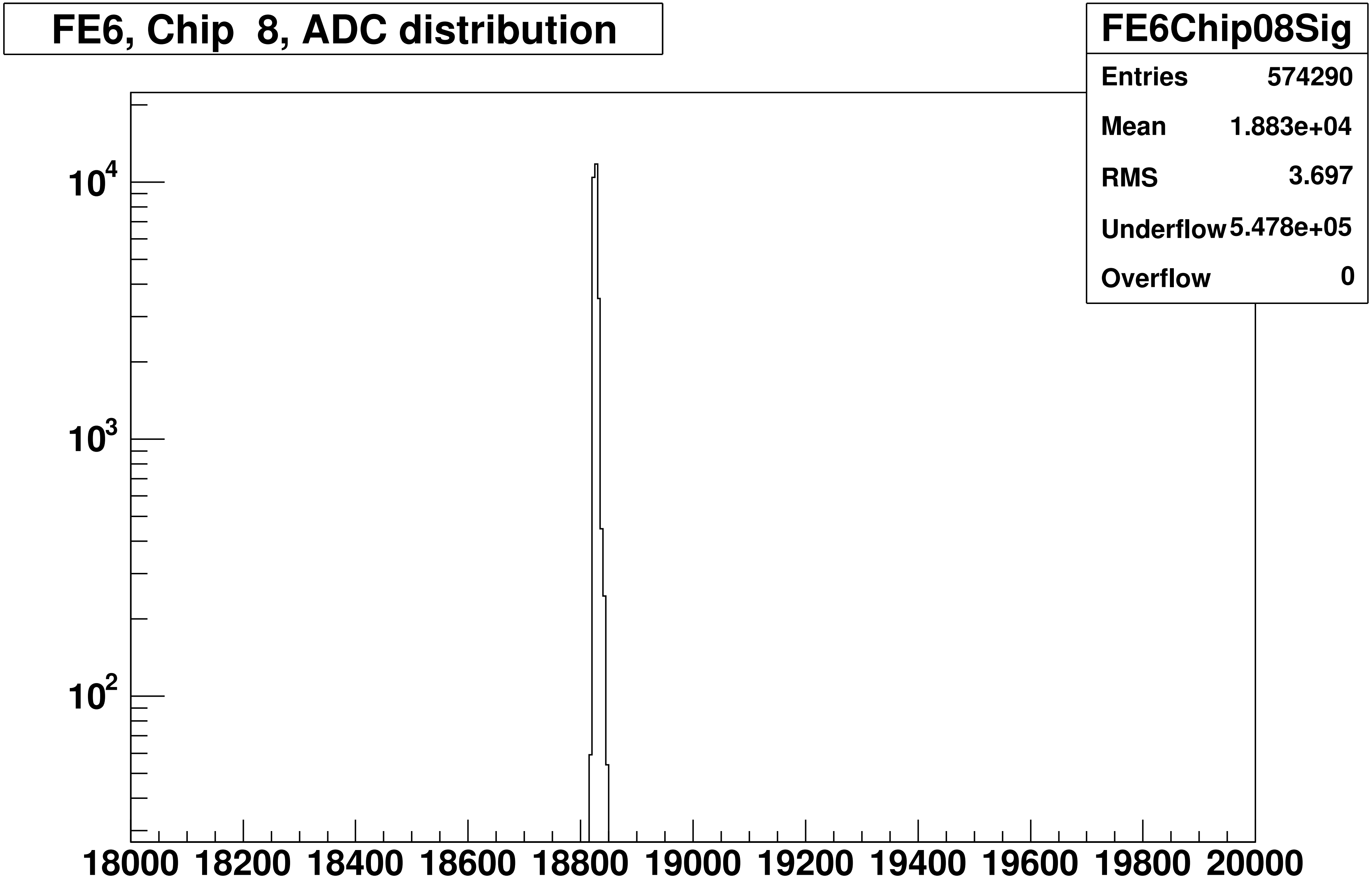

FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 574290
Mean 1.883e+04
RMS 3.697
Underflow 5.478e+05
Overflow 0
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10^2
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18600
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19000
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19800
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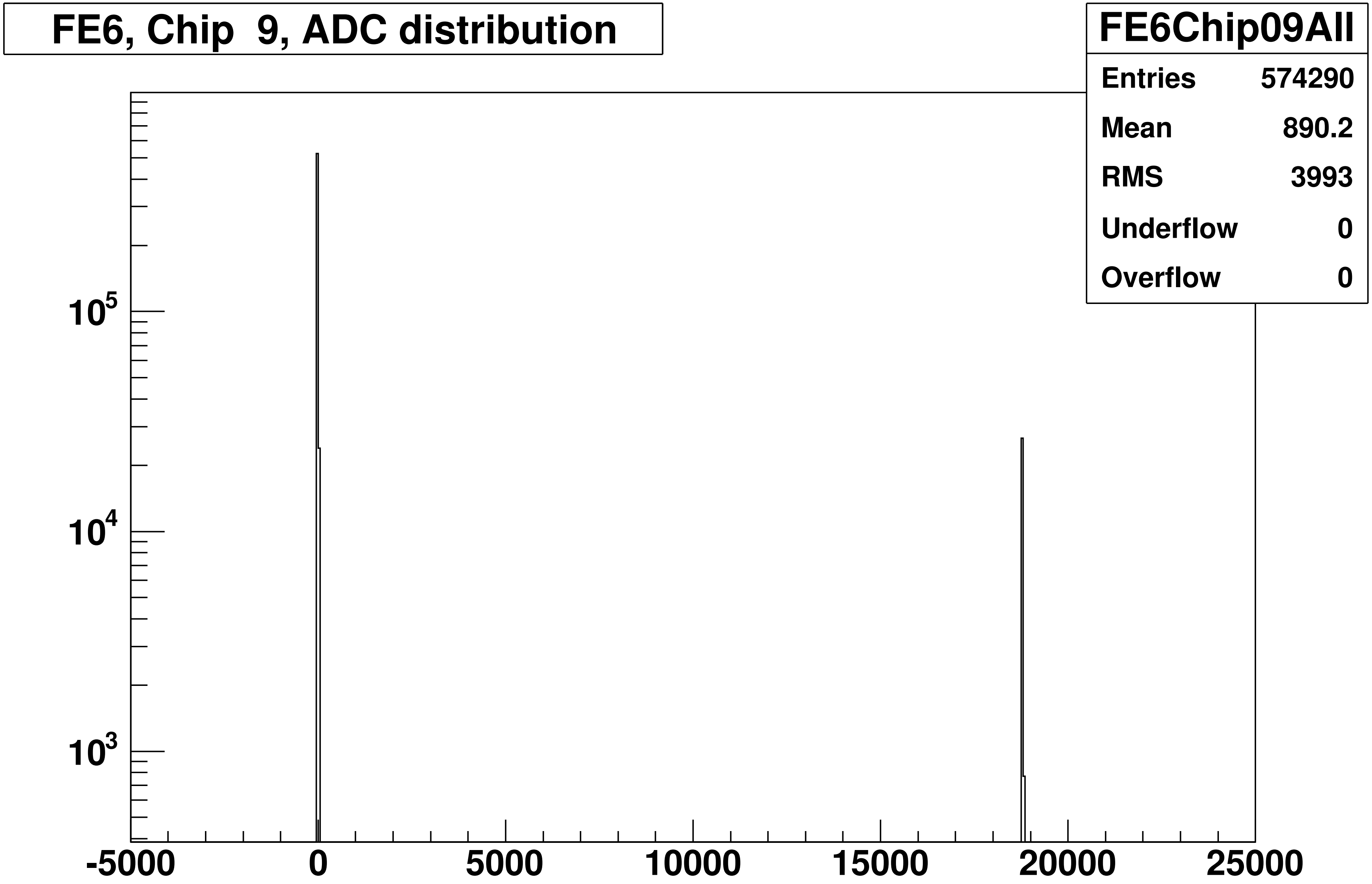
FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 574290
Mean 890.2
RMS 3993
Underflow 0
Overflow 0
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10^4
10^3
-5000
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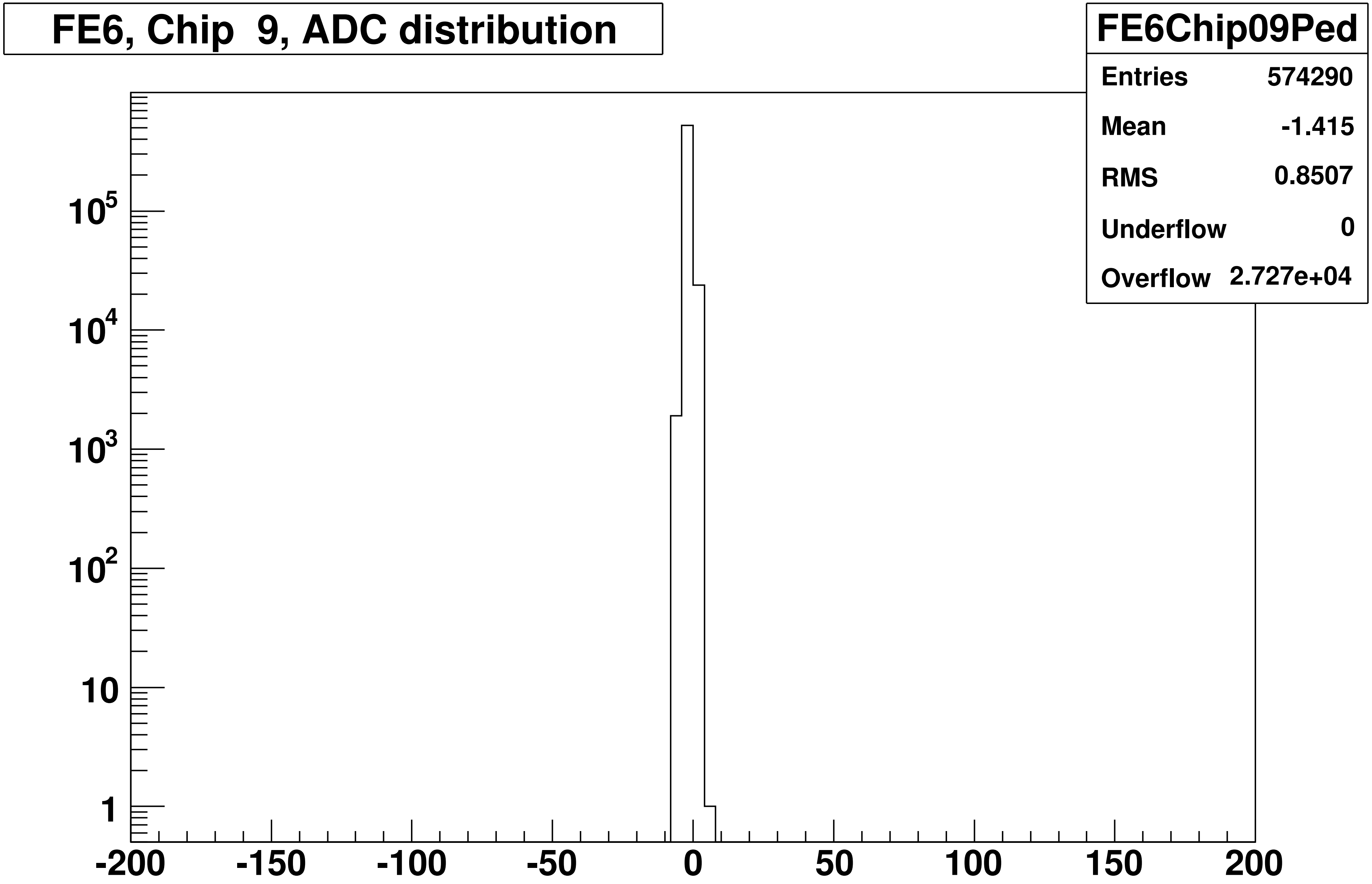
10000
15000
20000
25000


FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 574290
Mean -1.415
RMS 0.8507
Underflow 0
Overflow 2.727e+04
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10^3
10^2
10
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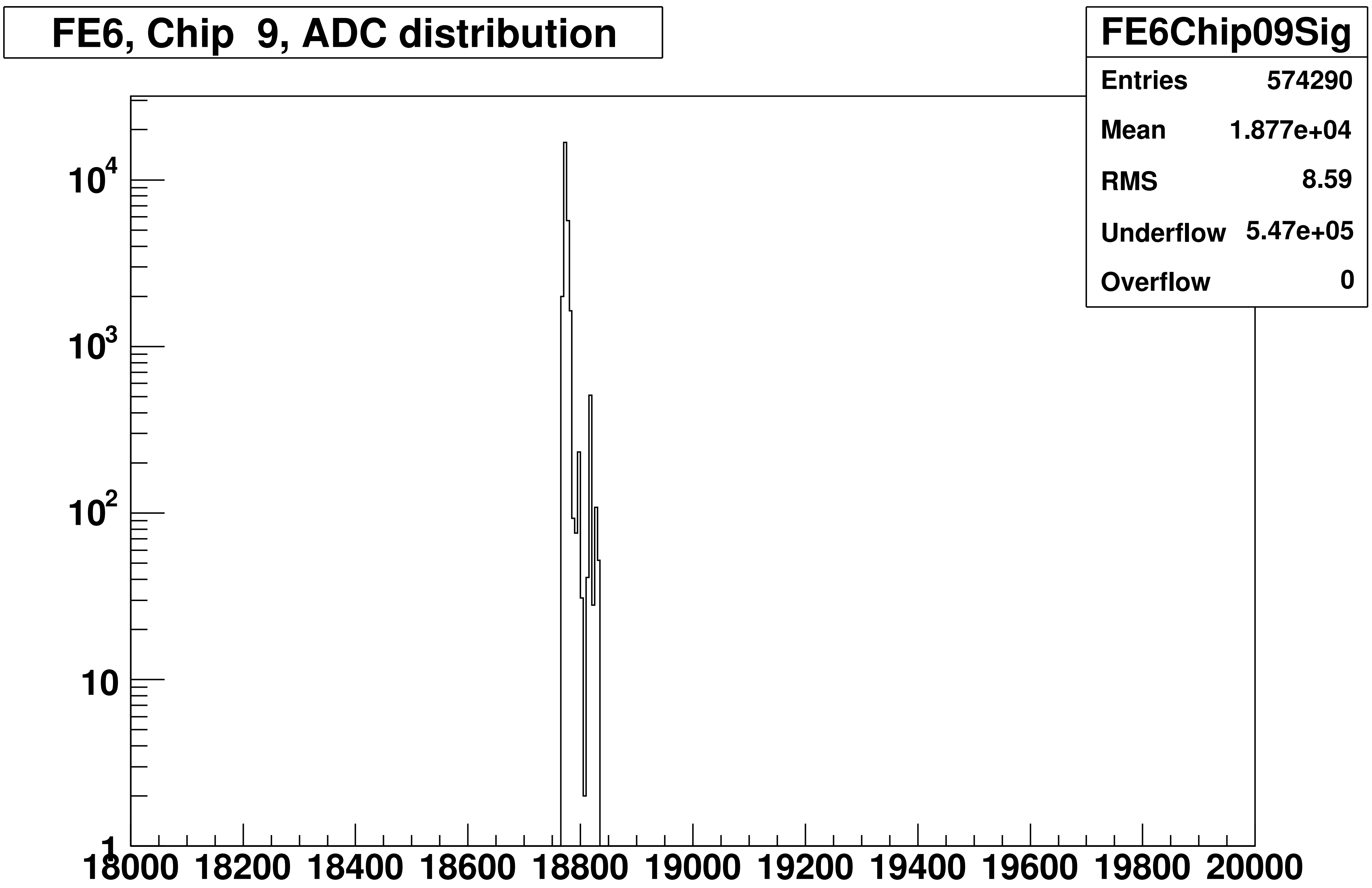
150
200


FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 574290
Mean 1.877e+04
RMS 8.59
Underflow 5.47e+05
Overflow 0
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10^3
10^2
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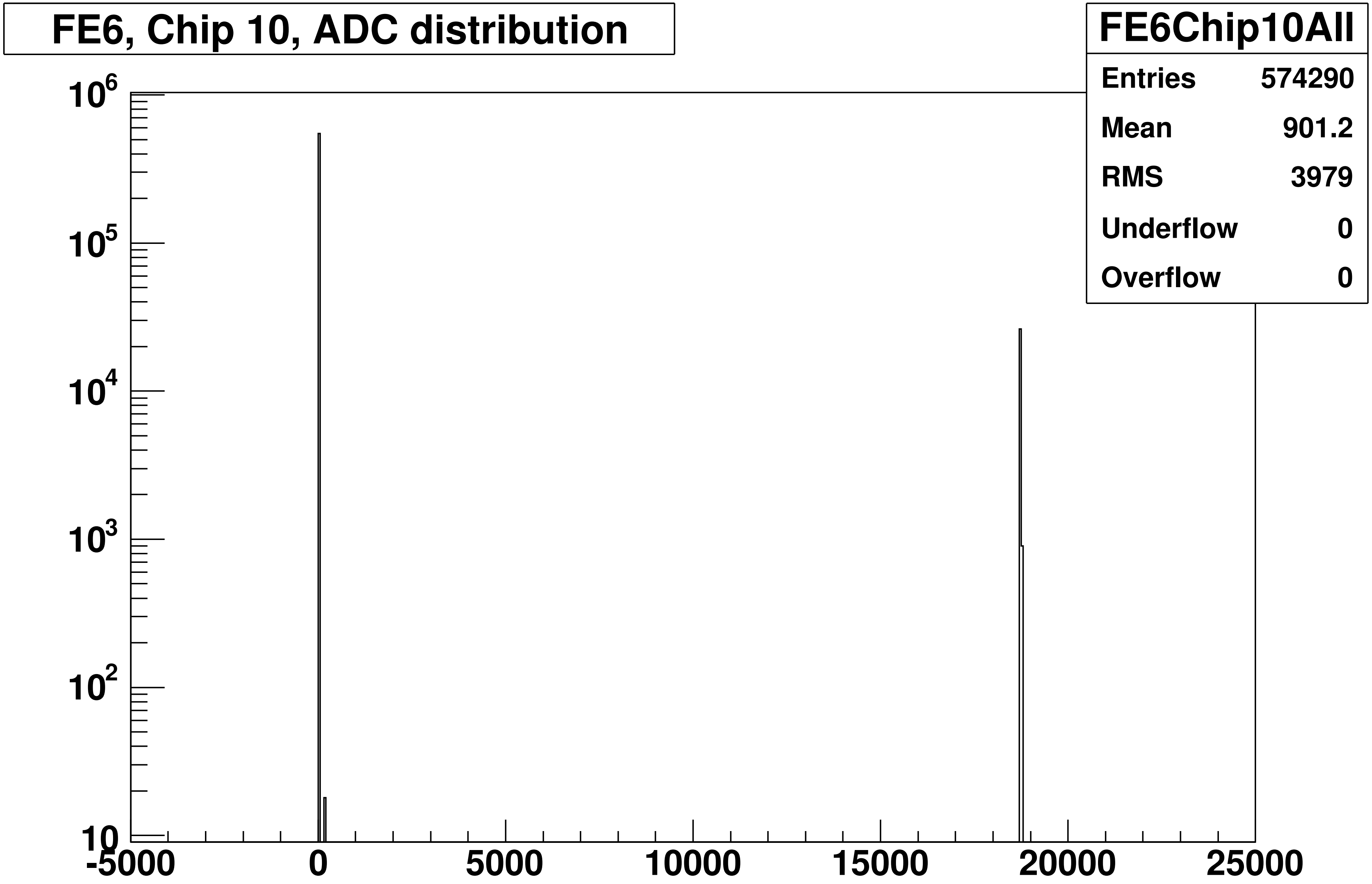
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 574290
Mean 901.2
RMS 3979
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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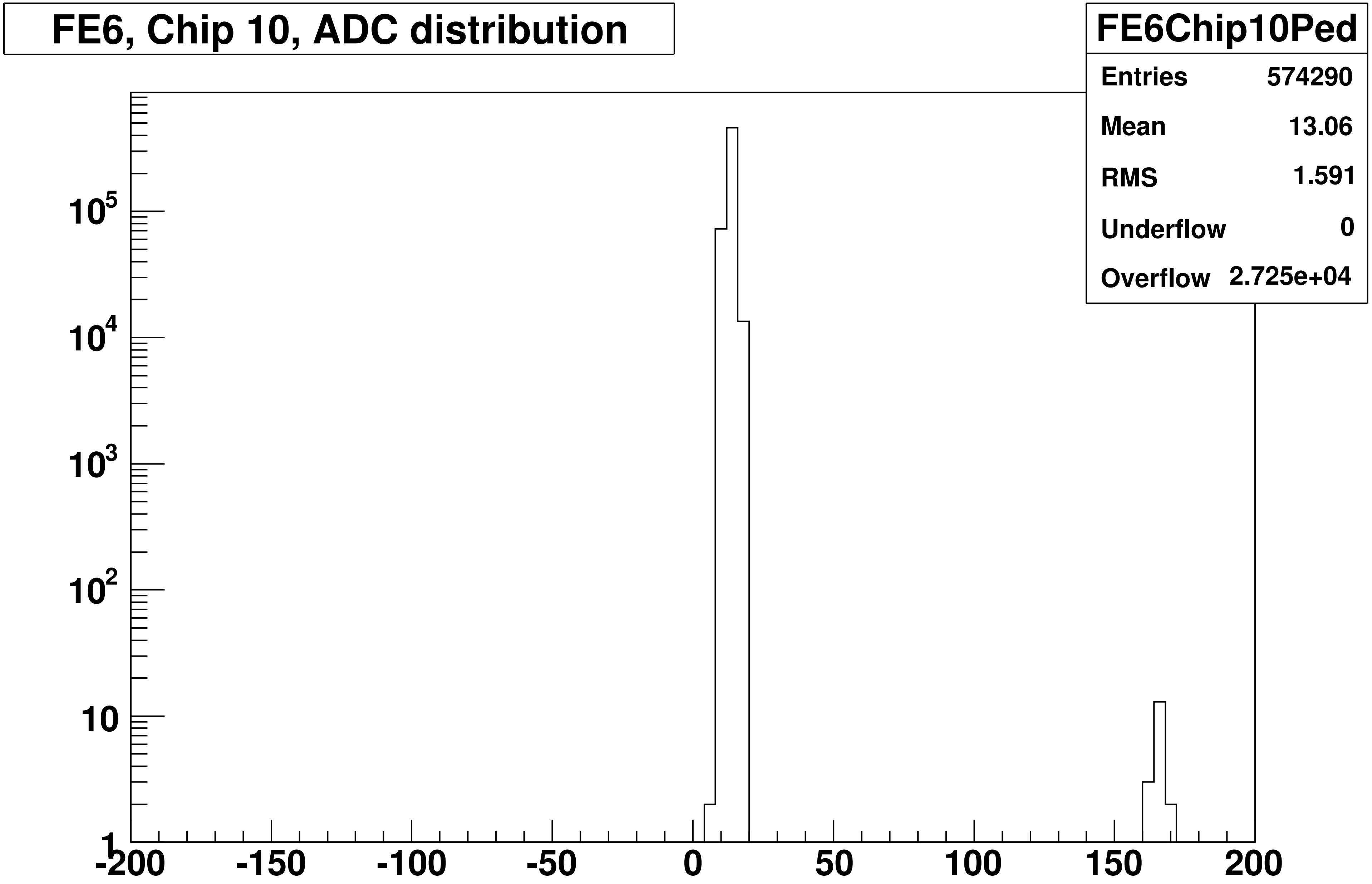
25000


FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 574290
Mean 13.06
RMS 1.591
Underflow 0
Overflow 2.725e+04
10^5
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10^3
10^2
10
1
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-100
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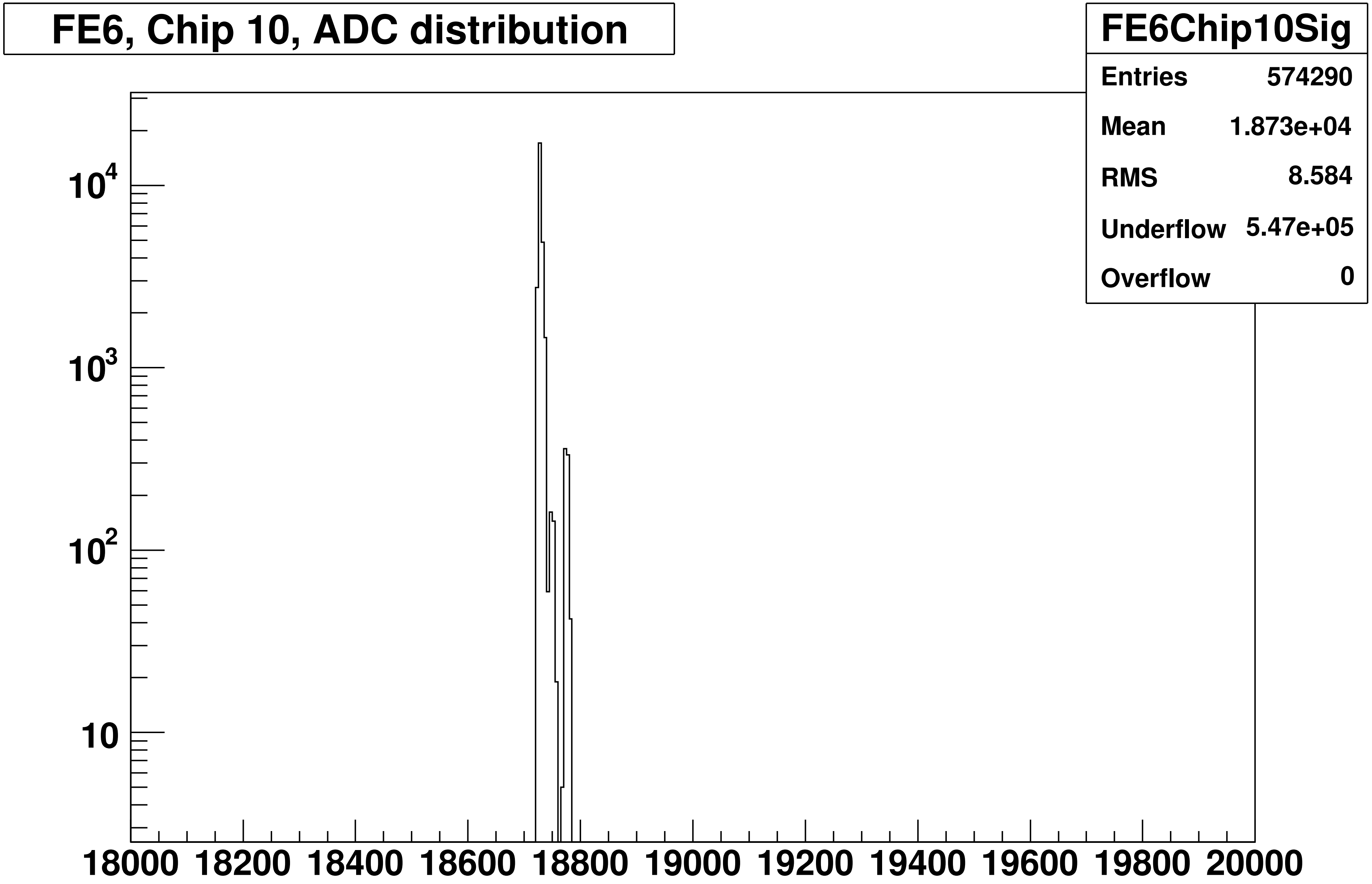
100
150
200


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 574290
Mean 1.873e+04
RMS 8.584
Underflow 5.47e+05
Overflow 0
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10^2
10^3
10^4
18000
18200
18400
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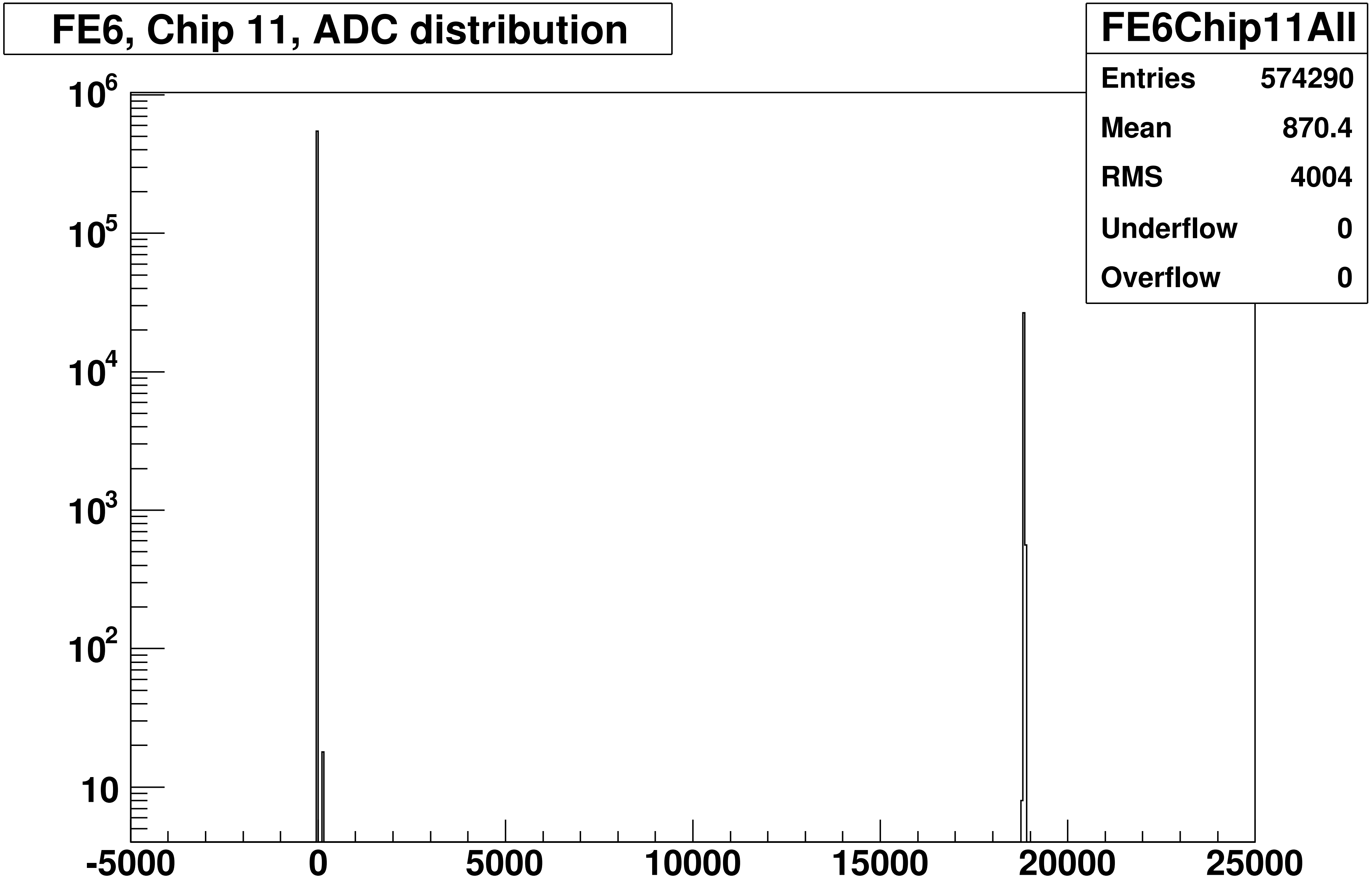
20000


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 574290
Mean 870.4
RMS 4004
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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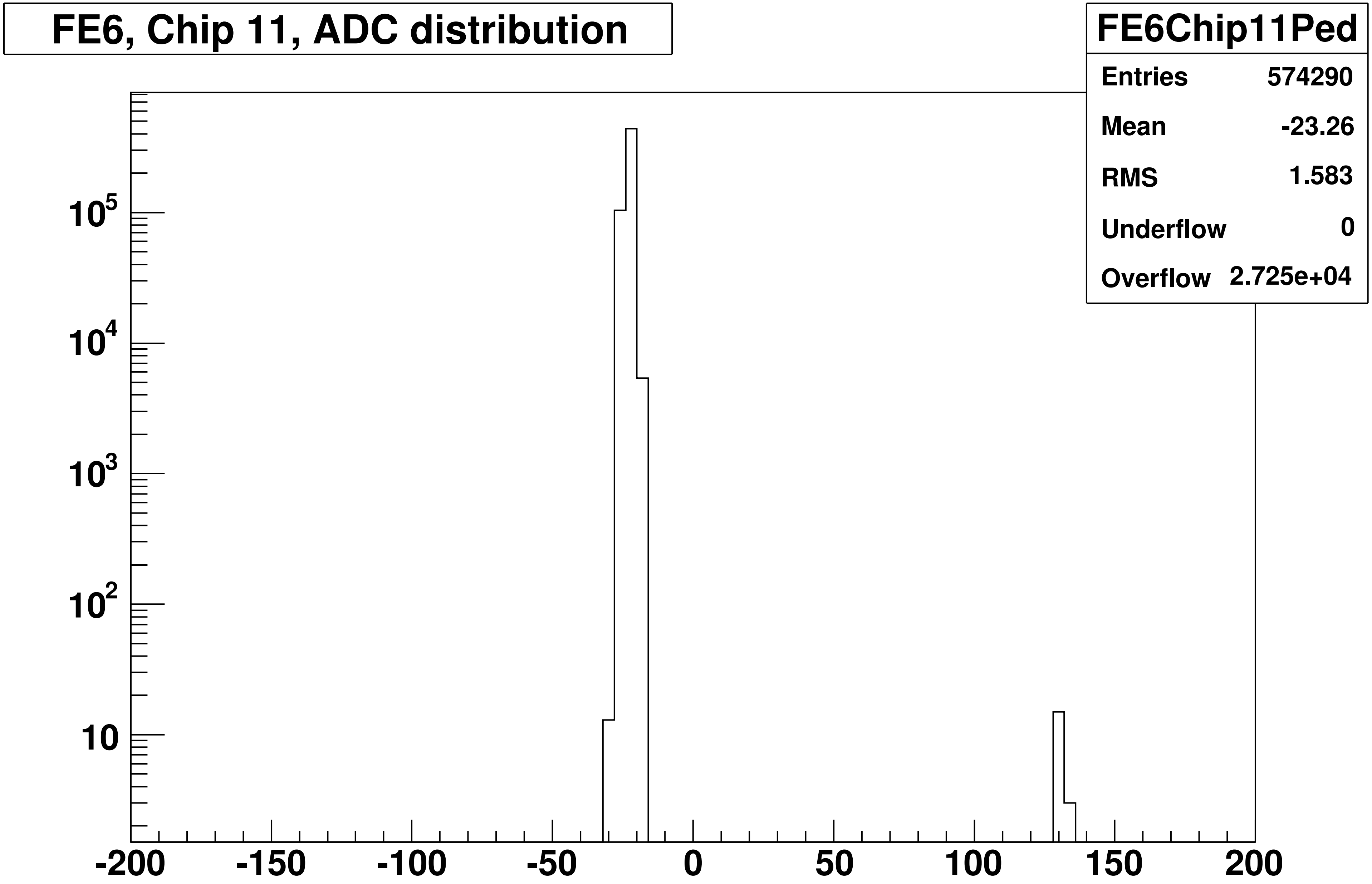
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 574290
Mean -23.26
RMS 1.583
Underflow 0
Overflow 2.725e+04
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10^3
10^4
10^5
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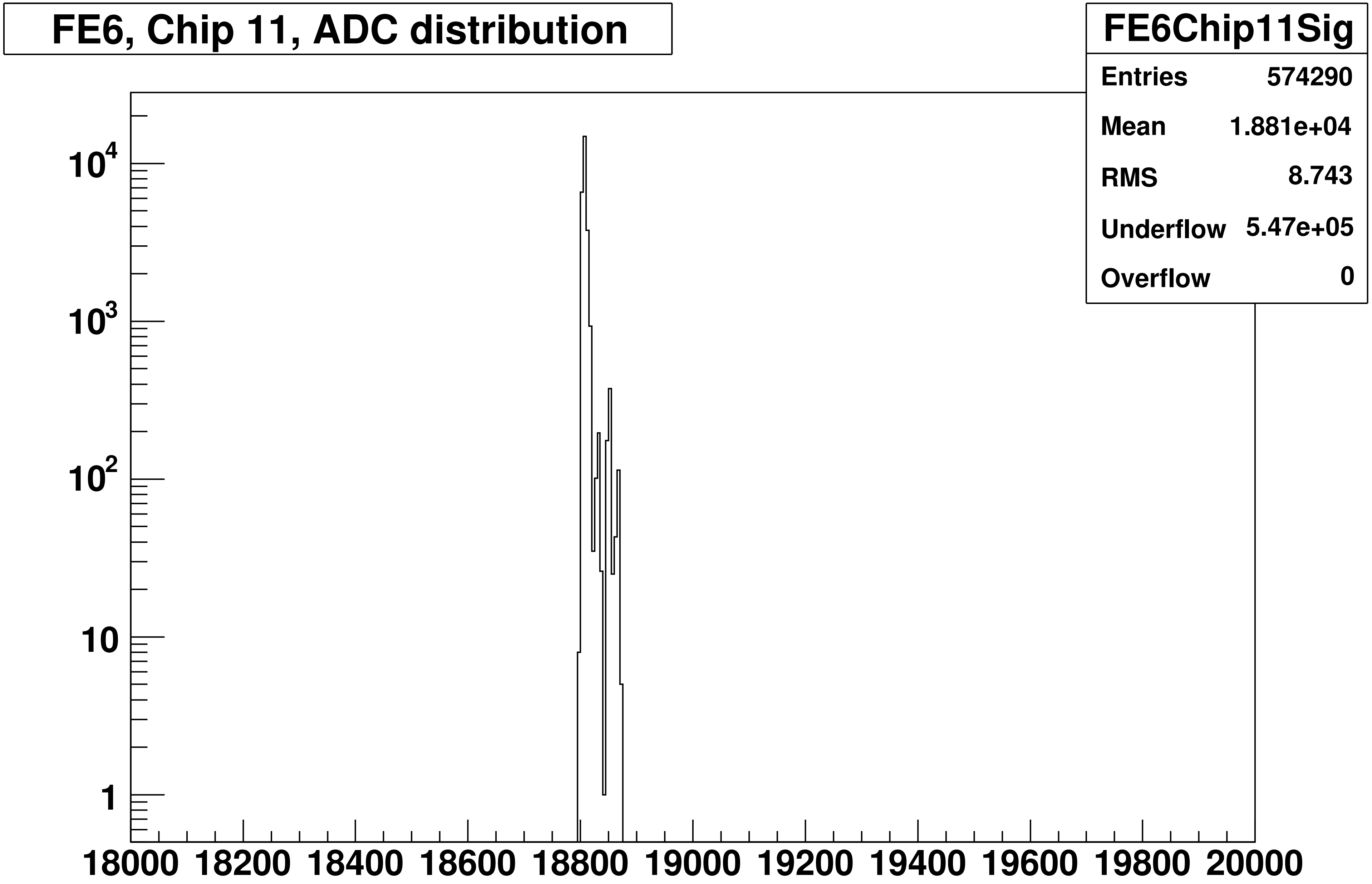
150
200



FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 574290
Mean 1.881e+04
RMS 8.743
Underflow 5.47e+05
Overflow 0
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10^3
10^2
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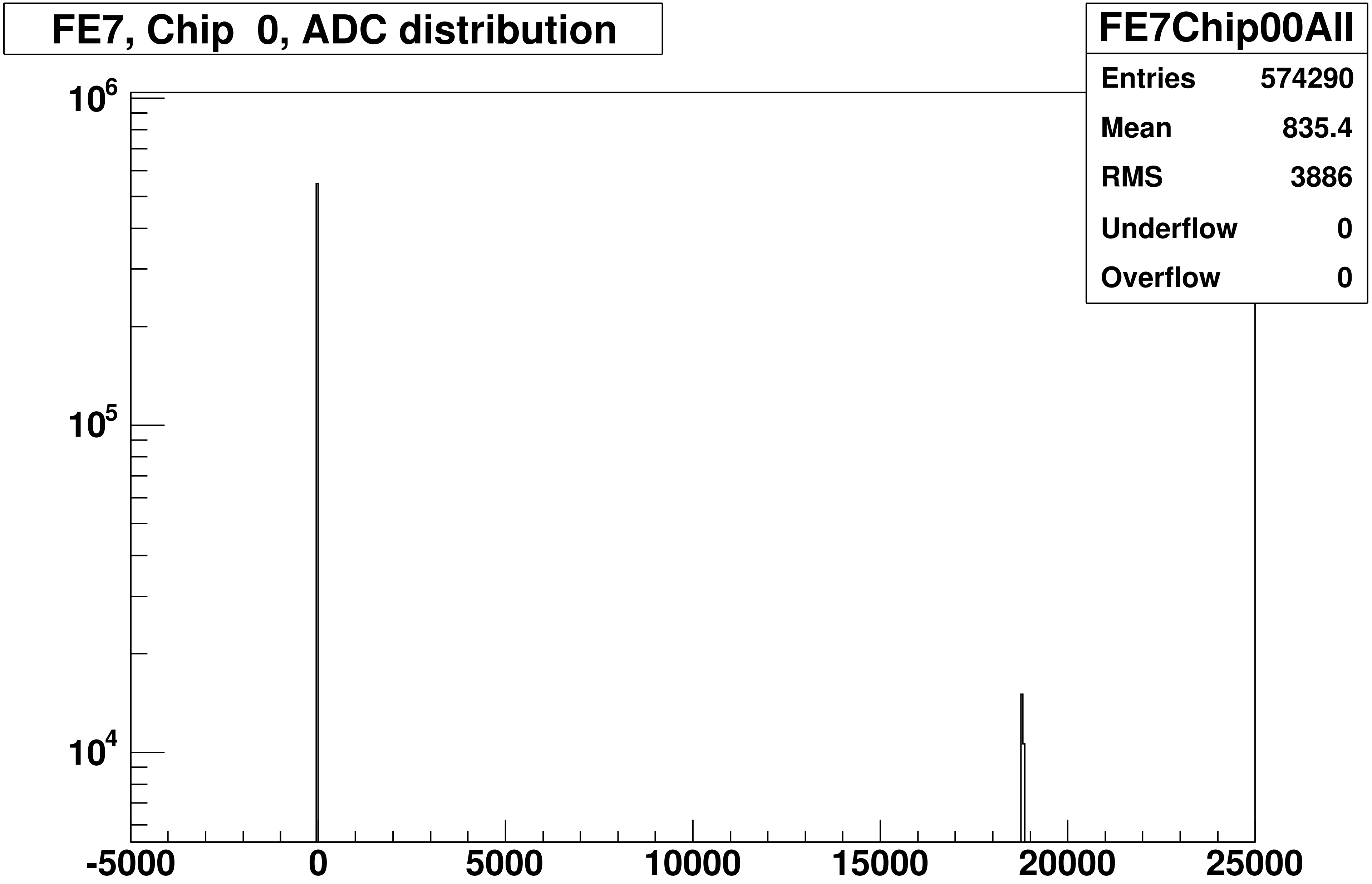
FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 574290
Mean 835.4
RMS 3886
Underflow 0
Overflow 0
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10^5
10^4
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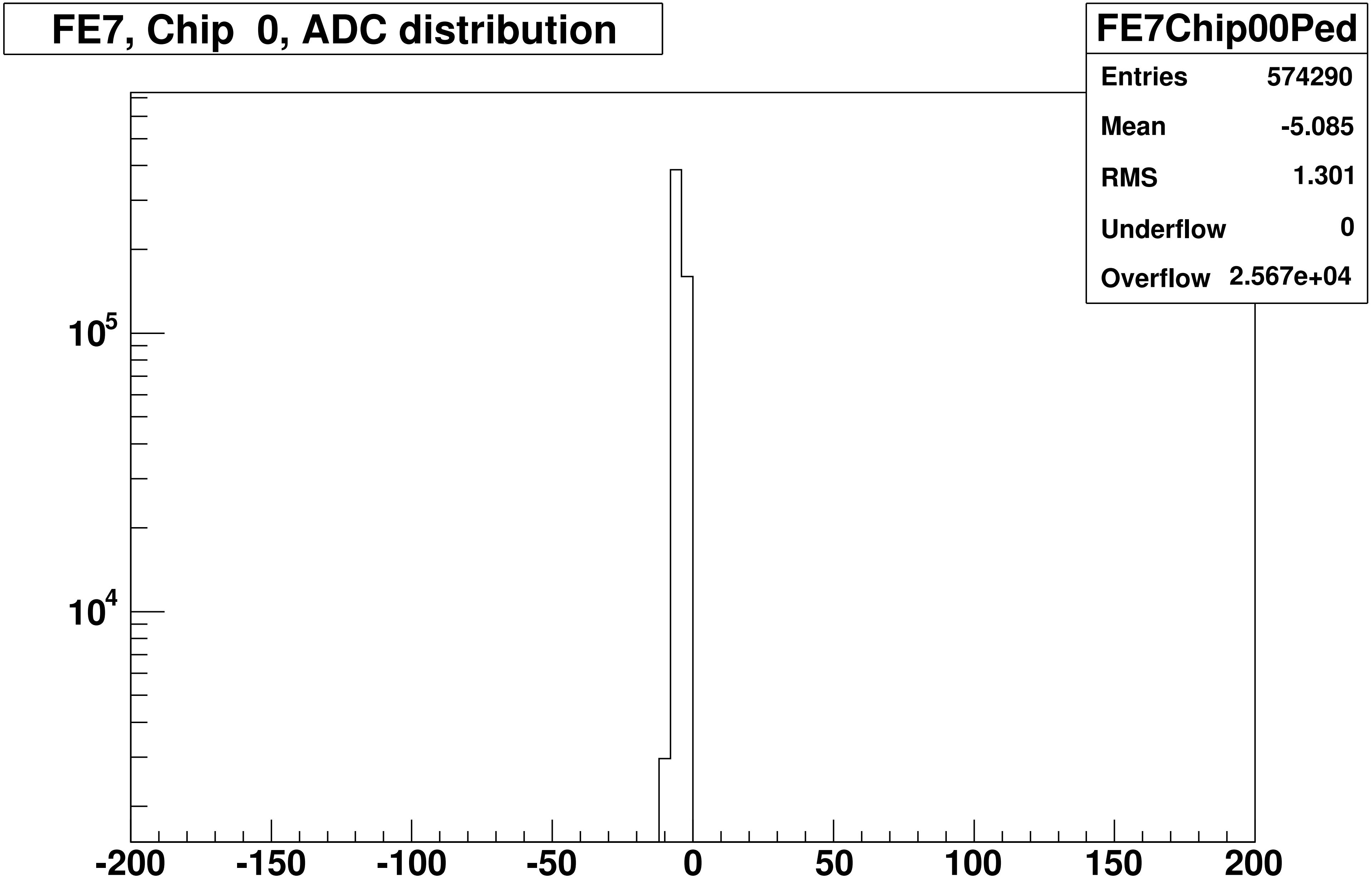
10000
15000
20000
25000



FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 574290
Mean -5.085
RMS 1.301
Underflow 0
Overflow 2.567e+04
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10^4
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-100
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0
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100
150
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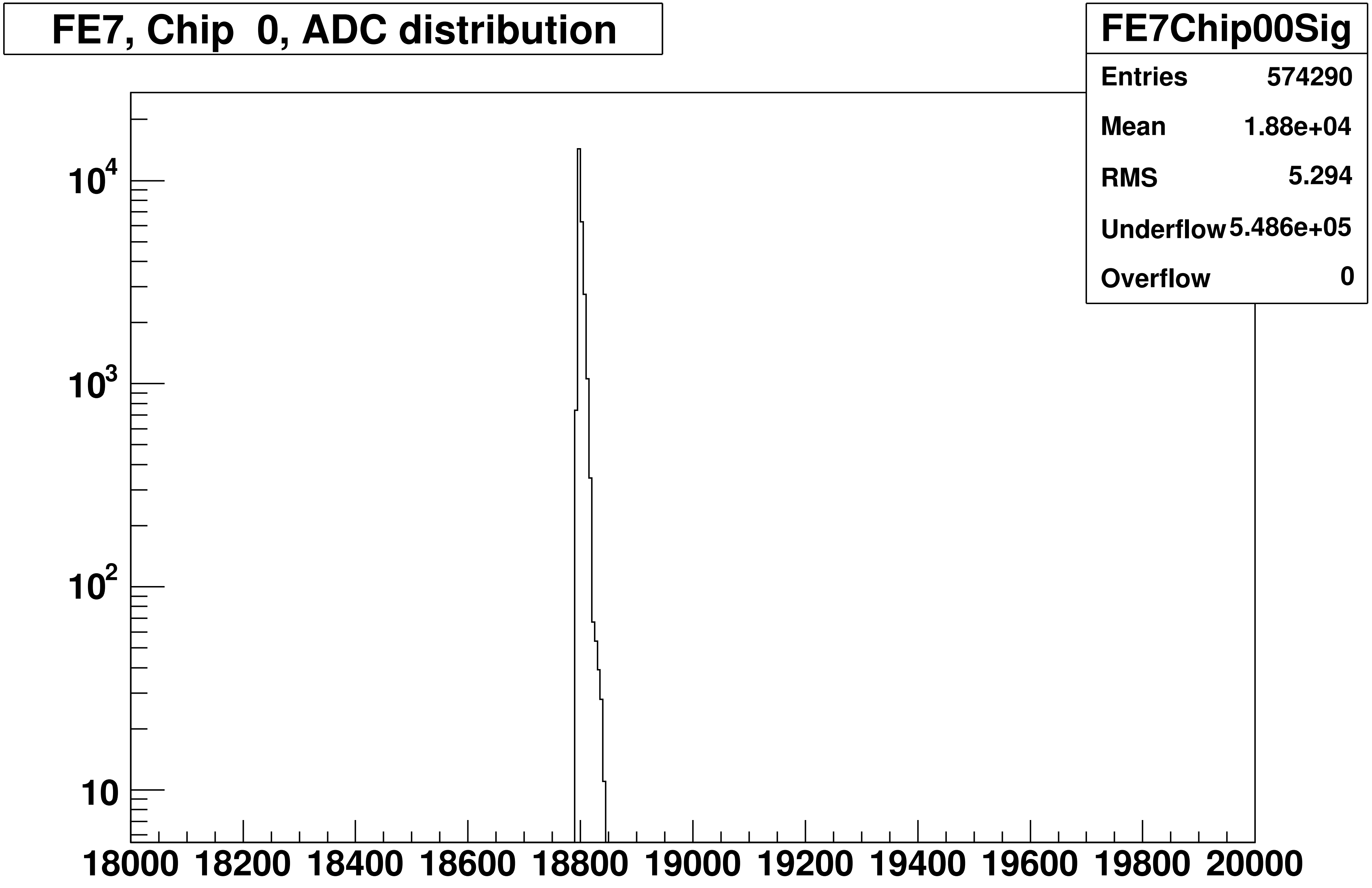
FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 574290
Mean 1.88e+04
RMS 5.294
Underflow 5.486e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
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19000
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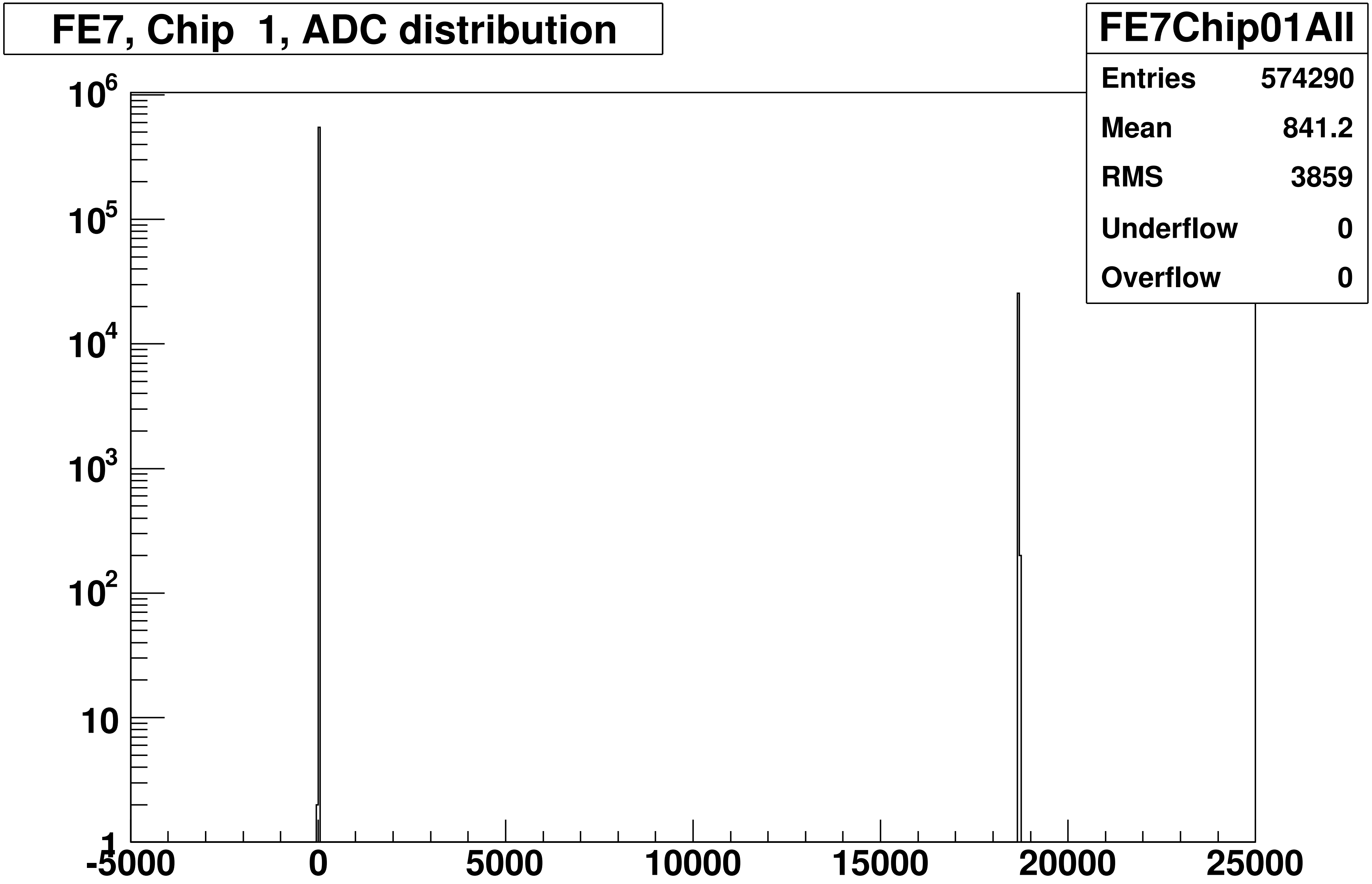
19600
19800
20000



FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 574290
Mean 841.2
RMS 3859
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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0
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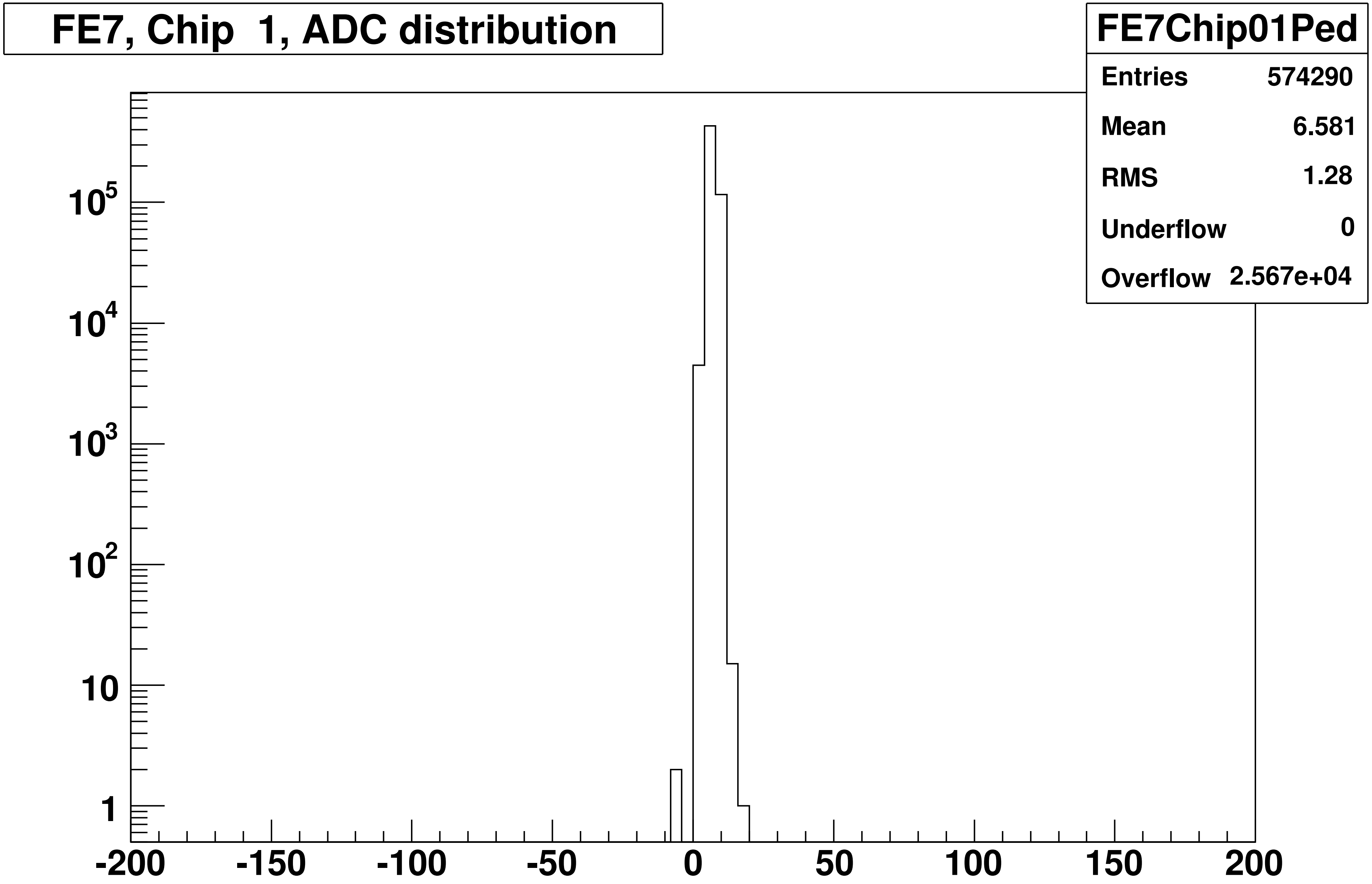
20000
25000


FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 574290
Mean 6.581
RMS 1.28
Underflow 0
Overflow 2.567e+04
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10^3
10^2
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150
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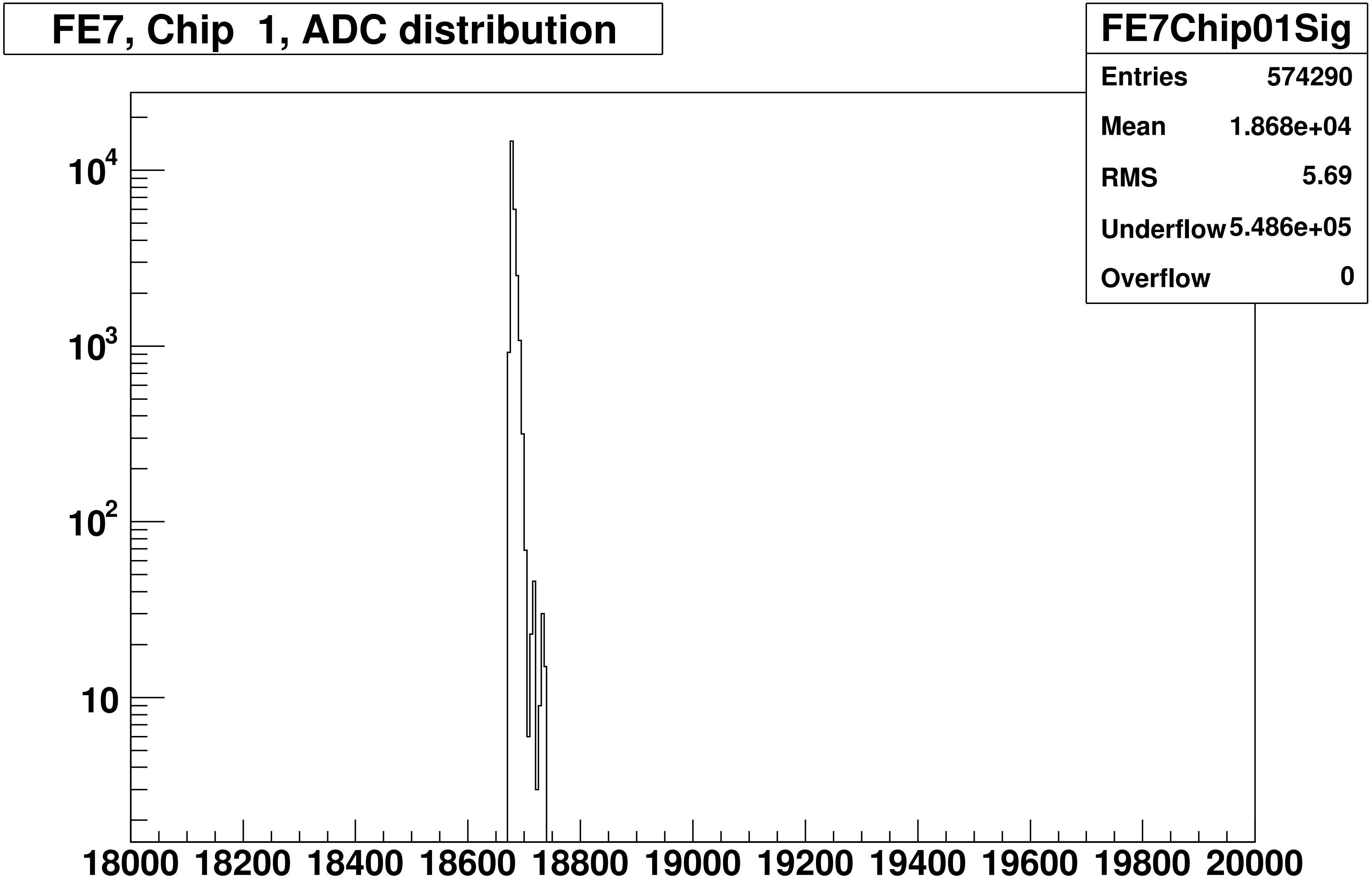
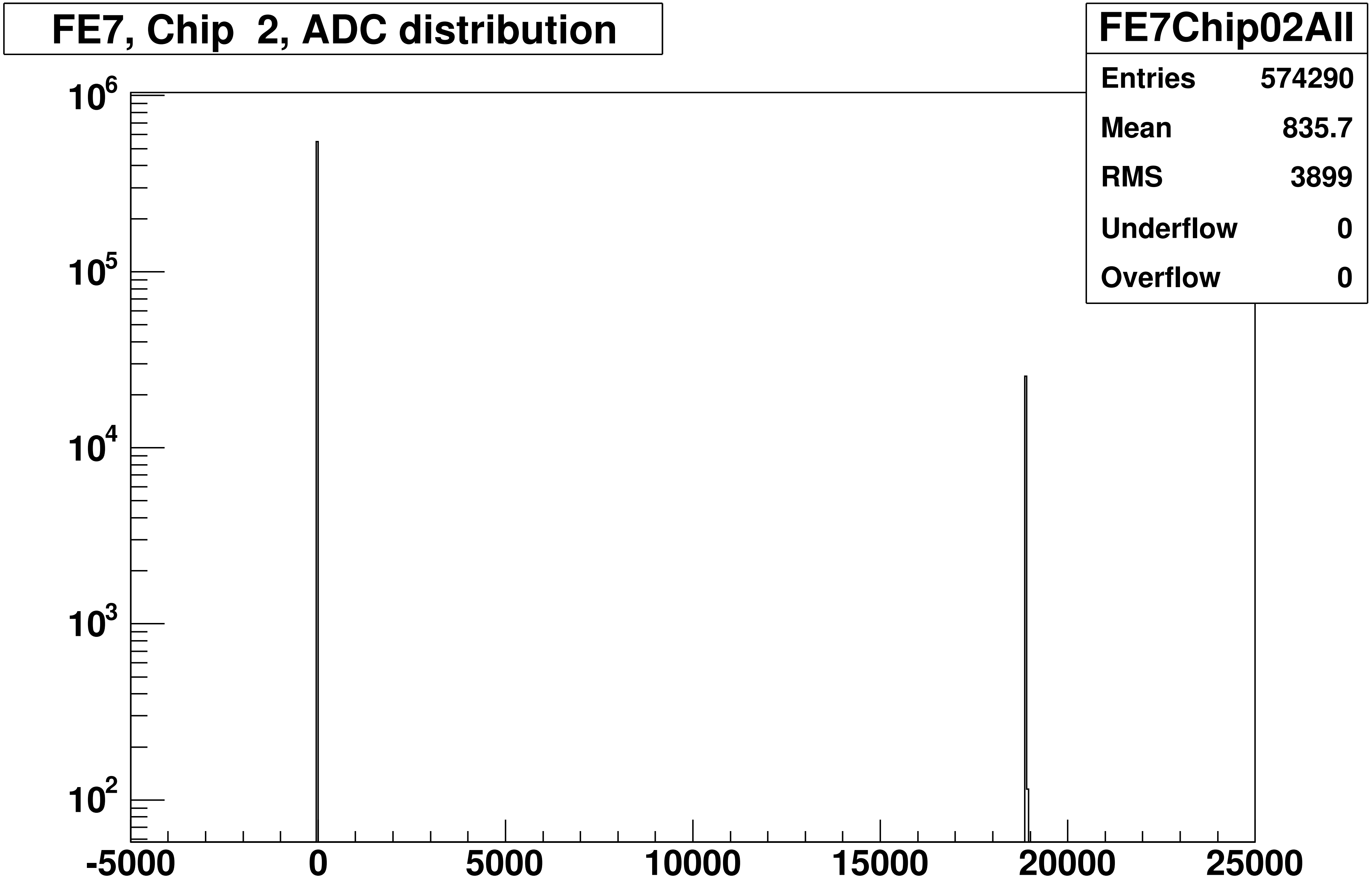


FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 574290
Mean 1.868e+04
RMS 5.69
Underflow 5.486e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 574290
Mean 835.7
RMS 3899
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
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0
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10000
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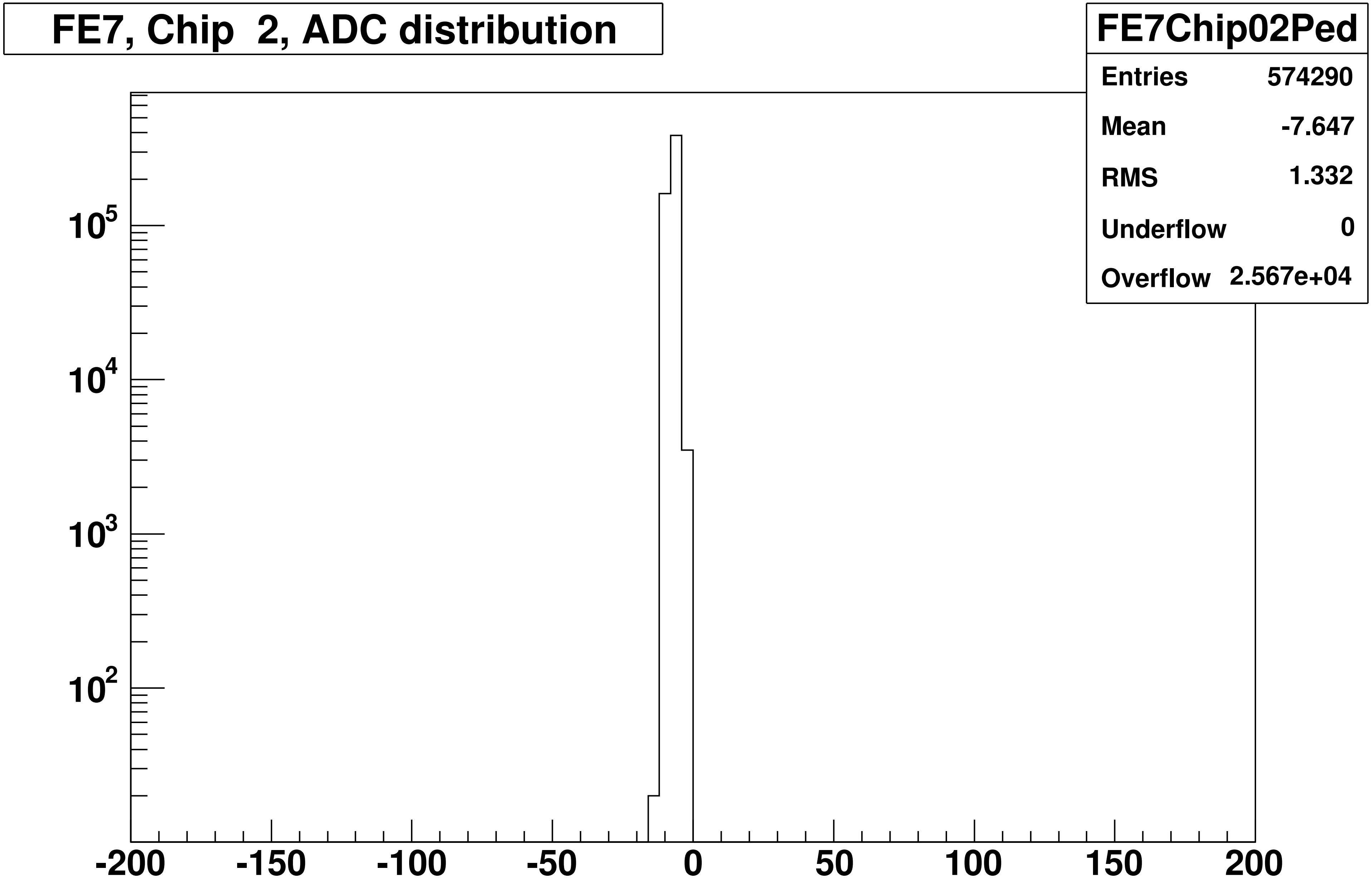
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 574290
Mean -7.647
RMS 1.332
Underflow 0
Overflow 2.567e+04
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10^4
10^3
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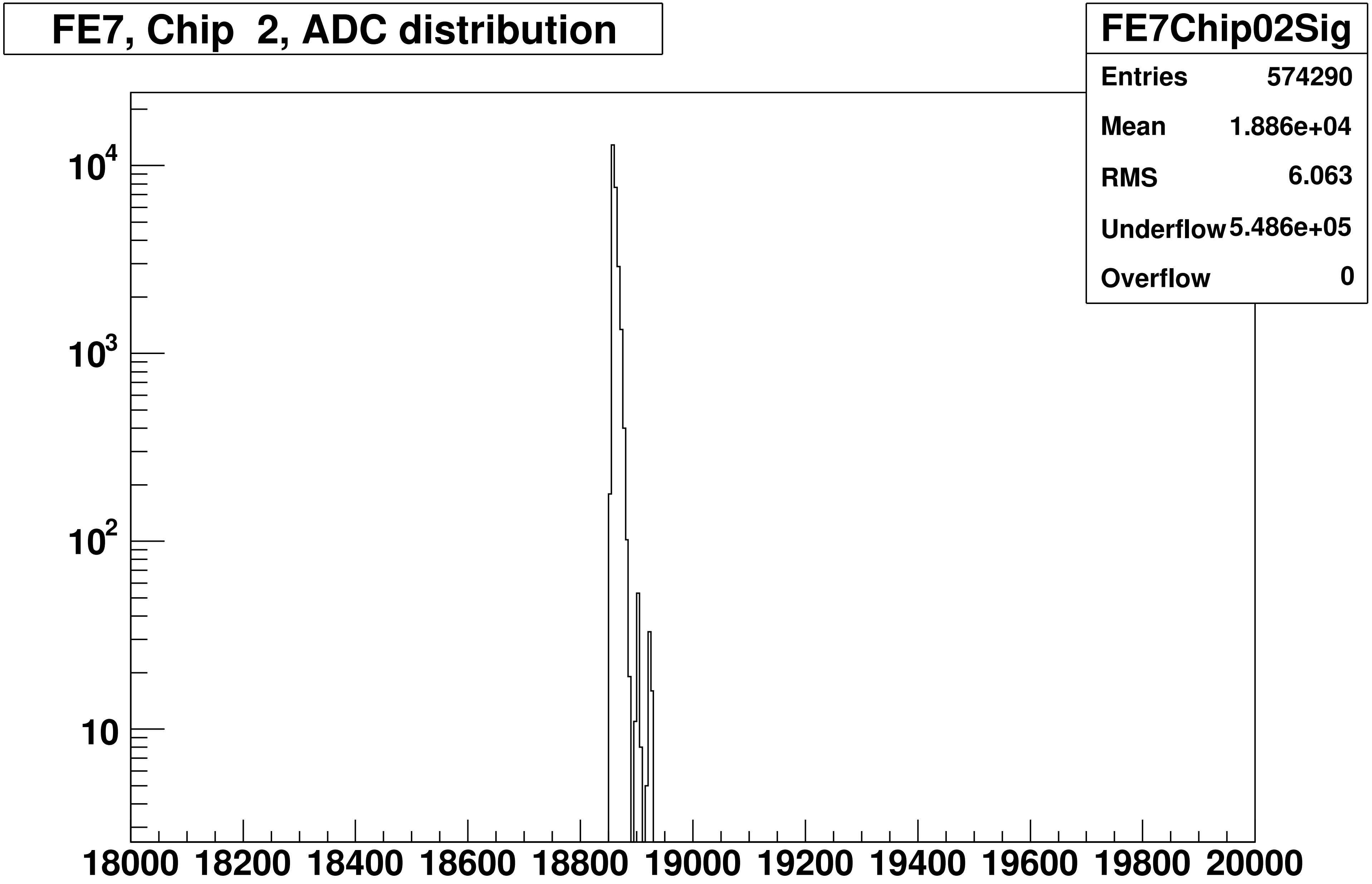

FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 574290
Mean 1.886e+04
RMS 6.063
Underflow 5.486e+05
Overflow 0
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10^2
10^3
10^4
18000
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18400
18600
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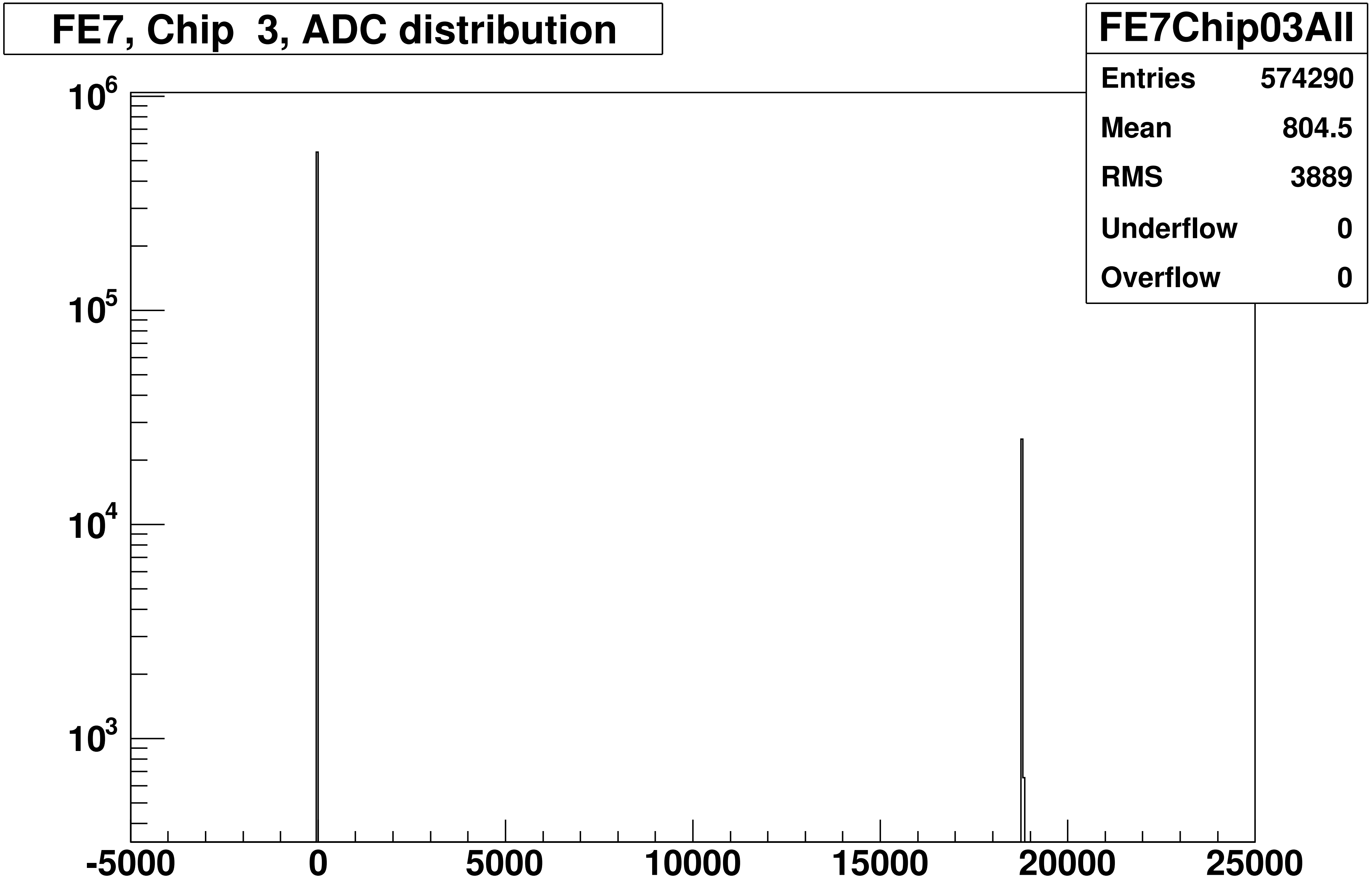
19600
19800
20000


FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 574290
Mean 804.5
RMS 3889
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
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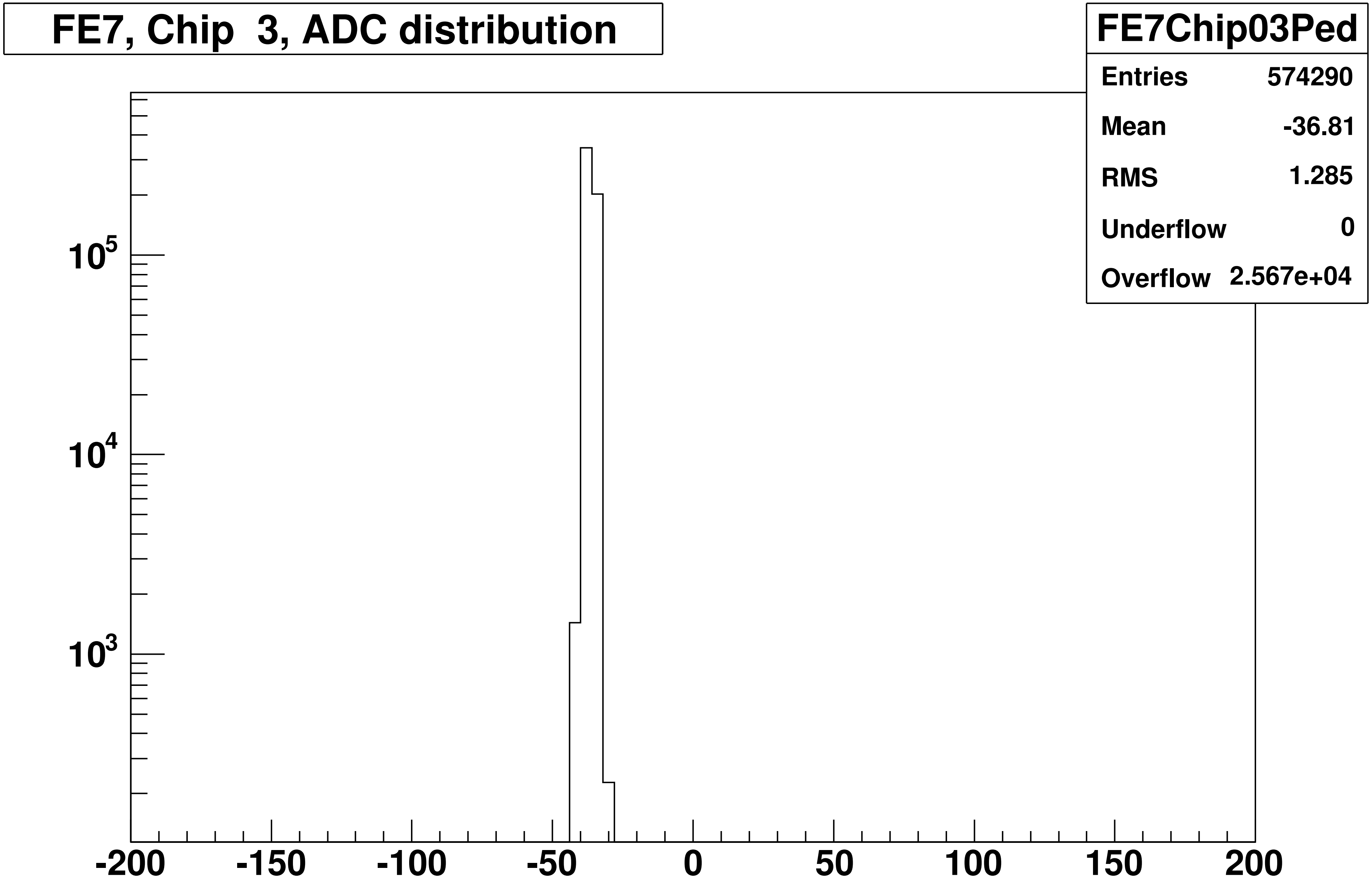
15000
20000
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 574290
Mean -36.81
RMS 1.285
Underflow 0
Overflow 2.567e+04
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10^4
10^3
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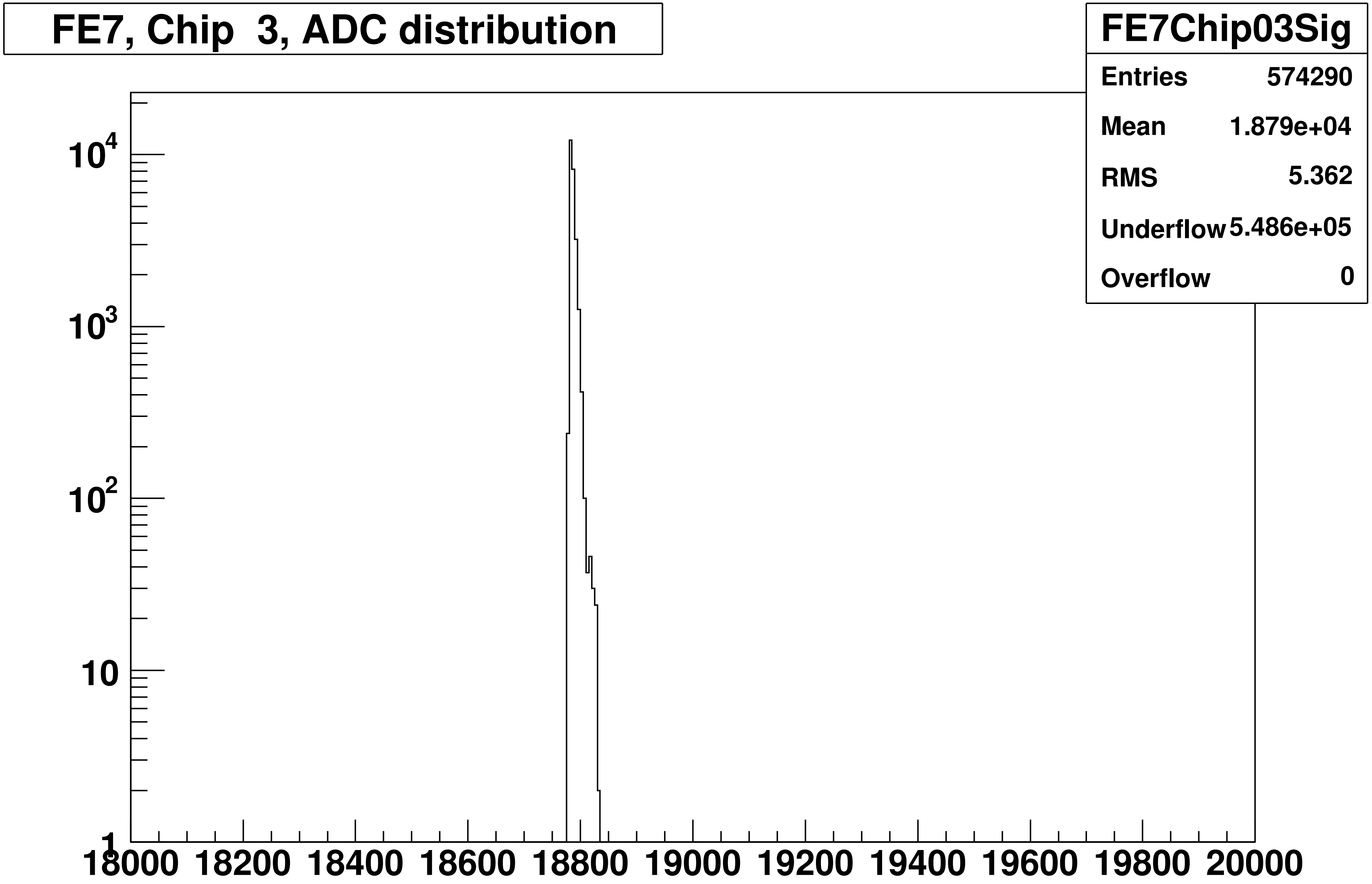

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 574290
Mean 1.879e+04
RMS 5.362
Underflow 5.486e+05
Overflow 0
10^4
10^3
10^2
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1
18000
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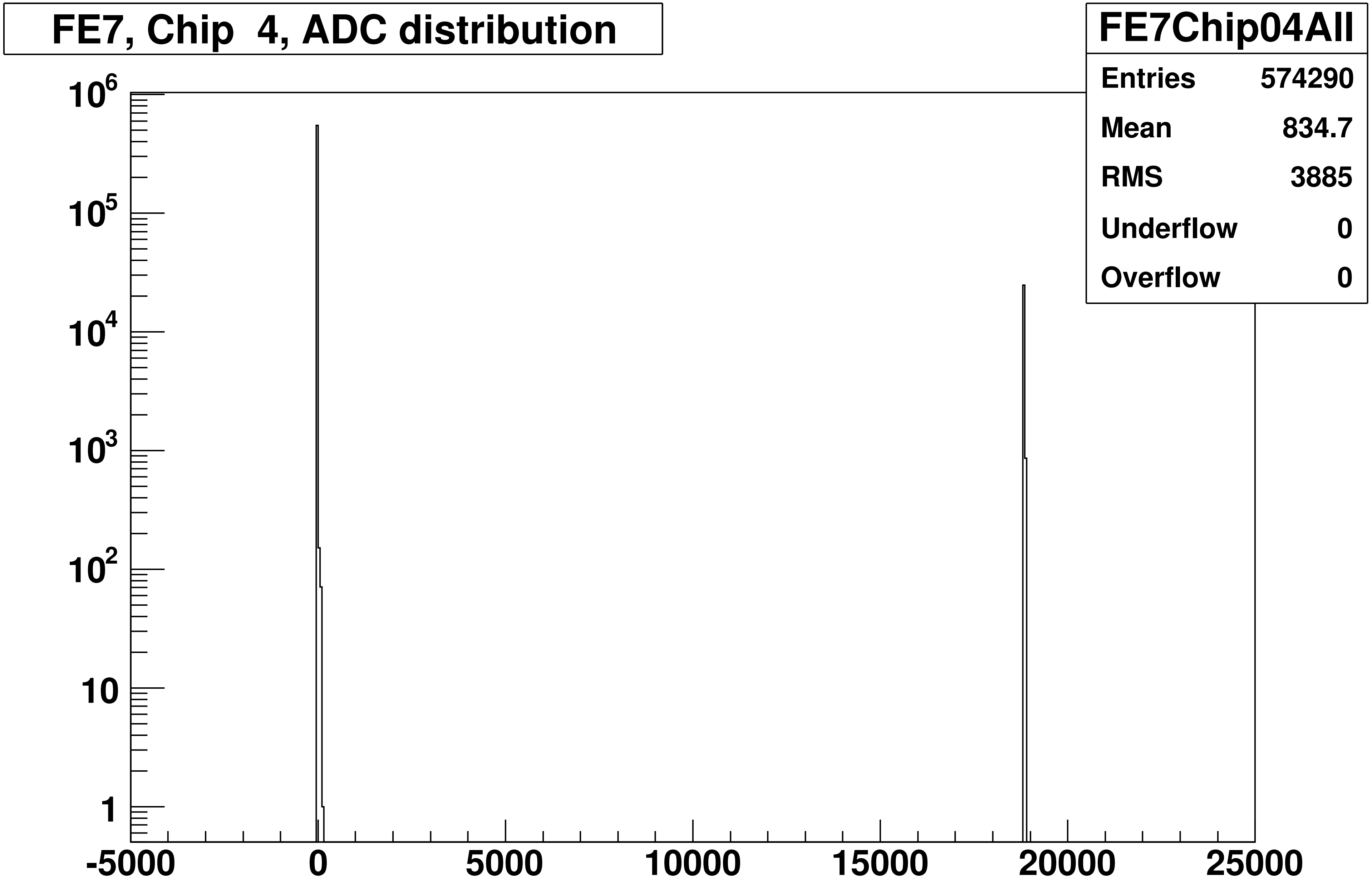
FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 574290
Mean 834.7
RMS 3885
Underflow 0
Overflow 0
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10^4
10^3
10^2
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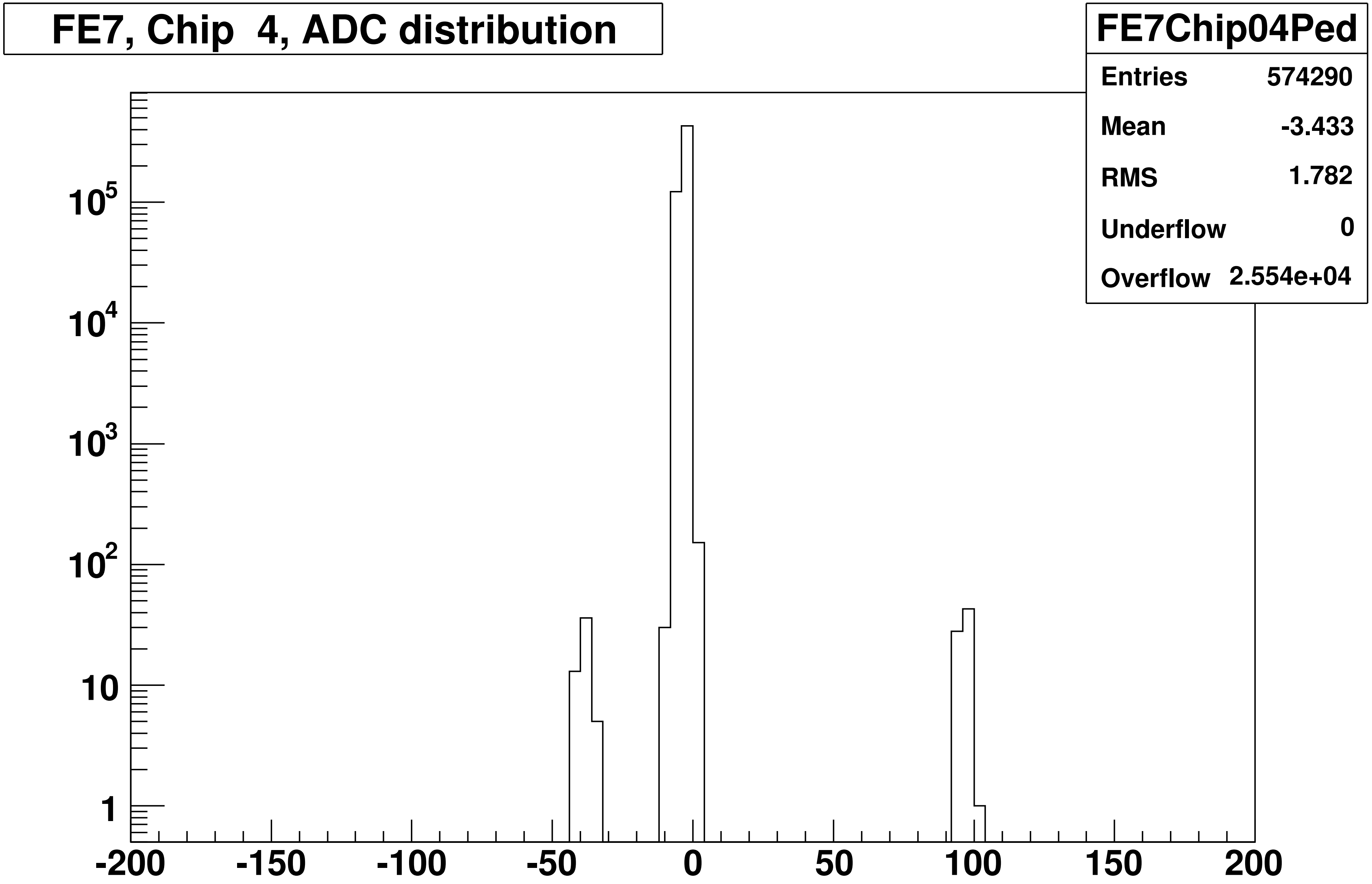
FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 574290
Mean -3.433
RMS 1.782
Underflow 0
Overflow 2.554e+04
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10^3
10^2
10
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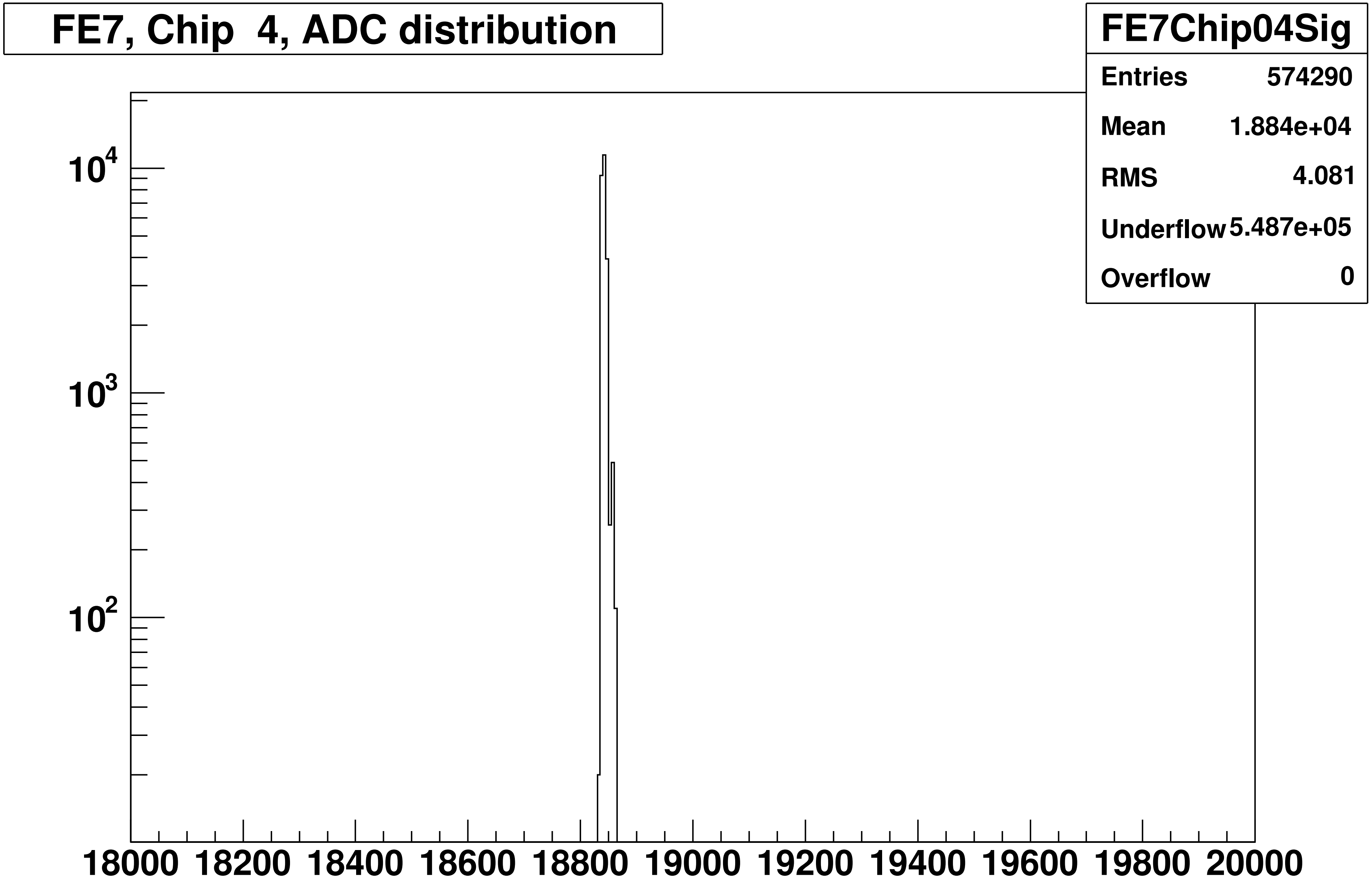
150
200


FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 574290
Mean 1.884e+04
RMS 4.081
Underflow 5.487e+05
Overflow 0
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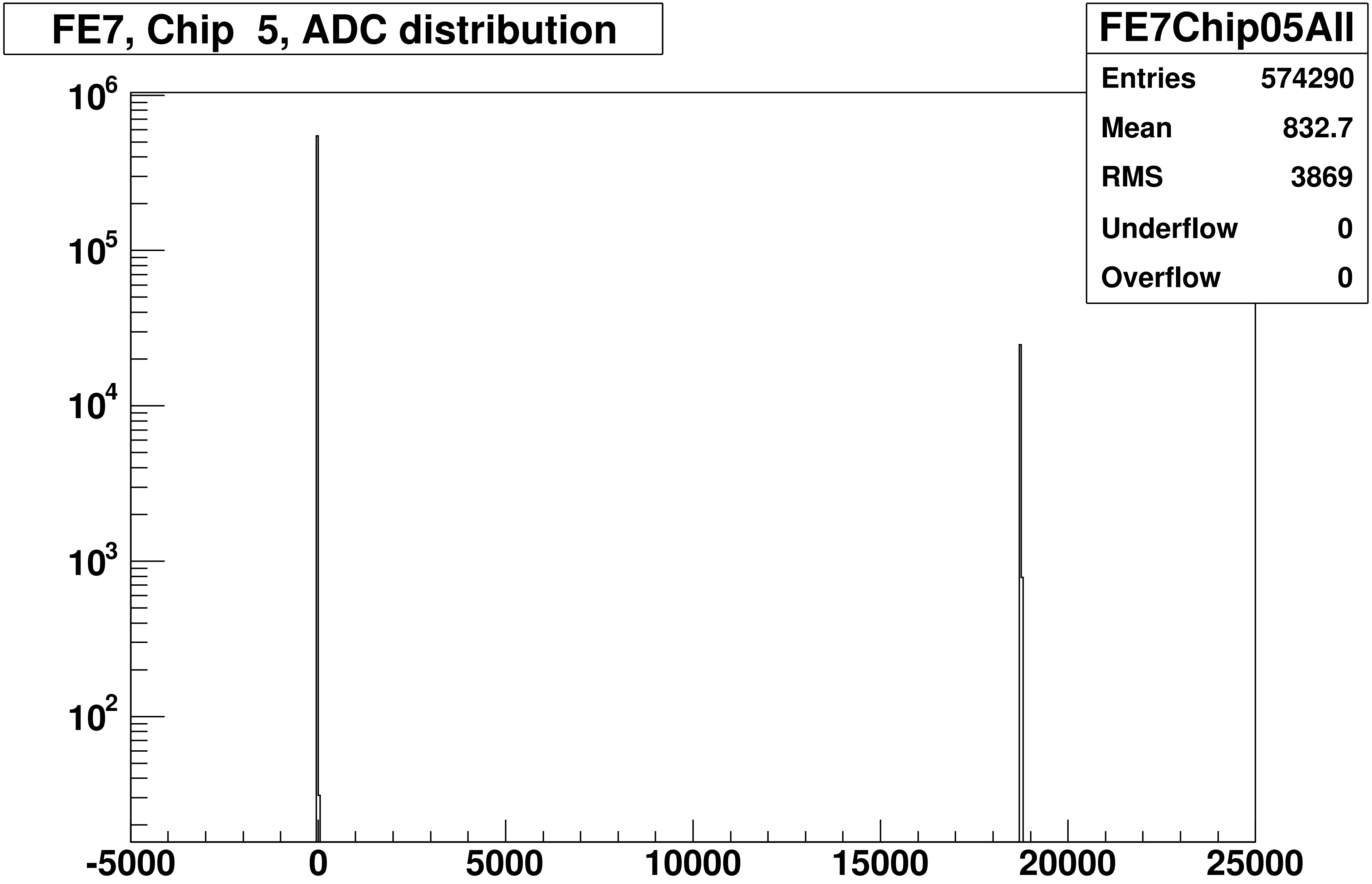
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 574290
Mean 832.7
RMS 3869
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
5000
10000
15000
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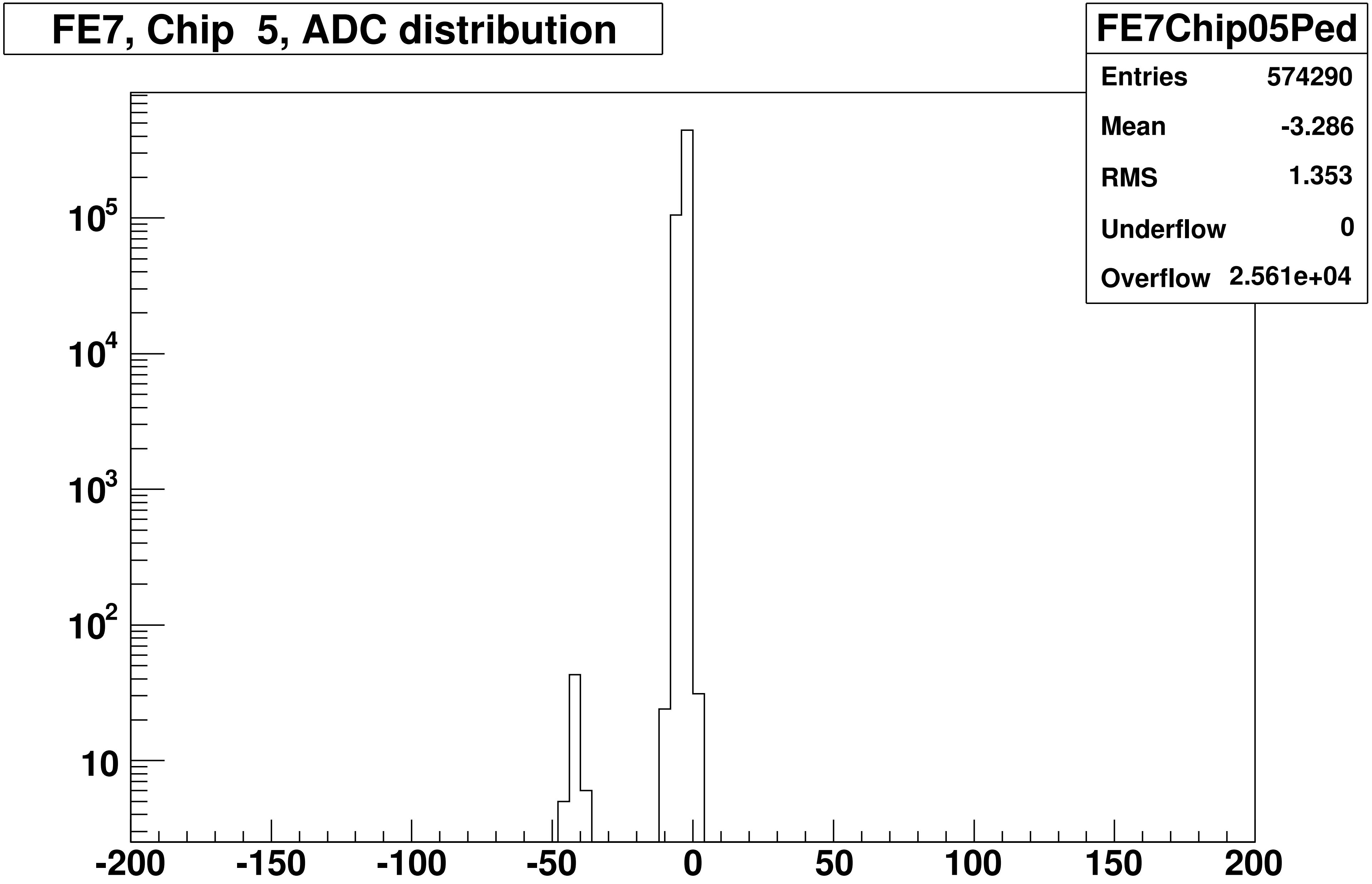
25000



FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 574290
Mean -3.286
RMS 1.353
Underflow 0
Overflow 2.561e+04
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10^3
10^4
10^5
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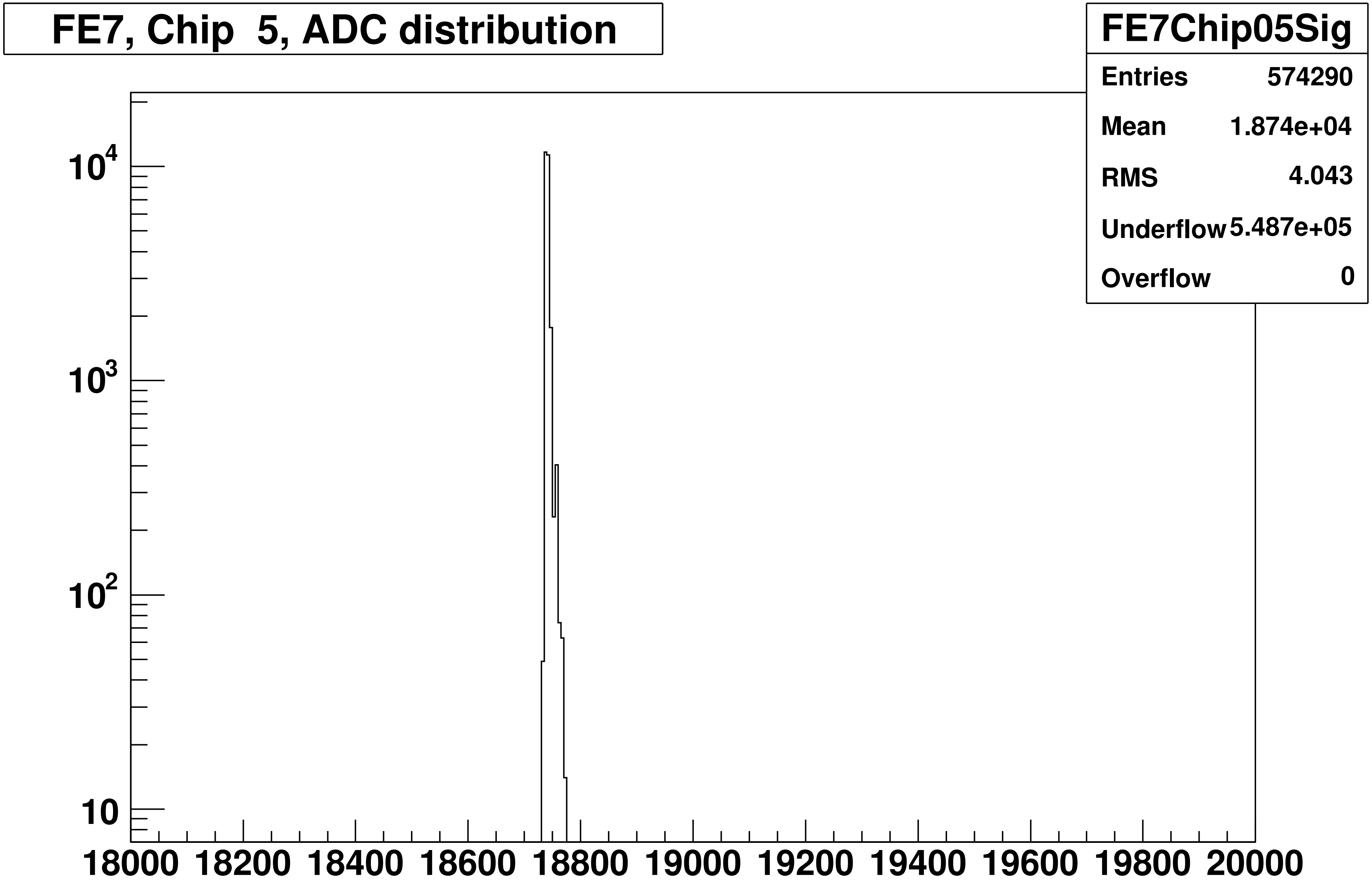
0
50
100
150
200



FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 574290
Mean 1.874e+04
RMS 4.043
Underflow 5.487e+05
Overflow 0
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10^2
10^3
10^4
18000
18200
18400
18600
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19000
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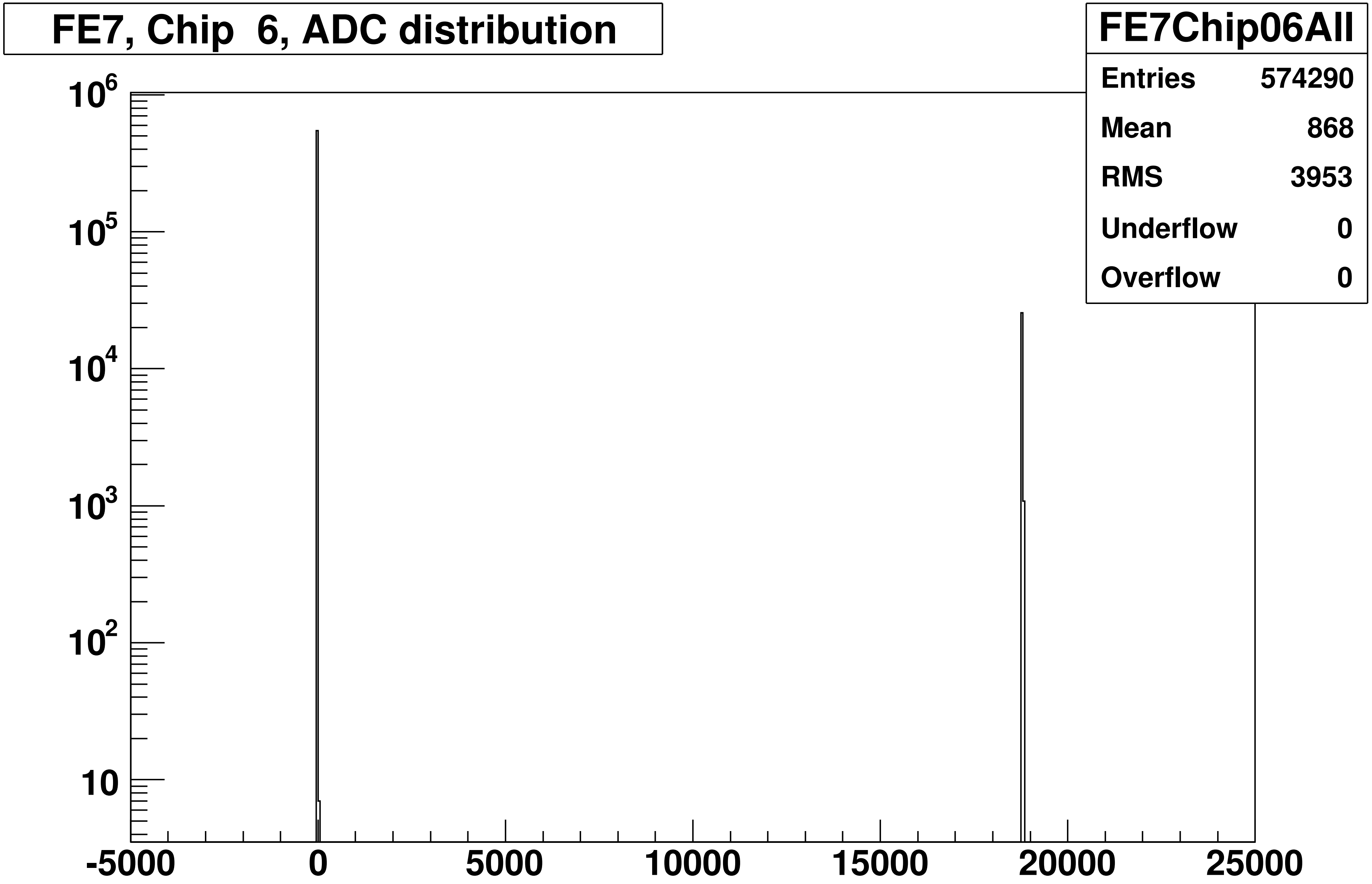
19600
19800
20000



FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 574290
Mean 868
RMS 3953
Underflow 0
Overflow 0
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10^3
10^4
10^5
10^6
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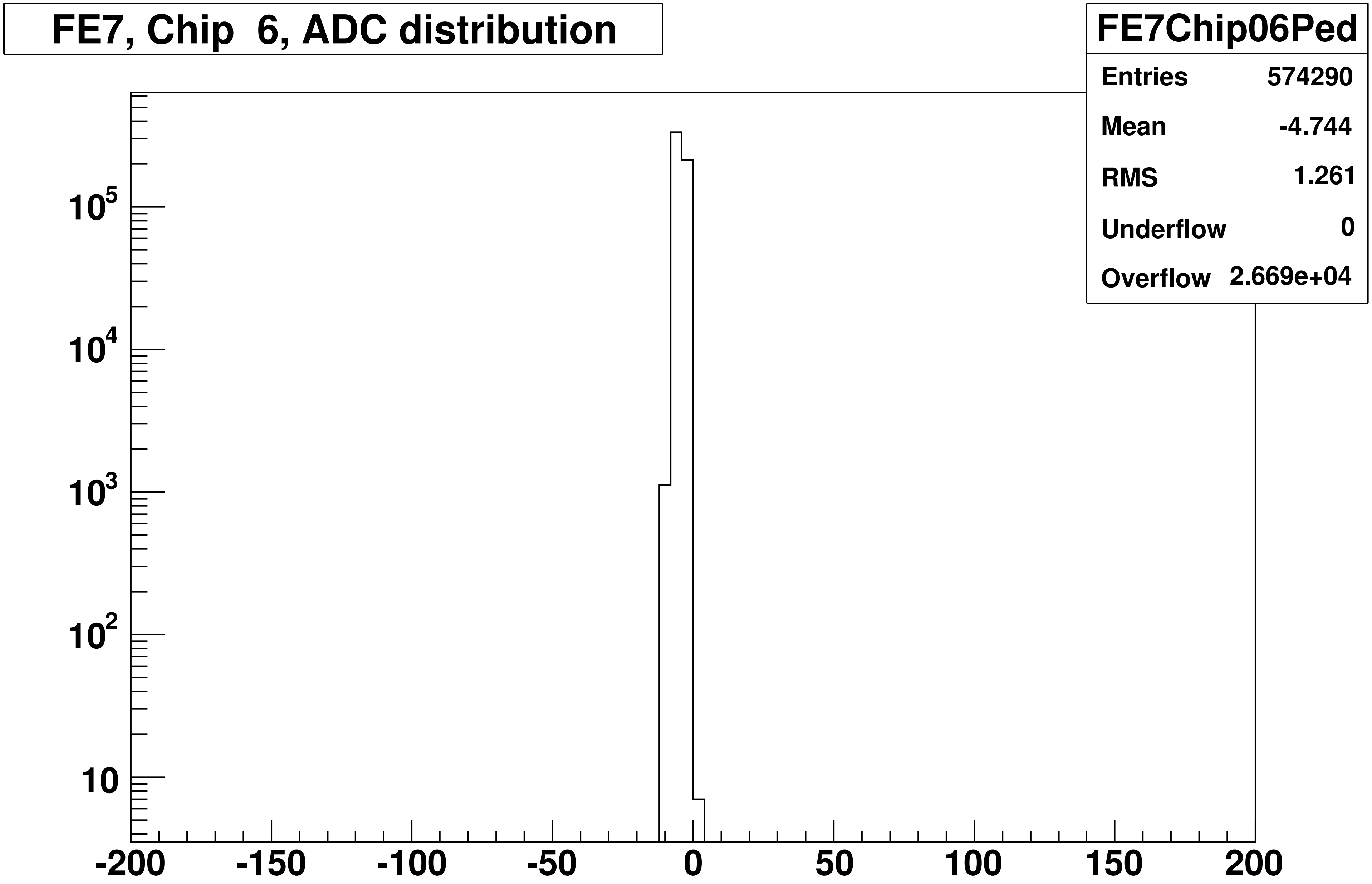
20000
25000



FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 574290
Mean -4.744
RMS 1.261
Underflow 0
Overflow 2.669e+04
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10^3
10^4
10^5
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0
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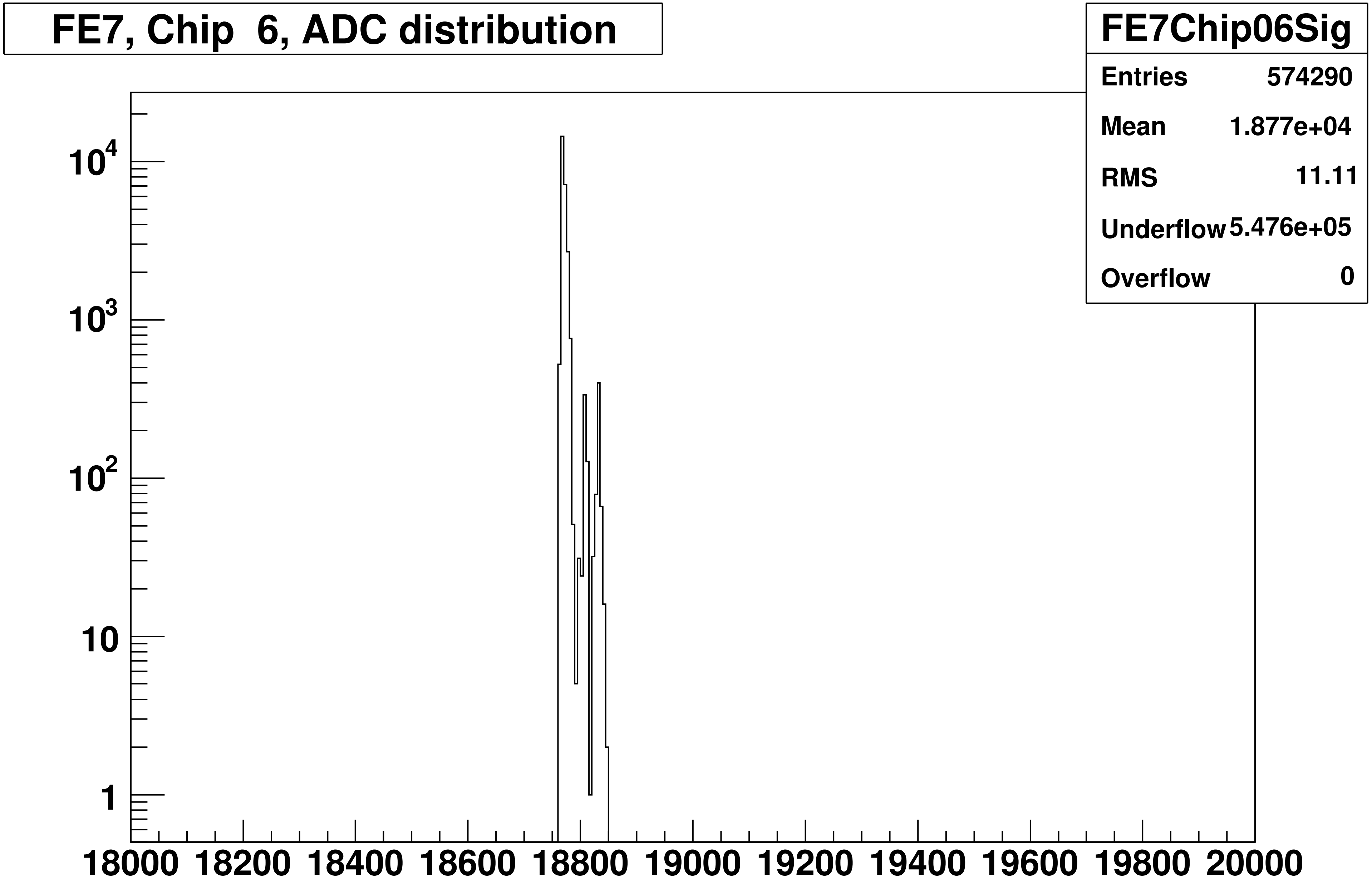
100
150
200



FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 574290
Mean 1.877e+04
RMS 11.11
Underflow 5.476e+05
Overflow 0
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10^3
10^2
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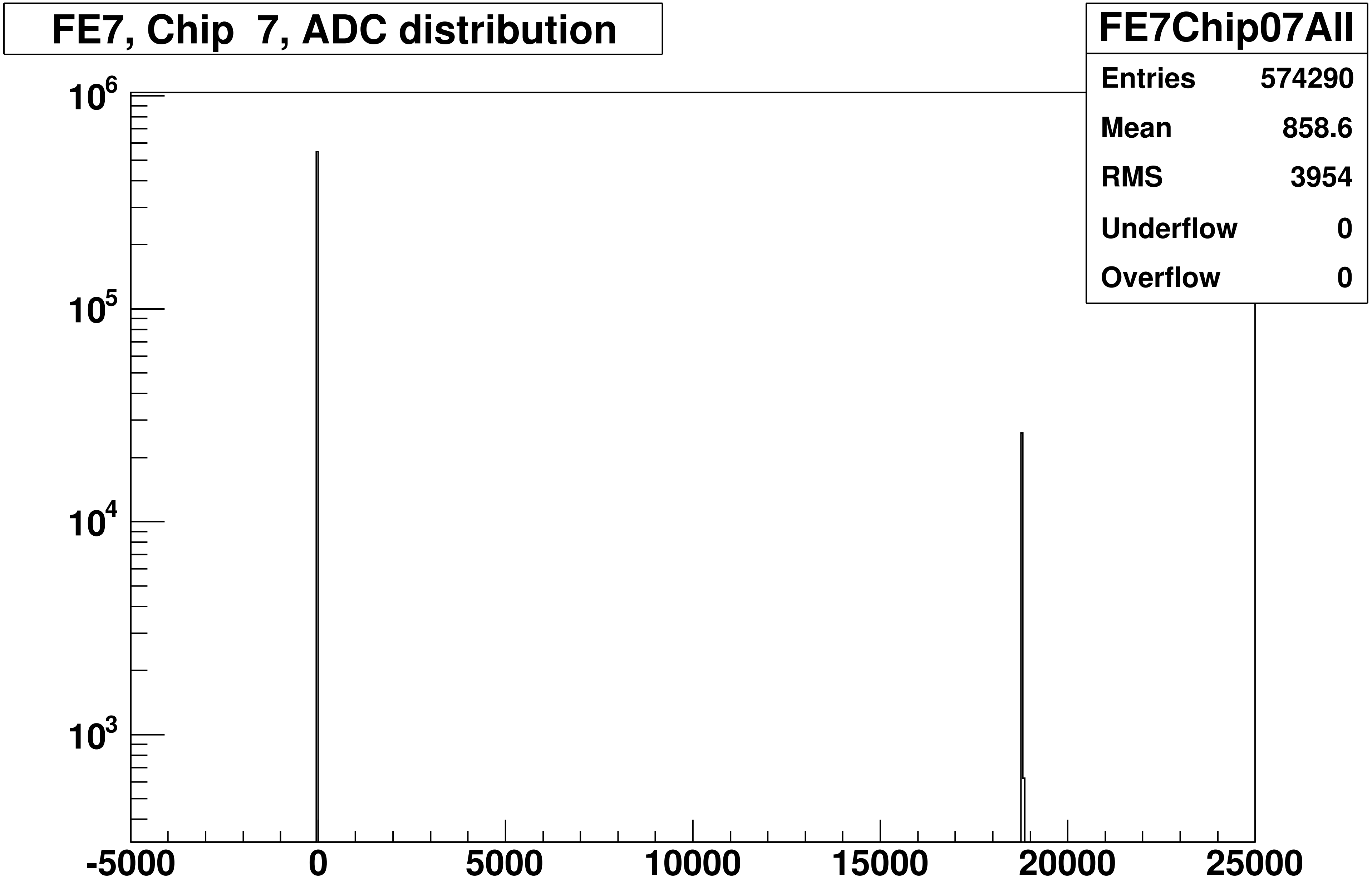

FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 574290
Mean 858.6
RMS 3954
Underflow 0
Overflow 0
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10^3
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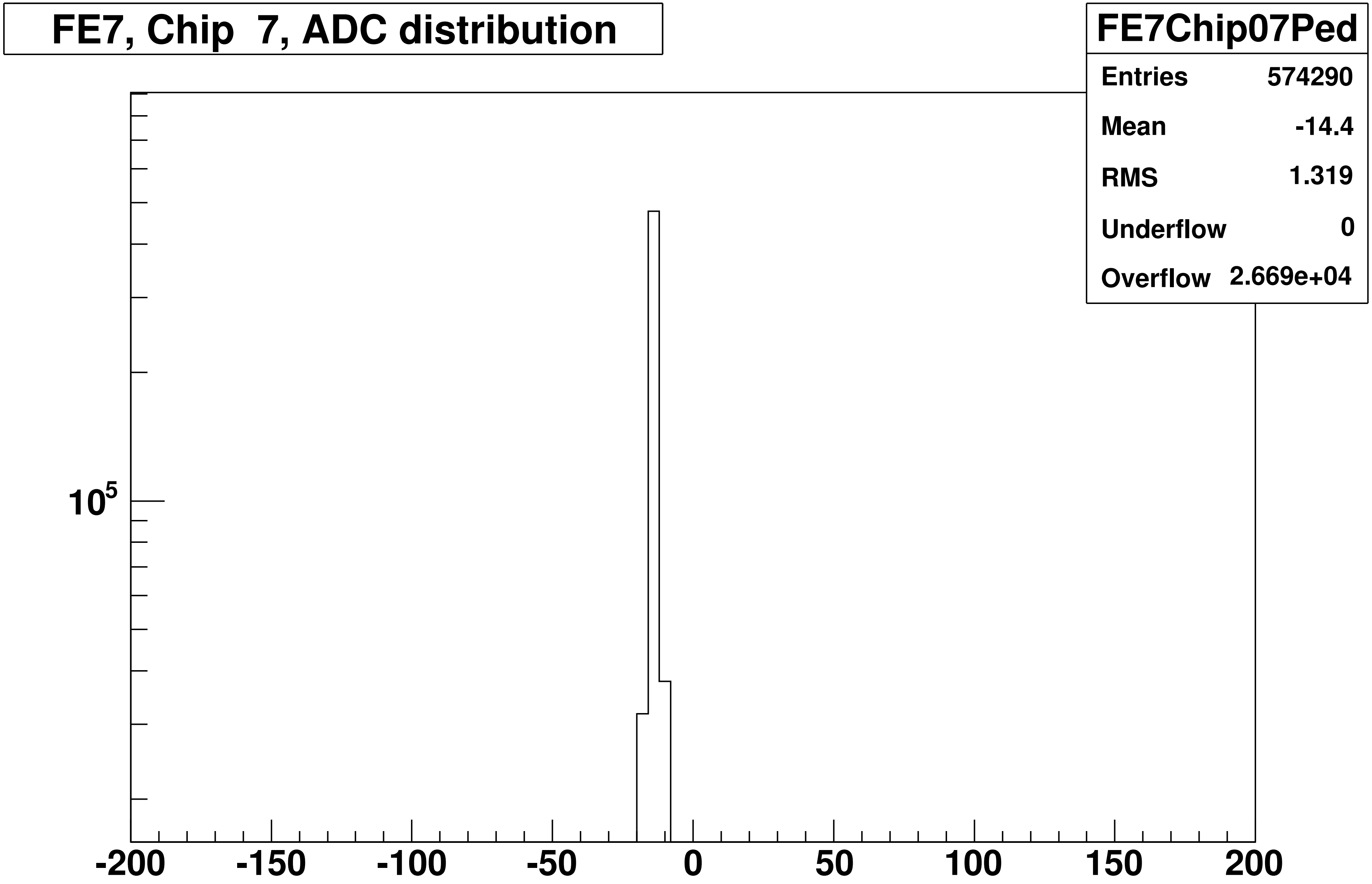
25000



FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 574290
Mean -14.4
RMS 1.319
Underflow 0
Overflow 2.669e+04
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-100
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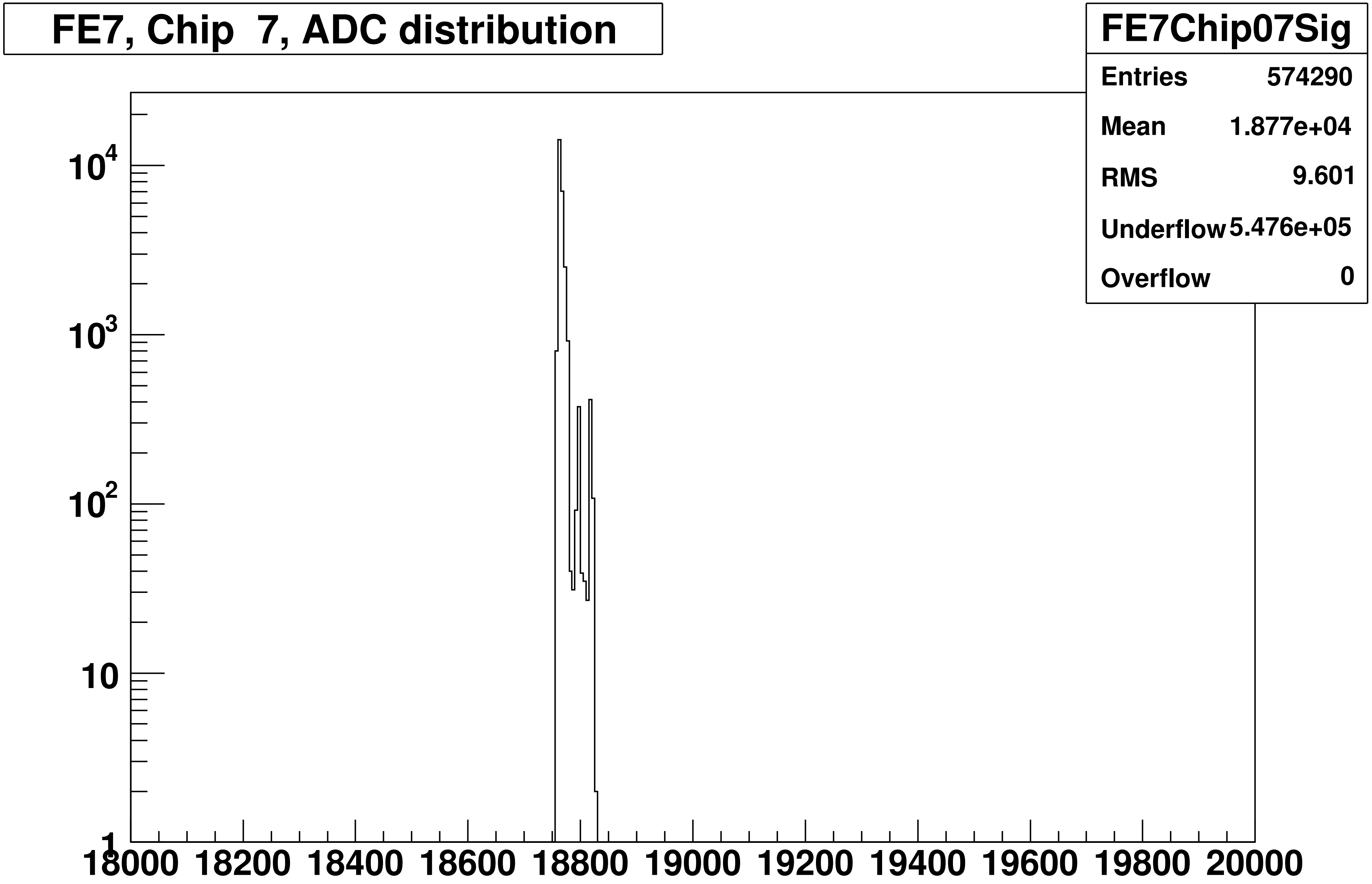
150
200


FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 574290
Mean 1.877e+04
RMS 9.601
Underflow 5.476e+05
Overflow 0
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10^3
10^2
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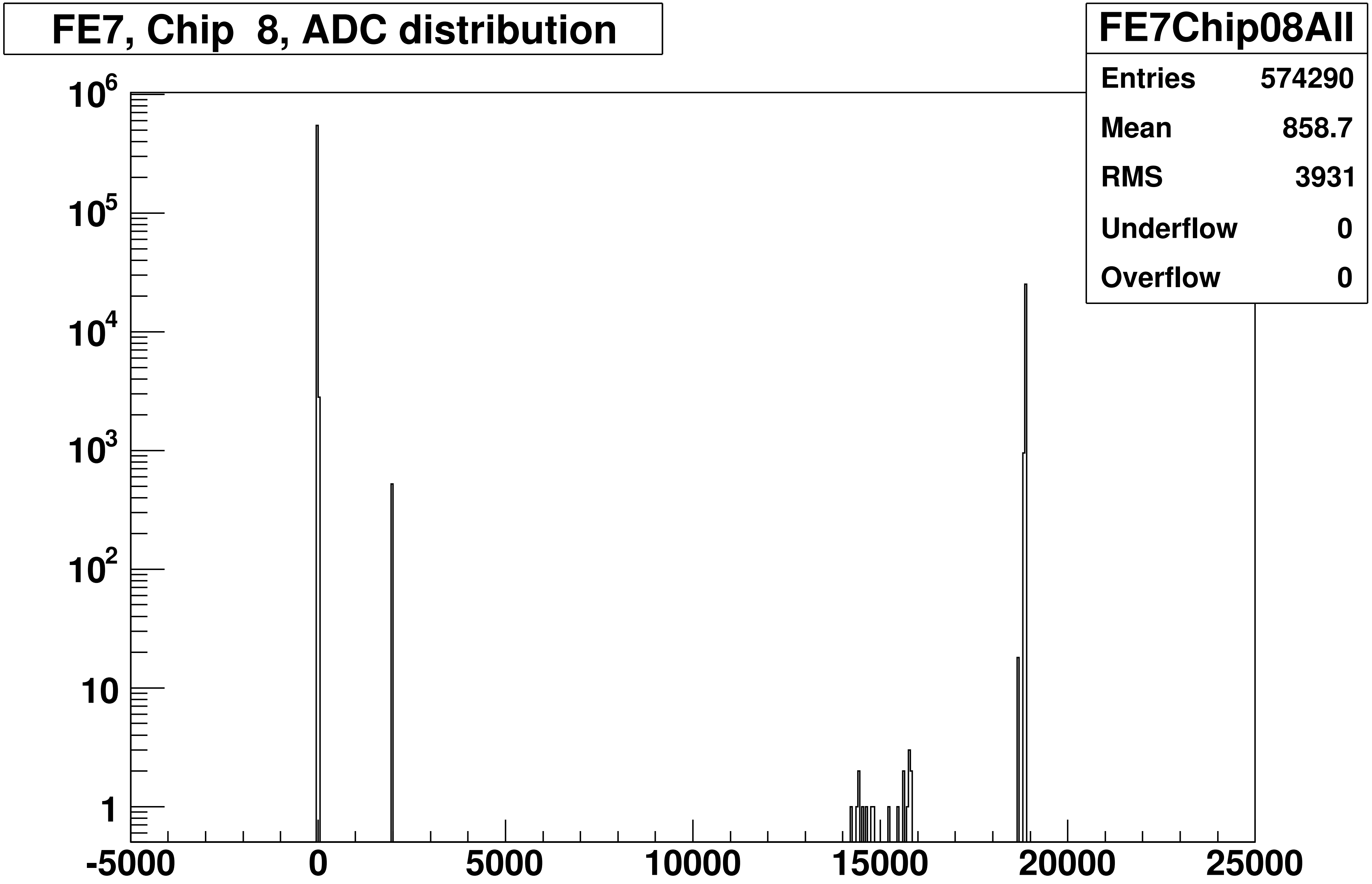
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 574290
Mean 858.7
RMS 3931
Underflow 0
Overflow 0
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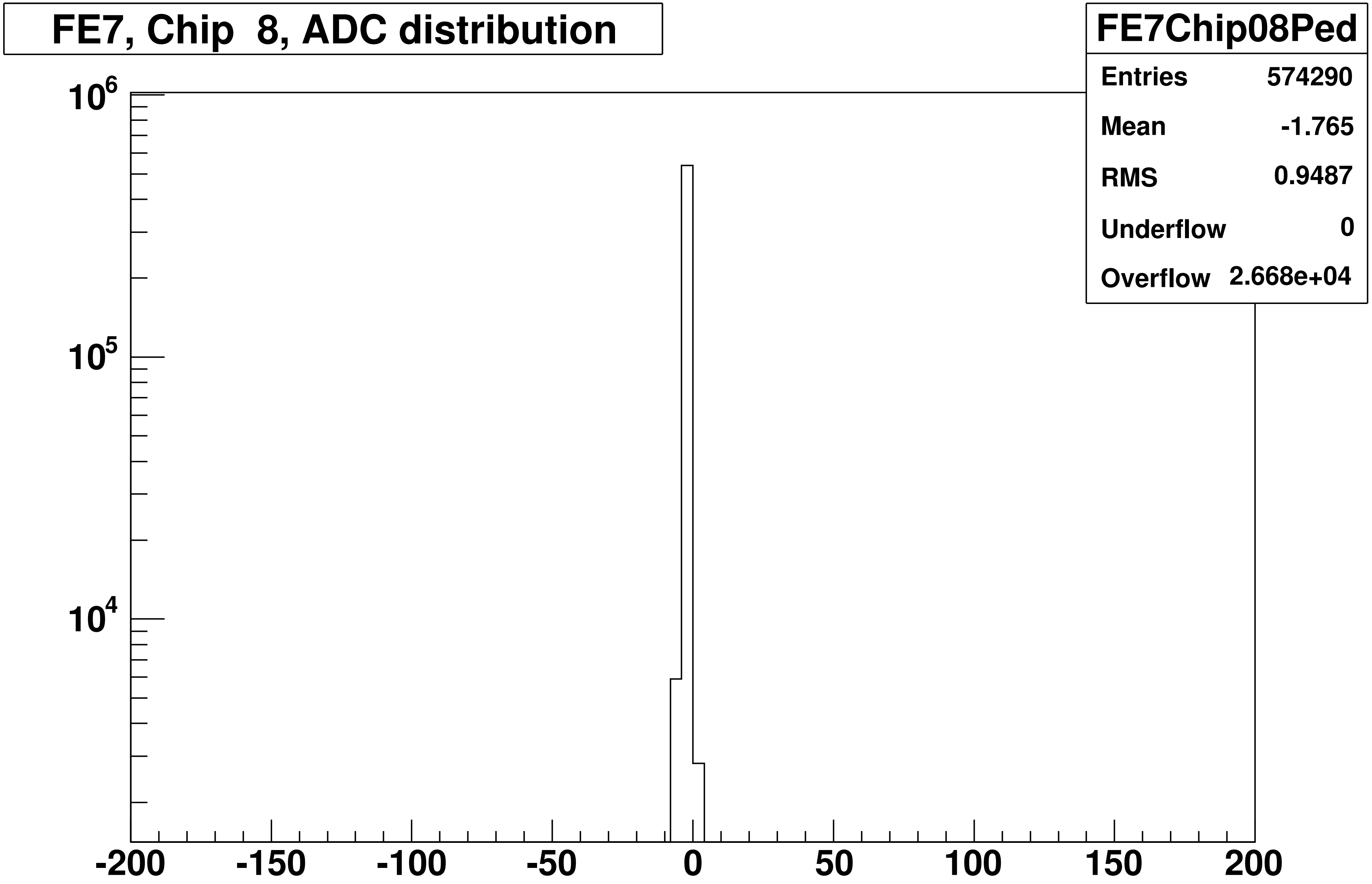

FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 574290
Mean -1.765
RMS 0.9487
Underflow 0
Overflow 2.668e+04
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10^4
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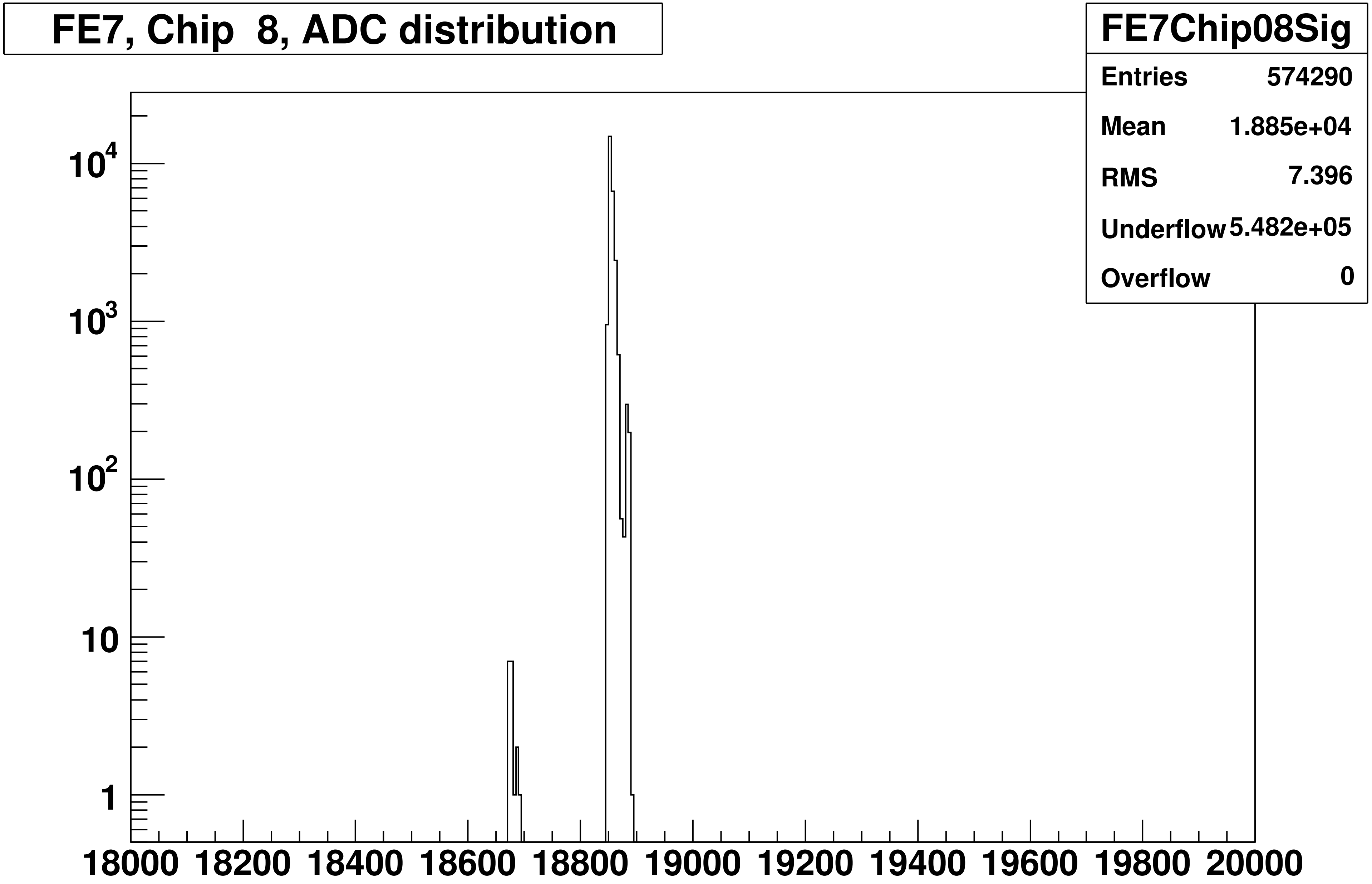
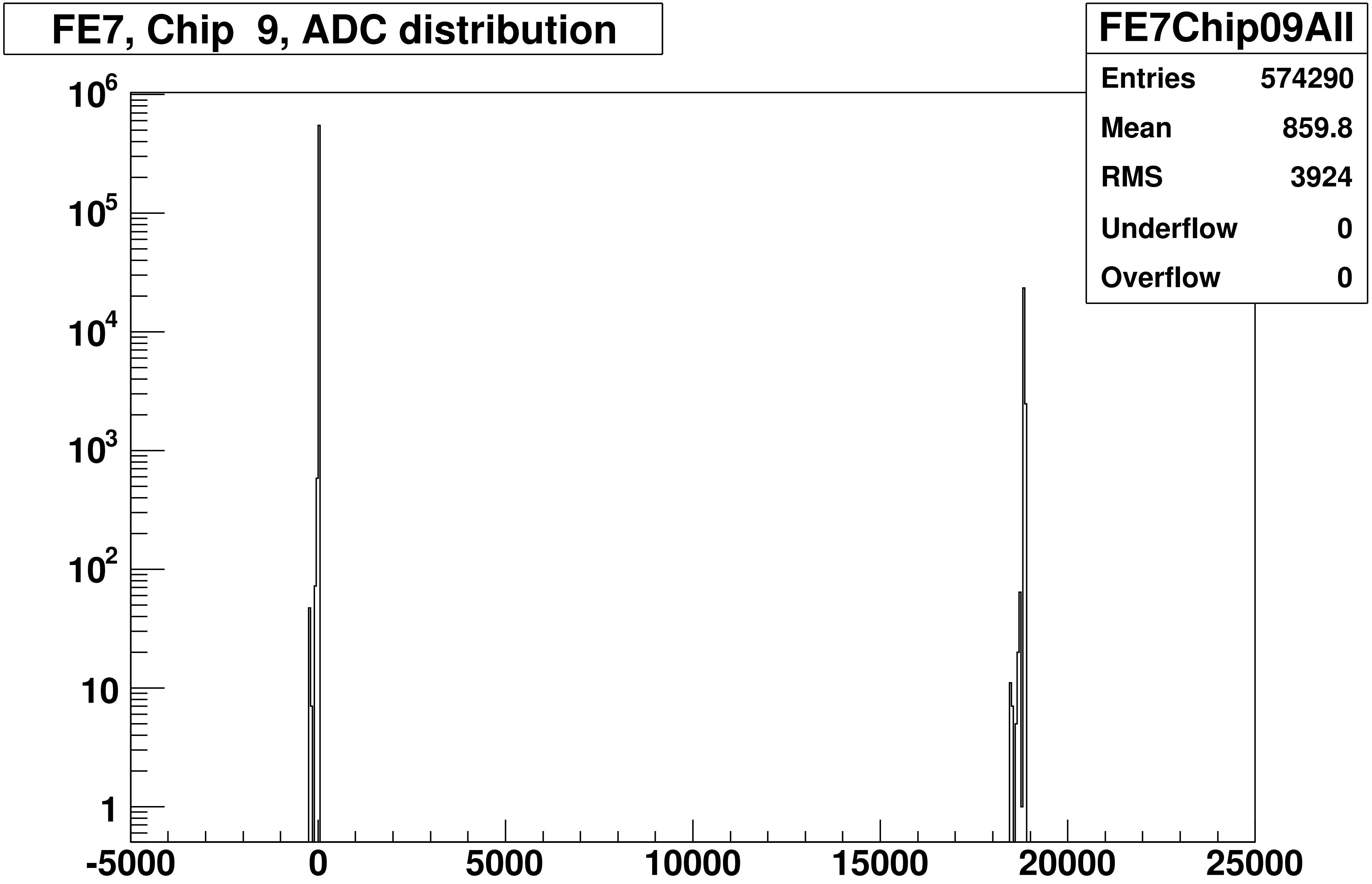
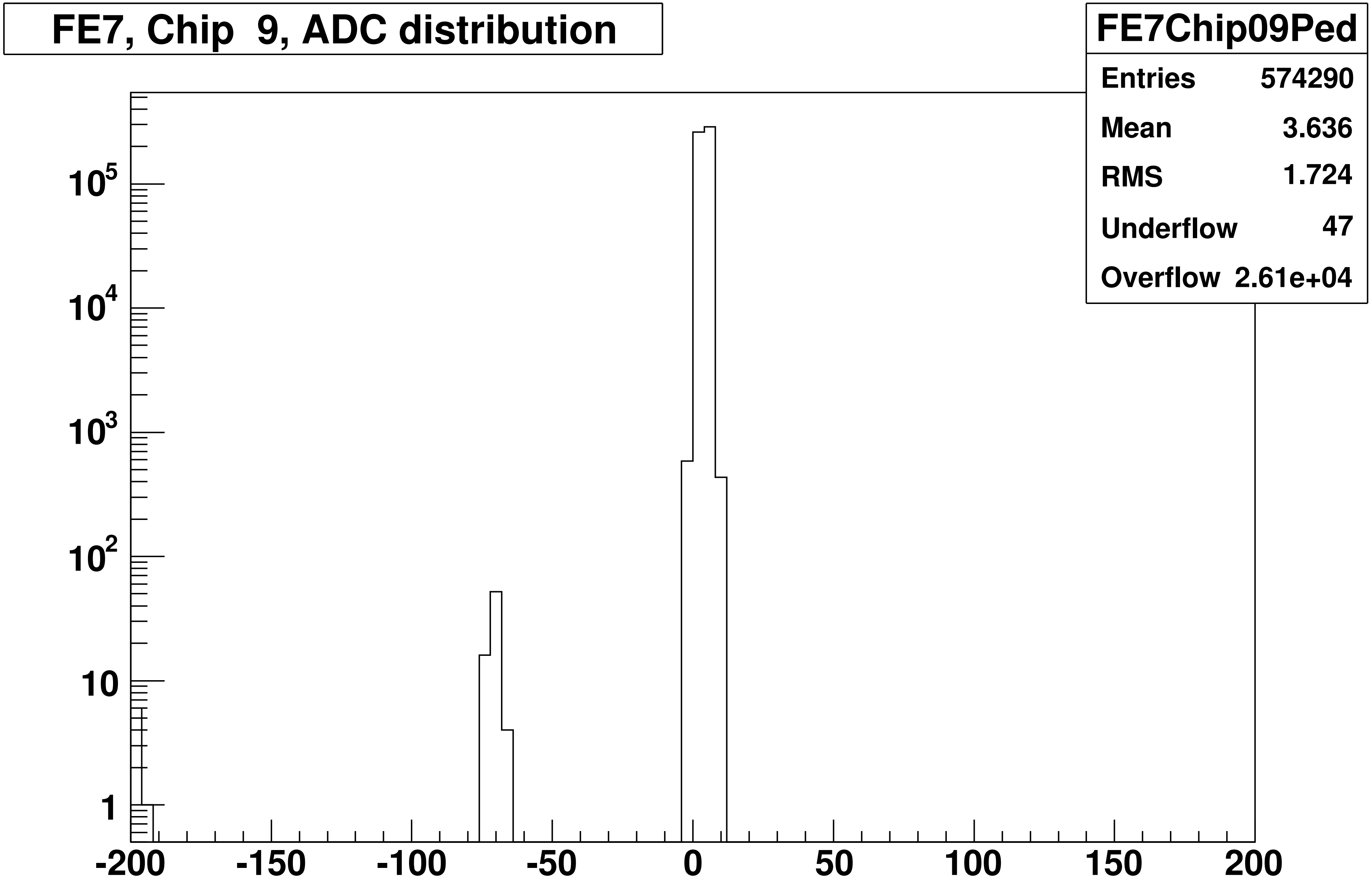
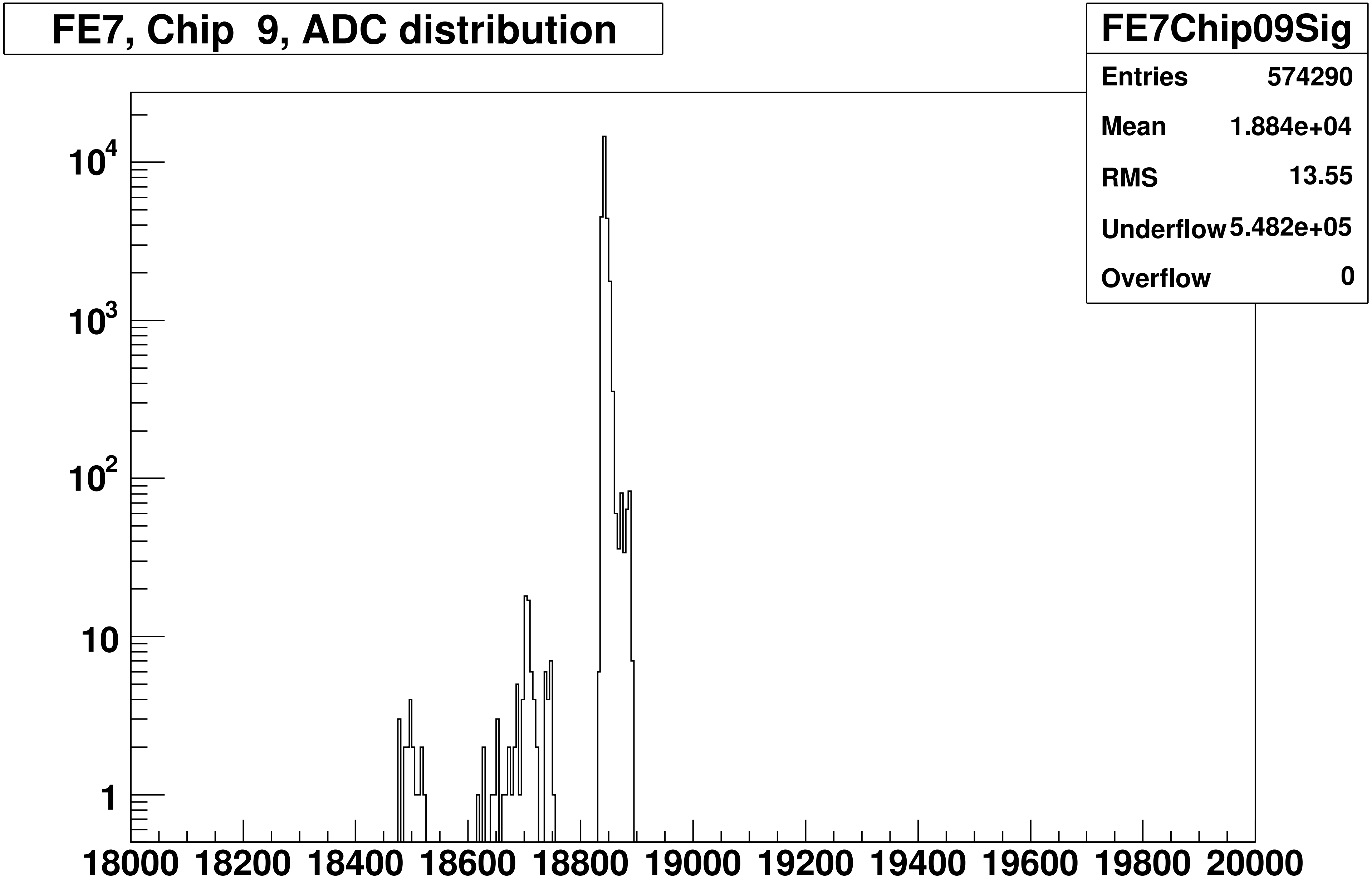
150
200


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 574290
Mean 1.885e+04
RMS 7.396
Underflow 5.482e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

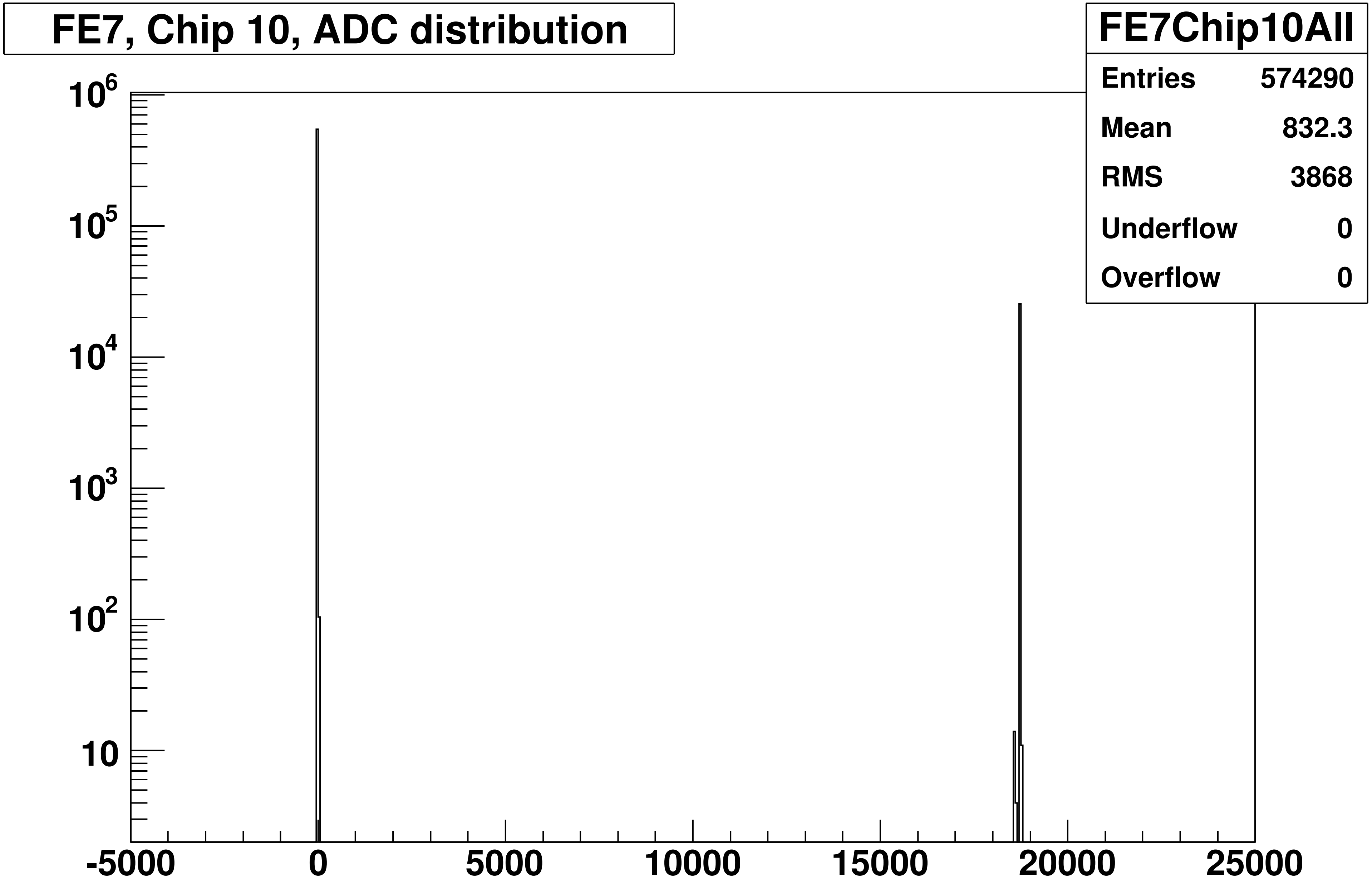


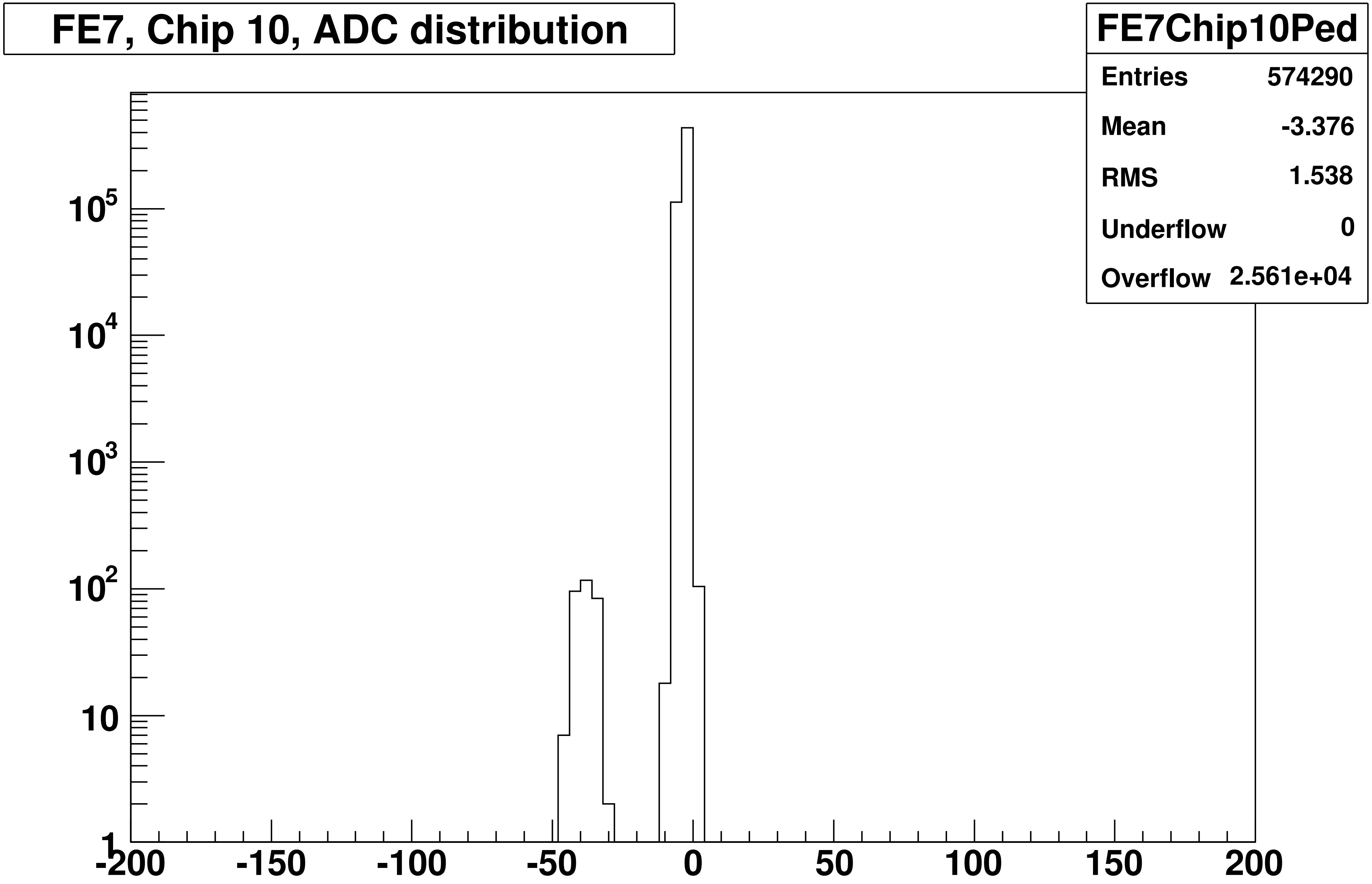
FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 574290
Mean 859.8
RMS 3924
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

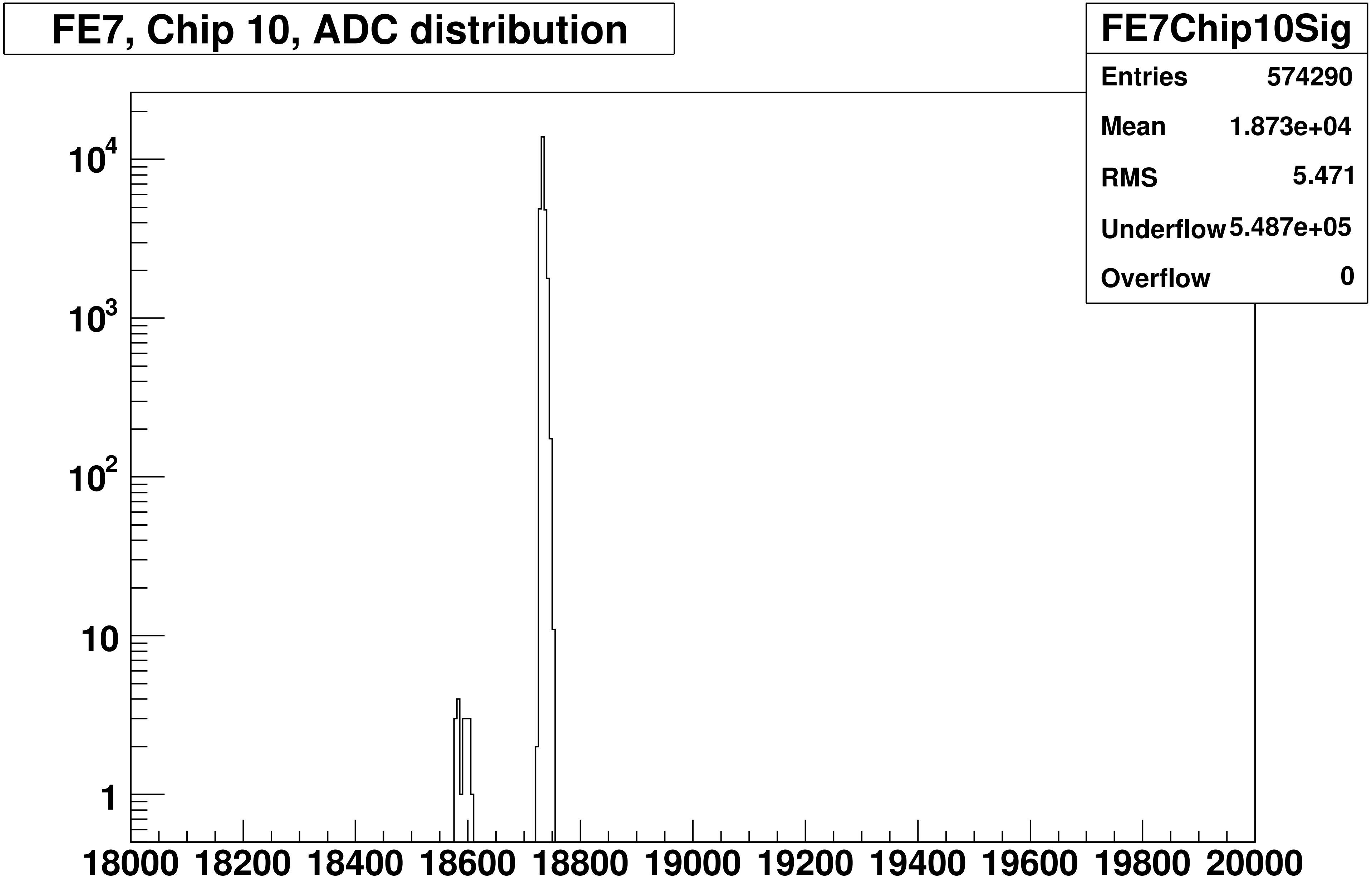

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 574290
Mean 3.636
RMS 1.724
Underflow 47
Overflow 2.61e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200

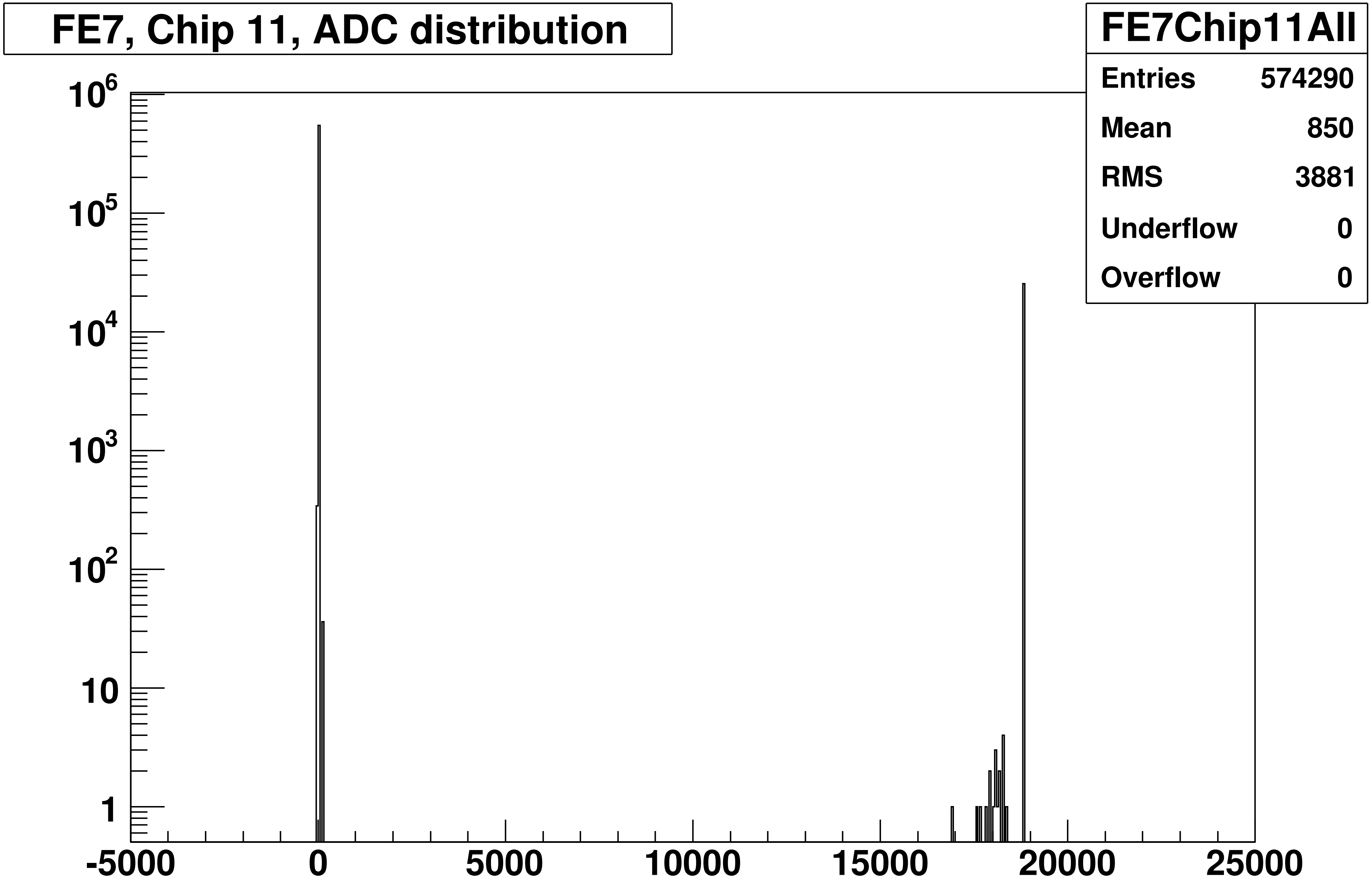


FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 574290
Mean 1.884e+04
RMS 13.55
Underflow 5.482e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

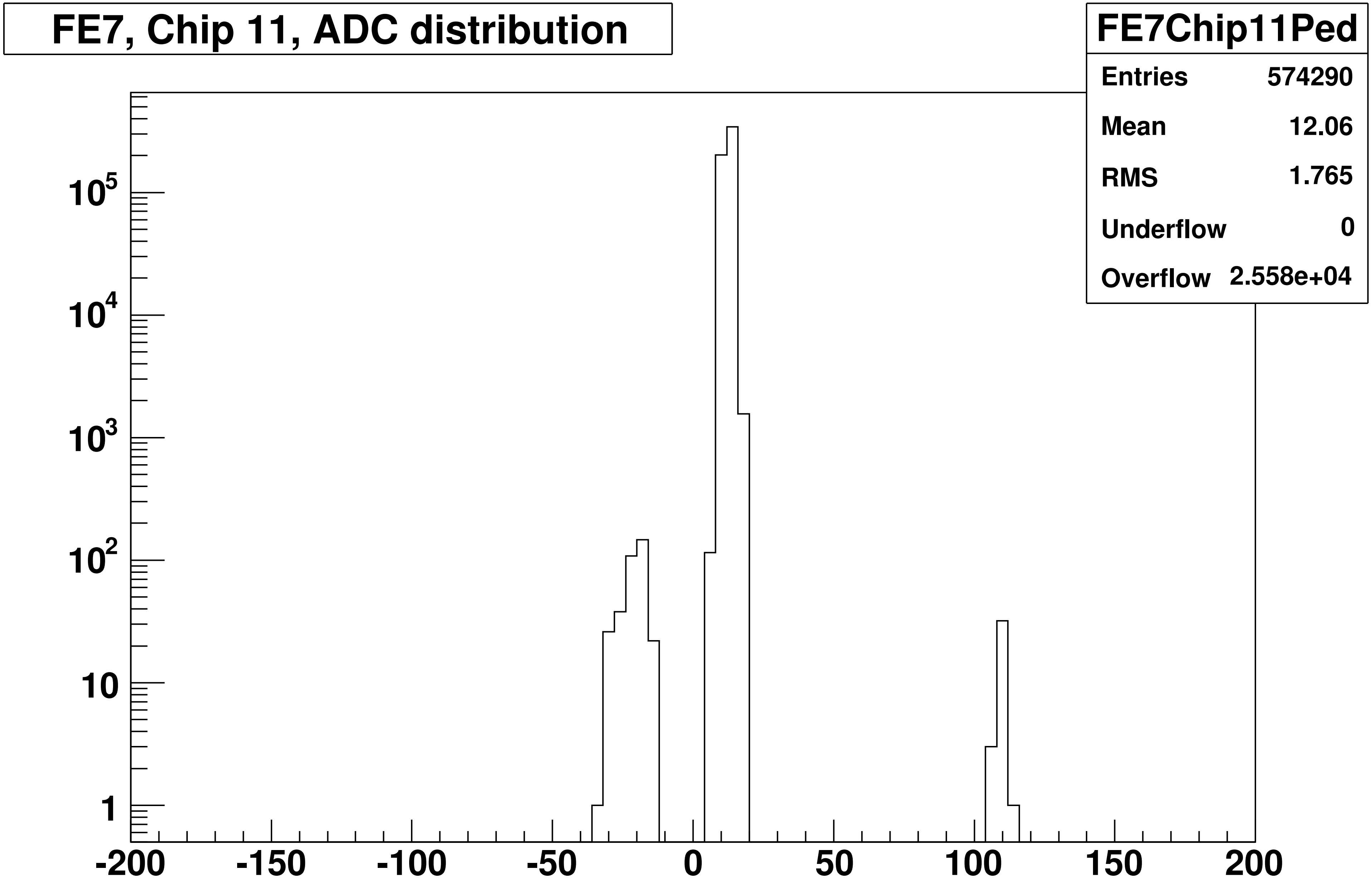

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 574290
Mean 832.3
RMS 3868
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

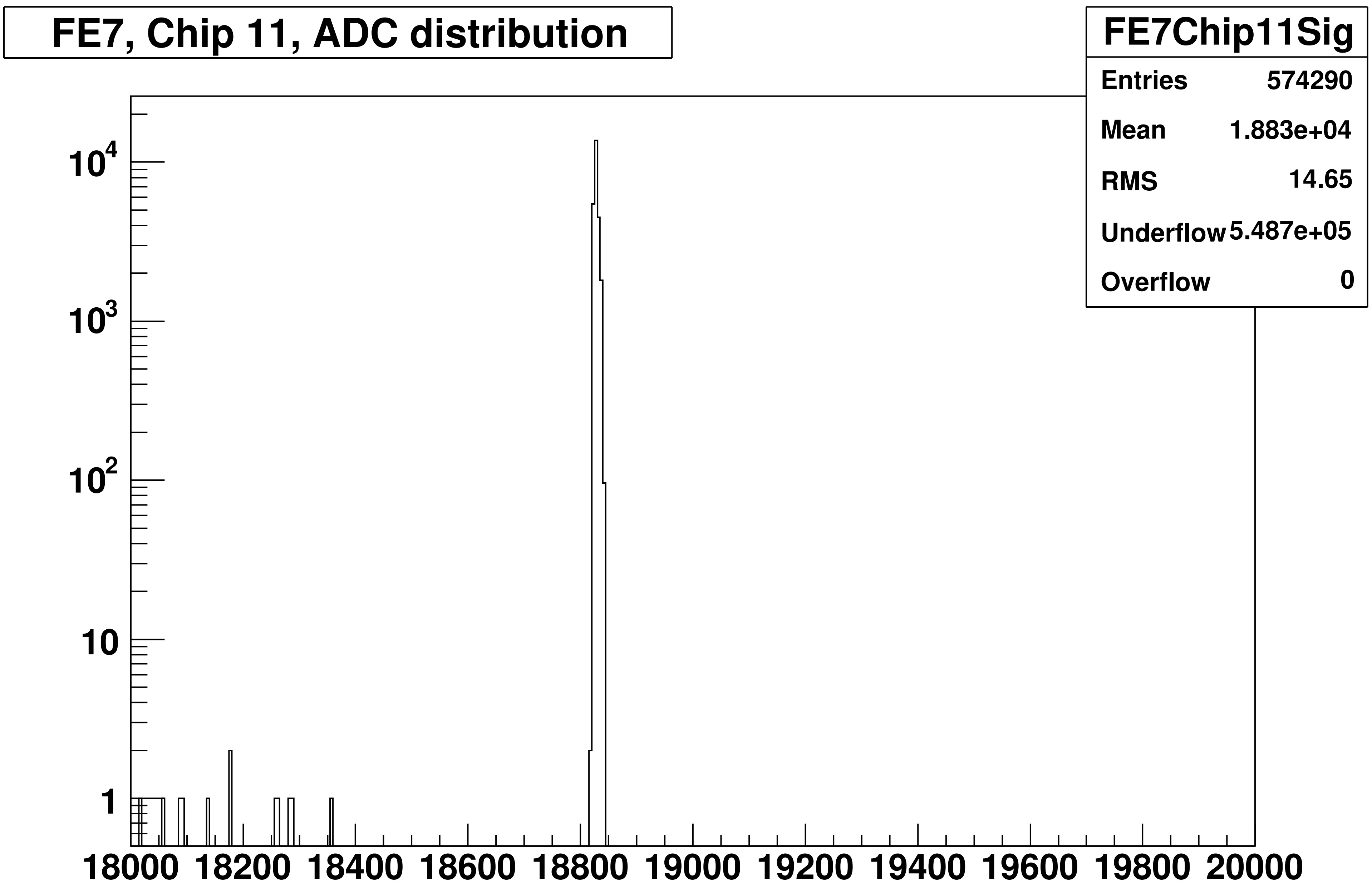

FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 574290
Mean -3.376
RMS 1.538
Underflow 0
Overflow 2.561e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 574290
Mean 1.873e+04
RMS 5.471
Underflow 5.487e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

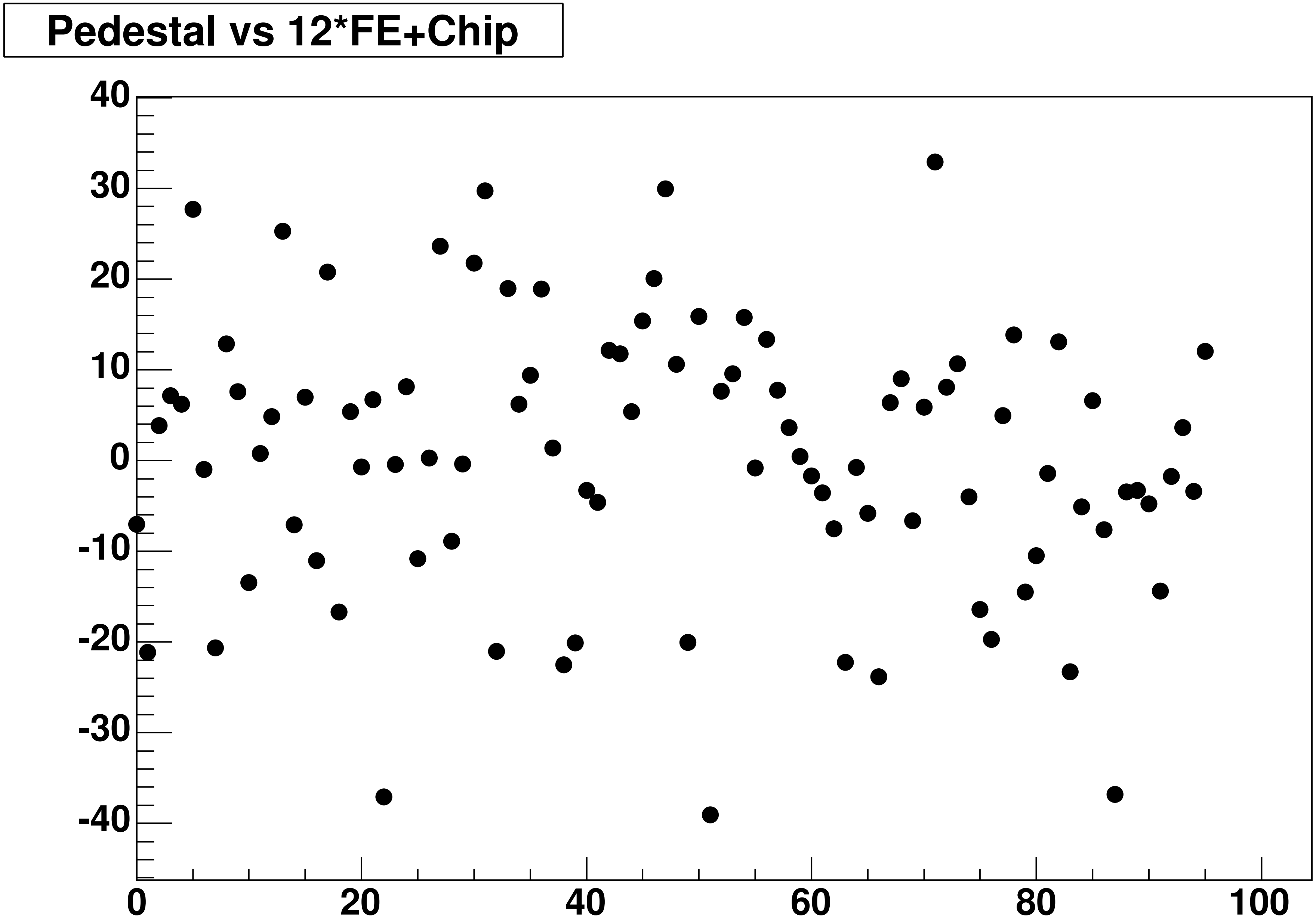


FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 574290
Mean 850
RMS 3881
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

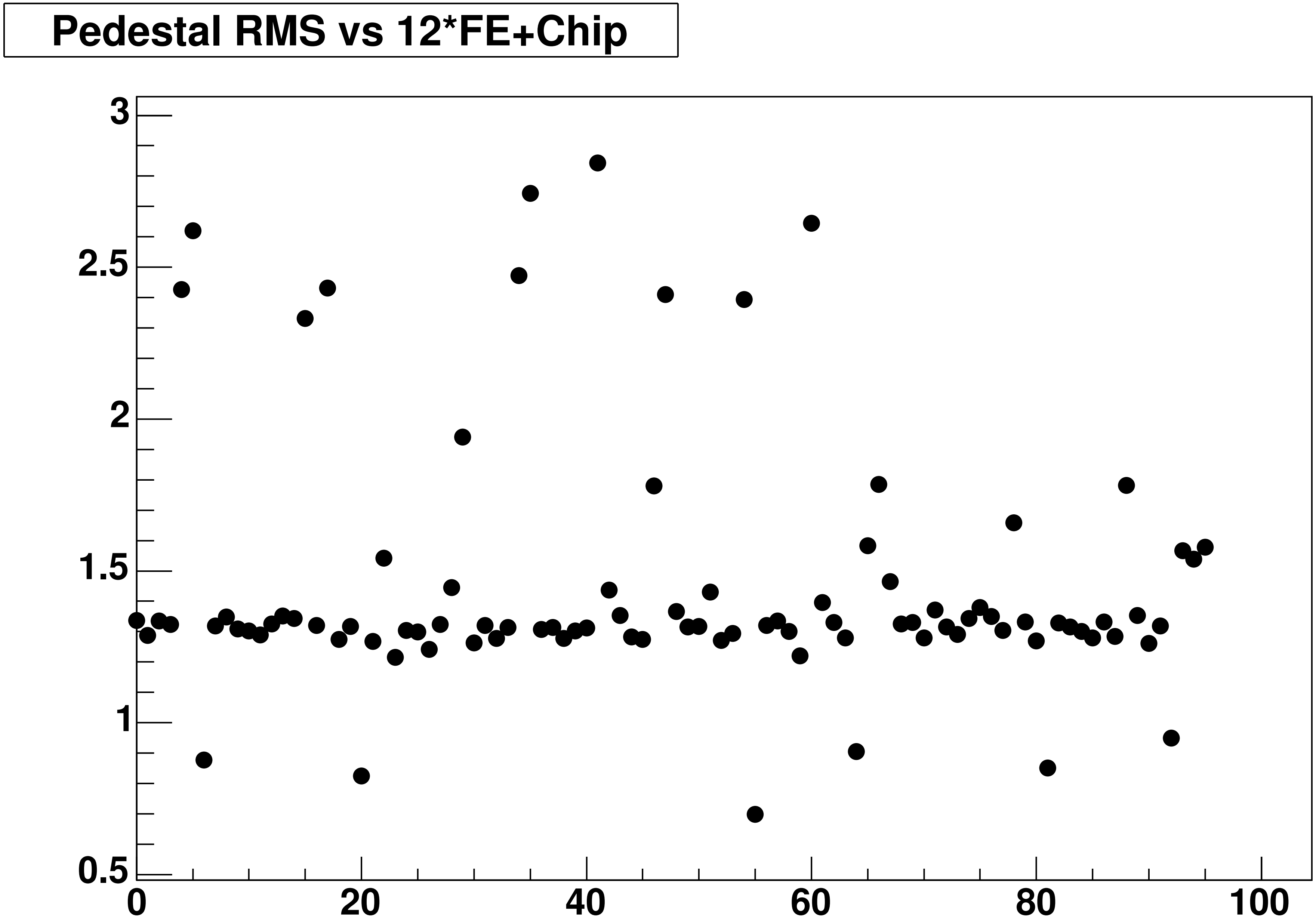

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 574290
Mean 12.06
RMS 1.765
Underflow 0
Overflow 2.558e+04
10^5
10^4
10^3
10^2
10
1
-200
-150
-100
-50
0
50
100
150
200



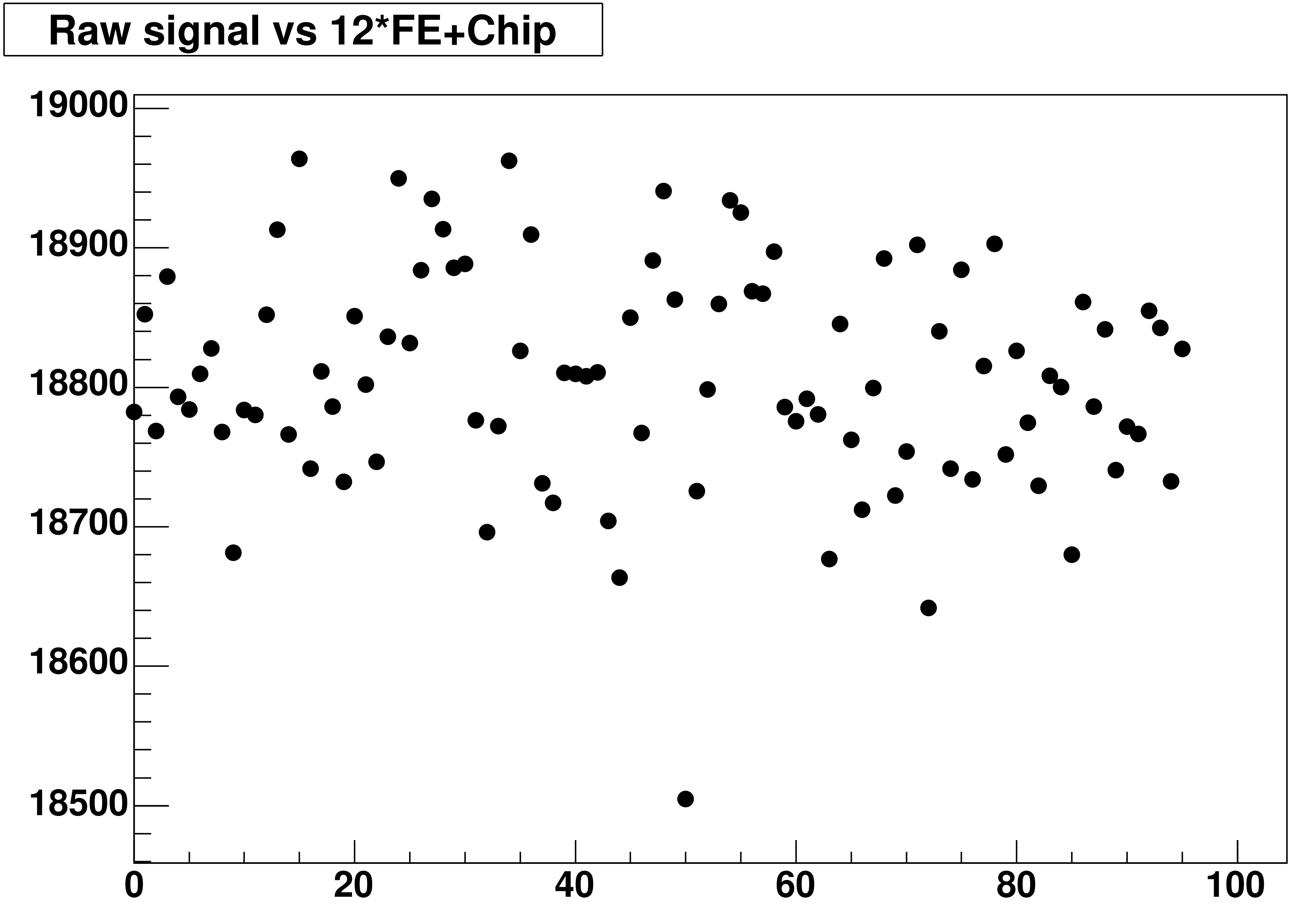
FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 574290
Mean 1.883e+04
RMS 14.65
Underflow 5.487e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

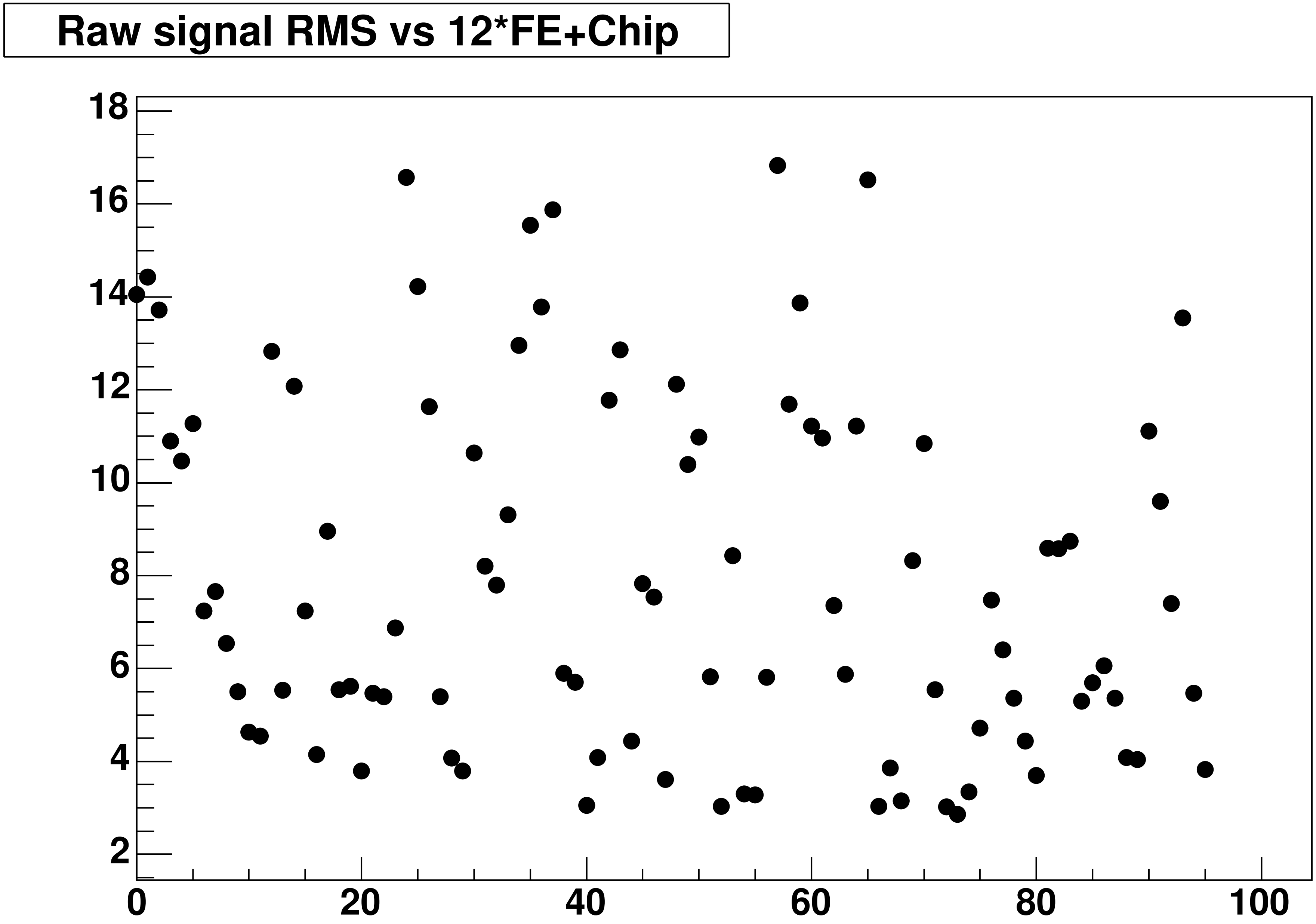


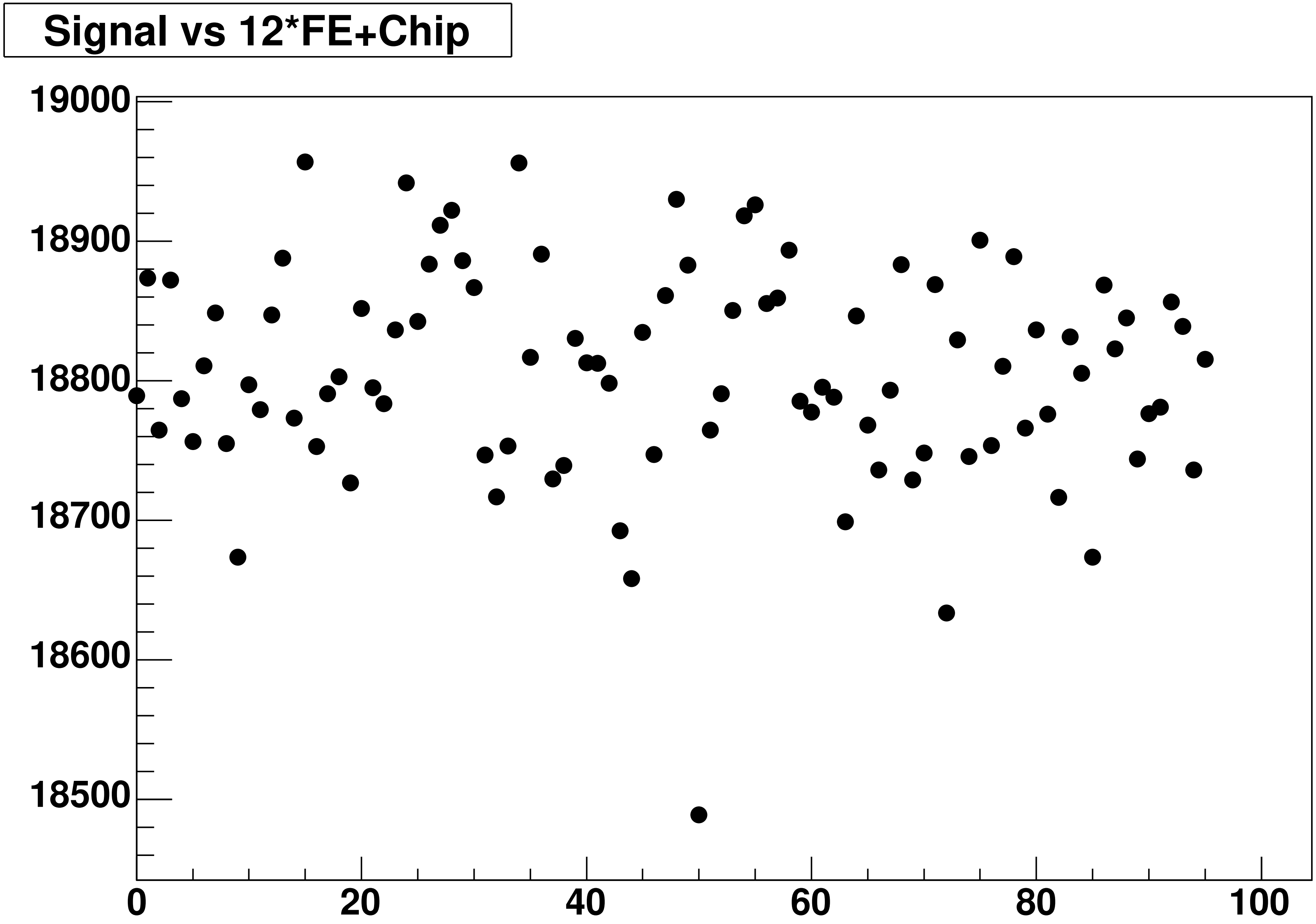
Pedestal vs 12*FE+Chip
40
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100

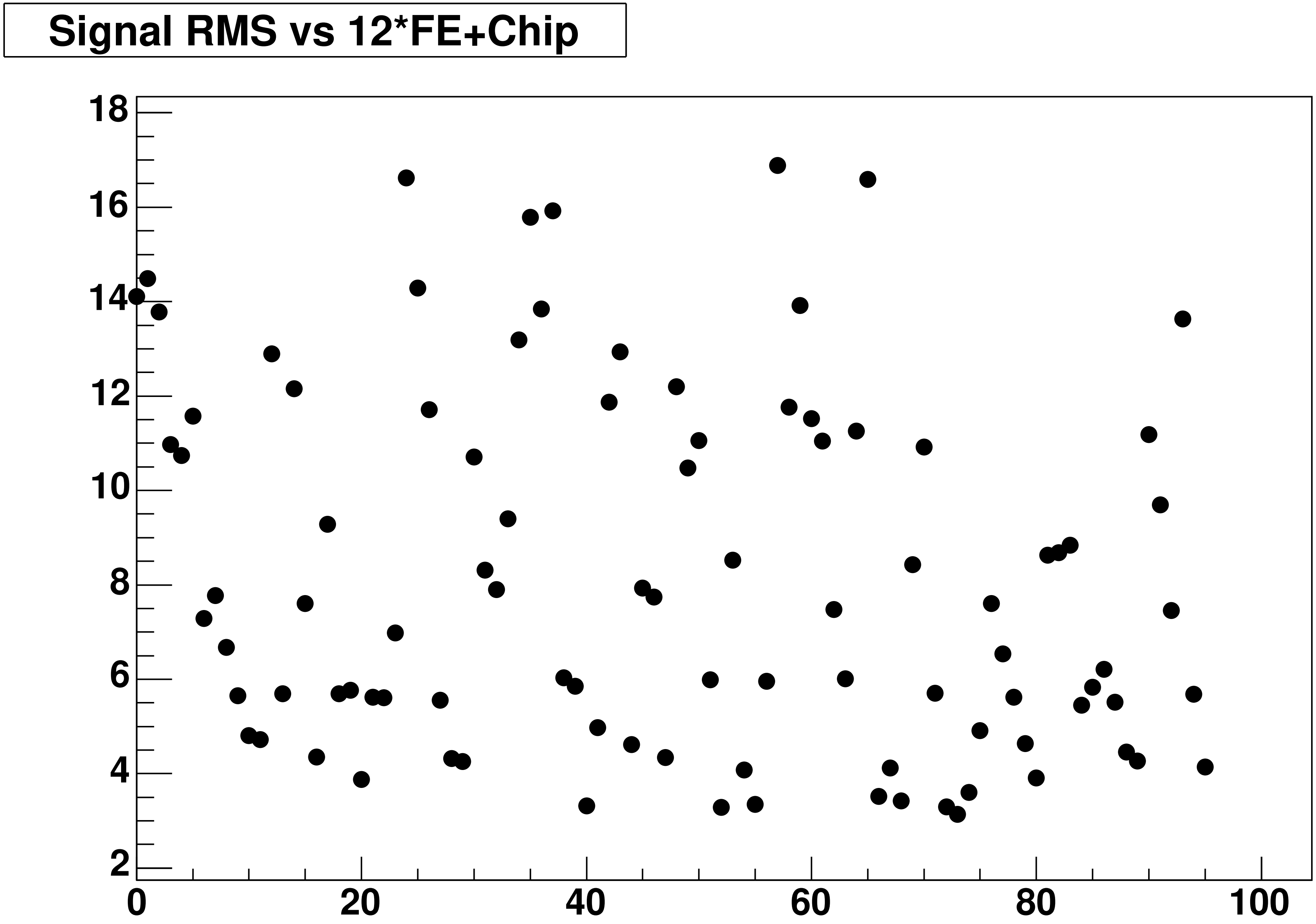


Pedestal RMS vs 12*FE+Chip
0.5
1
1.5
2
2.5
3
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19000
18900
18800
18700
18600
18500
0
20
40
60
80
100


Raw signal RMS vs 12*FE+Chip
18
16
14
12
10
8
6
4
2
0
20
40
60
80
100


Signal vs 12*FE+Chip
19000
18900
18800
18700
18600
18500
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
2
4
6
8
10
12
14
16
18
0
20
40
60
80
100