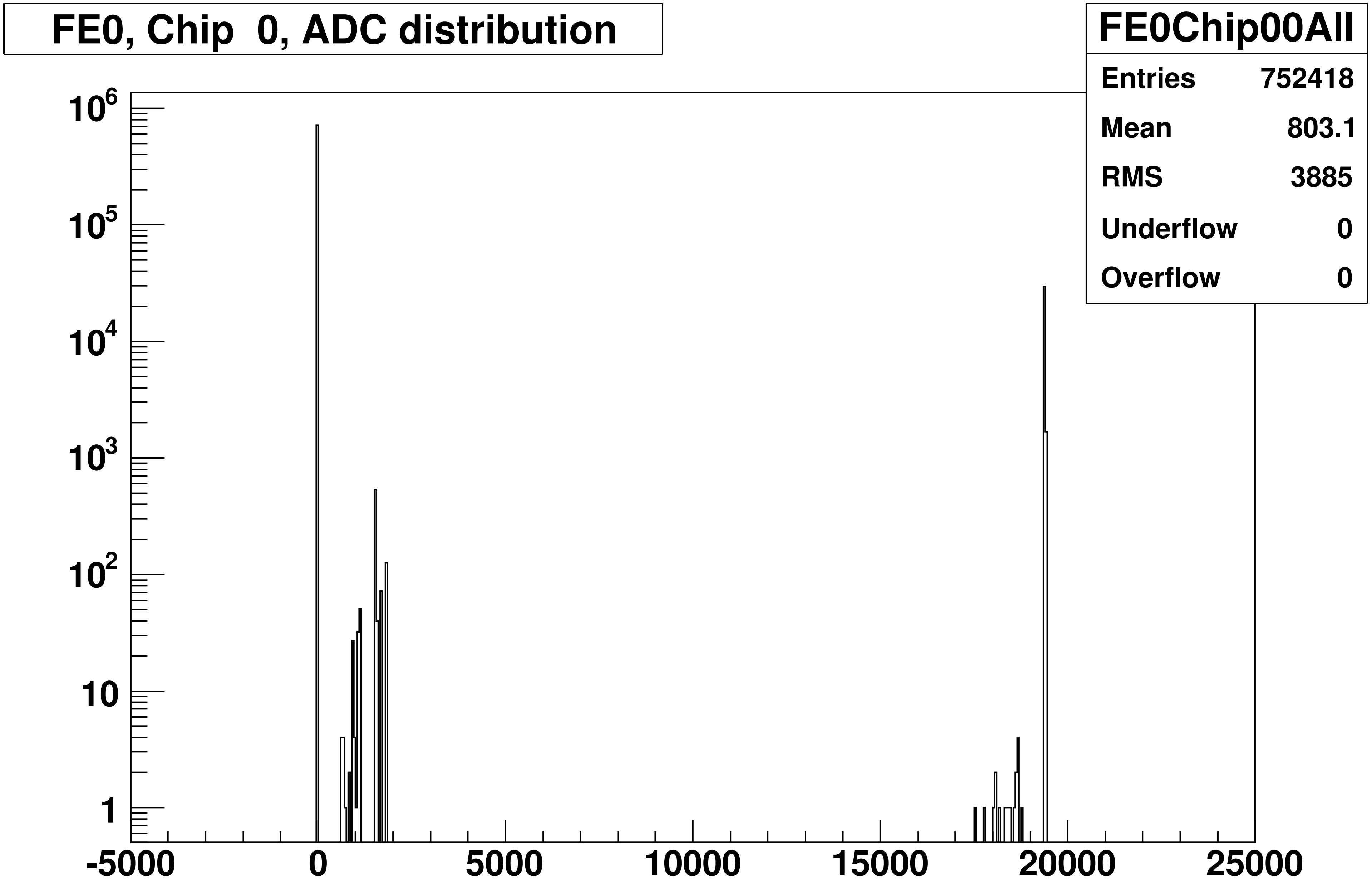
FE0, Chip  0, ADC distribution
FE0Chip00All
Entries 752418
Mean 803.1
RMS 3885
Underflow 0
Overflow 0
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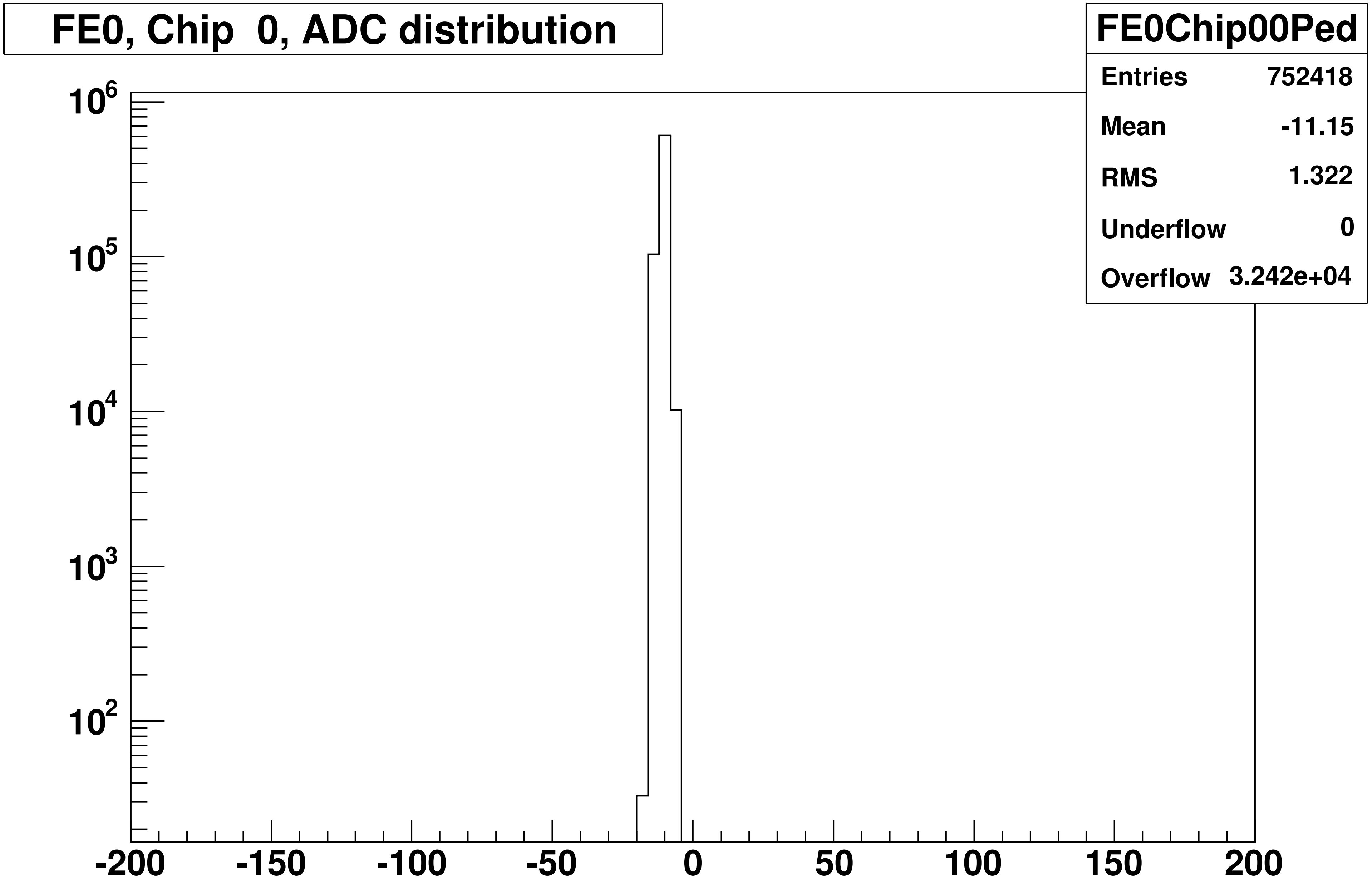
FE0, Chip  0, ADC distribution
FE0Chip00Ped
Entries 752418
Mean -11.15
RMS 1.322
Underflow 0
Overflow 3.242e+04
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10^3
10^2
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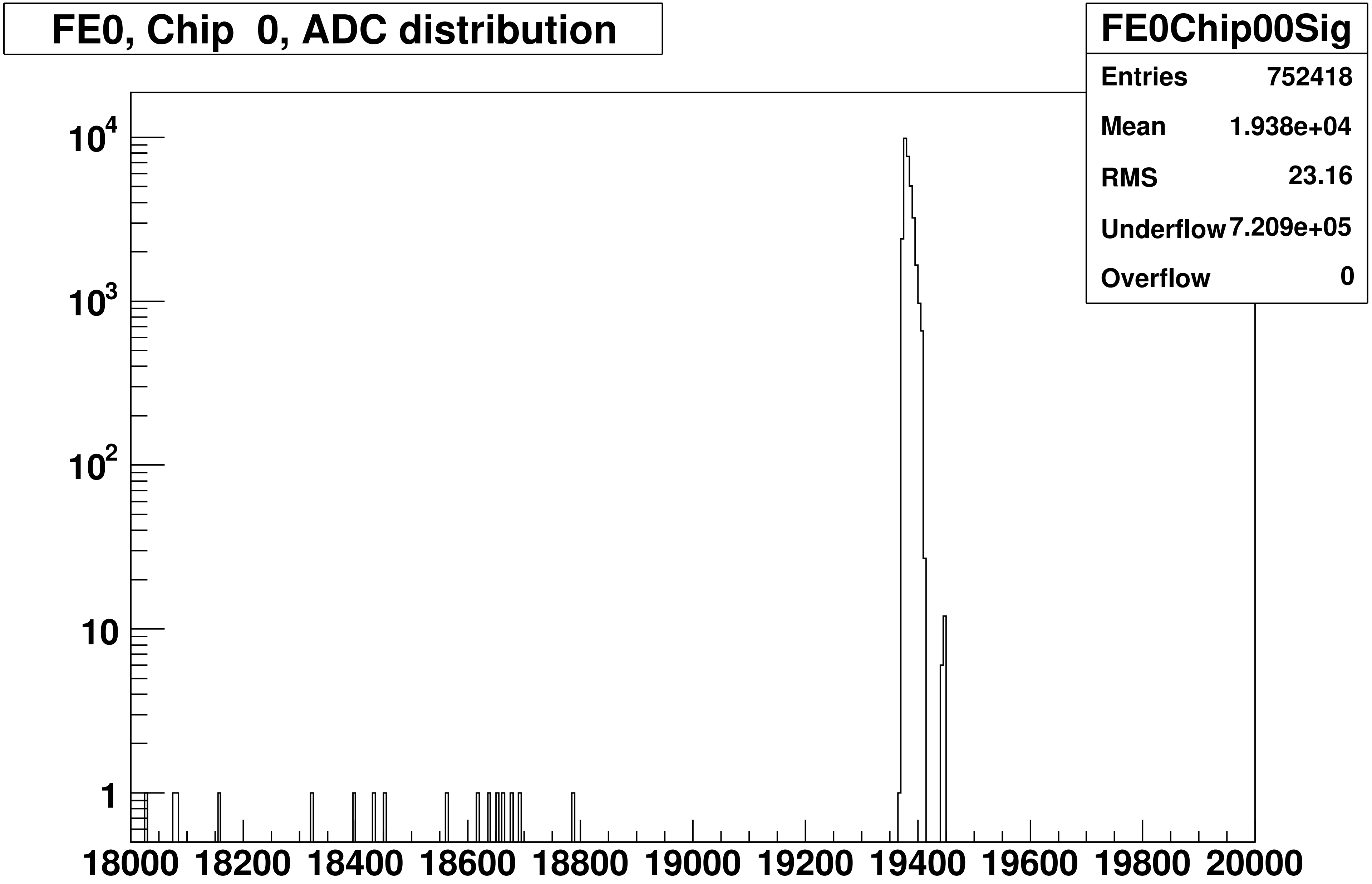

FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 752418
Mean 1.938e+04
RMS 23.16
Underflow 7.209e+05
Overflow 0
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$10^2$
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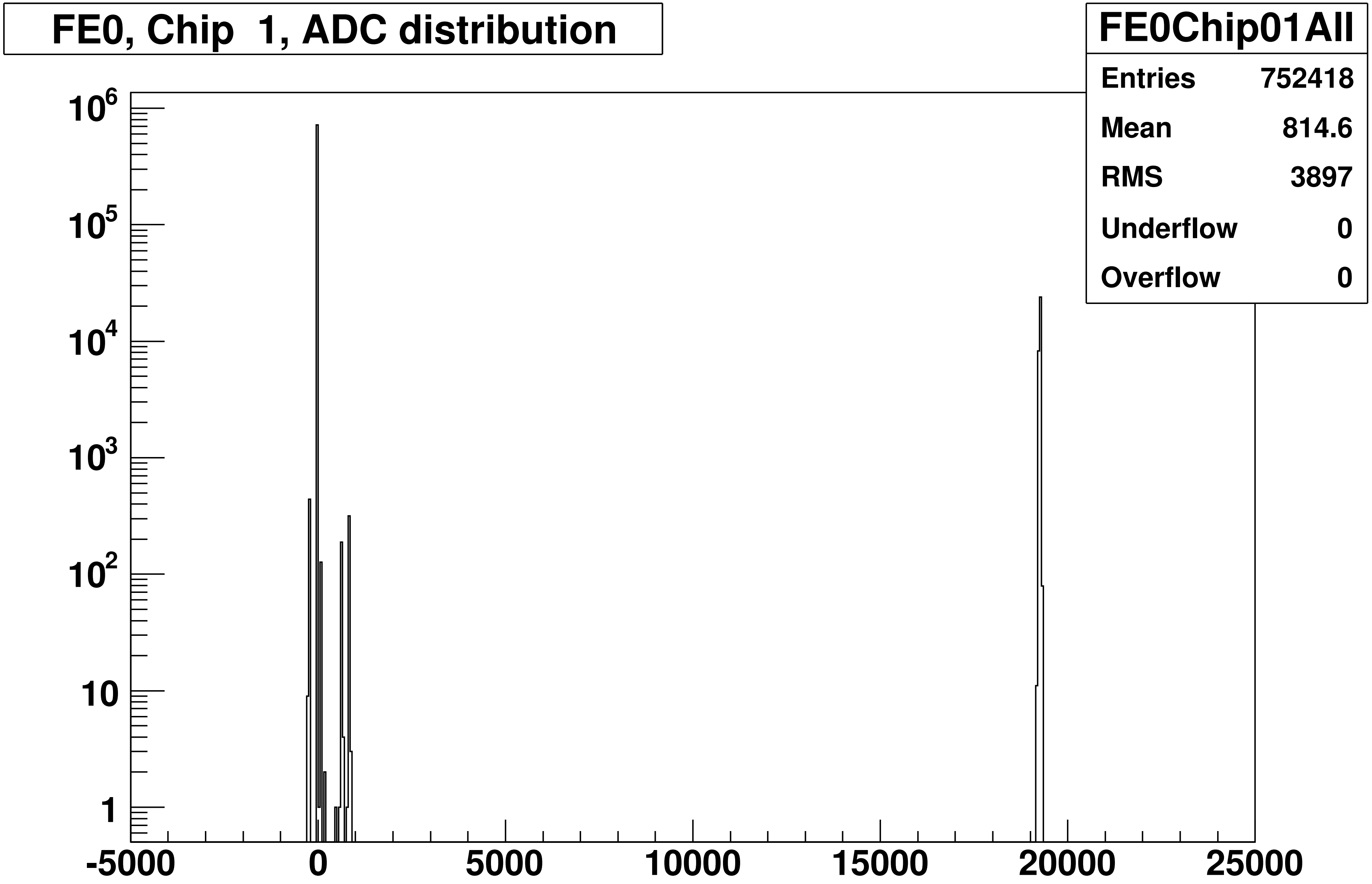
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 752418
Mean 814.6
RMS 3897
Underflow 0
Overflow 0
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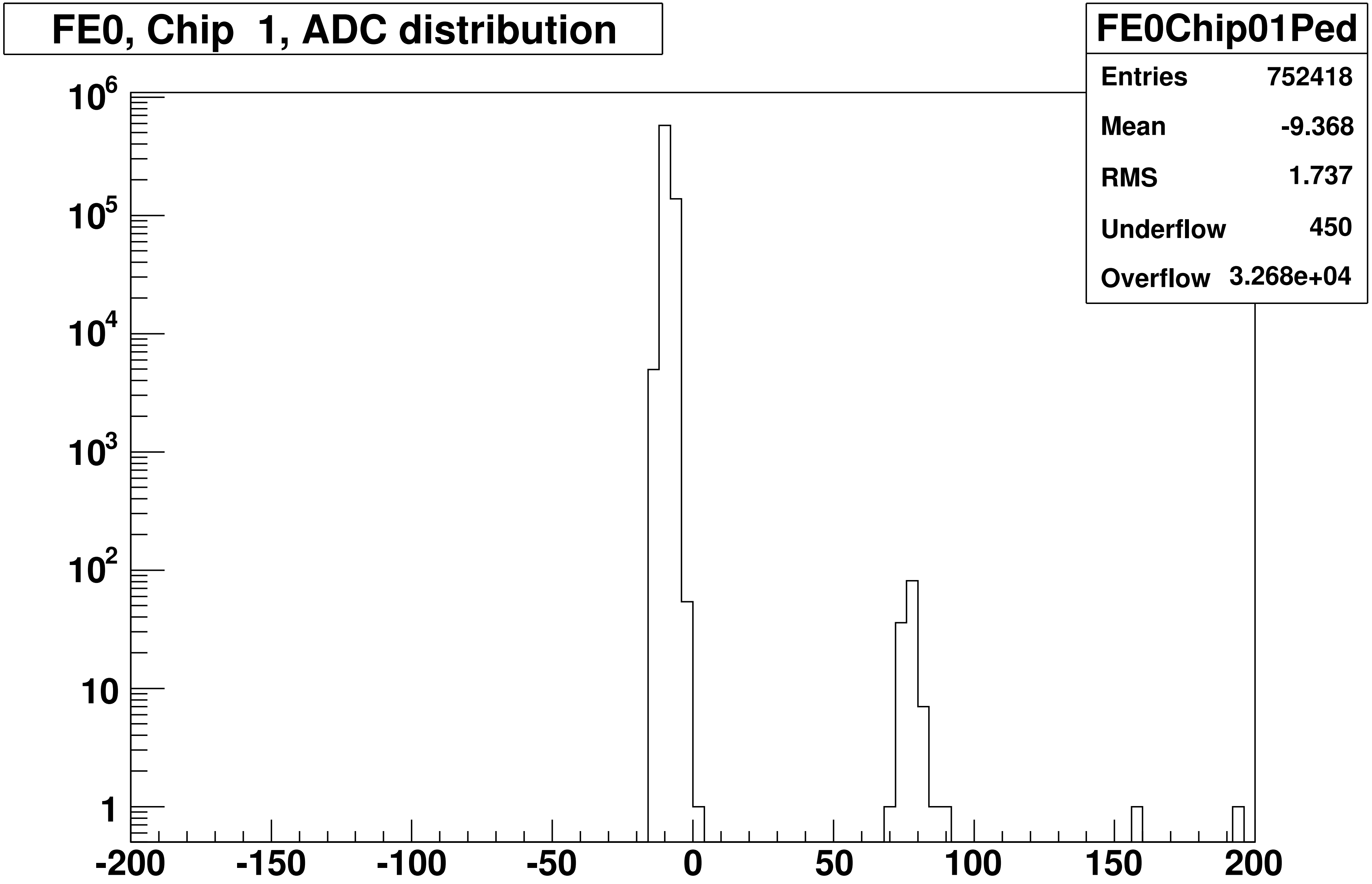

FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 752418
Mean -9.368
RMS 1.737
Underflow 450
Overflow 3.268e+04
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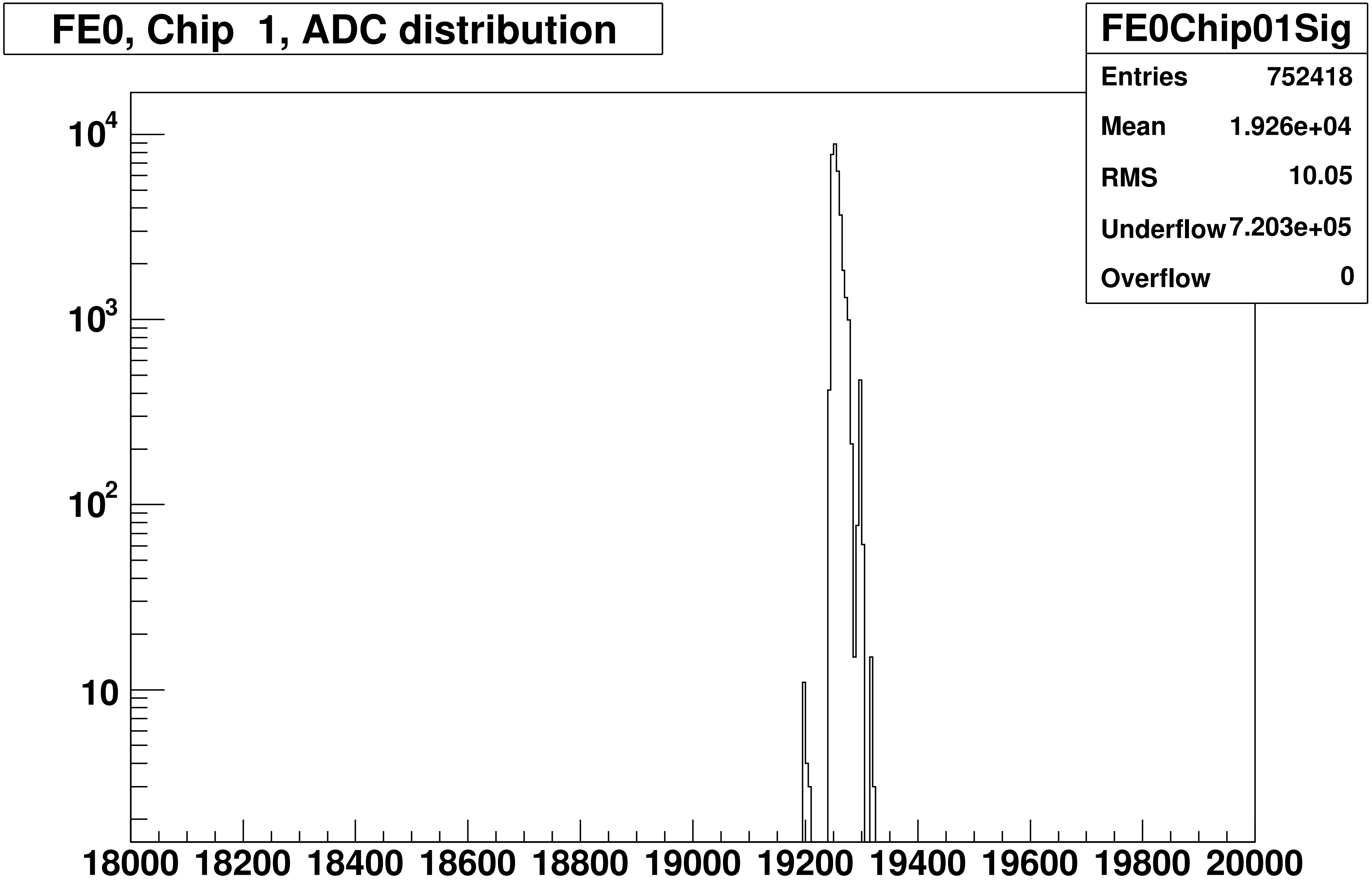

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 752418
Mean 1.926e+04
RMS 10.05
Underflow 7.203e+05
Overflow 0
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10^3
10^2
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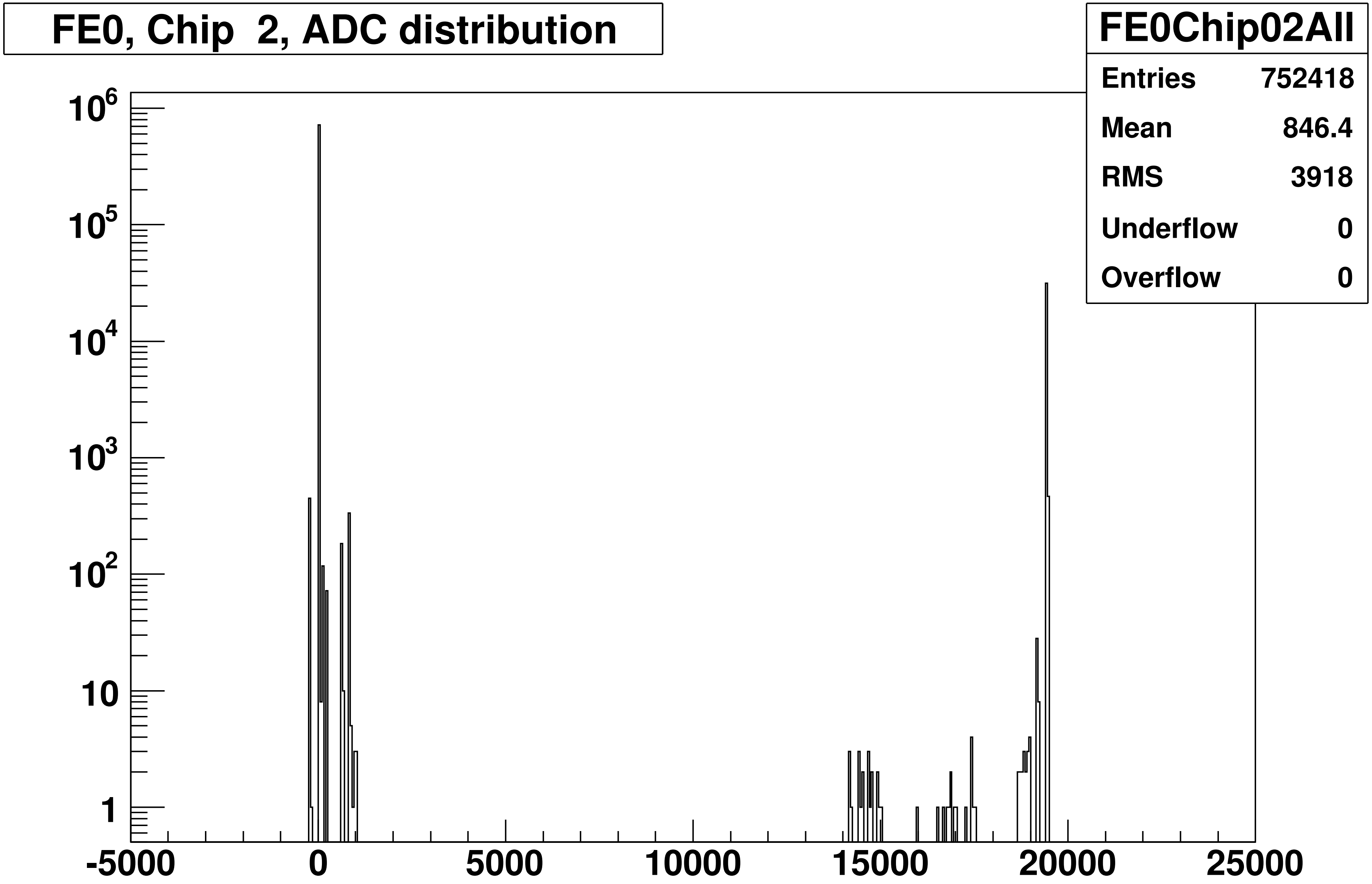
FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 752418
Mean 846.4
RMS 3918
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
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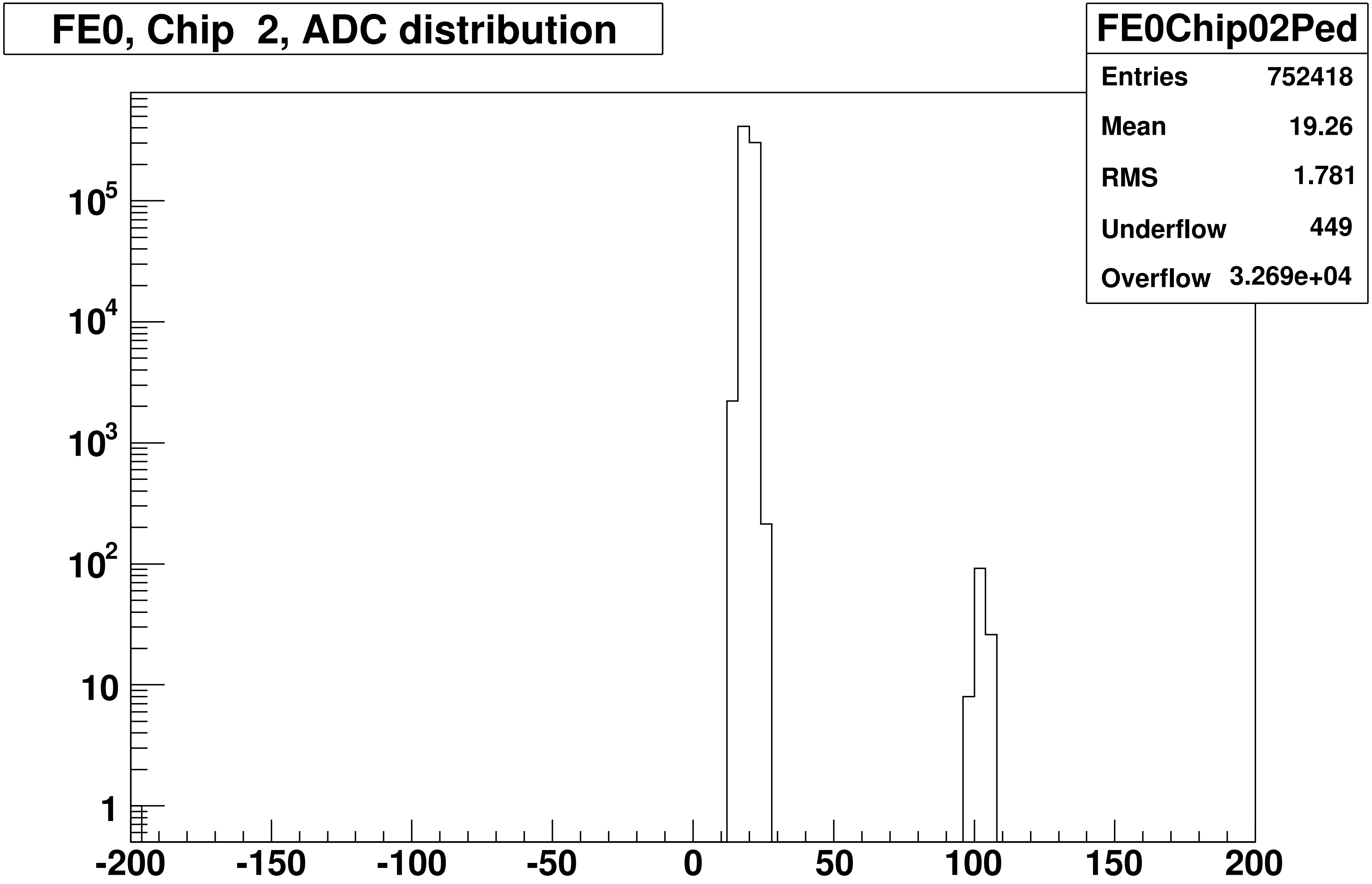

FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 752418
Mean 19.26
RMS 1.781
Underflow 449
Overflow 3.269e+04
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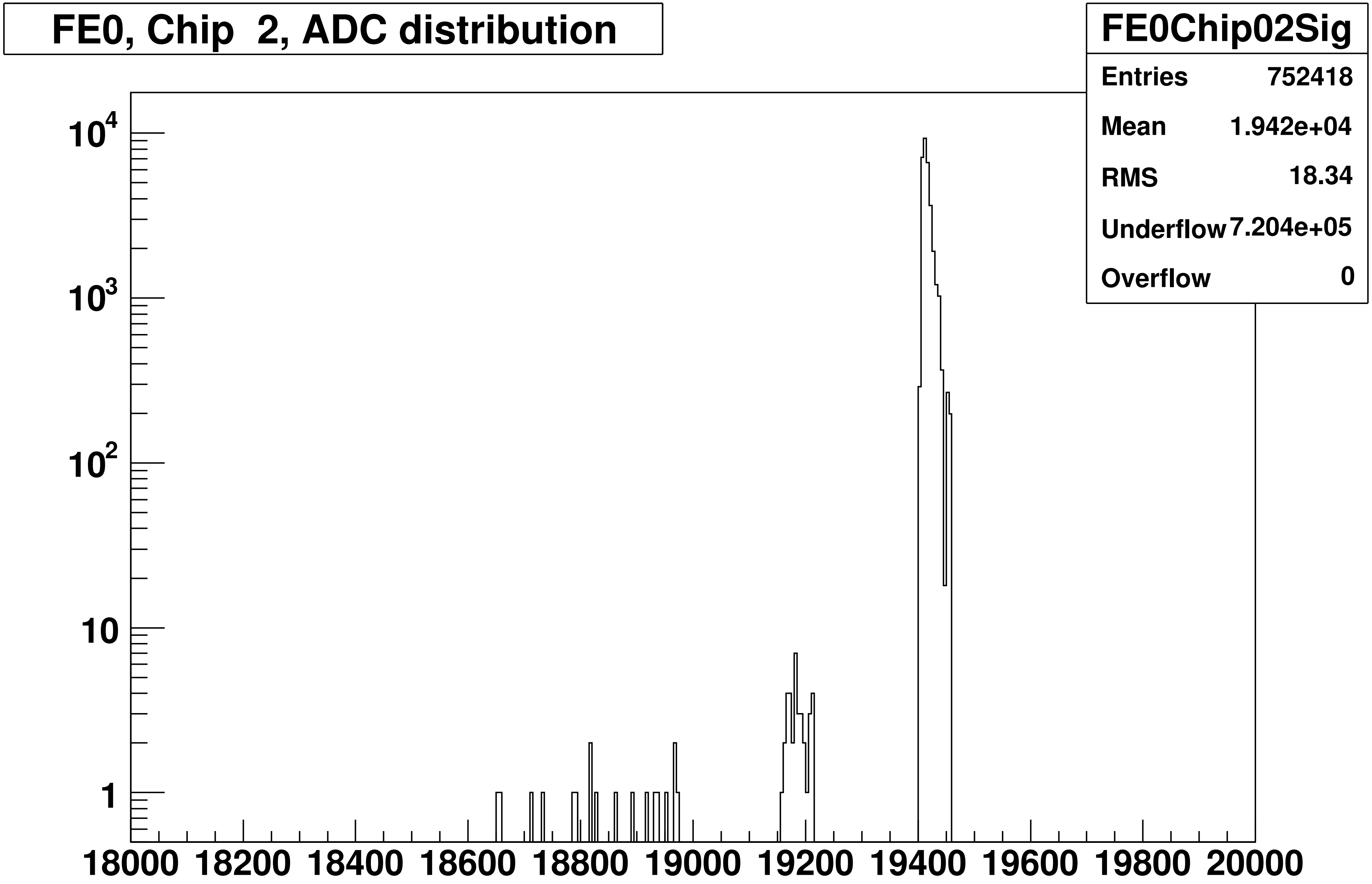
FE0, Chip  2, ADC distribution
FE0Chip02Sig
Entries 752418
Mean 1.942e+04
RMS 18.34
Underflow 7.204e+05
Overflow 0
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$10^3$
$10^2$
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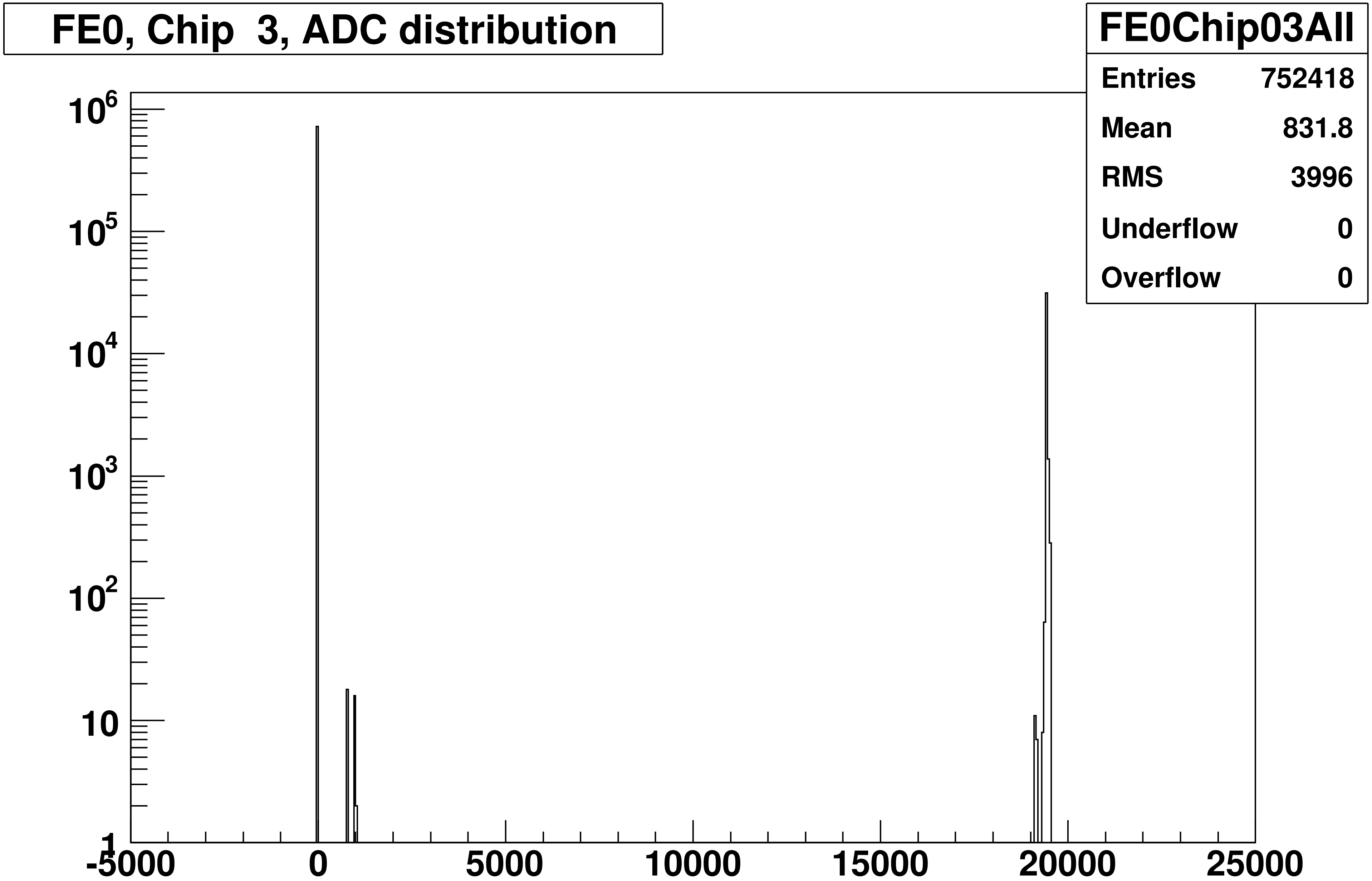
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip  3, ADC distribution
FE0Chip03All
Entries 752418
Mean 831.8
RMS 3996
Underflow 0
Overflow 0
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10^3
10^4
10^5
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20000
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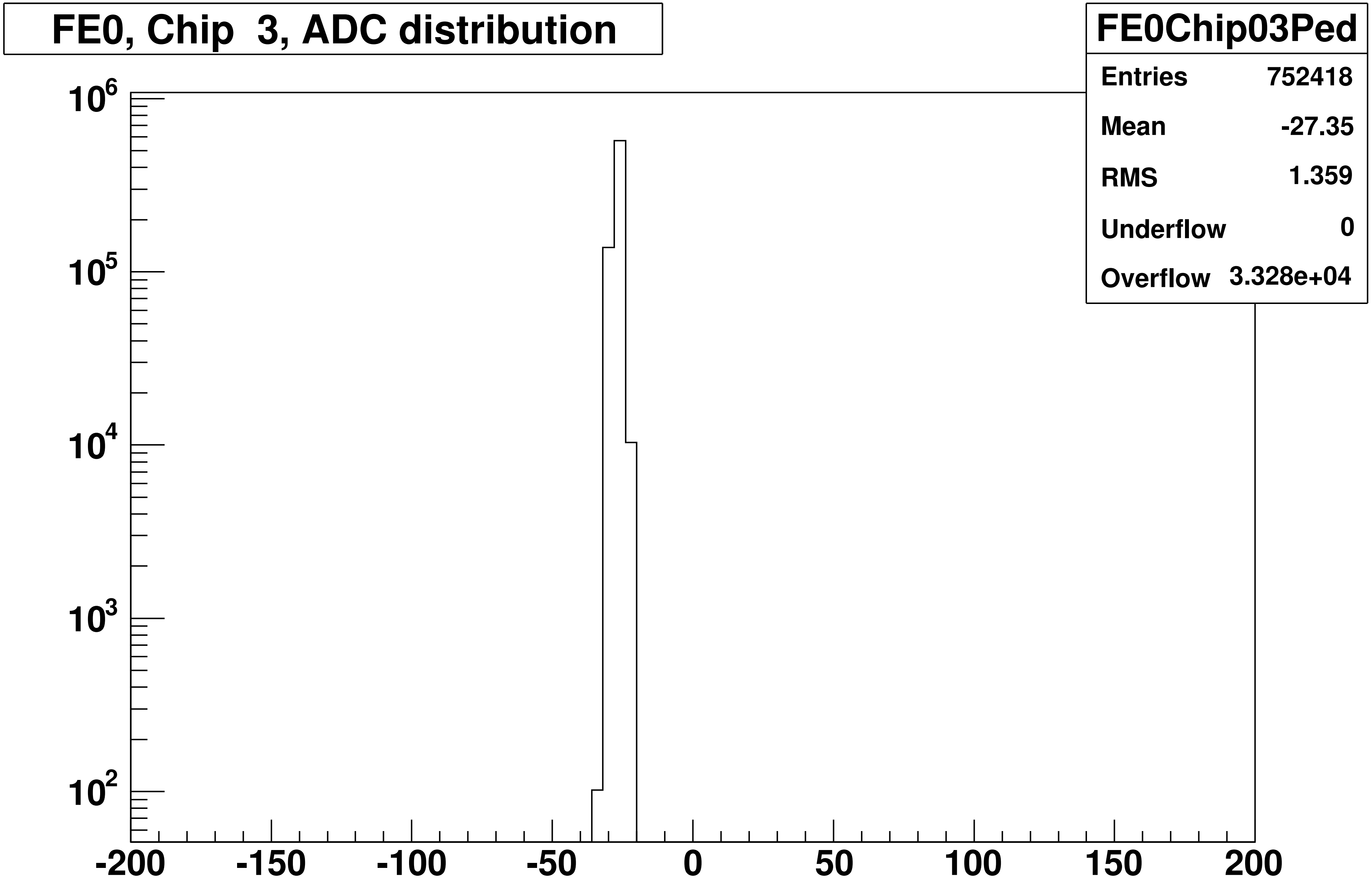
FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 752418
Mean -27.35
RMS 1.359
Underflow 0
Overflow 3.328e+04
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150
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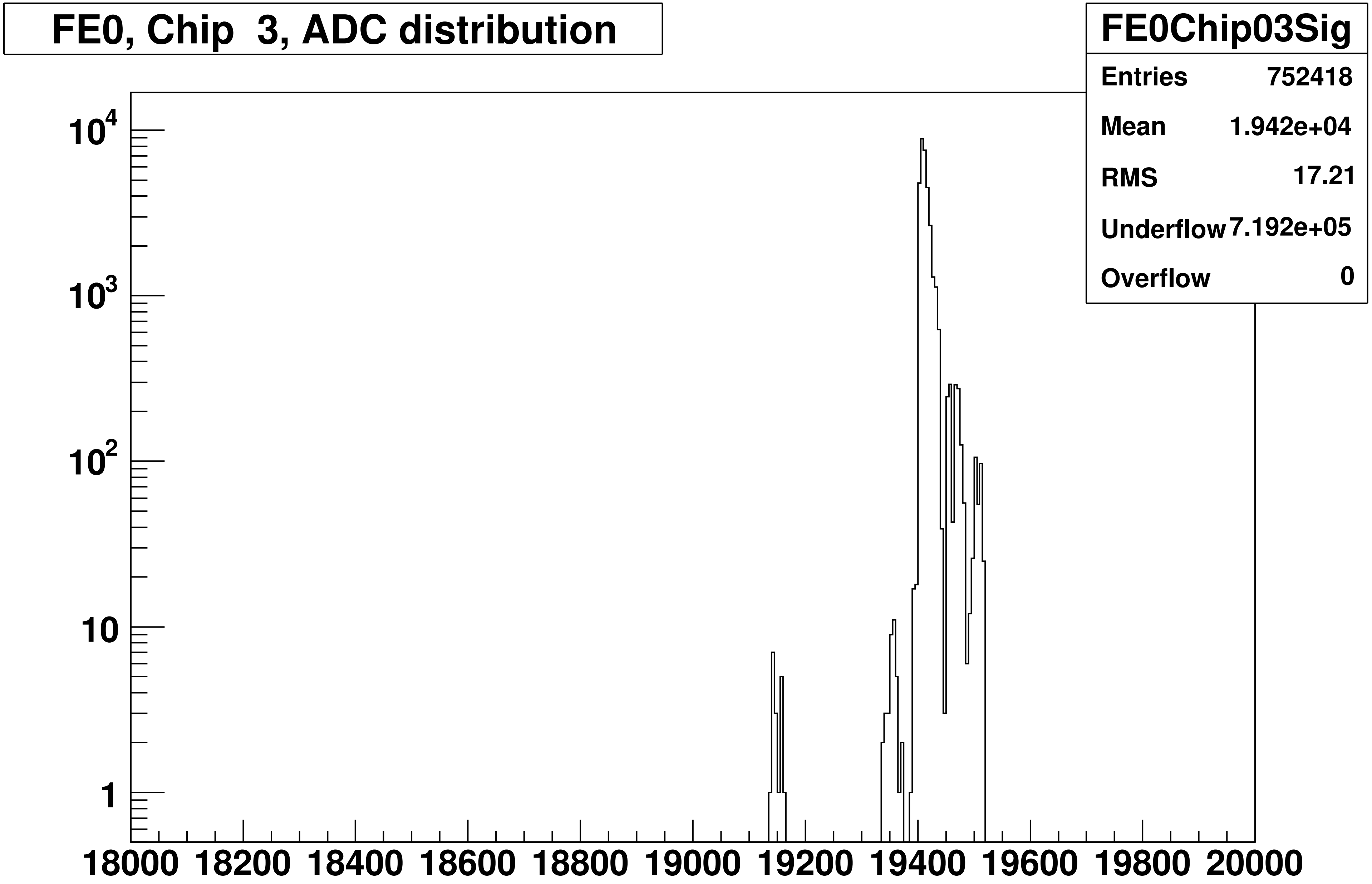

FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 752418
Mean 1.942e+04
RMS 17.21
Underflow 7.192e+05
Overflow 0
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$10^3$
$10^2$
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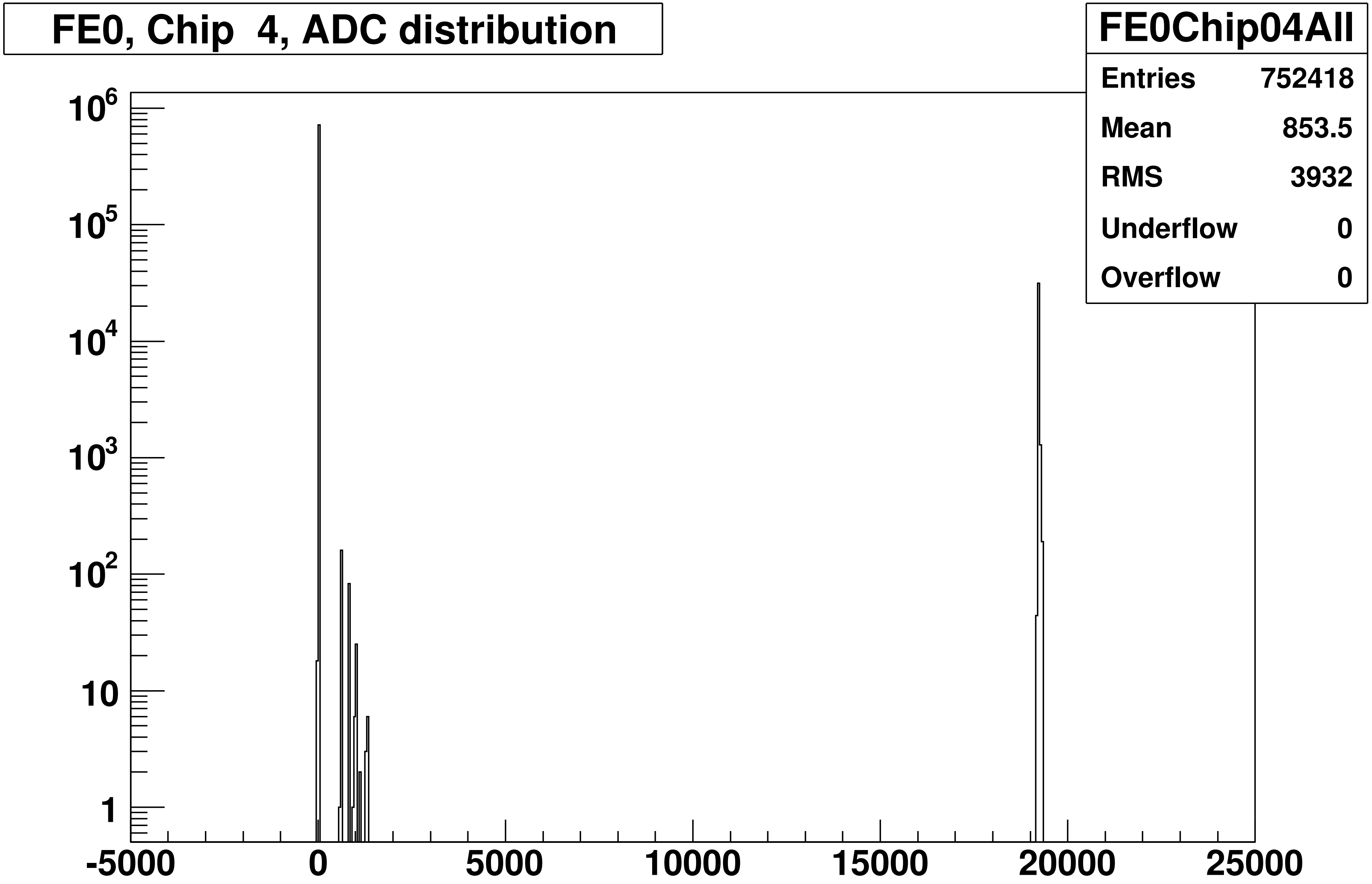
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 752418
Mean 853.5
RMS 3932
Underflow 0
Overflow 0
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$10^3$
$10^2$
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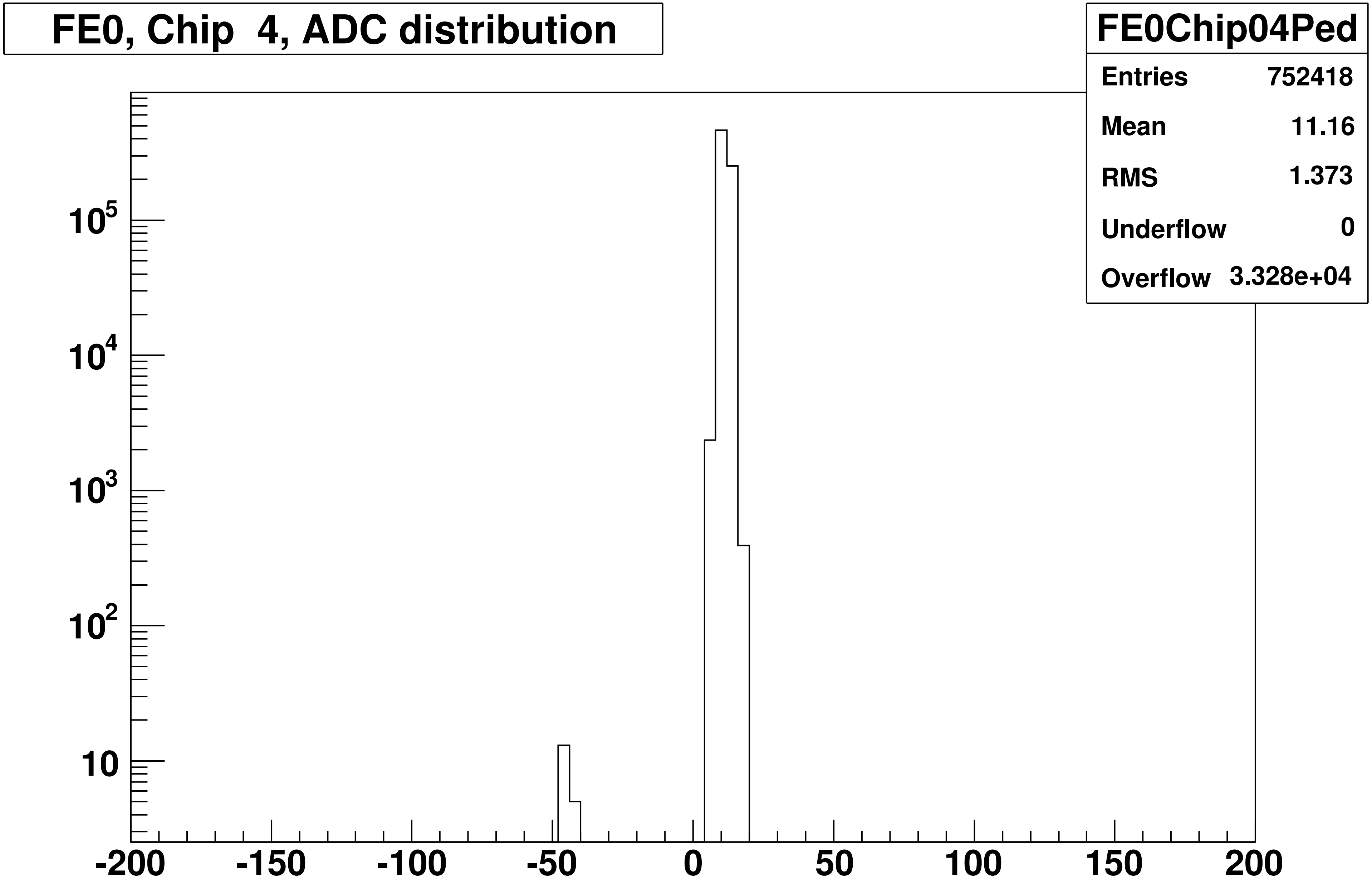
20000
25000



FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 752418
Mean 11.16
RMS 1.373
Underflow 0
Overflow 3.328e+04
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$10^3$
$10^2$
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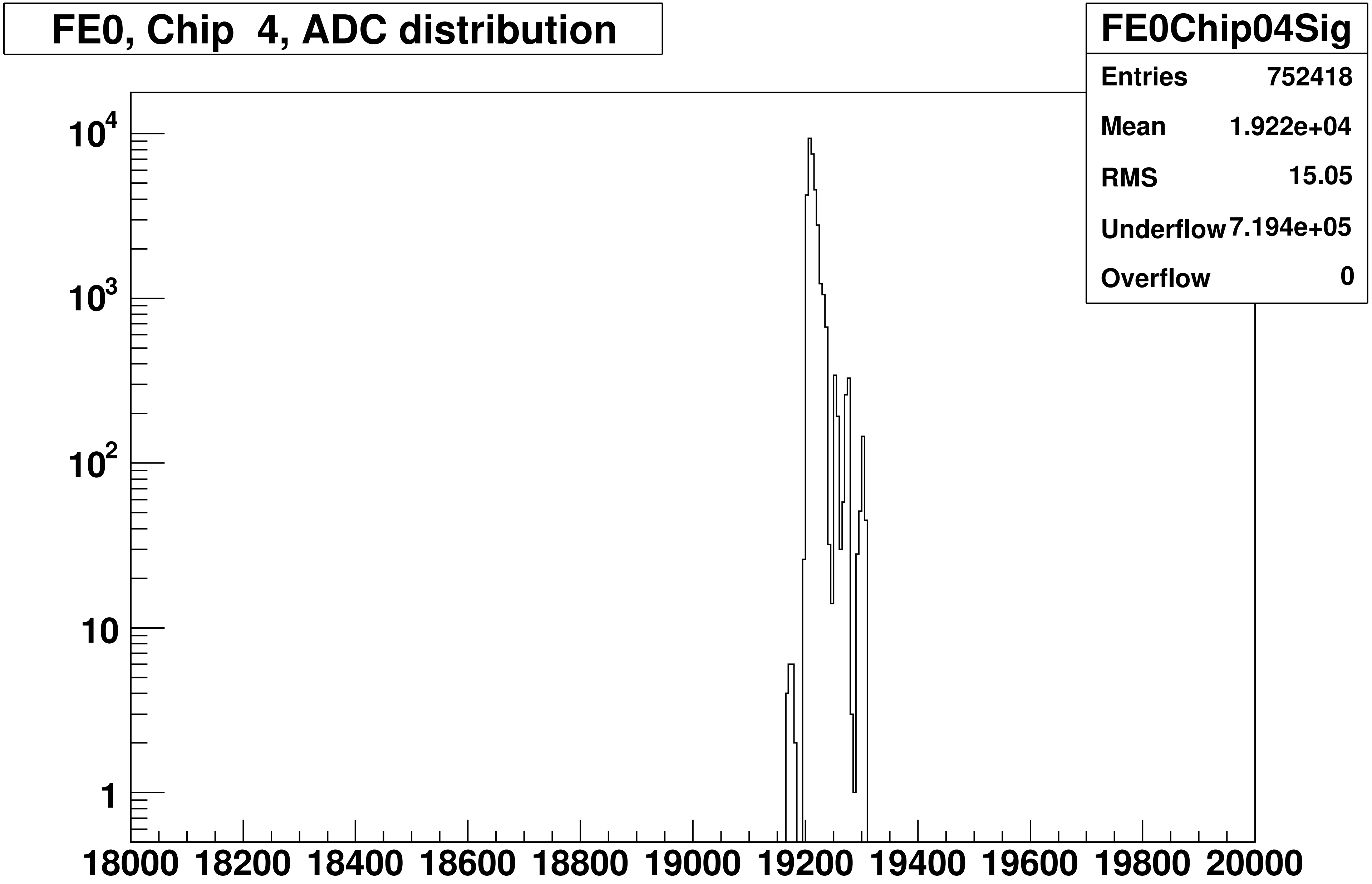
FE0, Chip  4, ADC distribution
FE0Chip04Sig
Entries 752418
Mean 1.922e+04
RMS 15.05
Underflow 7.194e+05
Overflow 0
$10^4$
$10^3$
$10^2$
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18000
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19800
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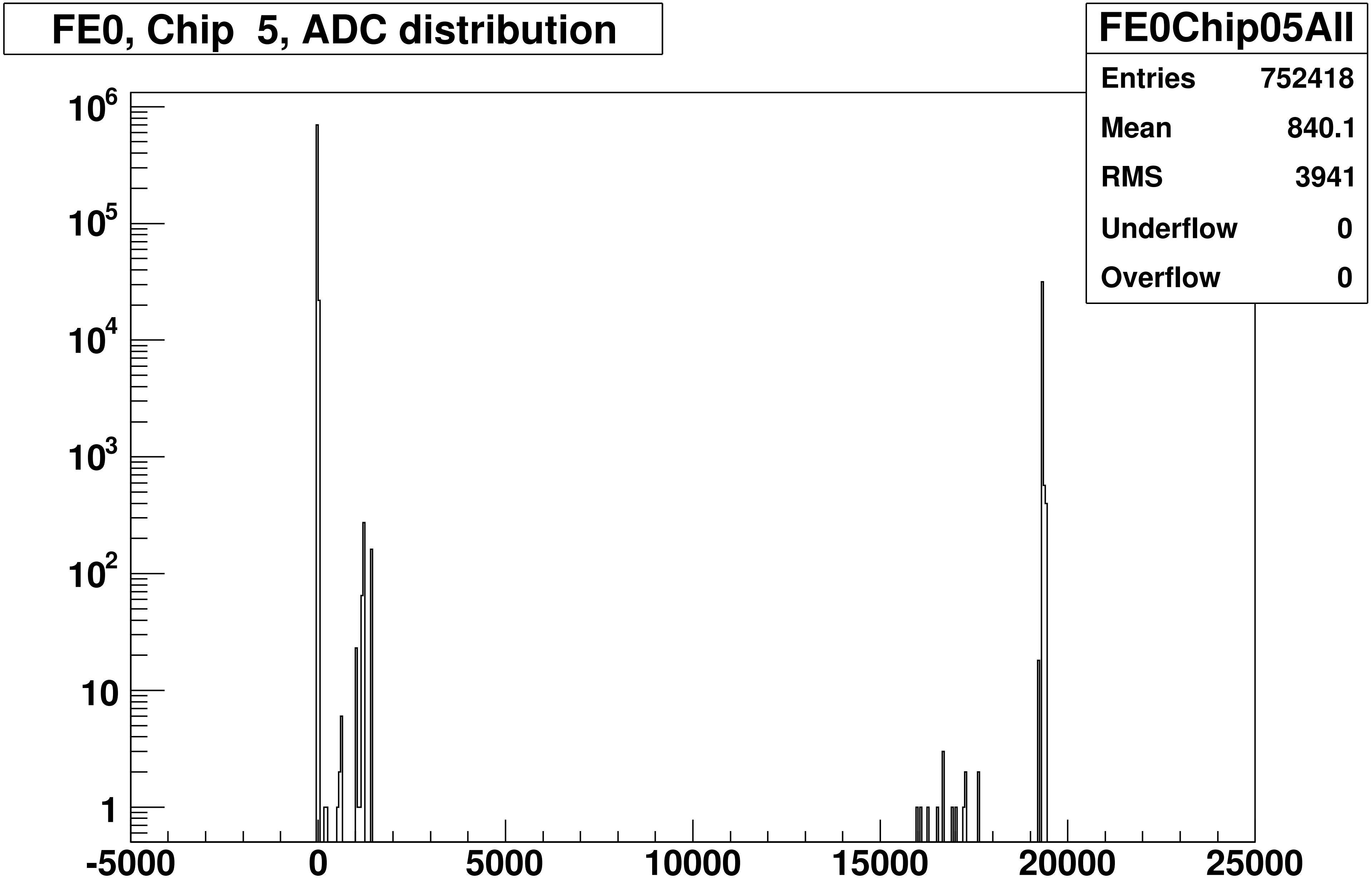

FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 752418
Mean 840.1
RMS 3941
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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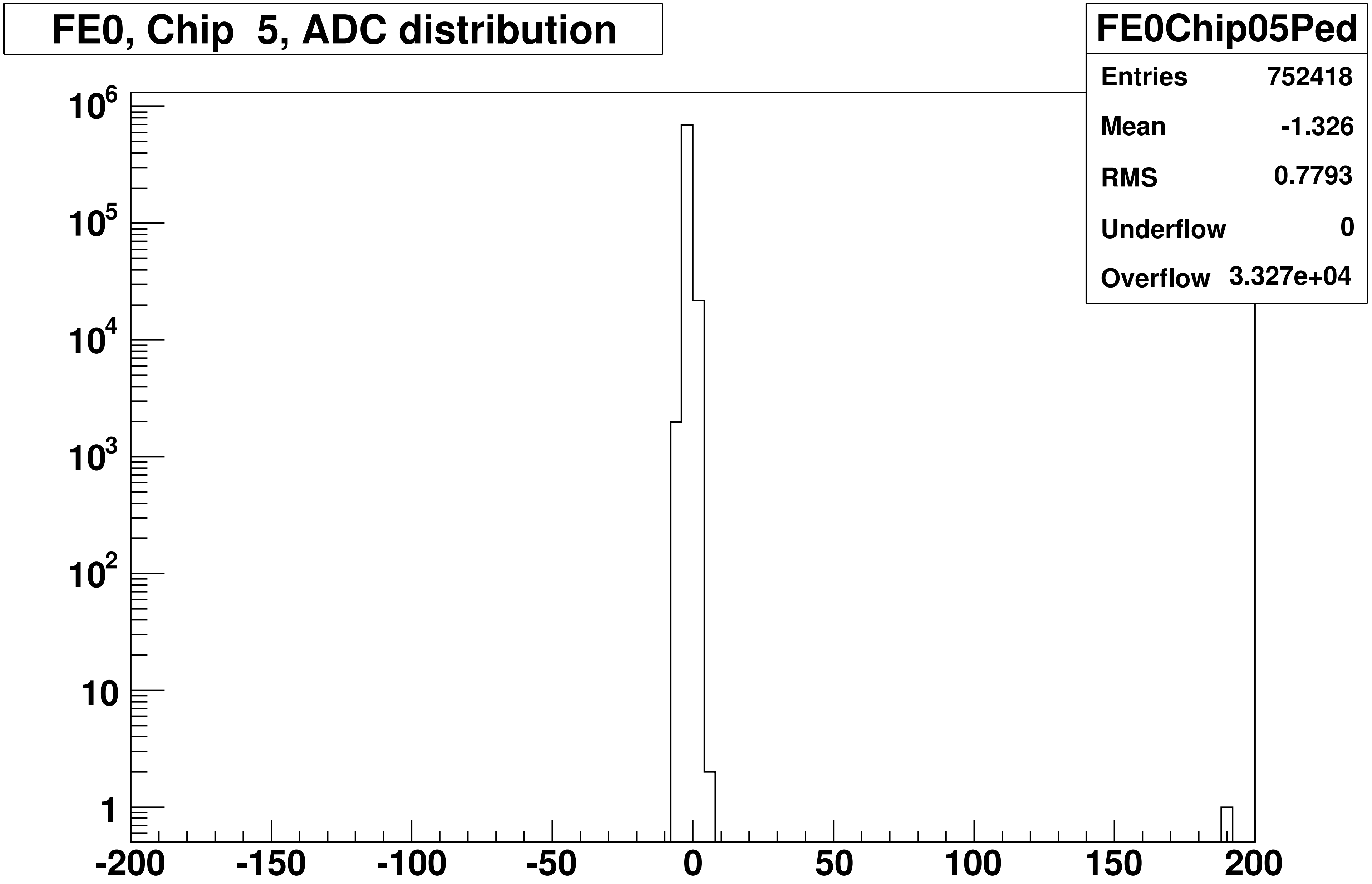
20000
25000



FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 752418
Mean -1.326
RMS 0.7793
Underflow 0
Overflow 3.327e+04
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10^4
10^3
10^2
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100
150
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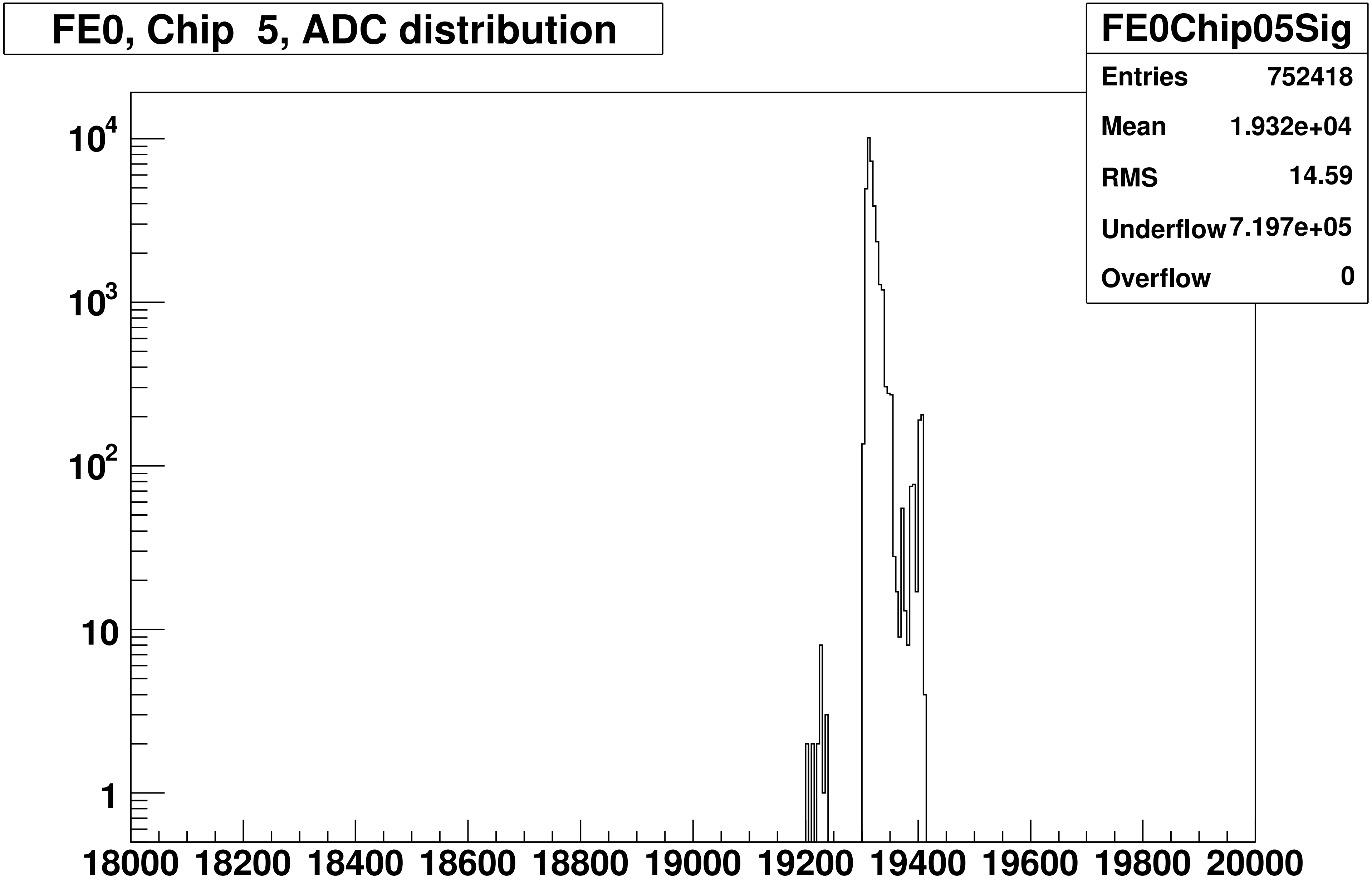
FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 752418
Mean 1.932e+04
RMS 14.59
Underflow 7.197e+05
Overflow 0
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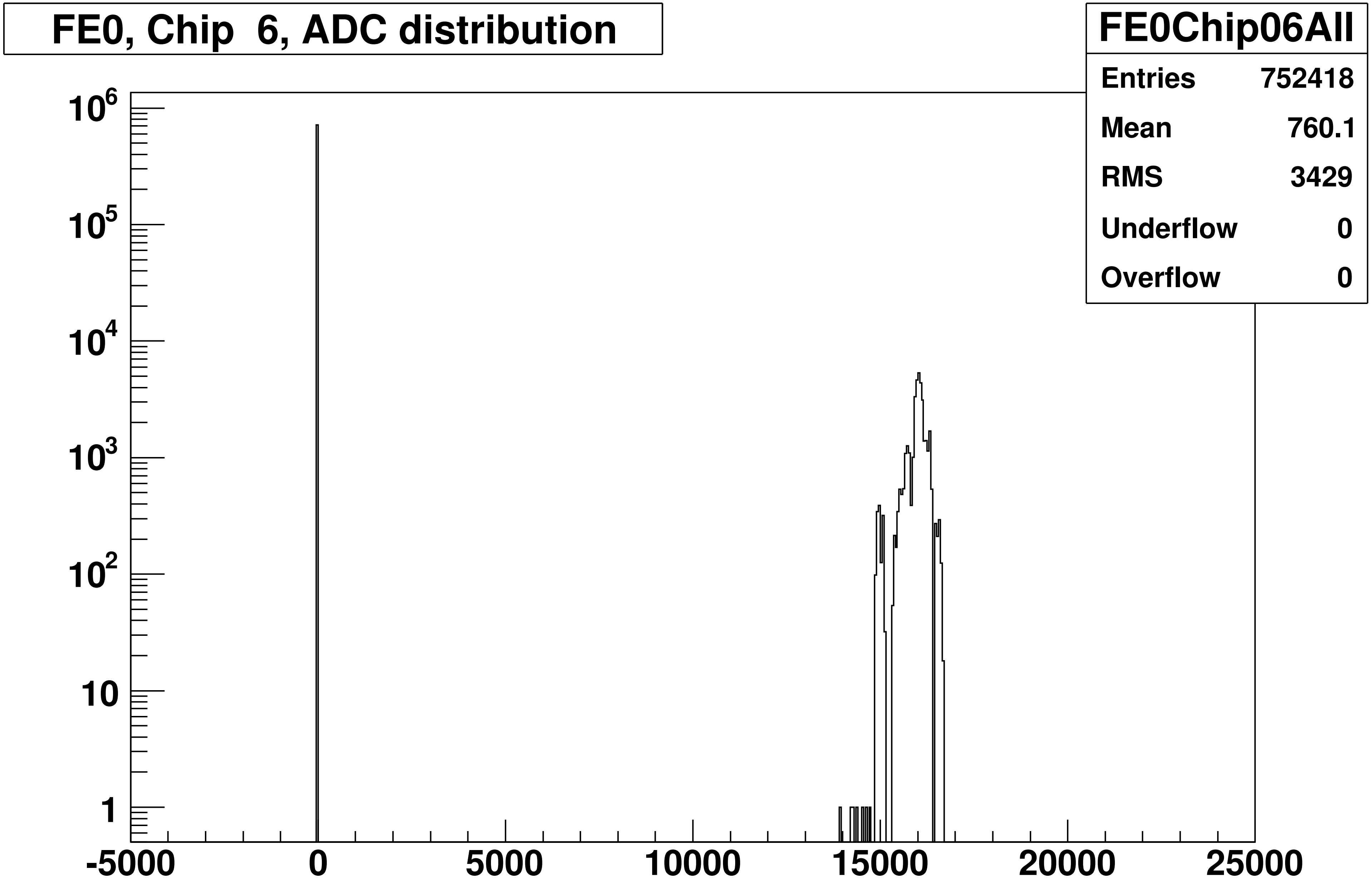
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 752418
Mean 760.1
RMS 3429
Underflow 0
Overflow 0
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$10^3$
$10^2$
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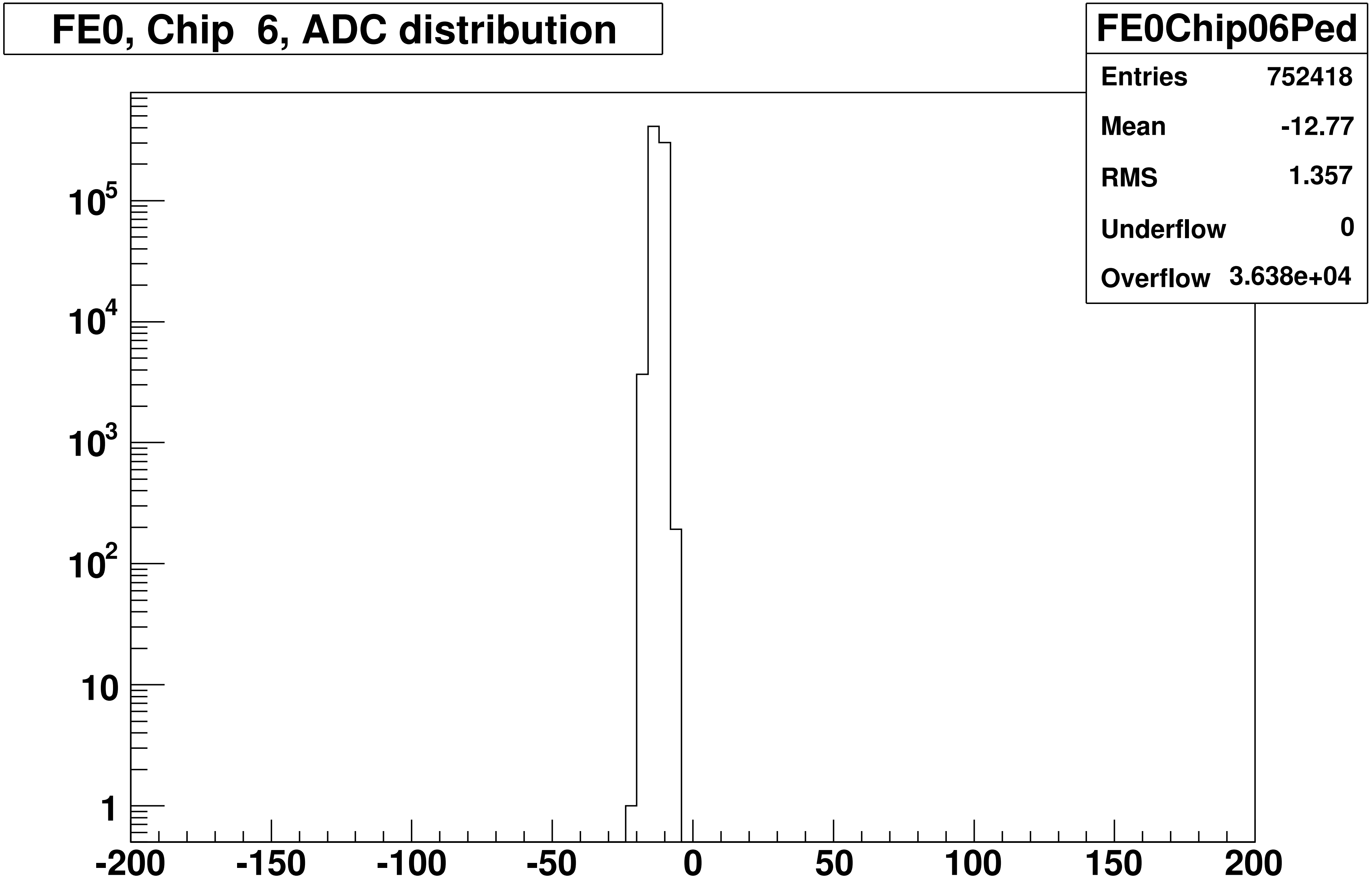
20000
25000


FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 752418
Mean -12.77
RMS 1.357
Underflow 0
Overflow 3.638e+04
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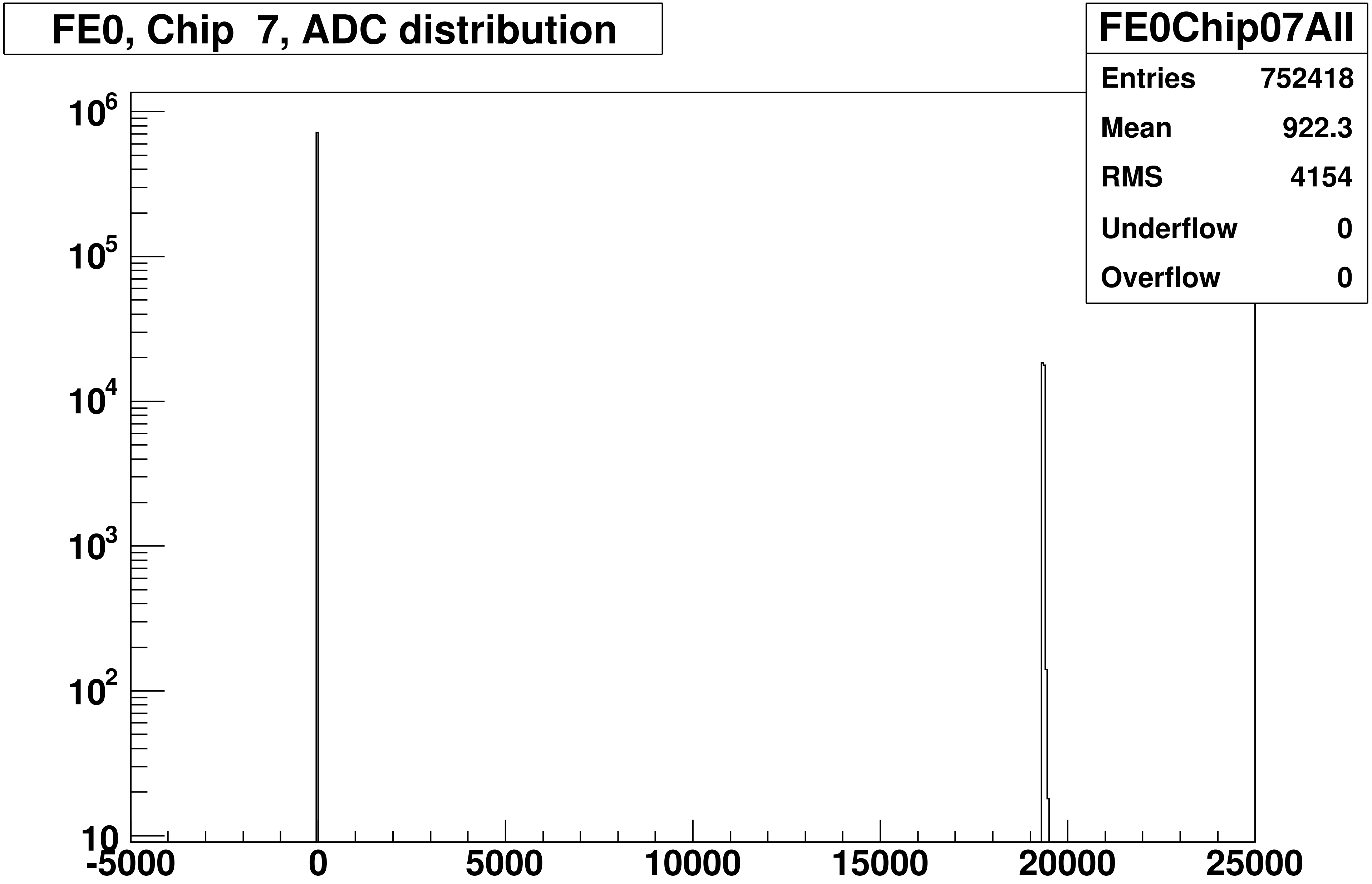
FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 752418
Mean 922.3
RMS 4154
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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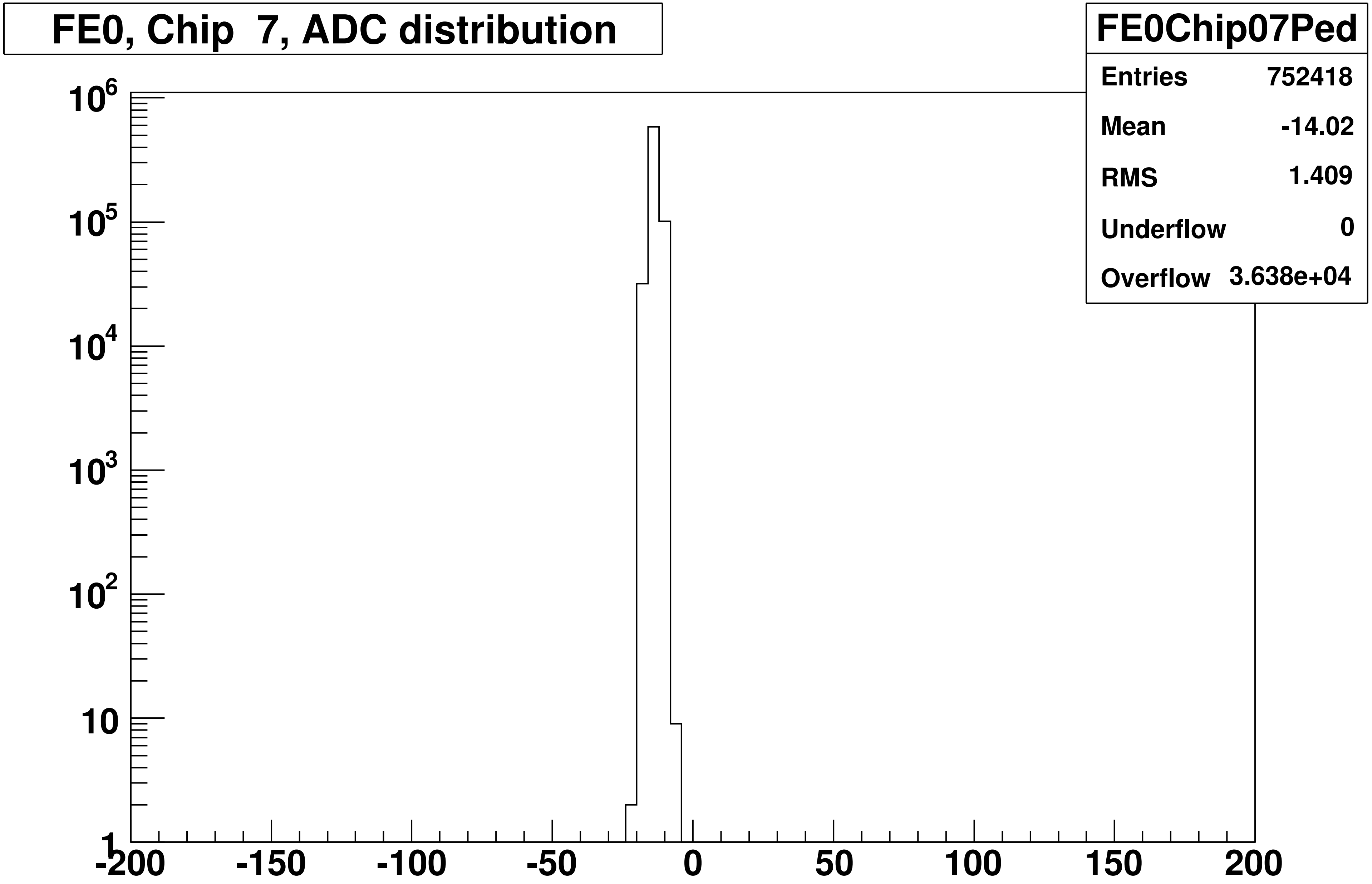
FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 752418
Mean -14.02
RMS 1.409
Underflow 0
Overflow 3.638e+04
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$10^5$
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$10^2$
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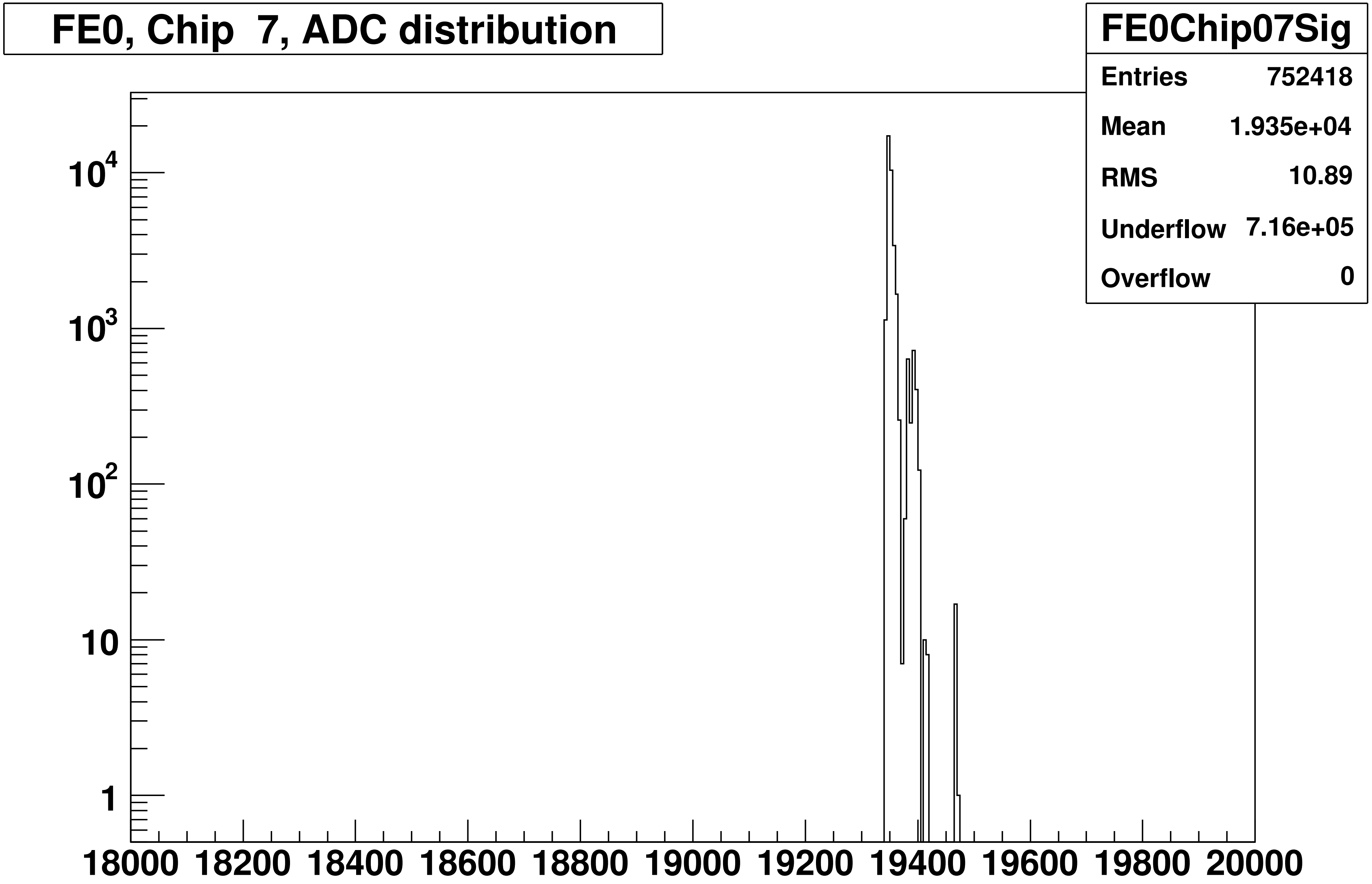
FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 752418
Mean 1.935e+04
RMS 10.89
Underflow 7.16e+05
Overflow 0
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10^3
10^2
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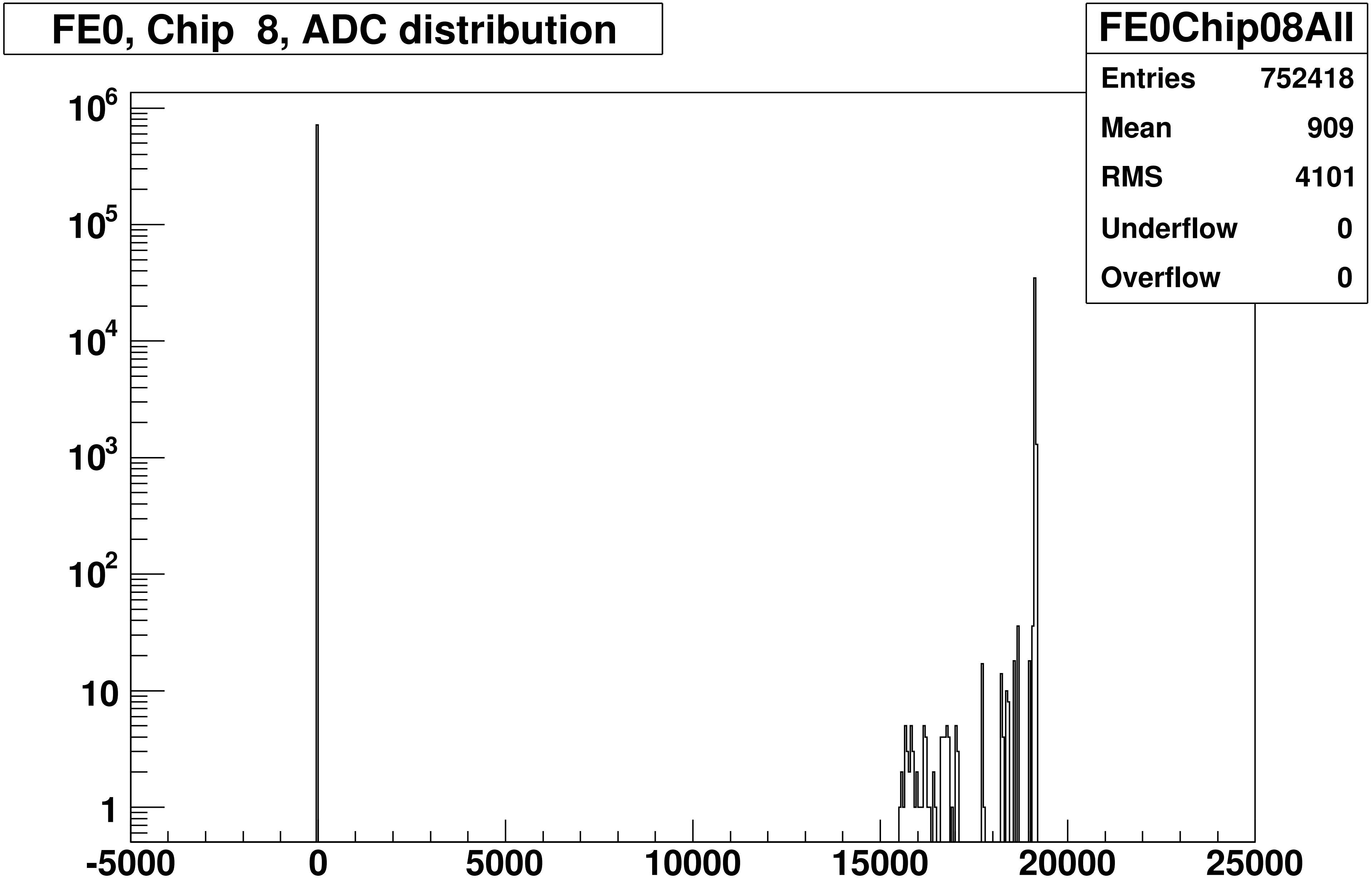
FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 752418
Mean 909
RMS 4101
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
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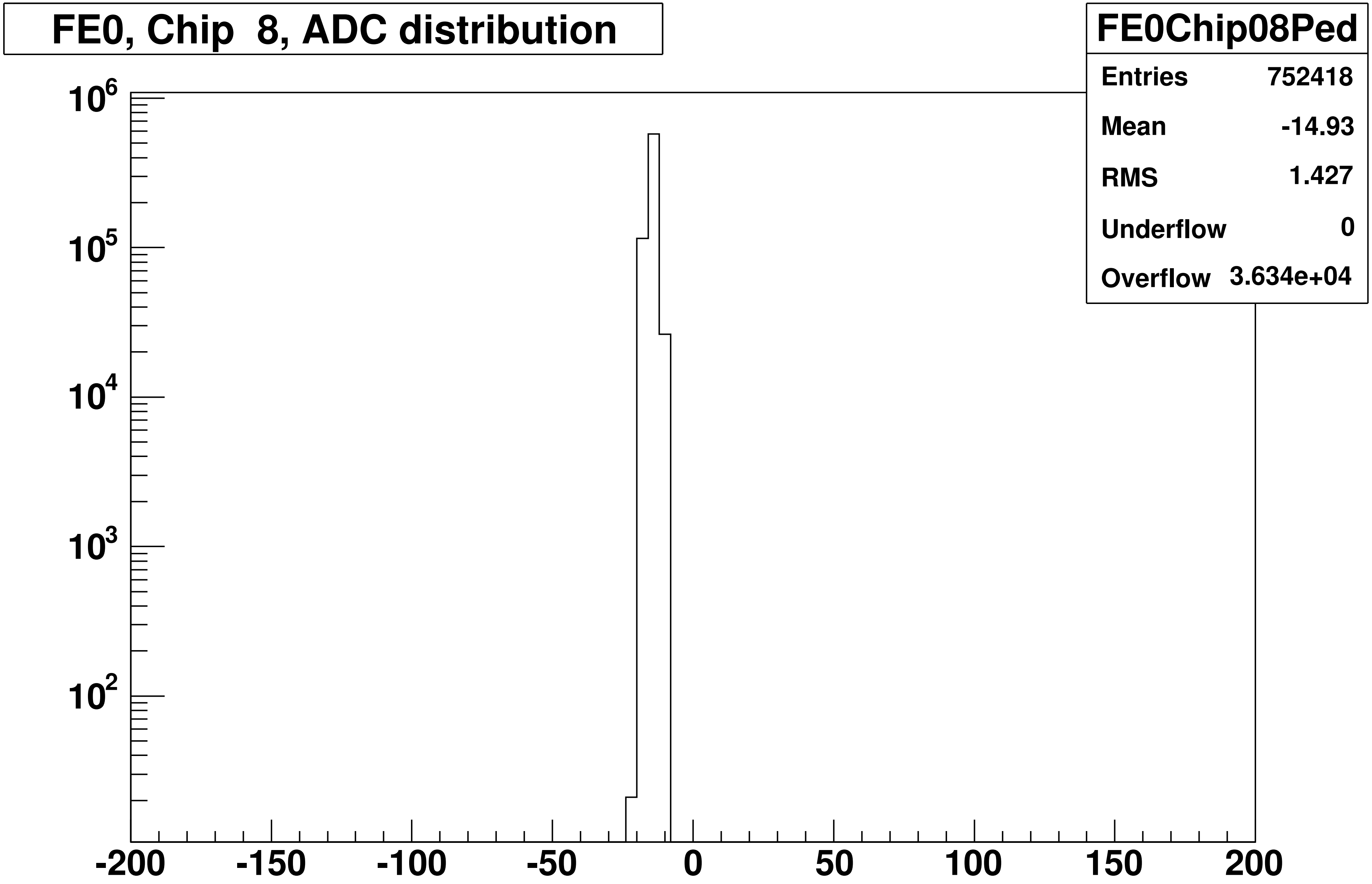
FE0, Chip  8, ADC distribution
FE0Chip08Ped
Entries 752418
Mean -14.93
RMS 1.427
Underflow 0
Overflow 3.634e+04
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$10^3$
$10^2$
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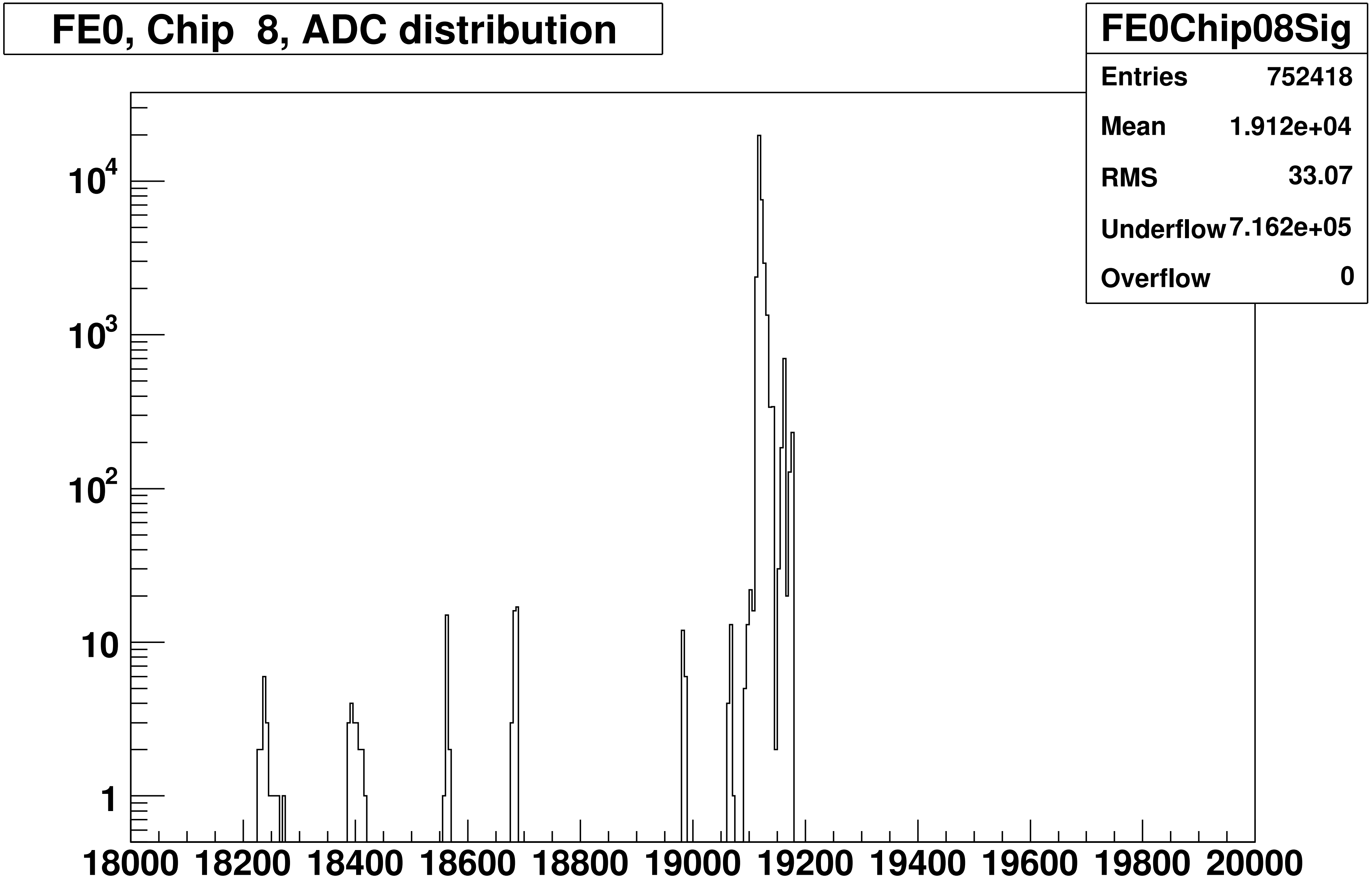
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 752418
Mean 1.912e+04
RMS 33.07
Underflow 7.162e+05
Overflow 0
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$10^3$
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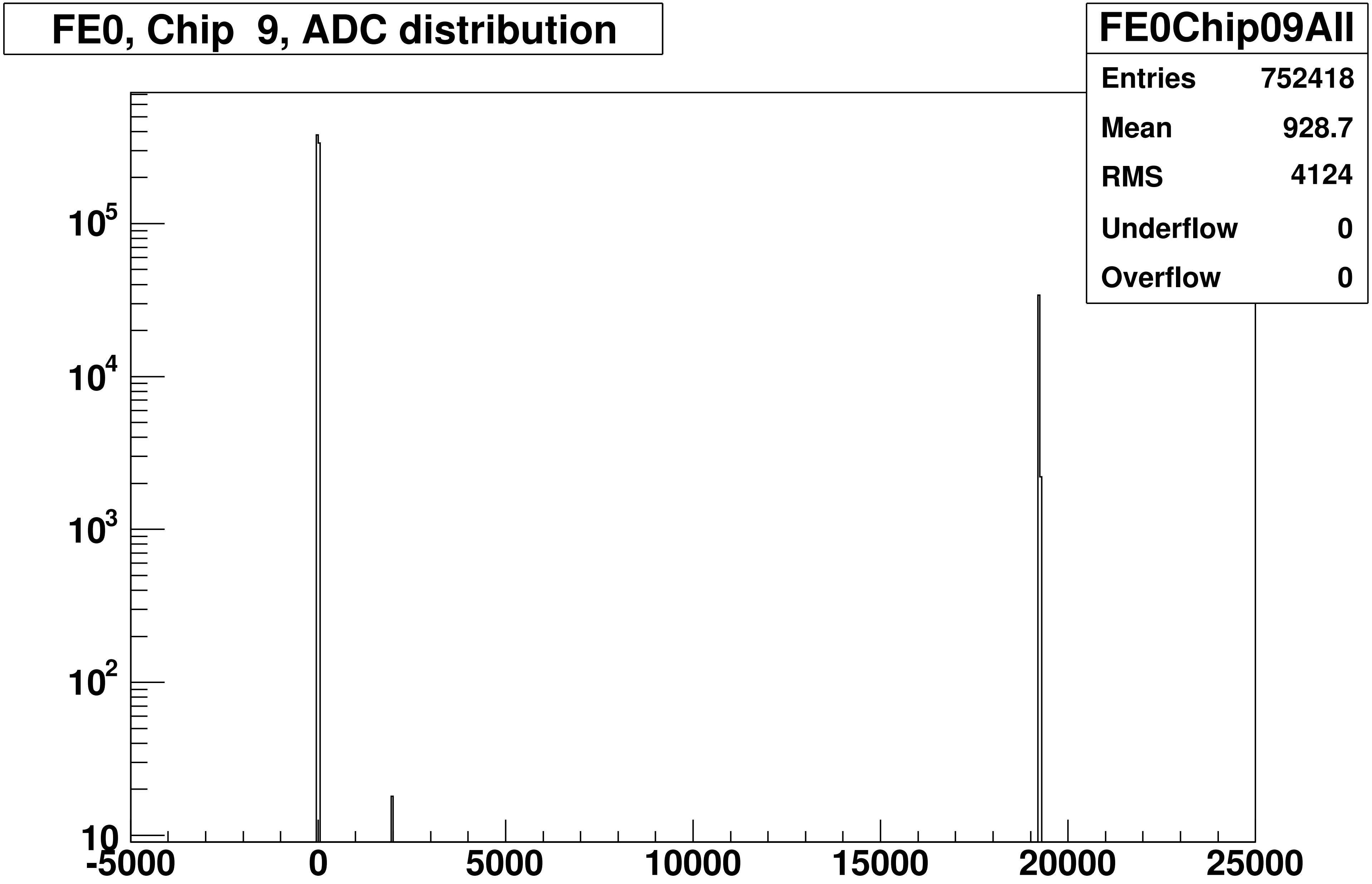
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 752418
Mean 928.7
RMS 4124
Underflow 0
Overflow 0
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$10^3$
$10^4$
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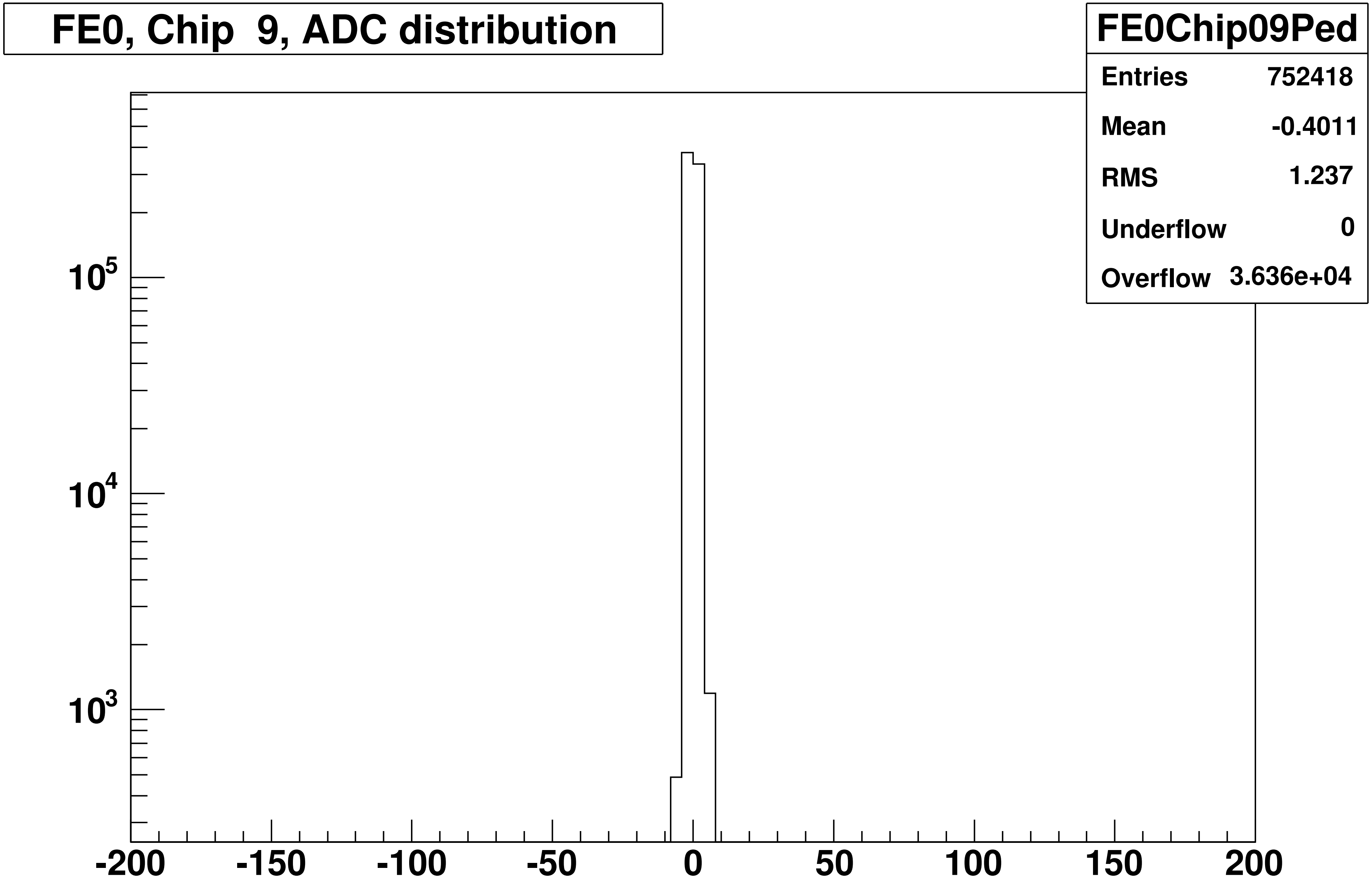
20000
25000


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 752418
Mean -0.4011
RMS 1.237
Underflow 0
Overflow 3.636e+04
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10^4
10^3
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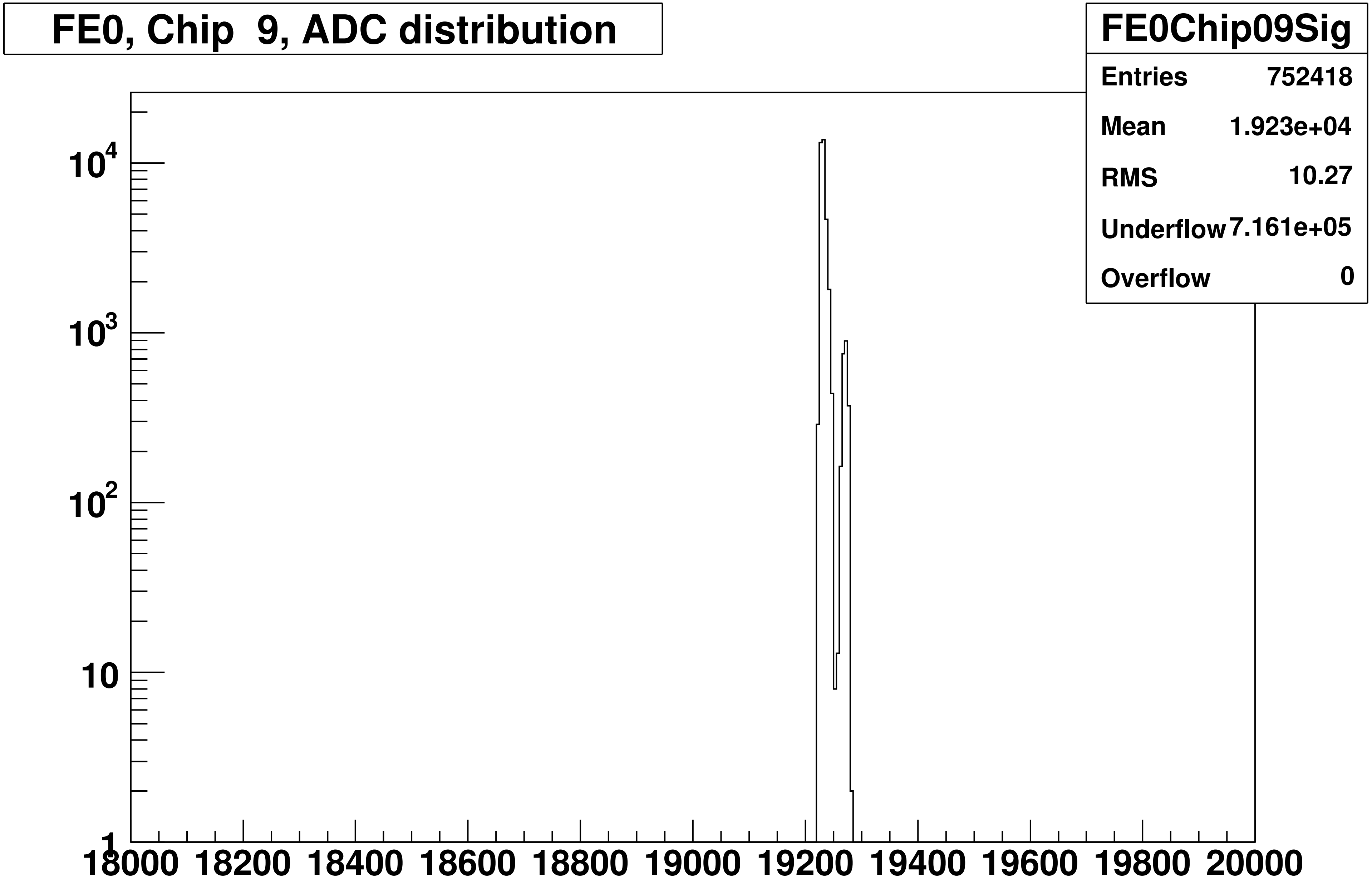
150
200



FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 752418
Mean 1.923e+04
RMS 10.27
Underflow 7.161e+05
Overflow 0
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$10^3$
$10^2$
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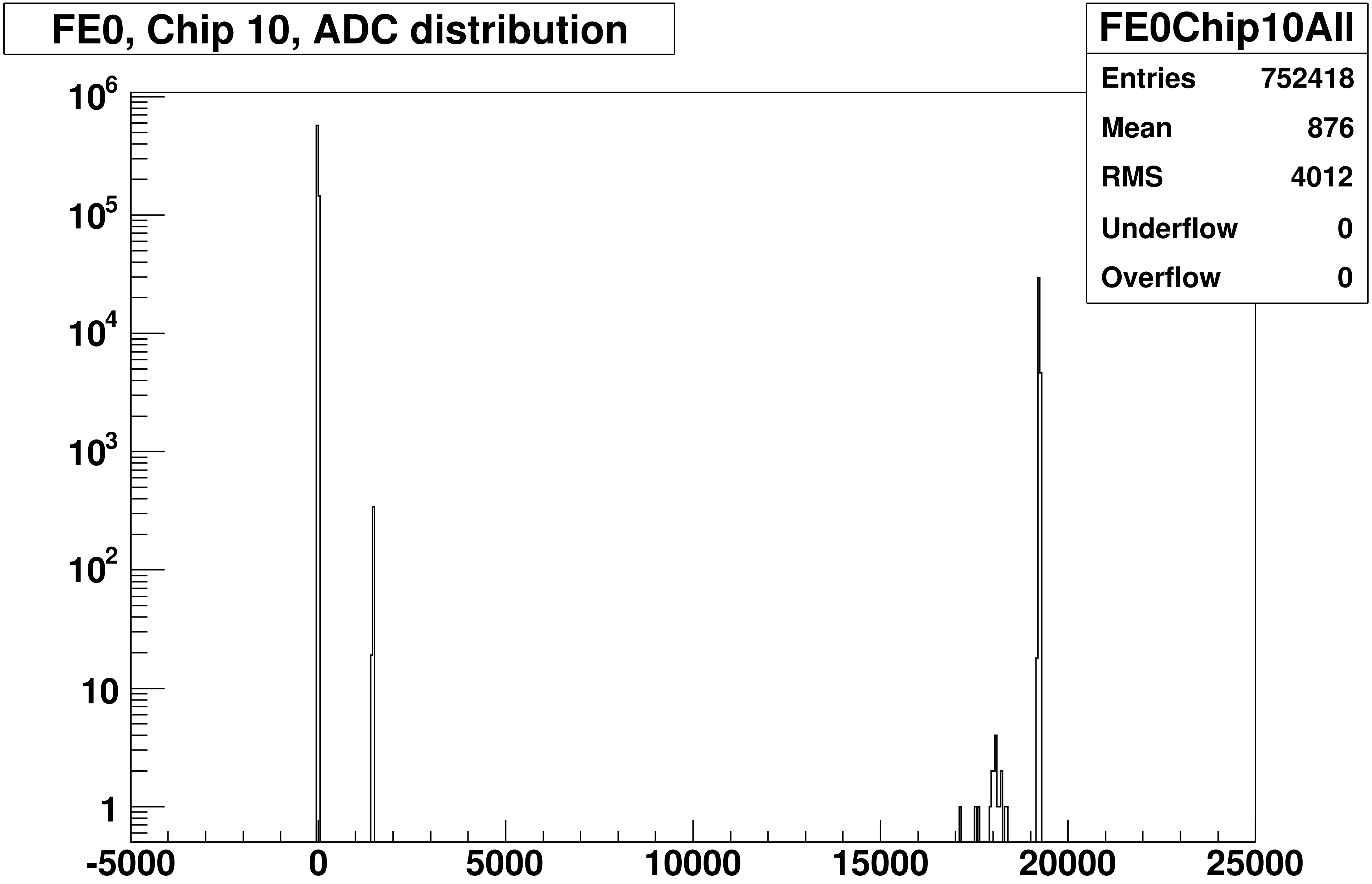
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 752418
Mean 876
RMS 4012
Underflow 0
Overflow 0
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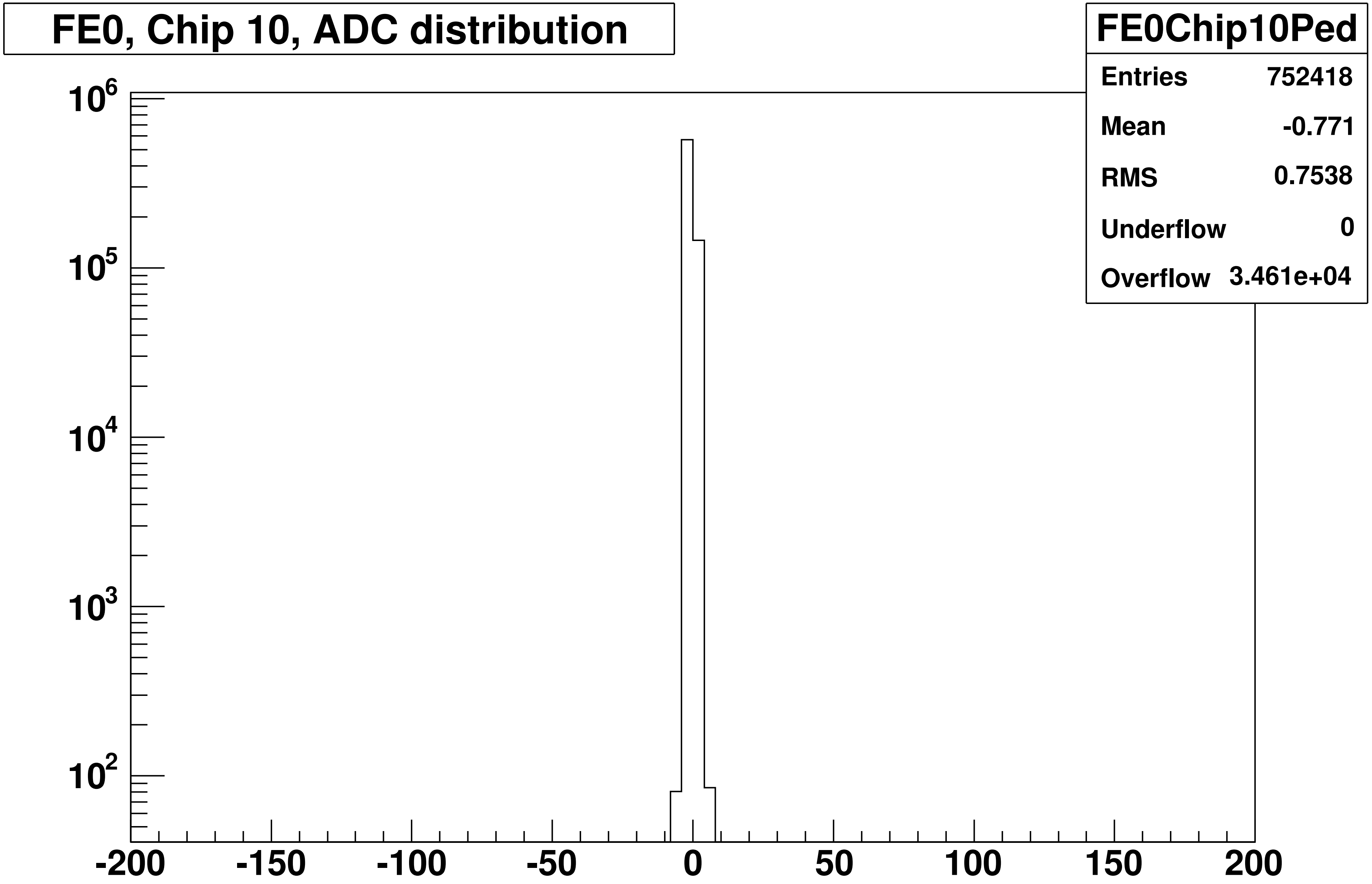
25000


FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 752418
Mean -0.771
RMS 0.7538
Underflow 0
Overflow 3.461e+04
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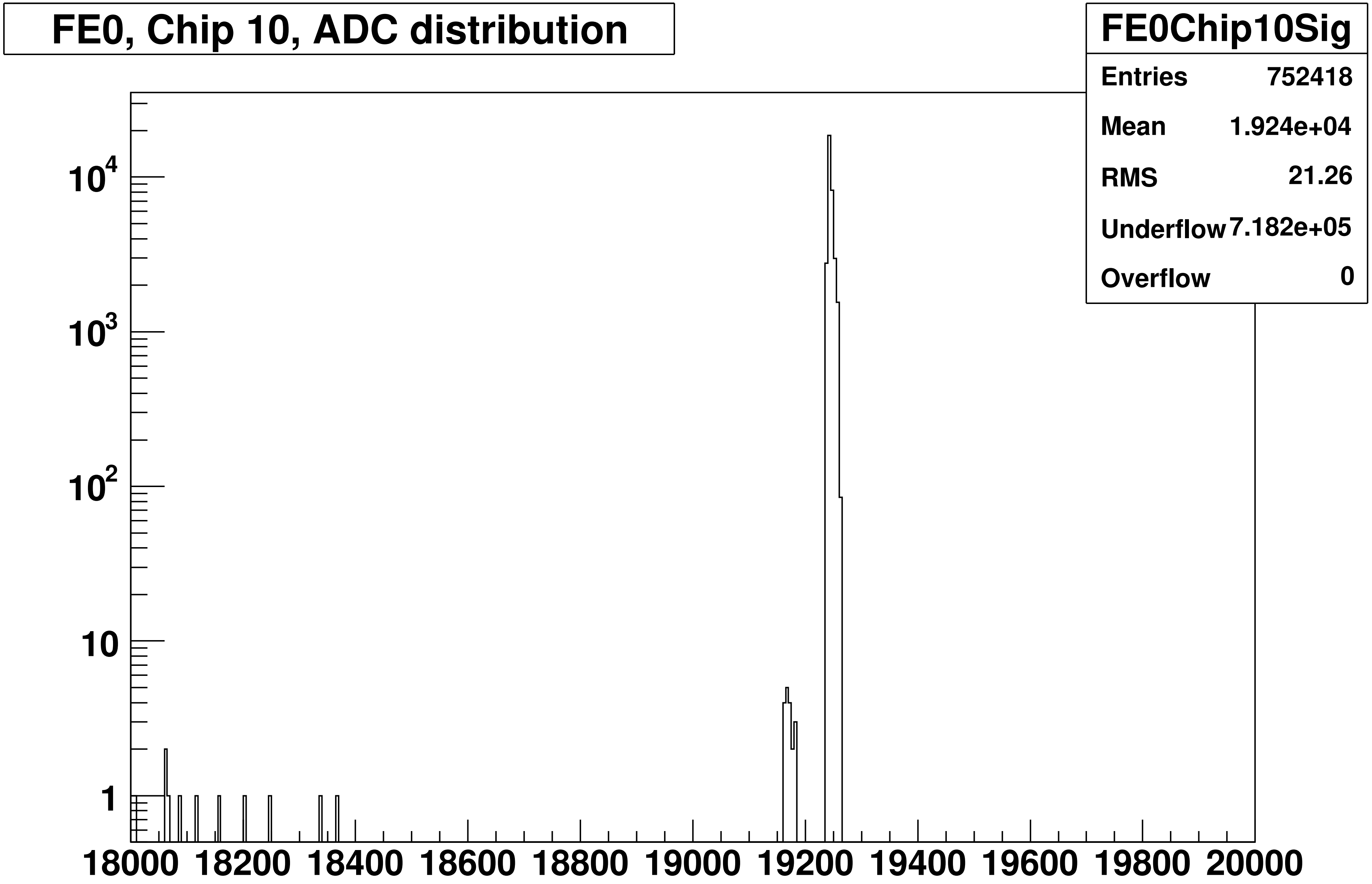
150
200


FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 752418
Mean 1.924e+04
RMS 21.26
Underflow 7.182e+05
Overflow 0
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10^3
10^2
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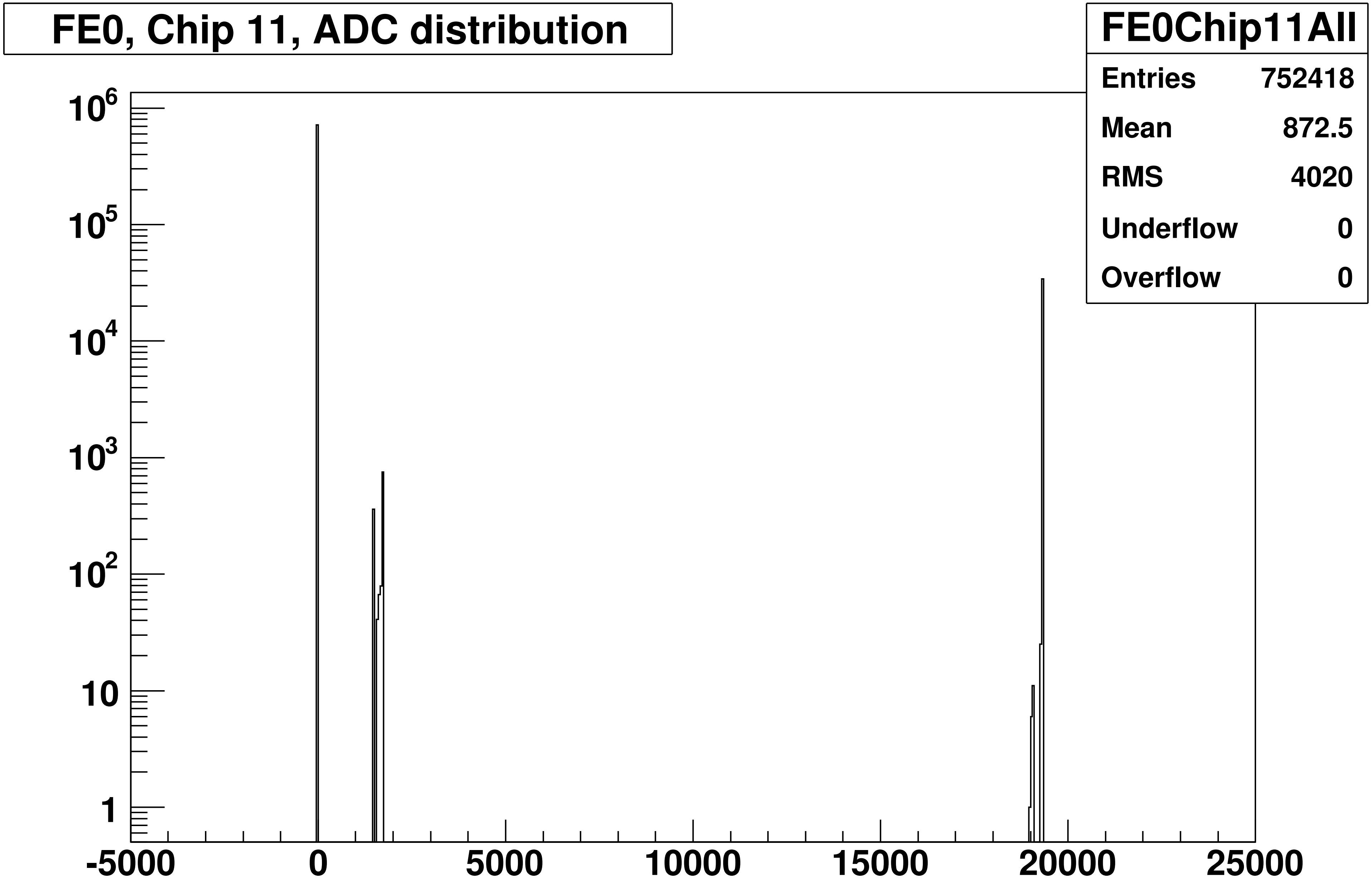
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 752418
Mean 872.5
RMS 4020
Underflow 0
Overflow 0
$10^6$
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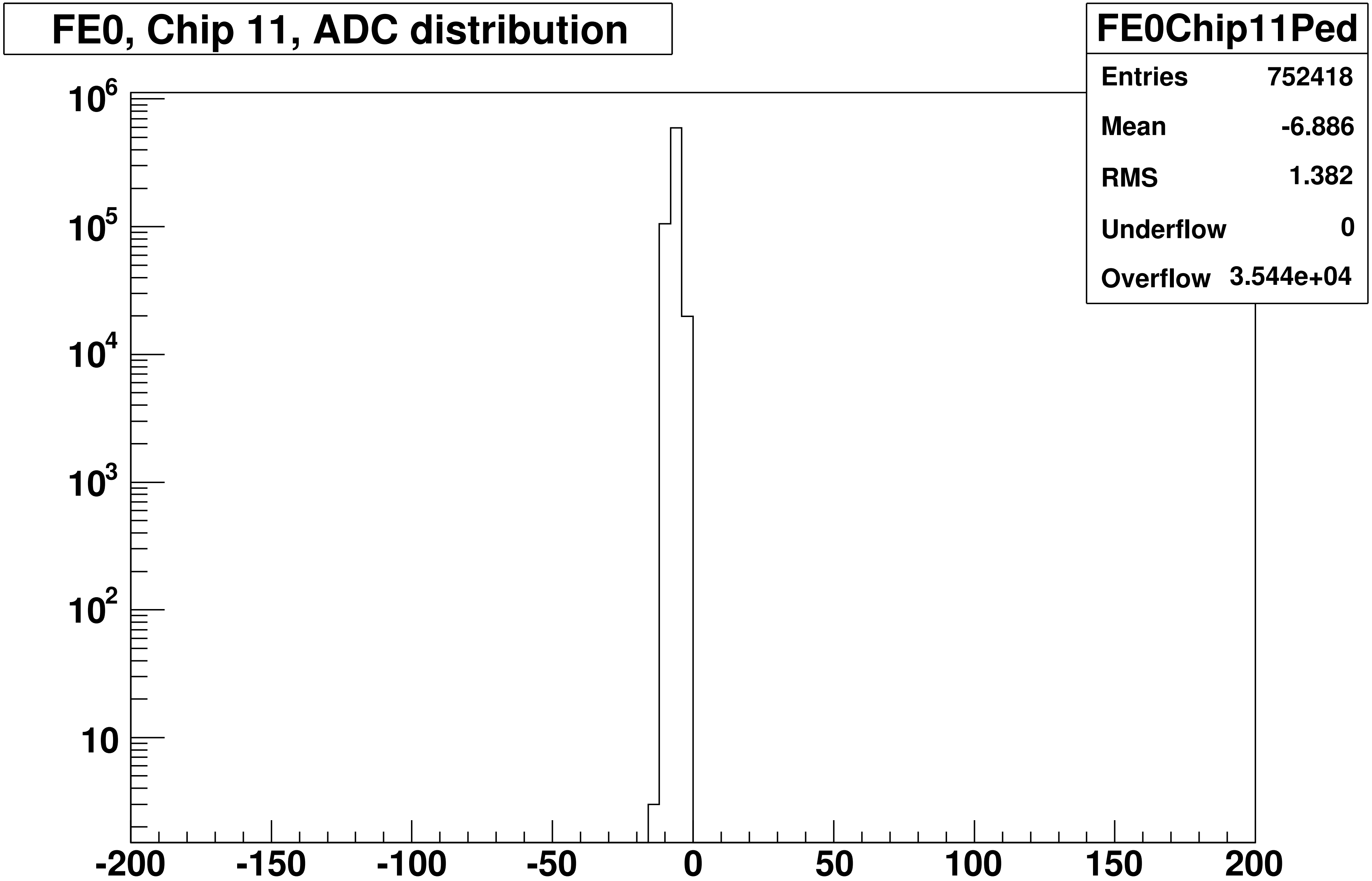
25000


FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 752418
Mean -6.886
RMS 1.382
Underflow 0
Overflow 3.544e+04
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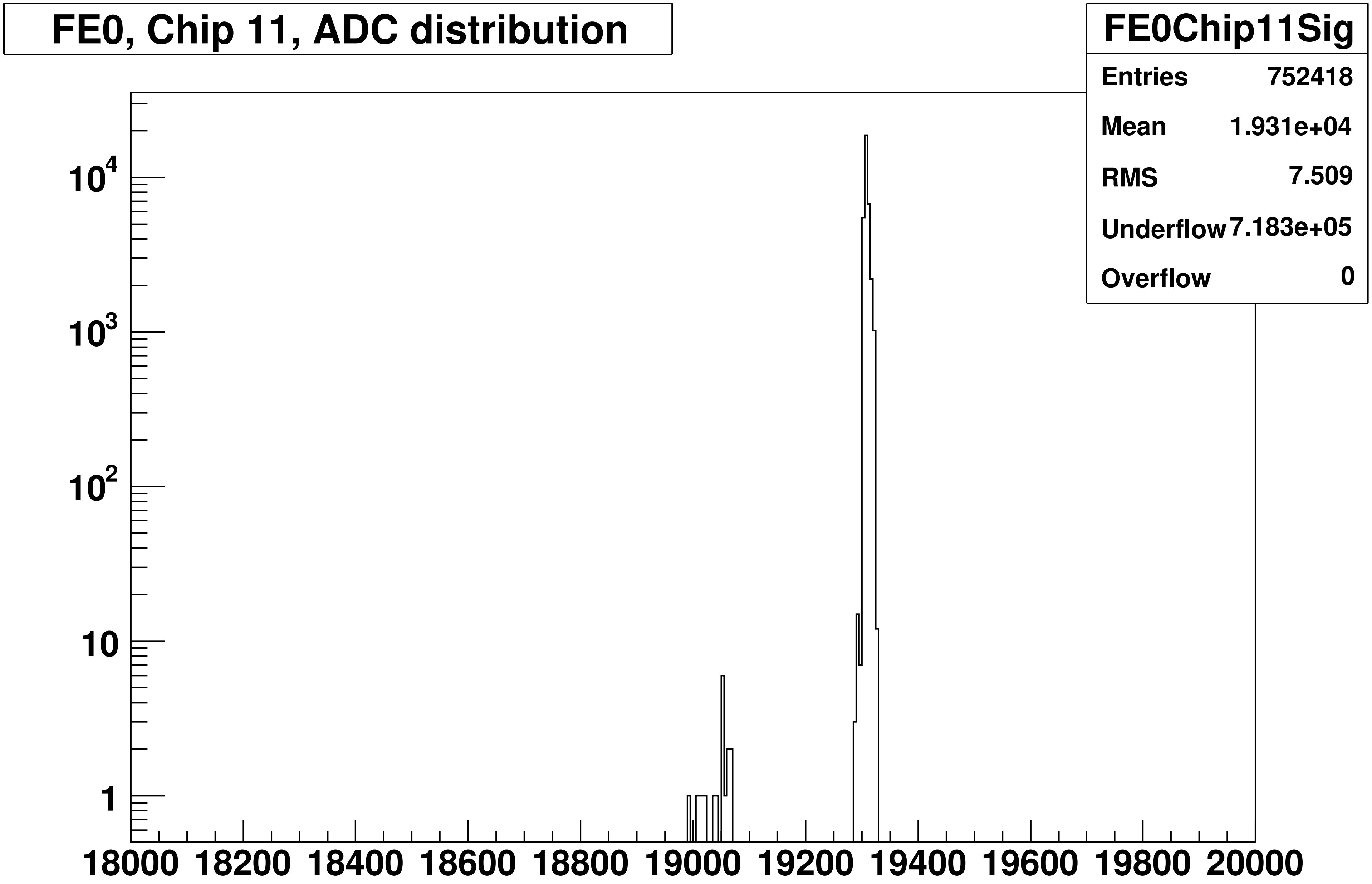

FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 752418
Mean 1.931e+04
RMS 7.509
Underflow 7.183e+05
Overflow 0
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$10^3$
$10^2$
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18000
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19800
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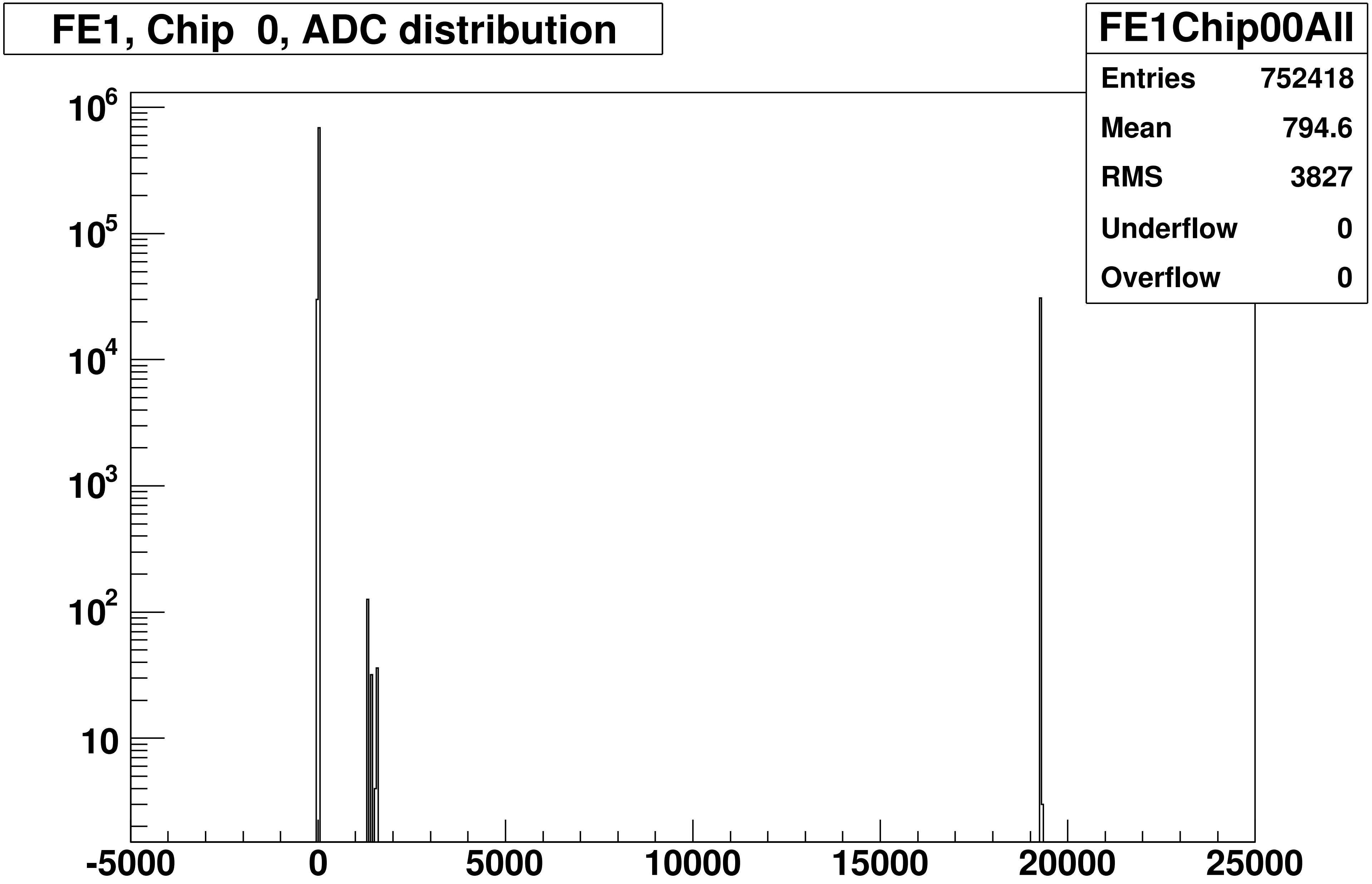

FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 752418
Mean 794.6
RMS 3827
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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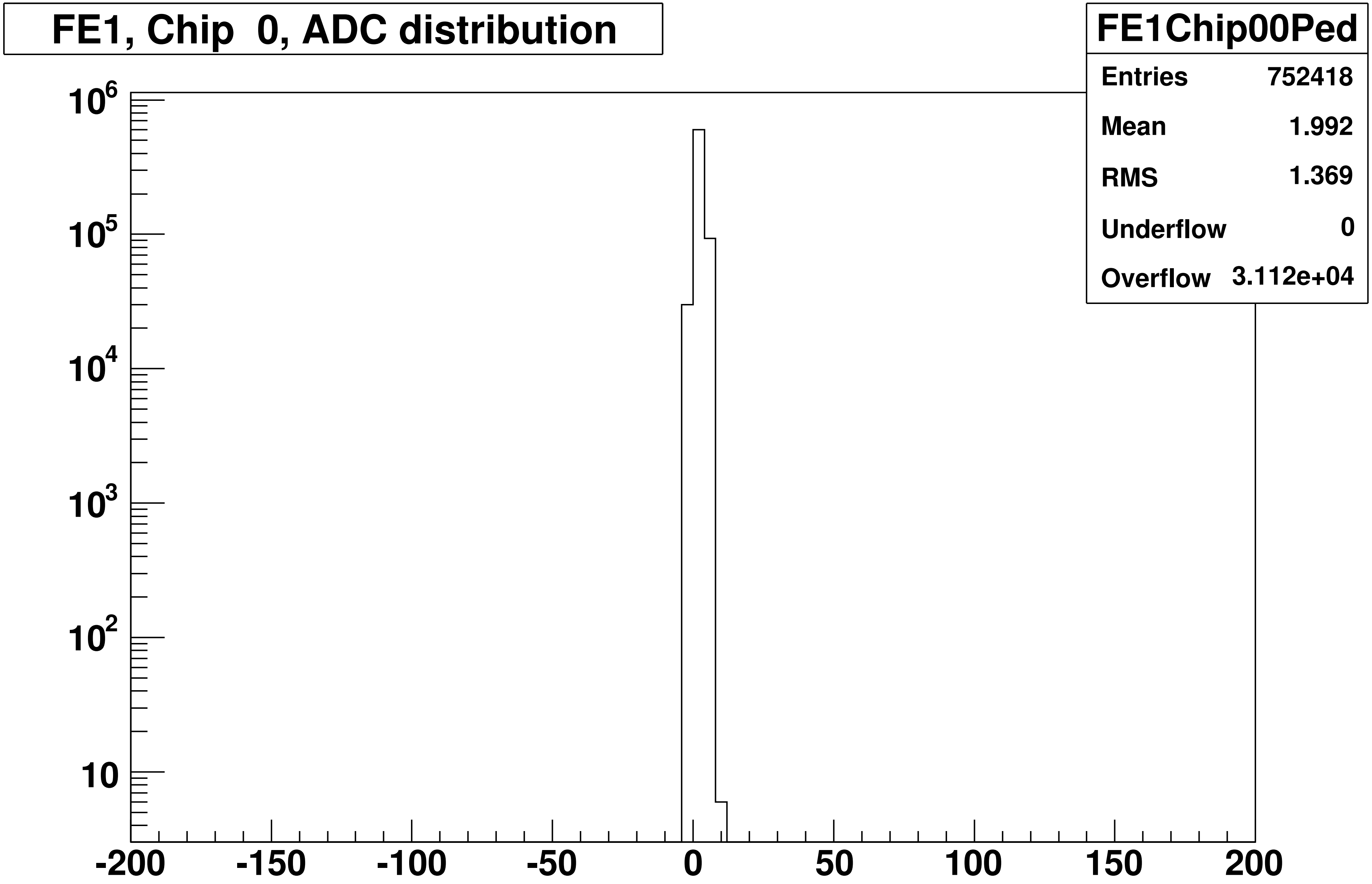
FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 752418
Mean 1.992
RMS 1.369
Underflow 0
Overflow 3.112e+04
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10^4
10^3
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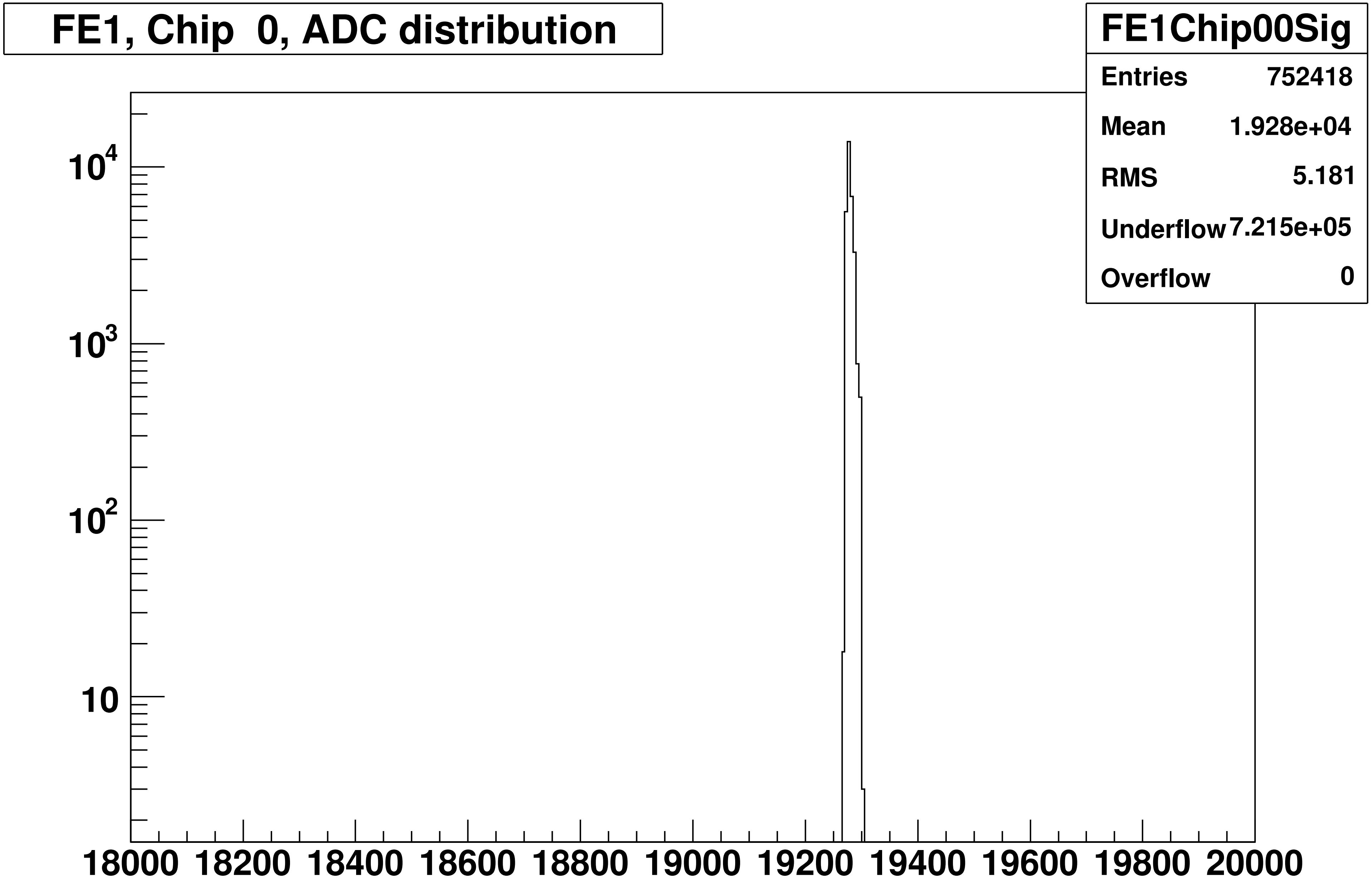
150
200


FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 752418
Mean 1.928e+04
RMS 5.181
Underflow 7.215e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
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19400
19600
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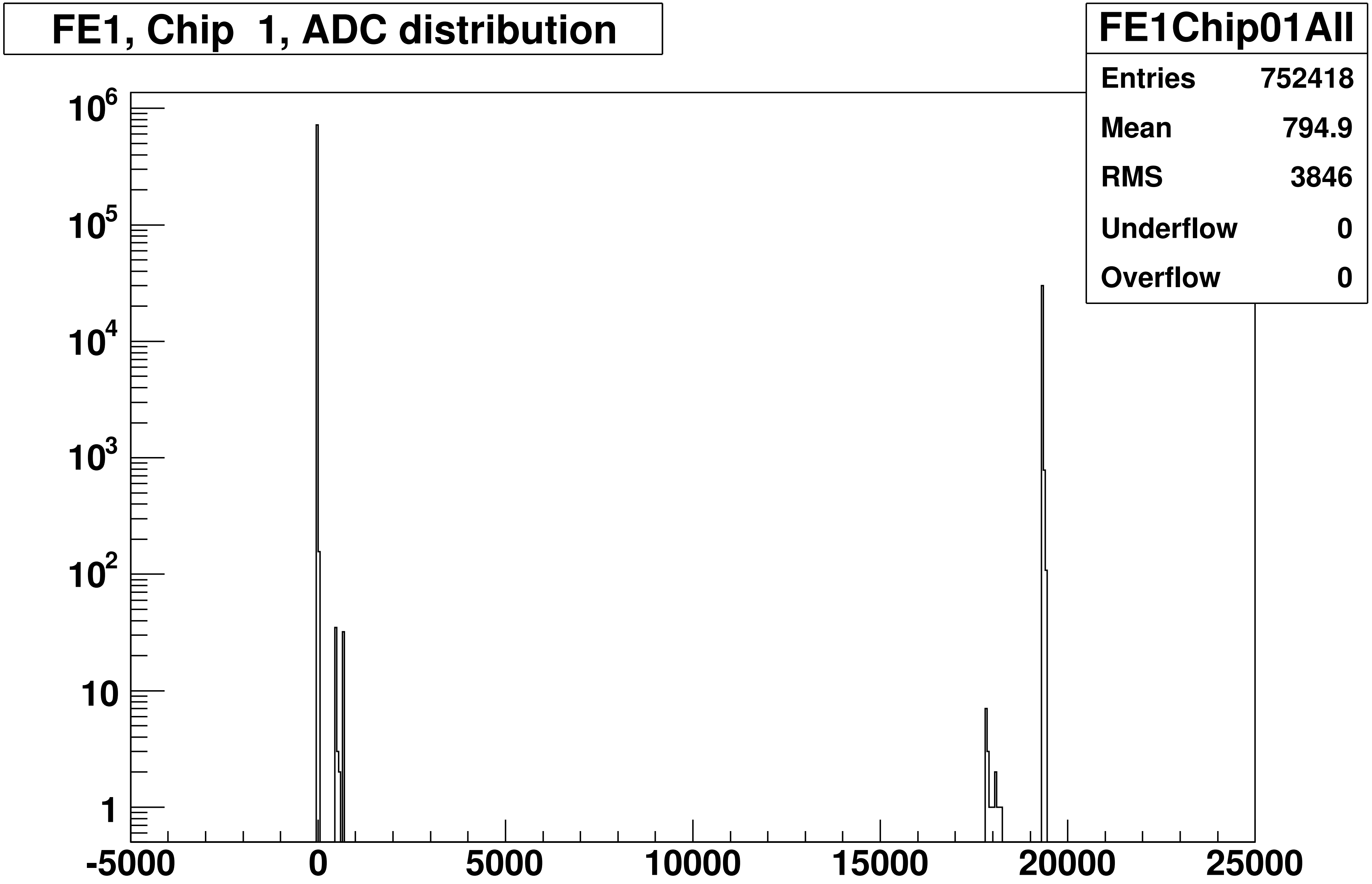
20000



FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 752418
Mean 794.9
RMS 3846
Underflow 0
Overflow 0
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$10^3$
$10^2$
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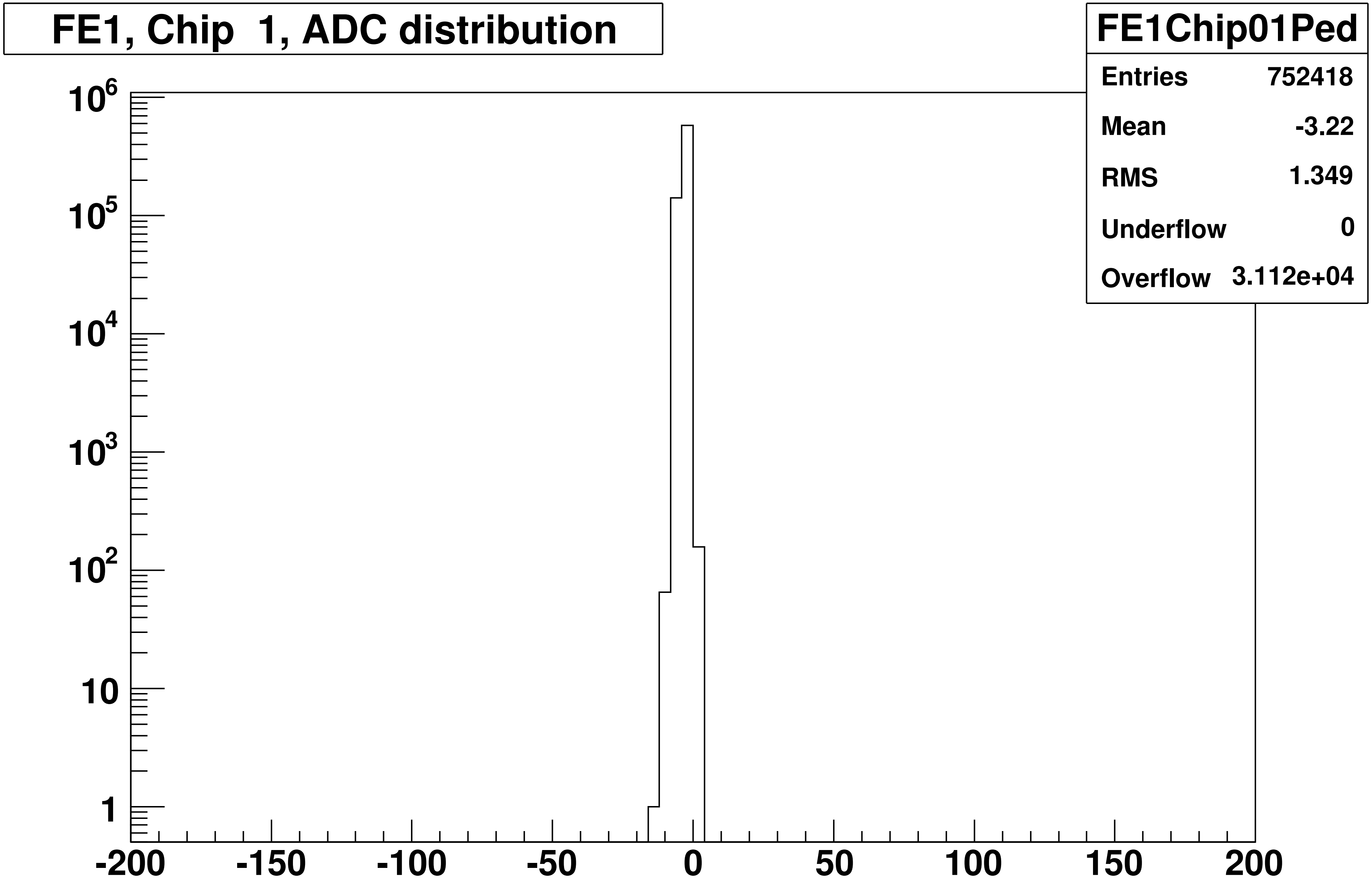
FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 752418
Mean -3.22
RMS 1.349
Underflow 0
Overflow 3.112e+04
1
10
$10^2$
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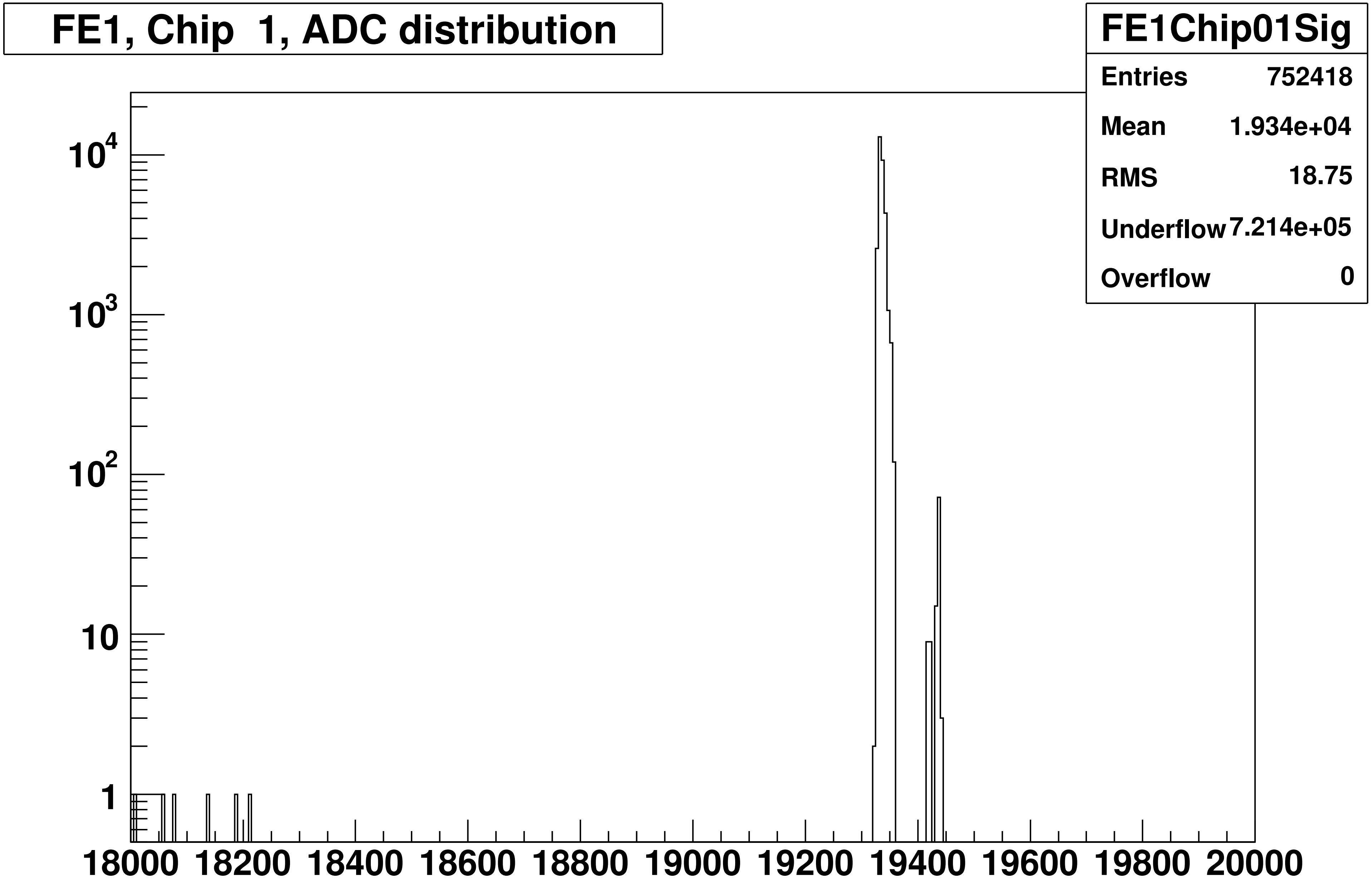
FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 752418
Mean 1.934e+04
RMS 18.75
Underflow 7.214e+05
Overflow 0
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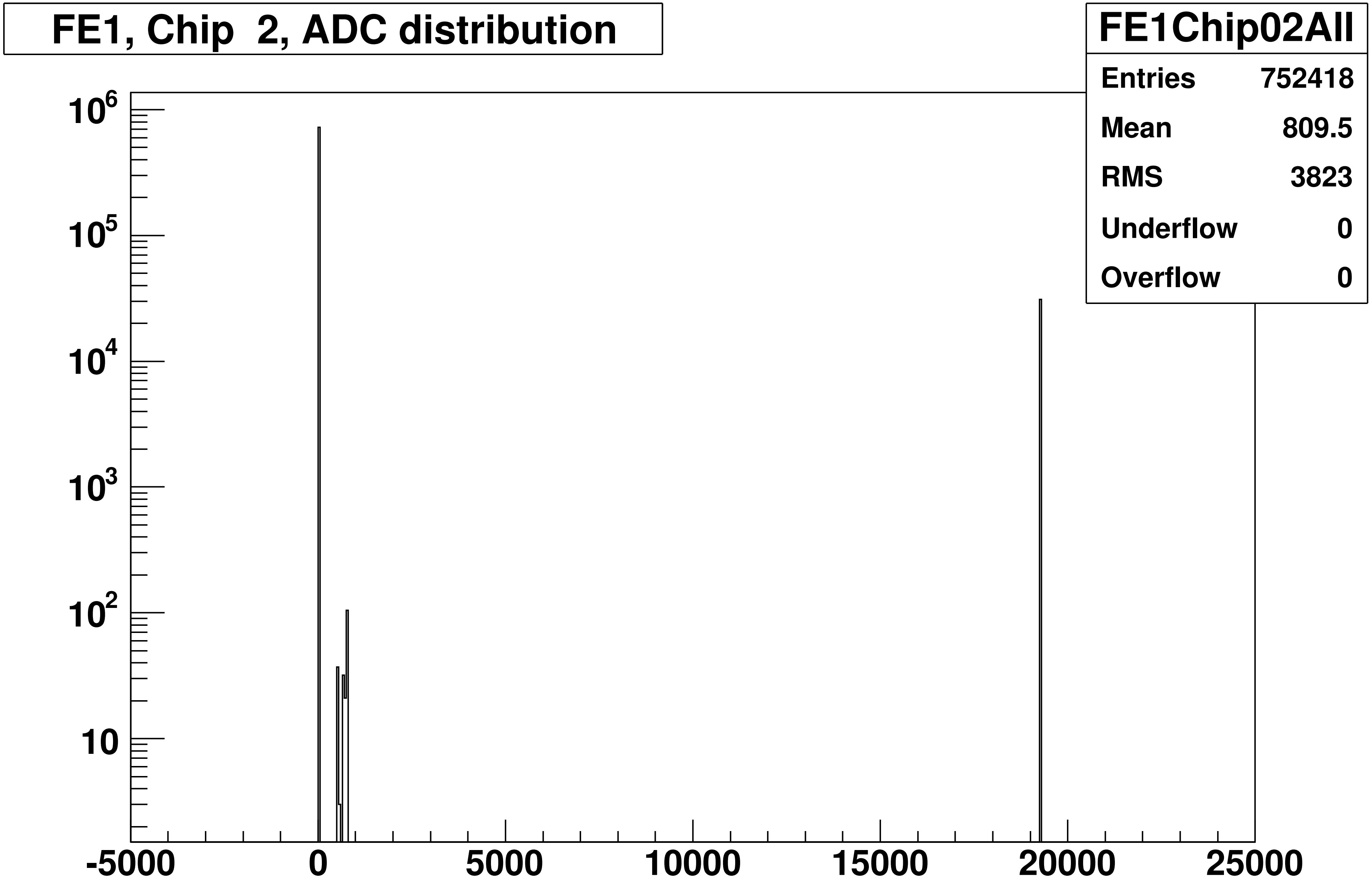
FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 752418
Mean 809.5
RMS 3823
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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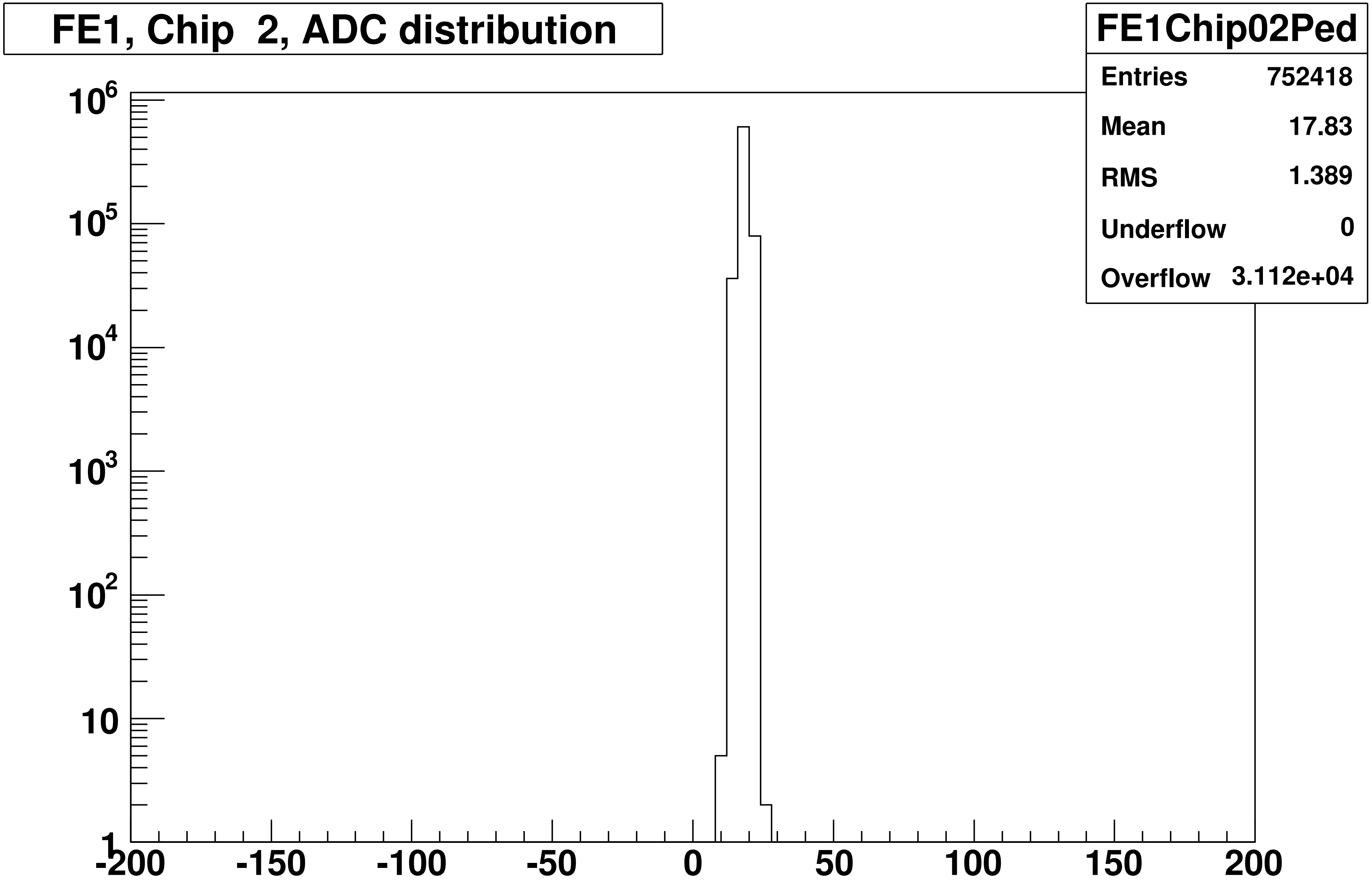
FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 752418
Mean 17.83
RMS 1.389
Underflow 0
Overflow 3.112e+04
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$10^2$
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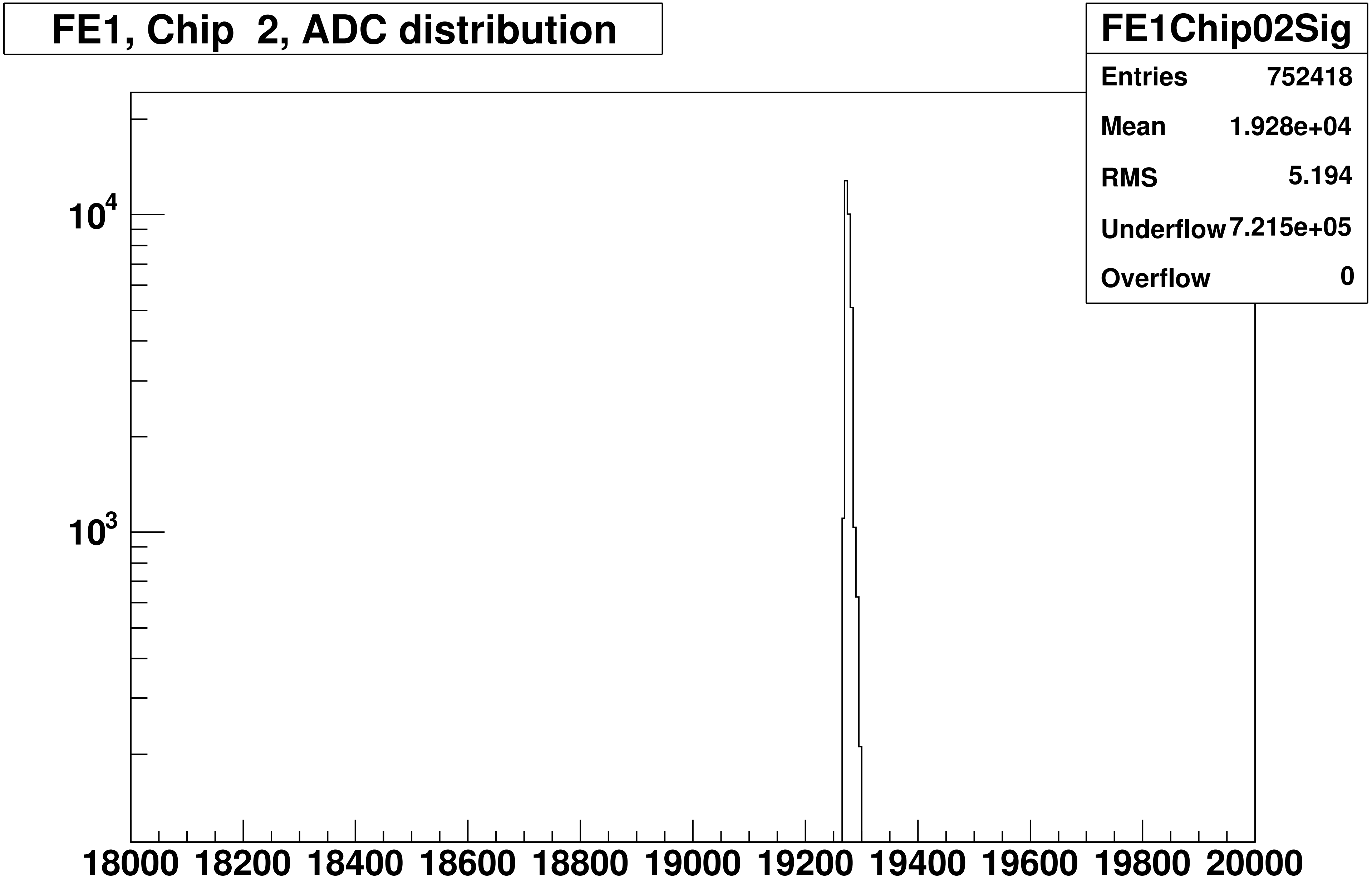
FE1, Chip  2, ADC distribution
FE1Chip02Sig
Entries 752418
Mean 1.928e+04
RMS 5.194
Underflow 7.215e+05
Overflow 0
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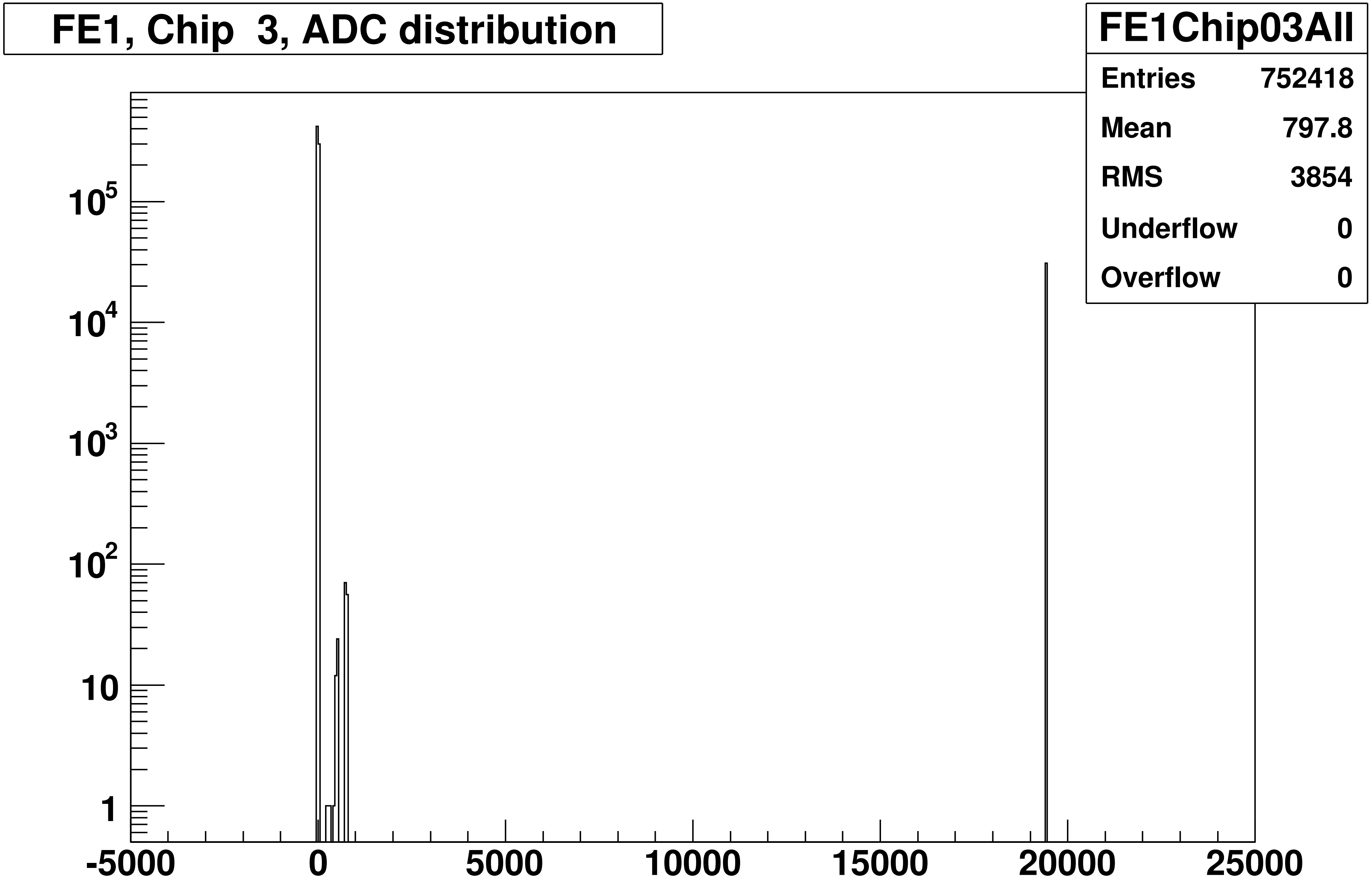
FE1, Chip  3, ADC distribution
FE1Chip03All
Entries 752418
Mean 797.8
RMS 3854
Underflow 0
Overflow 0
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$10^3$
$10^2$
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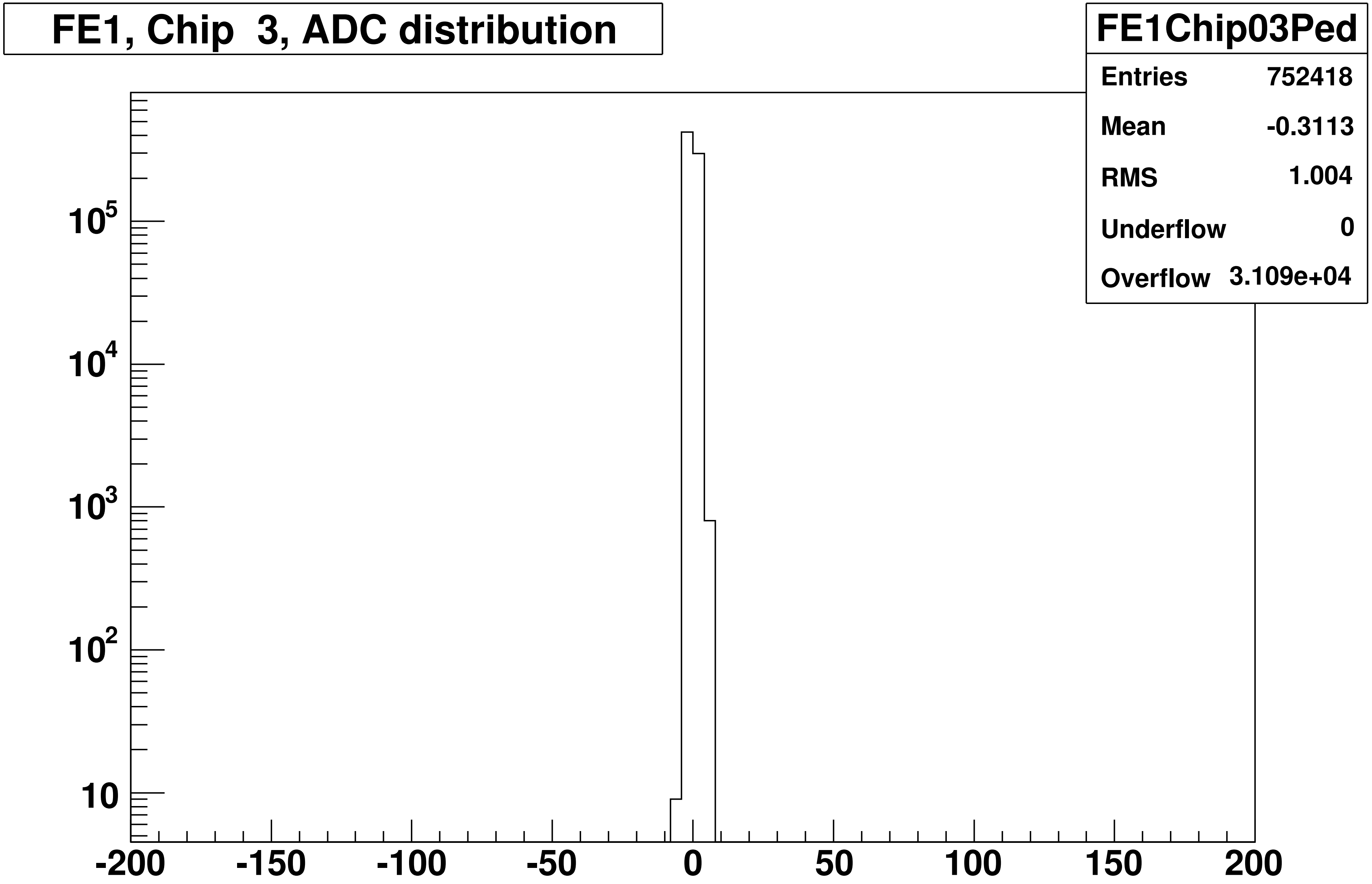
FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 752418
Mean -0.3113
RMS 1.004
Underflow 0
Overflow 3.109e+04
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$10^2$
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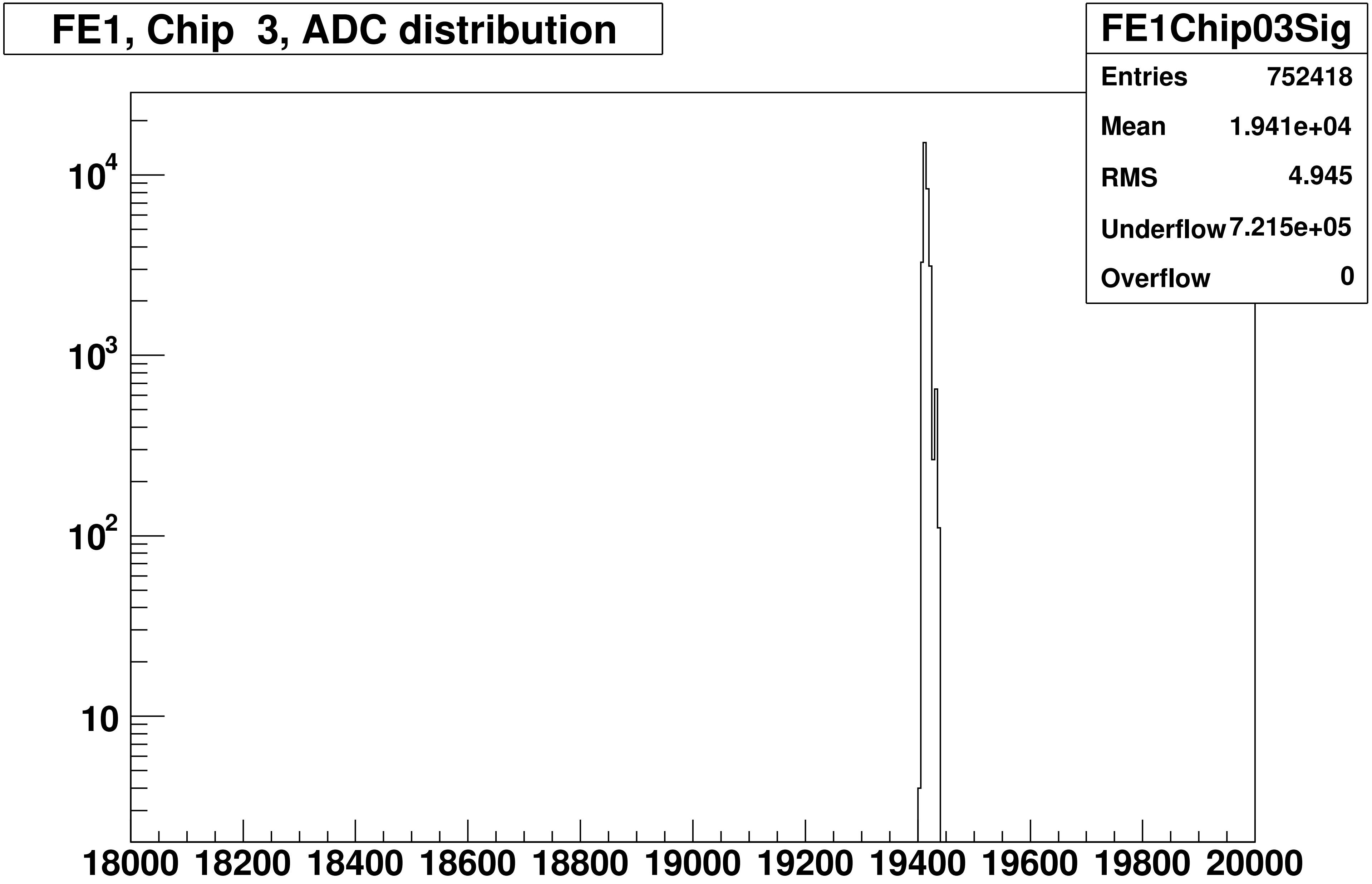
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 752418
Mean 1.941e+04
RMS 4.945
Underflow 7.215e+05
Overflow 0
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$10^2$
10
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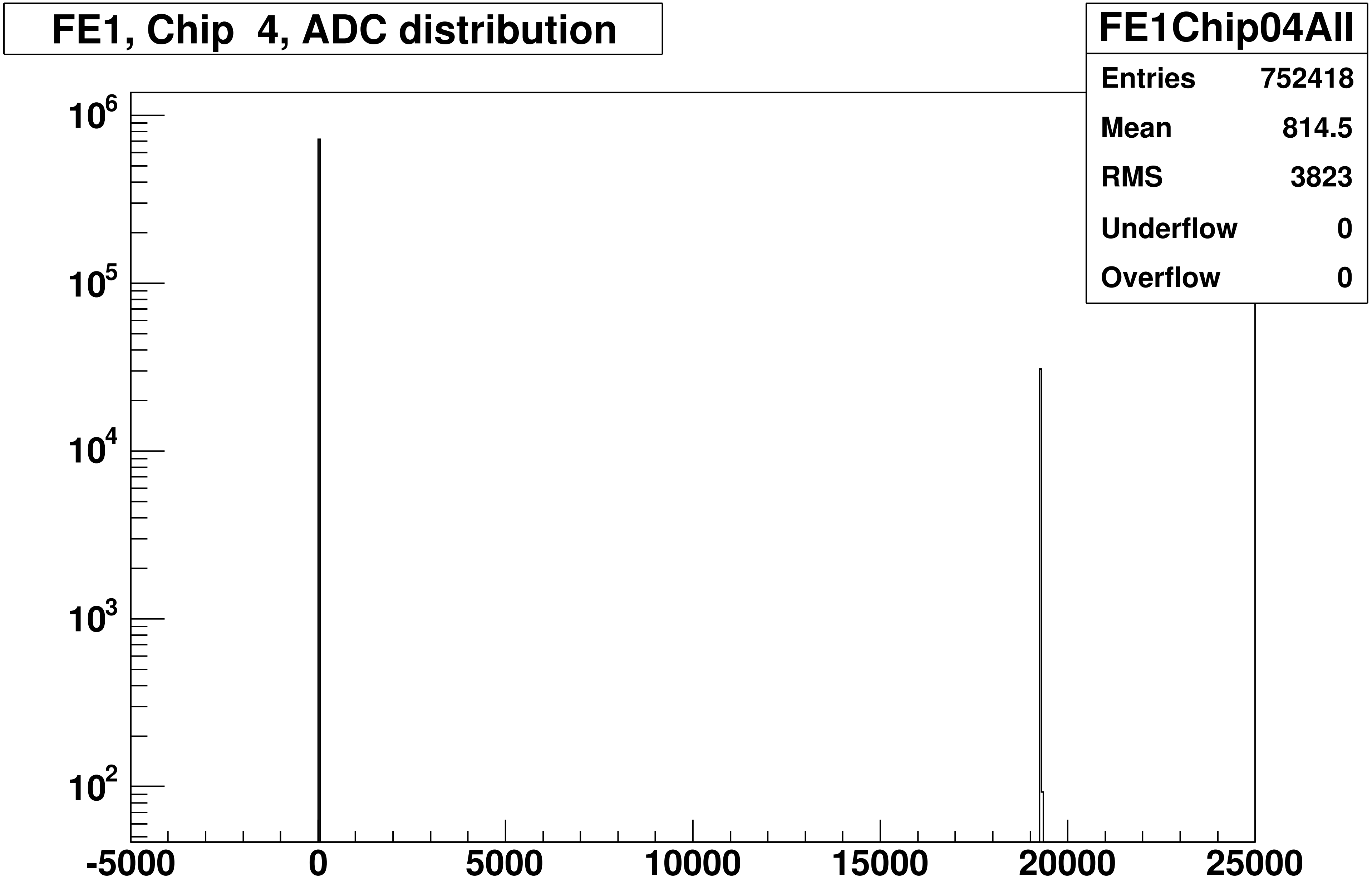
FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 752418
Mean 814.5
RMS 3823
Underflow 0
Overflow 0
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20000
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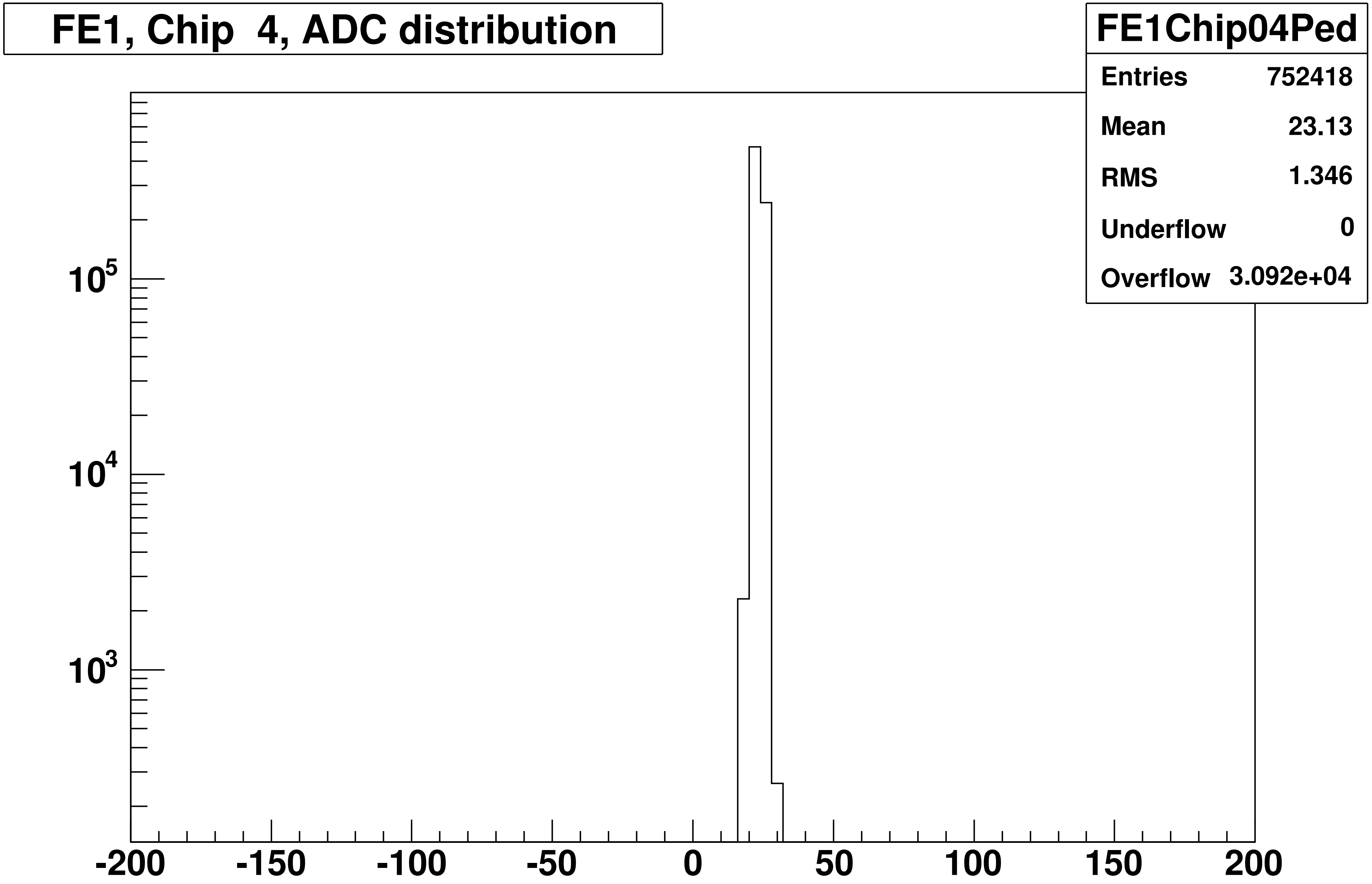
FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 752418
Mean 23.13
RMS 1.346
Underflow 0
Overflow 3.092e+04
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10^4
10^3
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-100
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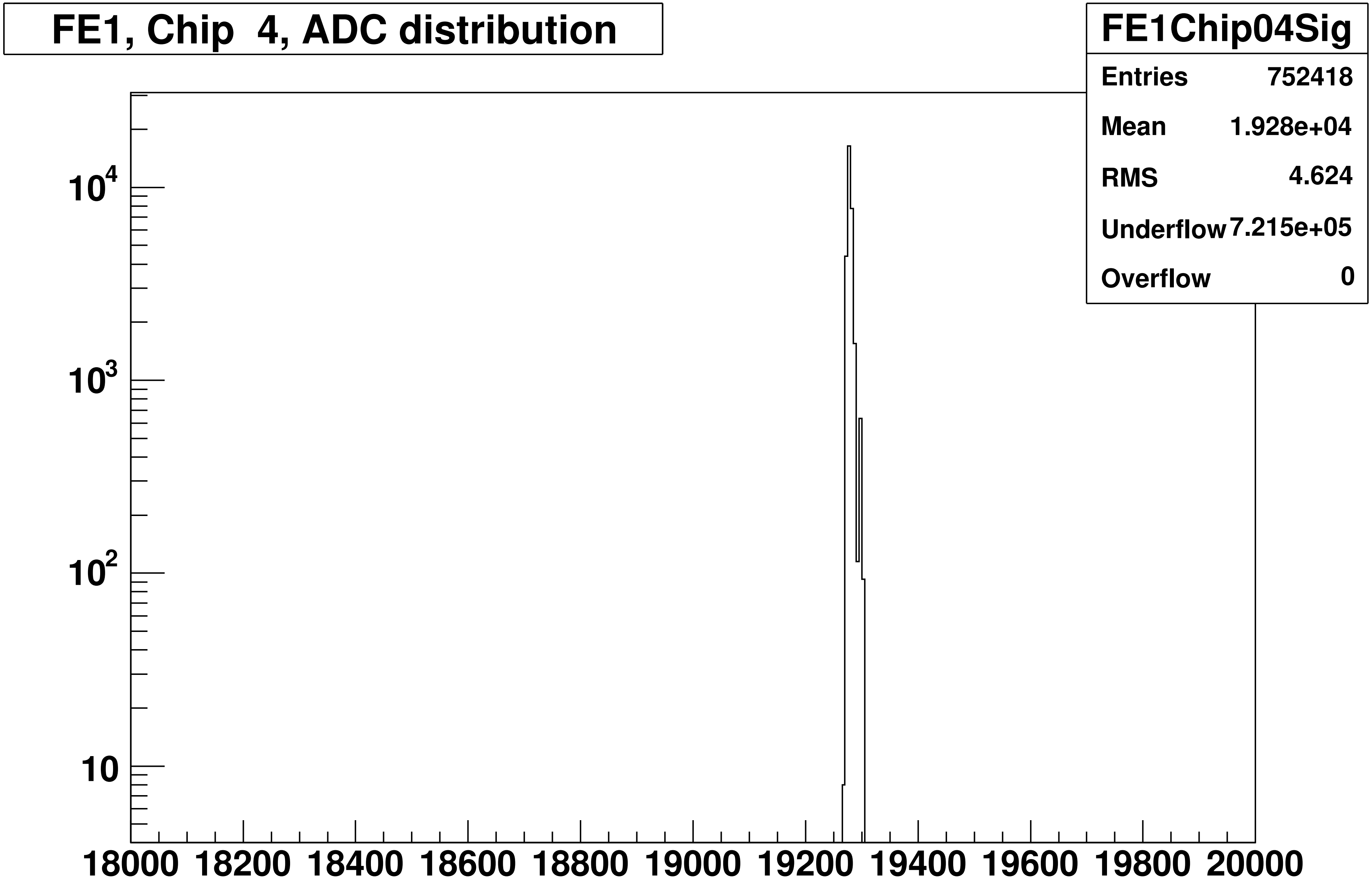
50
100
150
200


FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 752418
Mean 1.928e+04
RMS 4.624
Underflow 7.215e+05
Overflow 0
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$10^3$
$10^2$
10
18000
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18400
18600
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19000
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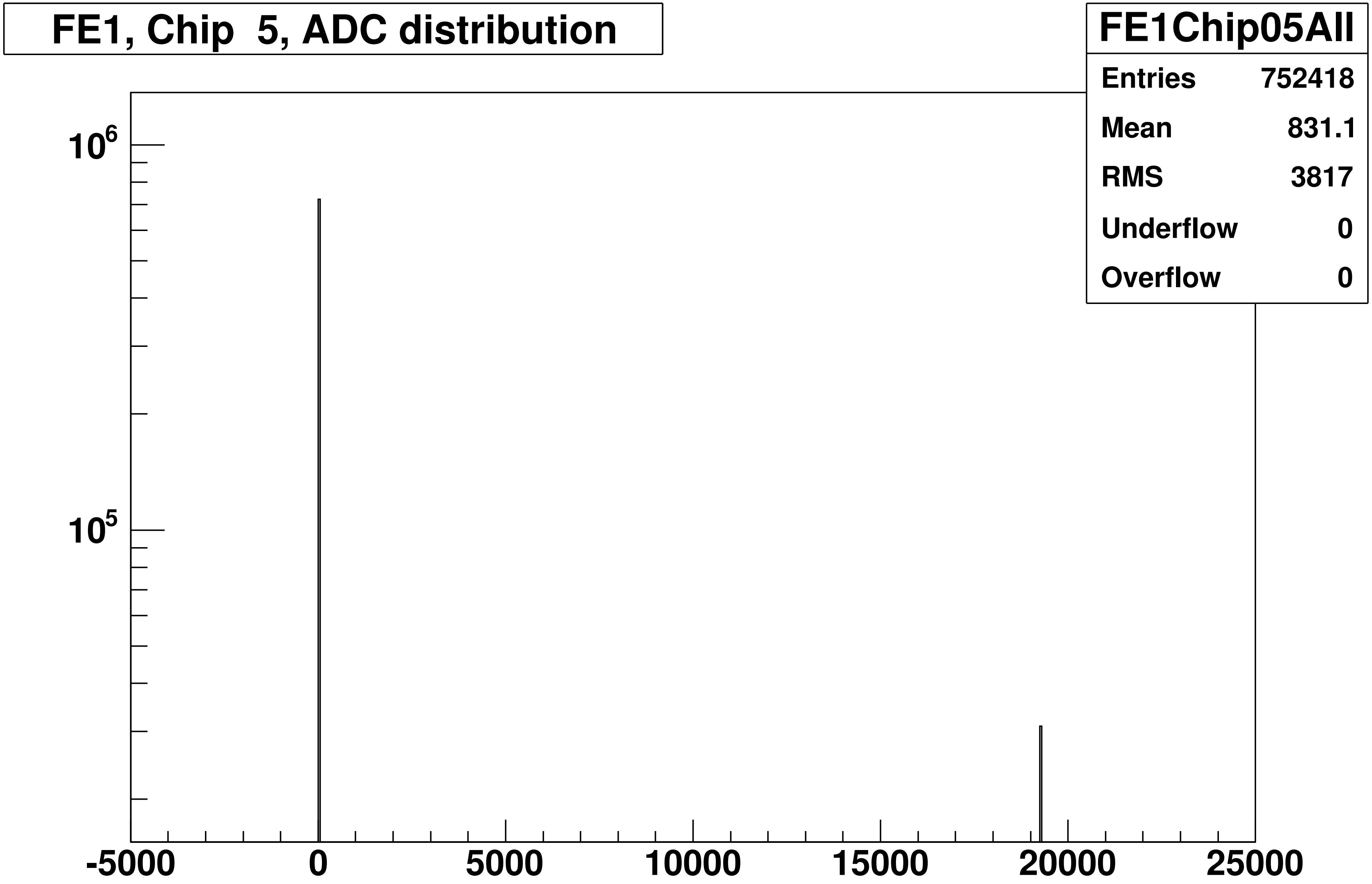
19400
19600
19800
20000


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 752418
Mean 831.1
RMS 3817
Underflow 0
Overflow 0
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10^5
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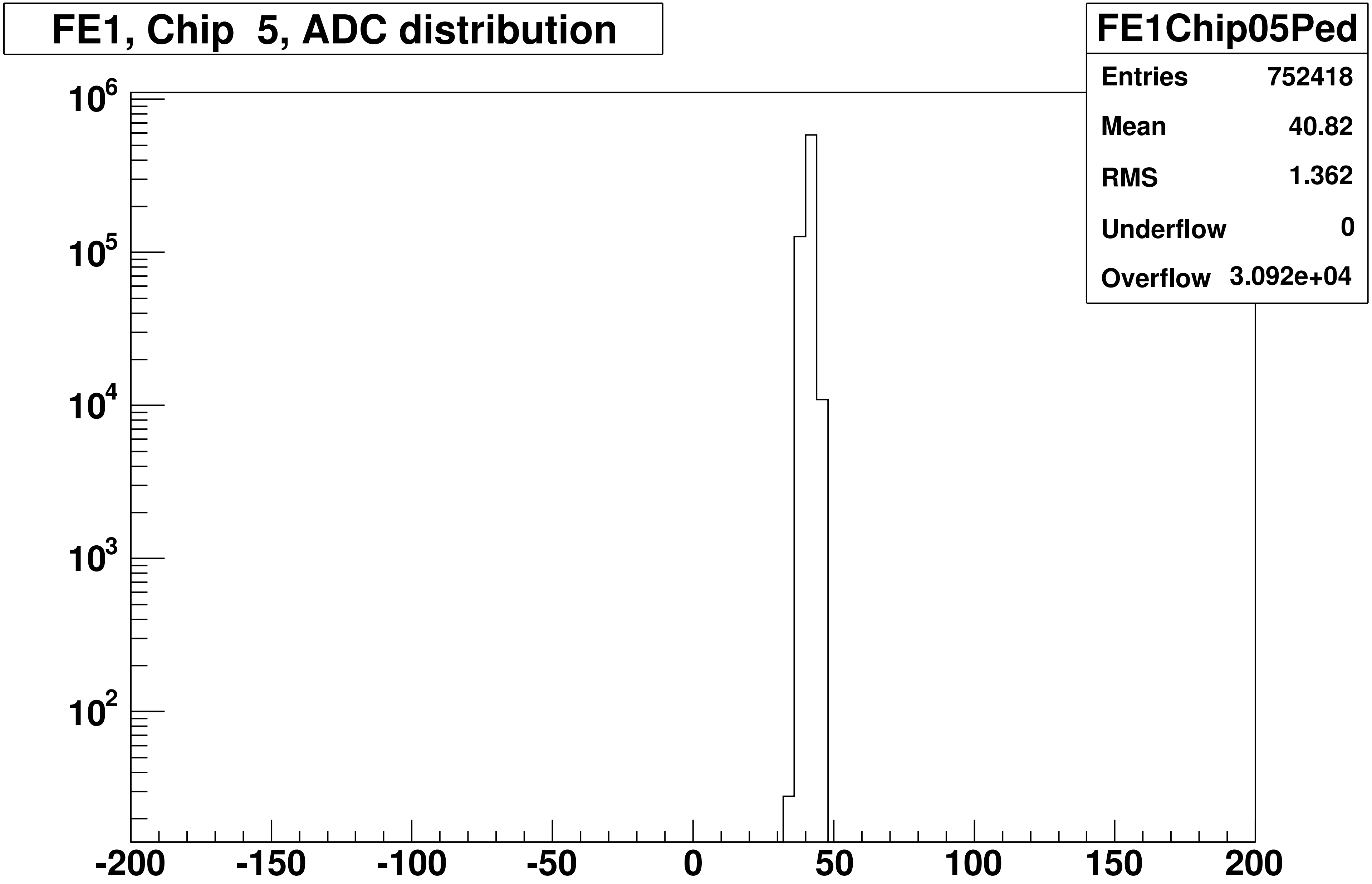
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 752418
Mean 40.82
RMS 1.362
Underflow 0
Overflow 3.092e+04
$10^6$
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$10^3$
$10^2$
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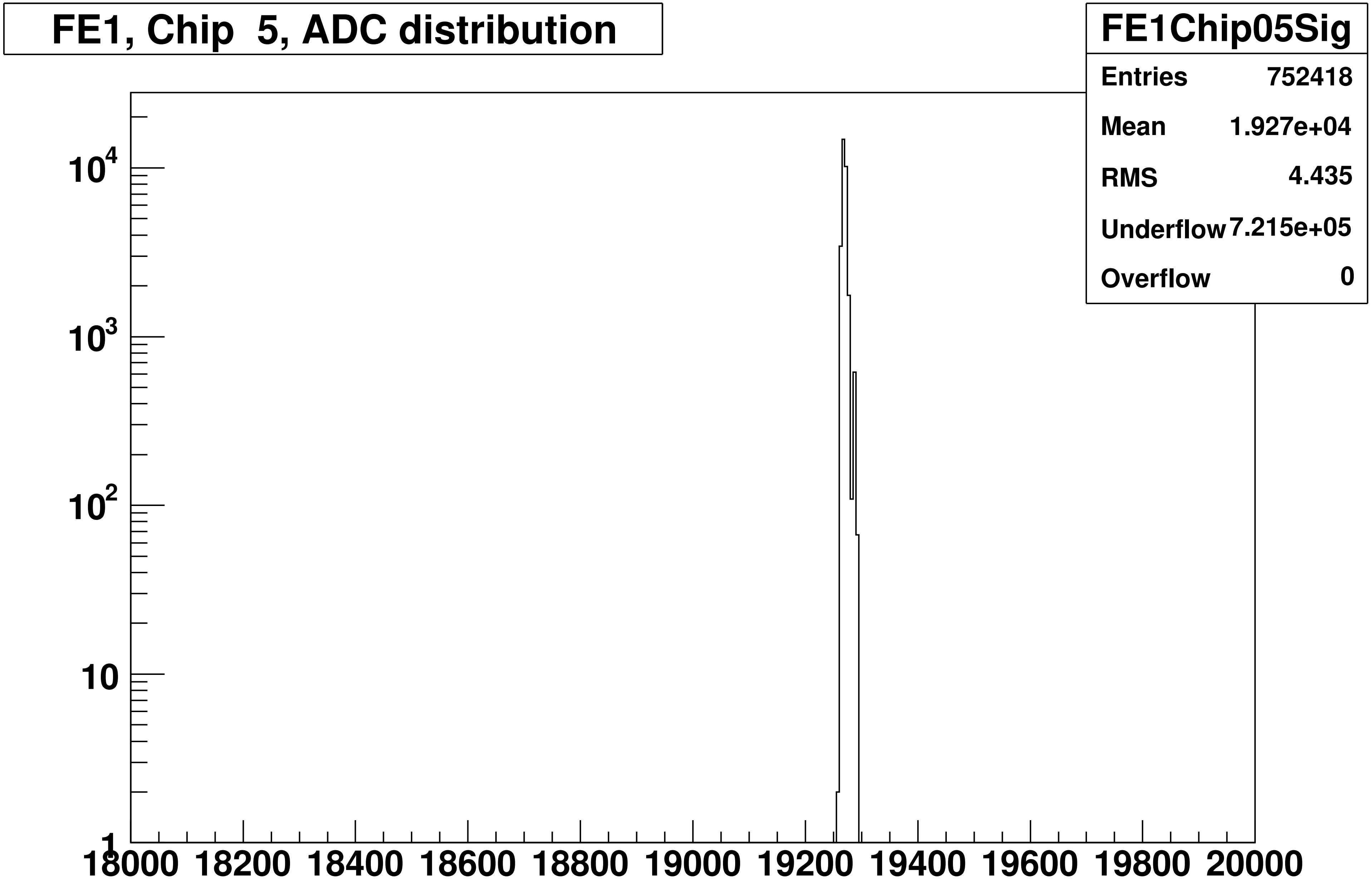
100
150
200


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 752418
Mean 1.927e+04
RMS 4.435
Underflow 7.215e+05
Overflow 0
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$10^3$
$10^2$
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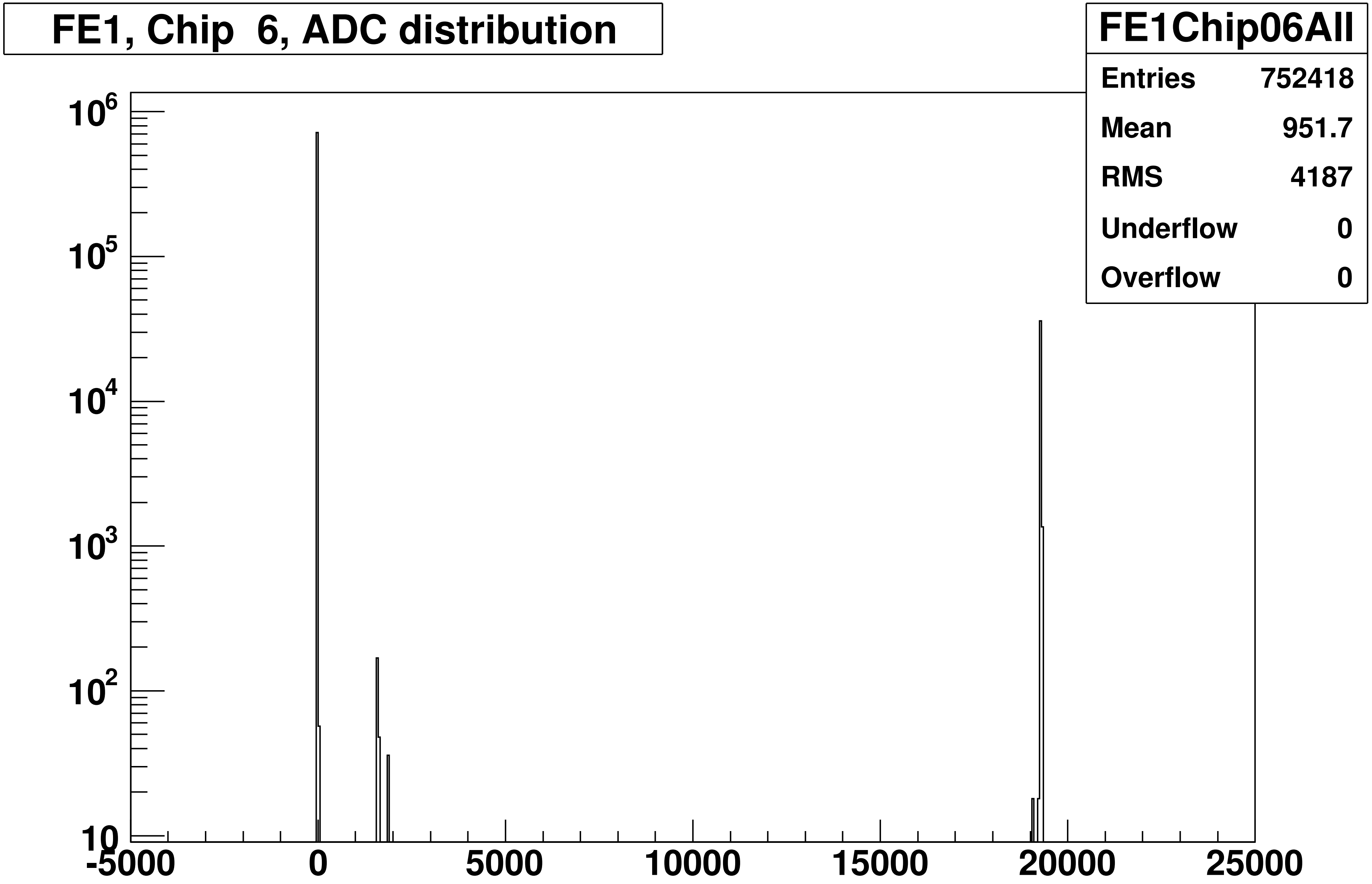
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 752418
Mean 951.7
RMS 4187
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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-5000
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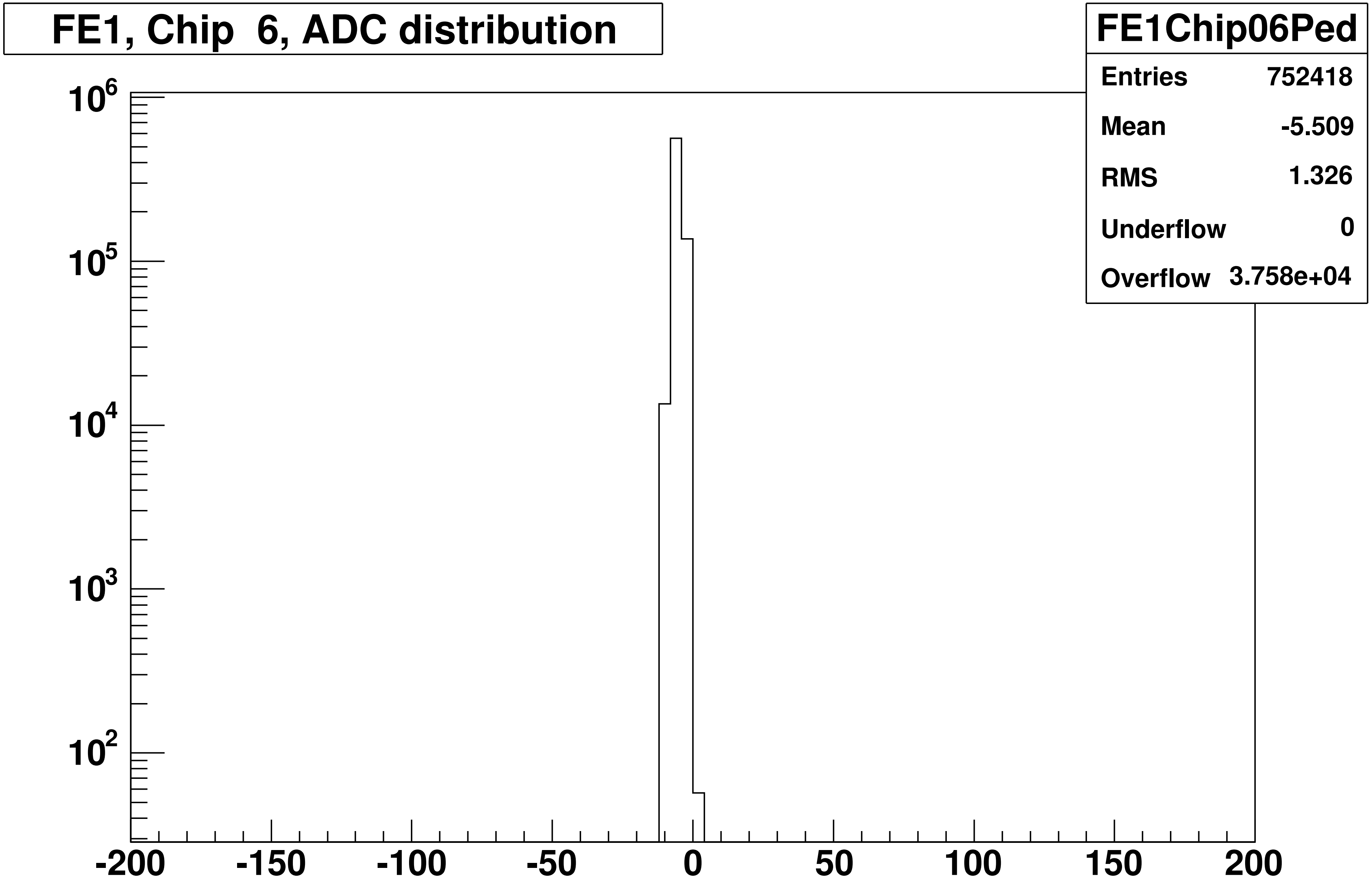
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 752418
Mean -5.509
RMS 1.326
Underflow 0
Overflow 3.758e+04
$10^6$
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$10^3$
$10^2$
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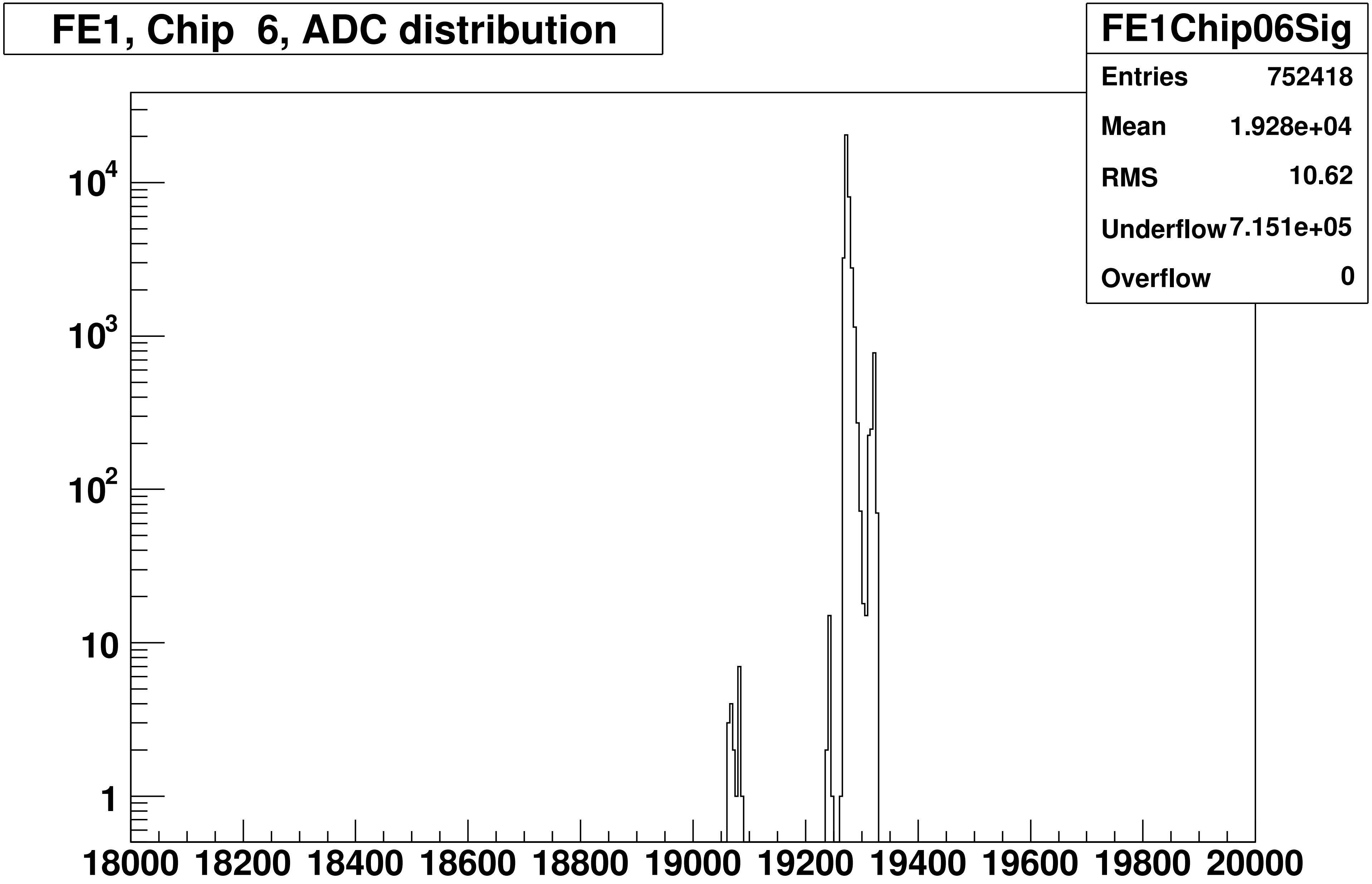
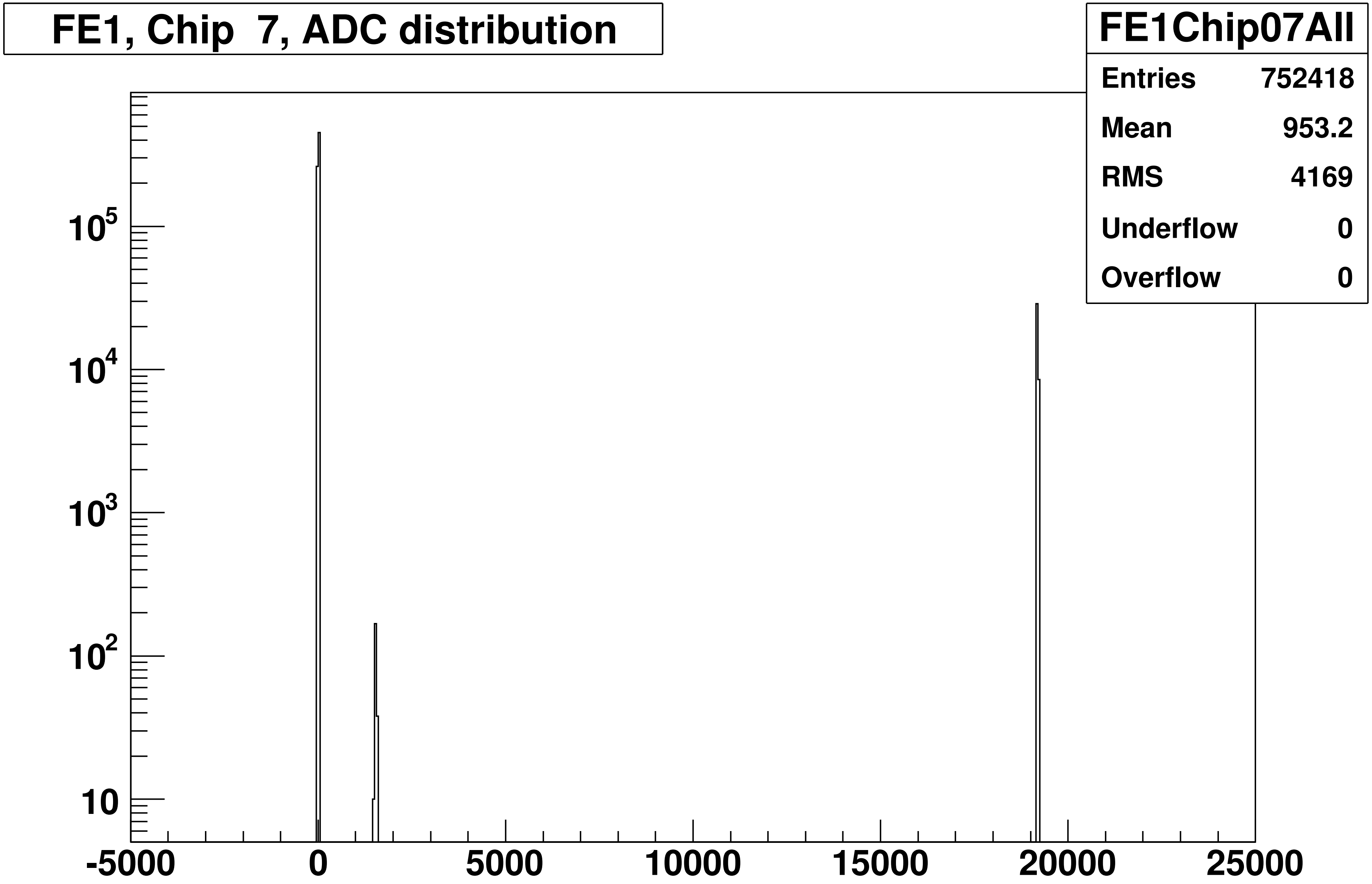
150
200



FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 752418
Mean 1.928e+04
RMS 10.62
Underflow 7.151e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE1, Chip  7, ADC distribution
FE1Chip07All
Entries 752418
Mean 953.2
RMS 4169
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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-5000
0
5000
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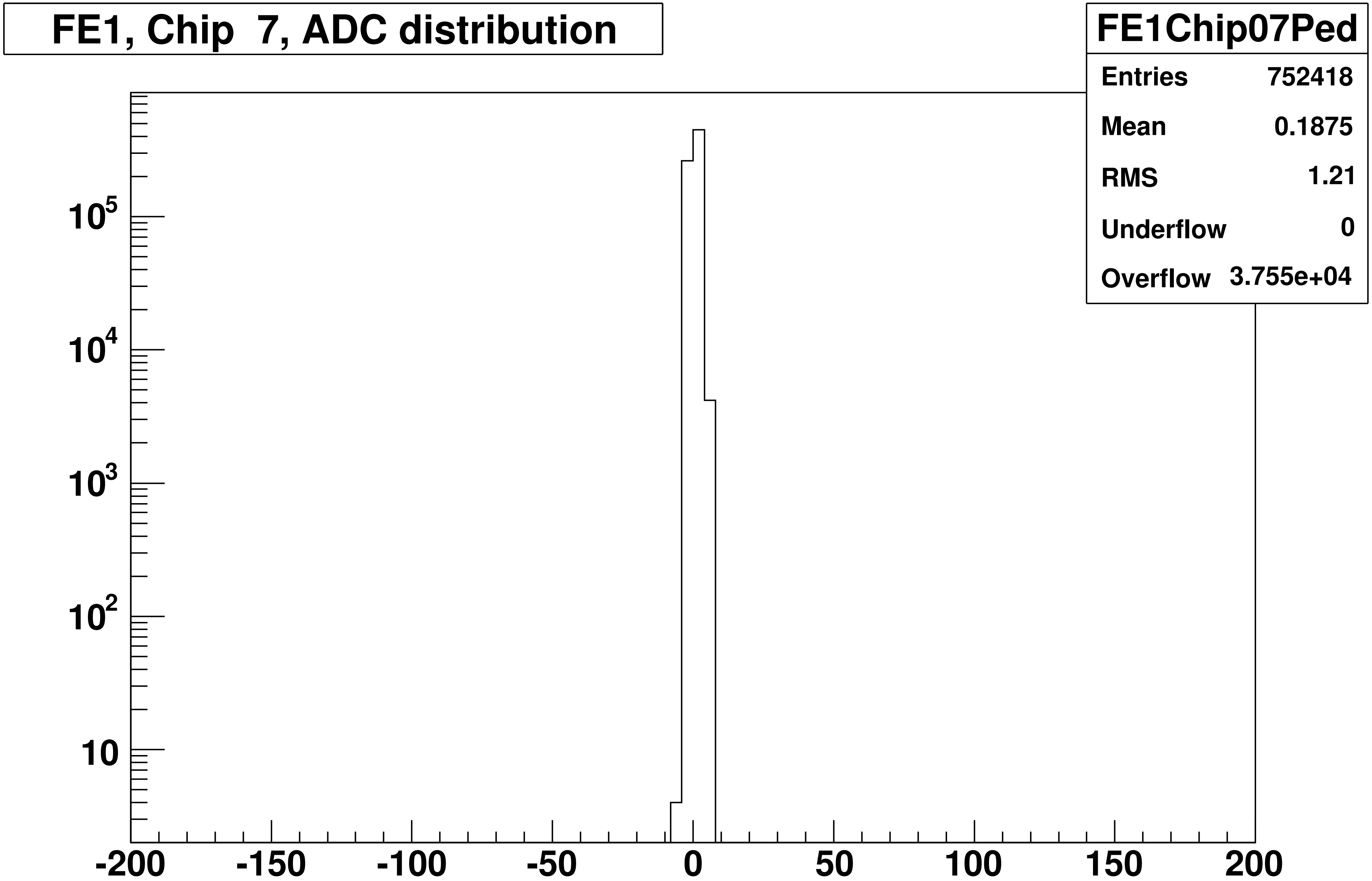
15000
20000
25000



FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 752418
Mean 0.1875
RMS 1.21
Underflow 0
Overflow 3.755e+04
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$10^4$
$10^3$
$10^2$
10
-200
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-100
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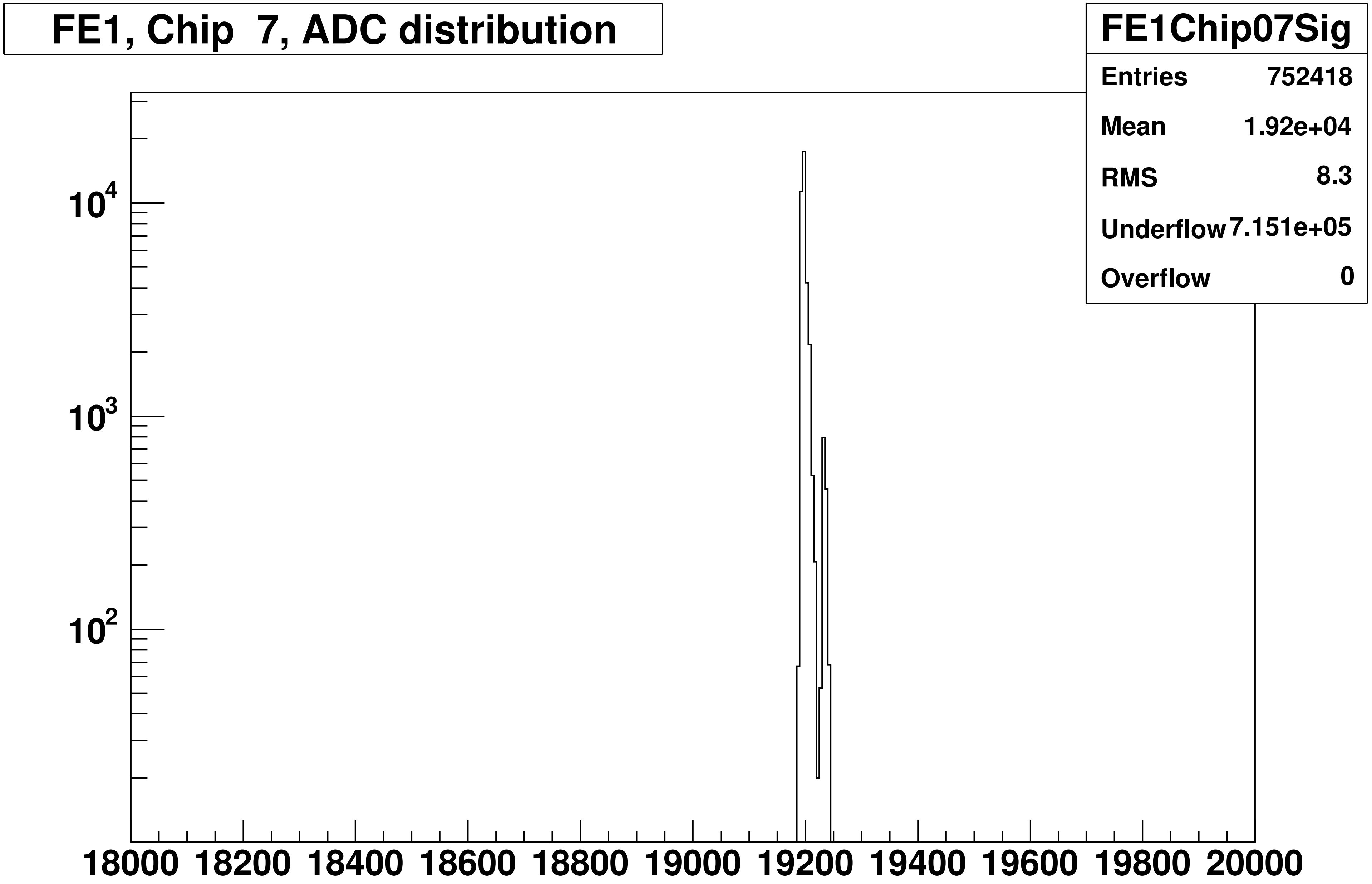
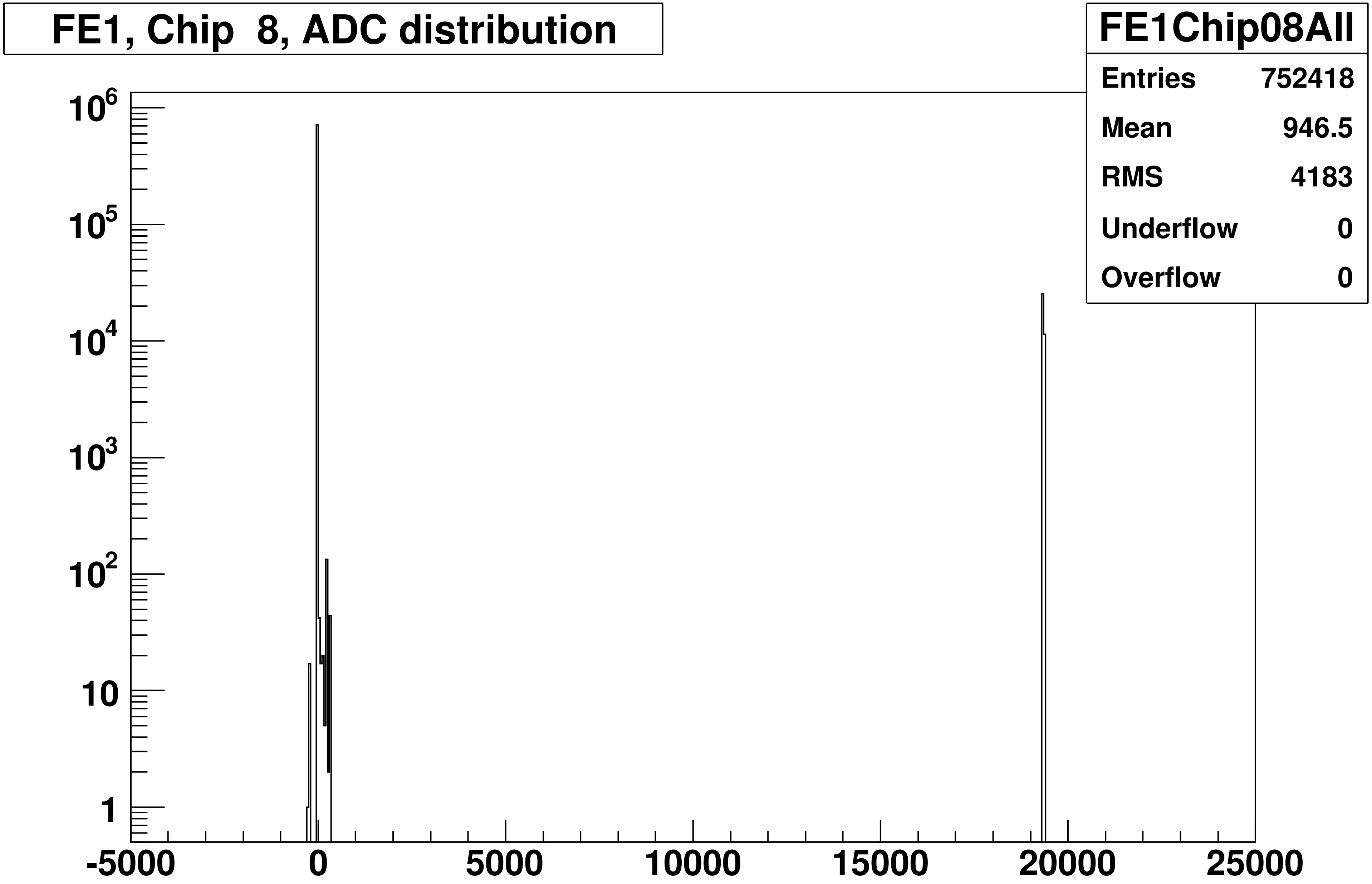
0
50
100
150
200


FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 752418
Mean 1.92e+04
RMS 8.3
Underflow 7.151e+05
Overflow 0
$10^4$
$10^3$
$10^2$
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 752418
Mean 946.5
RMS 4183
Underflow 0
Overflow 0
$10^6$
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$10^4$
$10^3$
$10^2$
10
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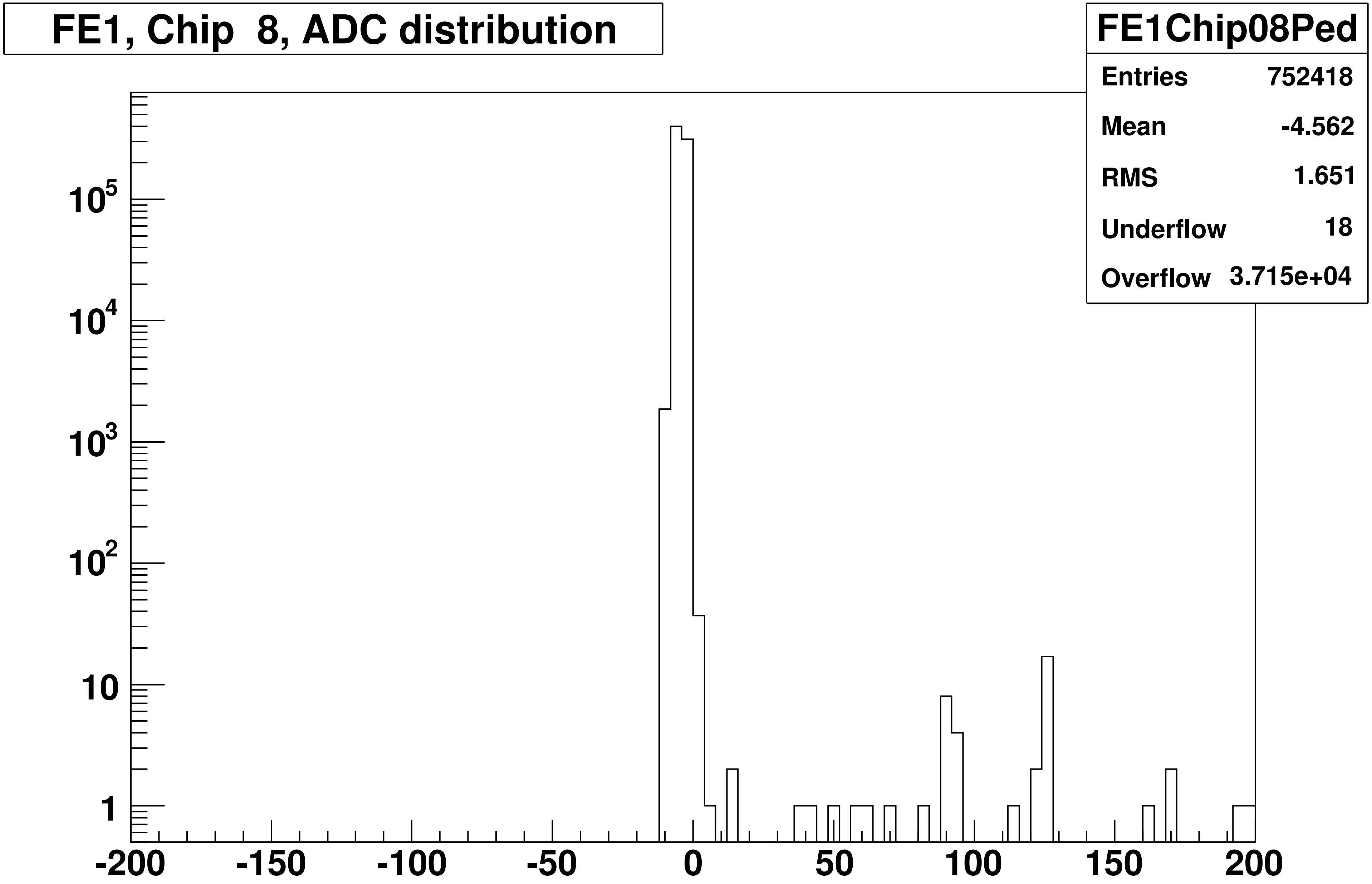
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 752418
Mean -4.562
RMS 1.651
Underflow 18
Overflow 3.715e+04
$10^5$
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$10^3$
$10^2$
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-100
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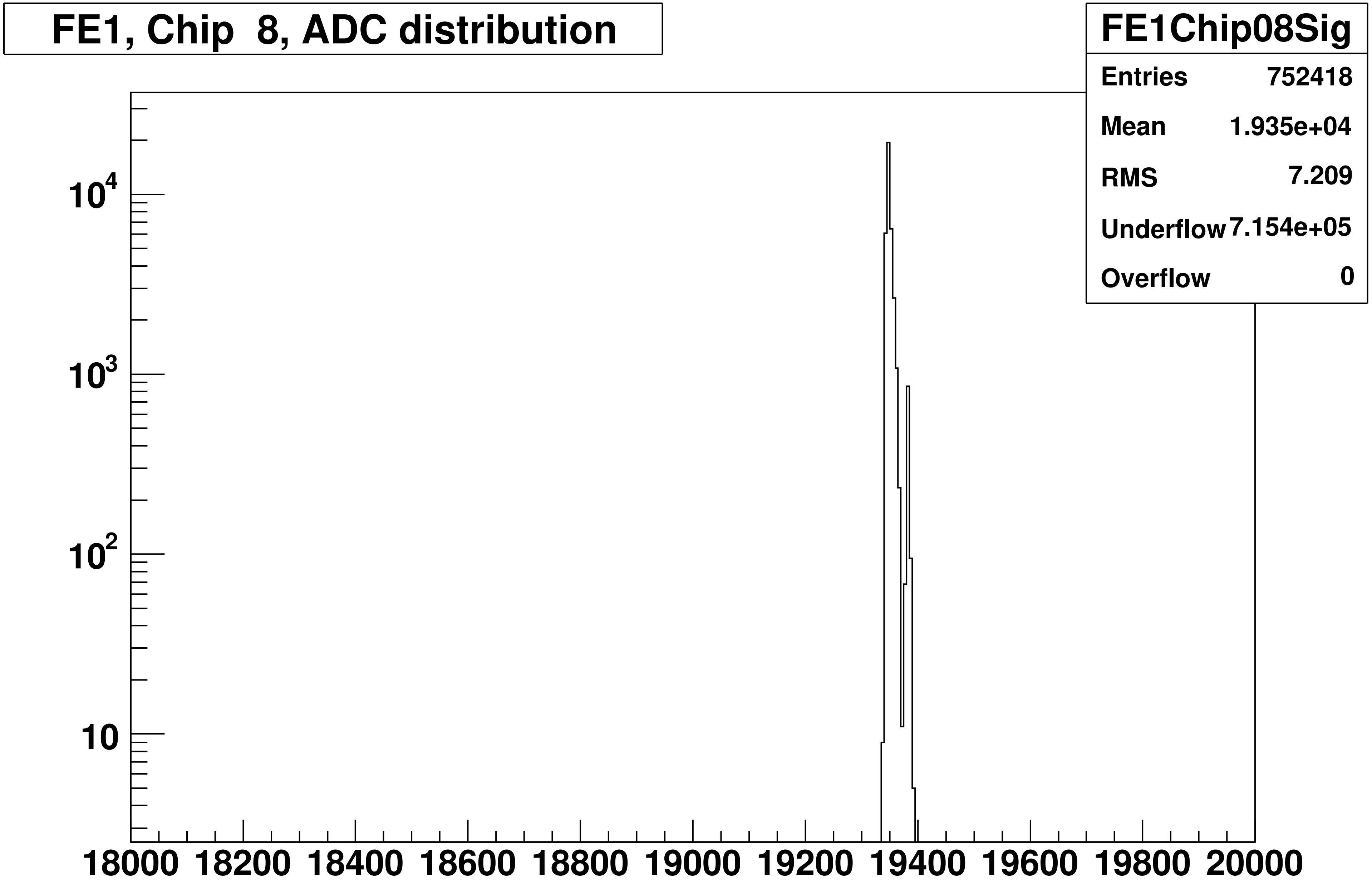
0
50
100
150
200


FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 752418
Mean 1.935e+04
RMS 7.209
Underflow 7.154e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000
18200
18400
18600
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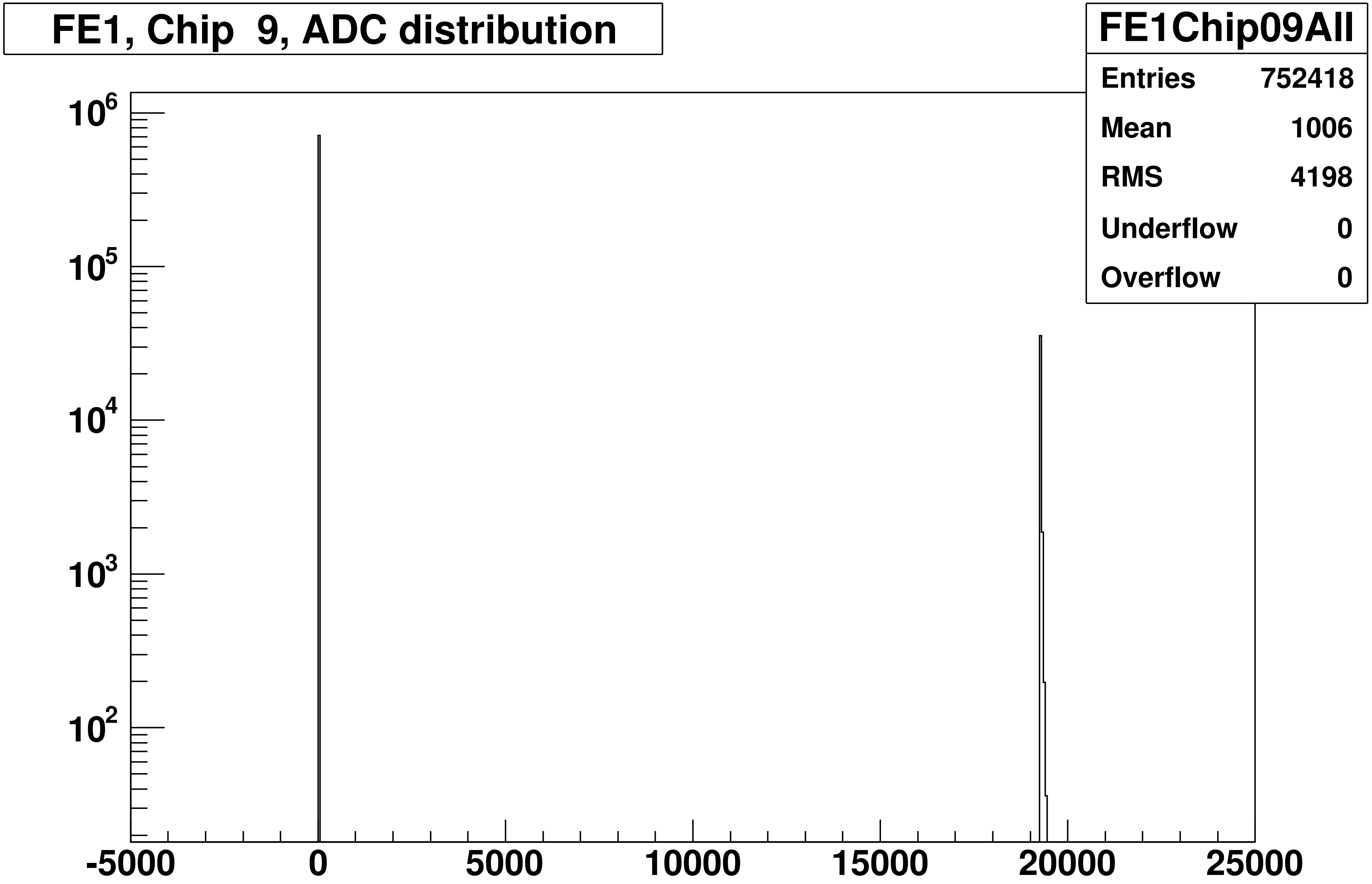
19000
19200
19400
19600
19800
20000



FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 752418
Mean 1006
RMS 4198
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
-5000
0
5000
10000
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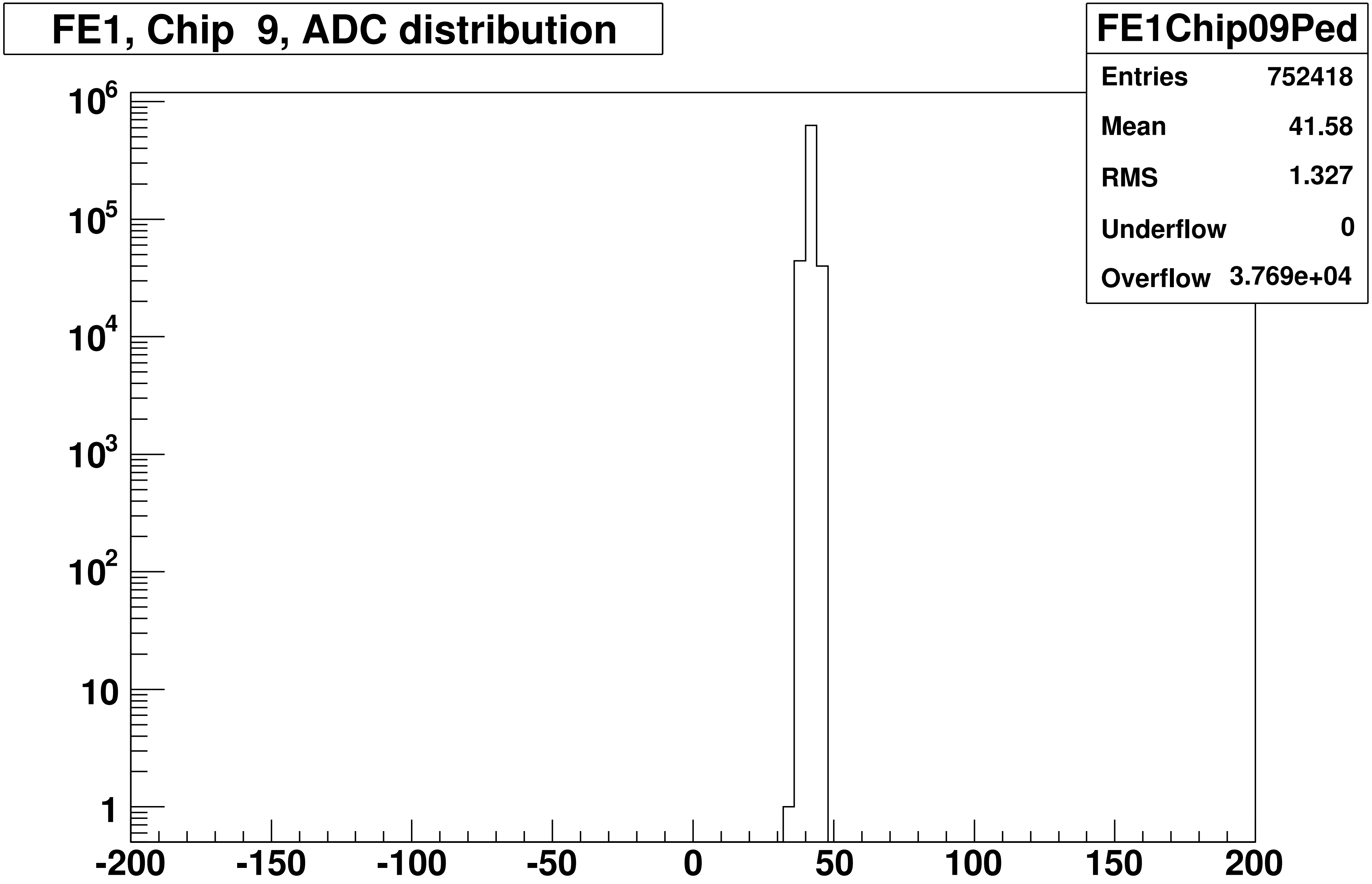
20000
25000


FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 752418
Mean 41.58
RMS 1.327
Underflow 0
Overflow 3.769e+04
10^6
10^5
10^4
10^3
10^2
10
1
-200
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-100
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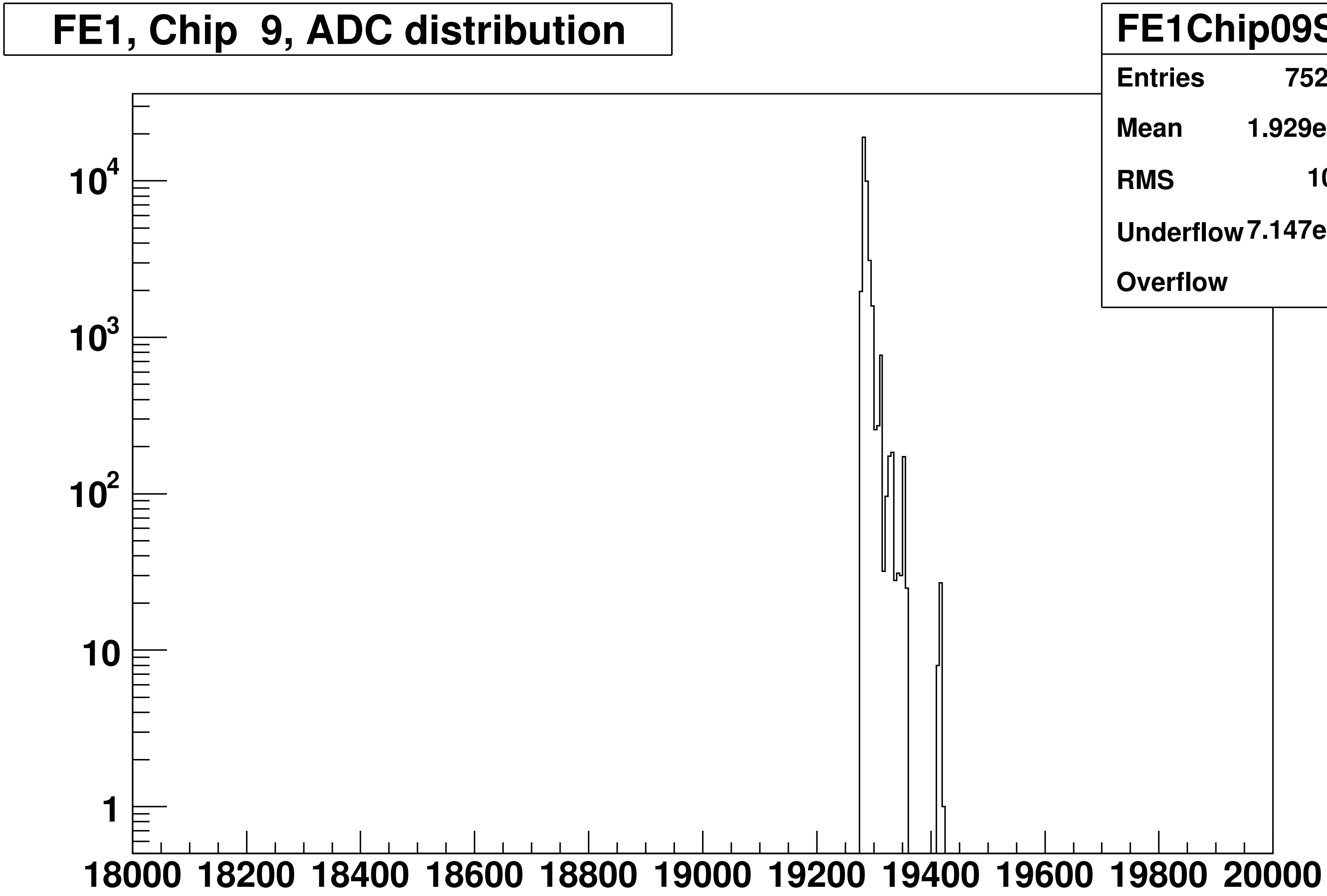
50
100
150
200


FE1, Chip  9, ADC distribution
FE1Chip09Sig
Entries 752418
Mean 1.929e+04
RMS 10.28
Underflow 7.147e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
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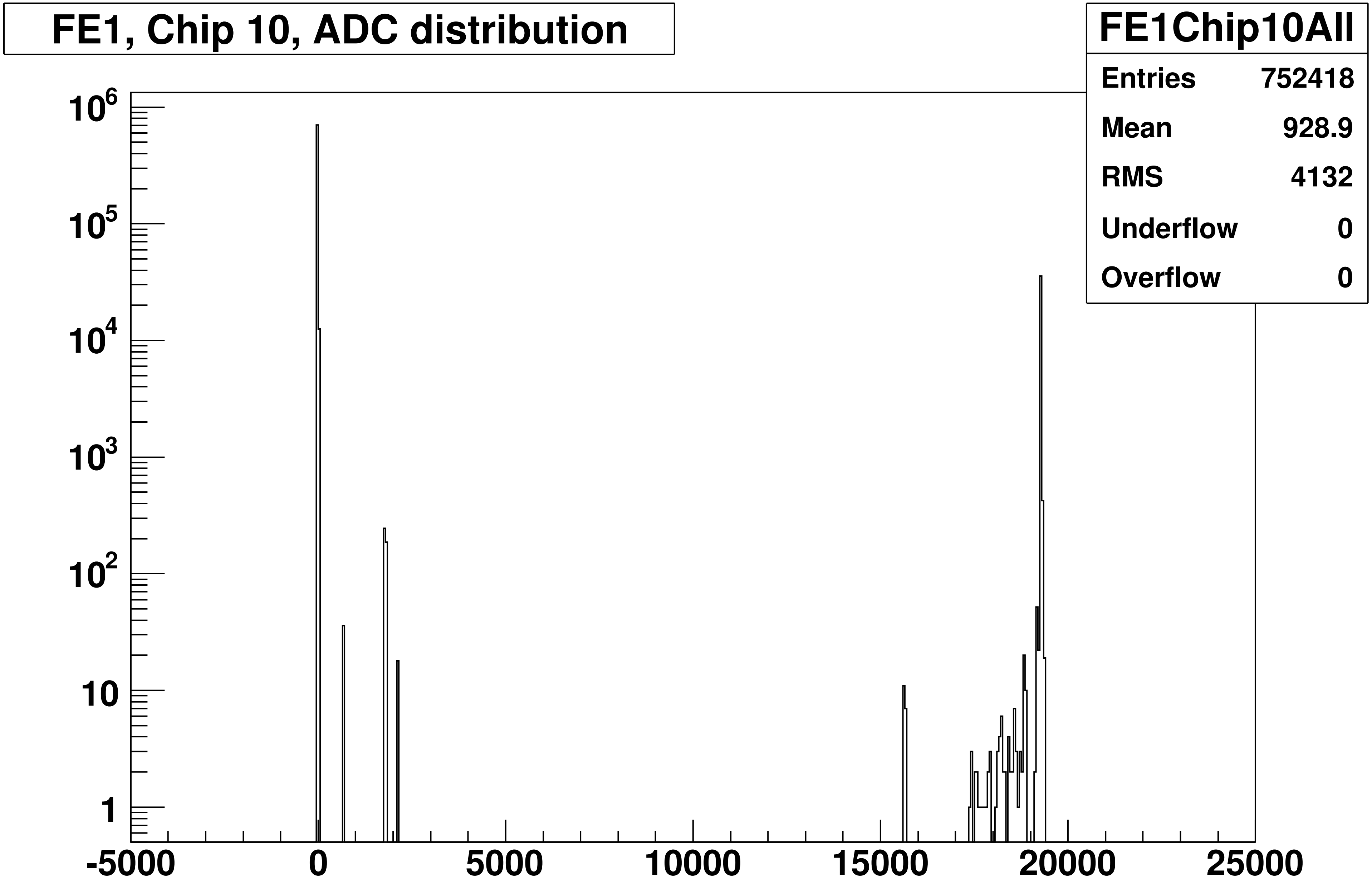
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 752418
Mean 928.9
RMS 4132
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
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0
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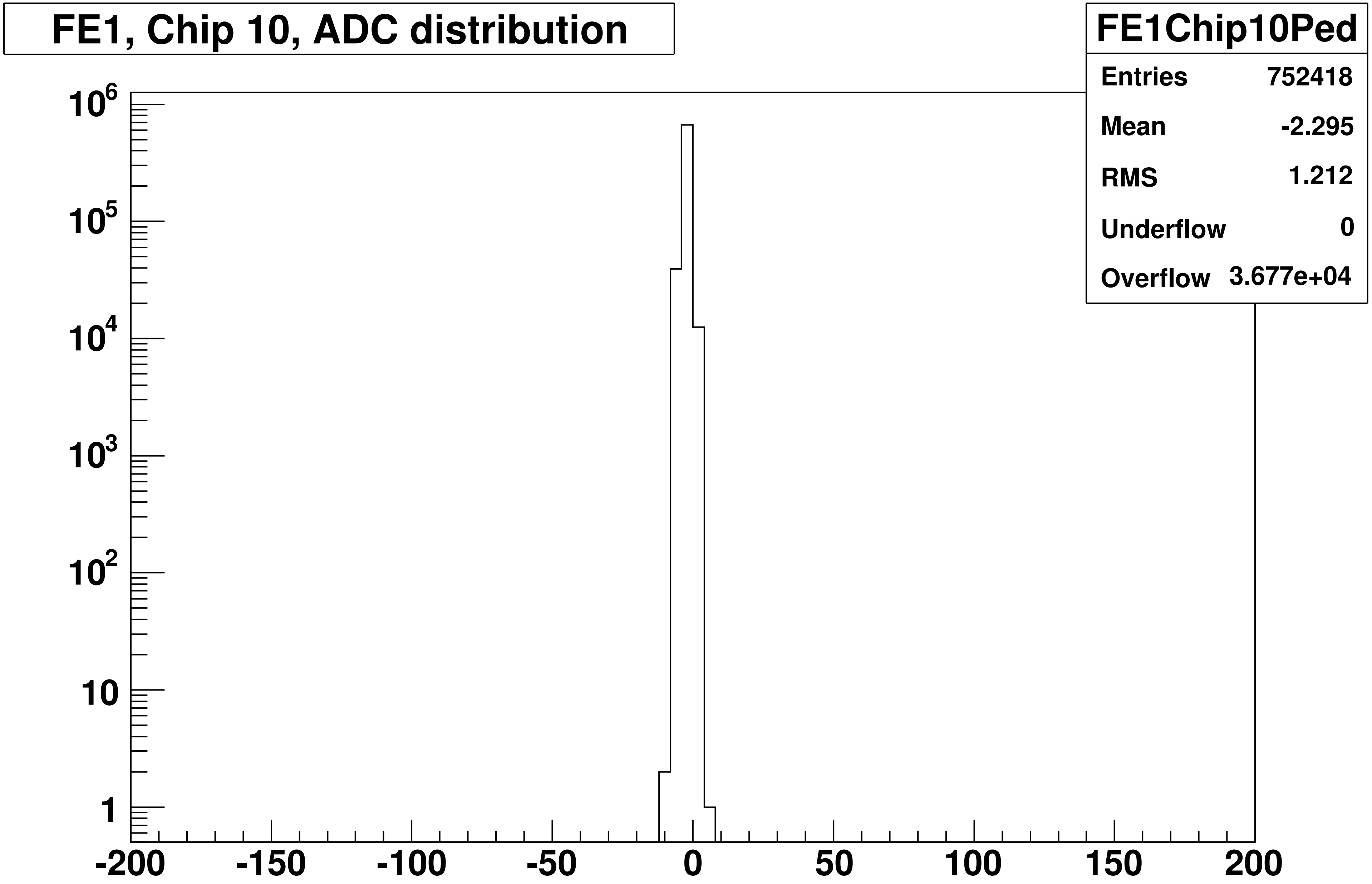
25000



FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 752418
Mean -2.295
RMS 1.212
Underflow 0
Overflow 3.677e+04
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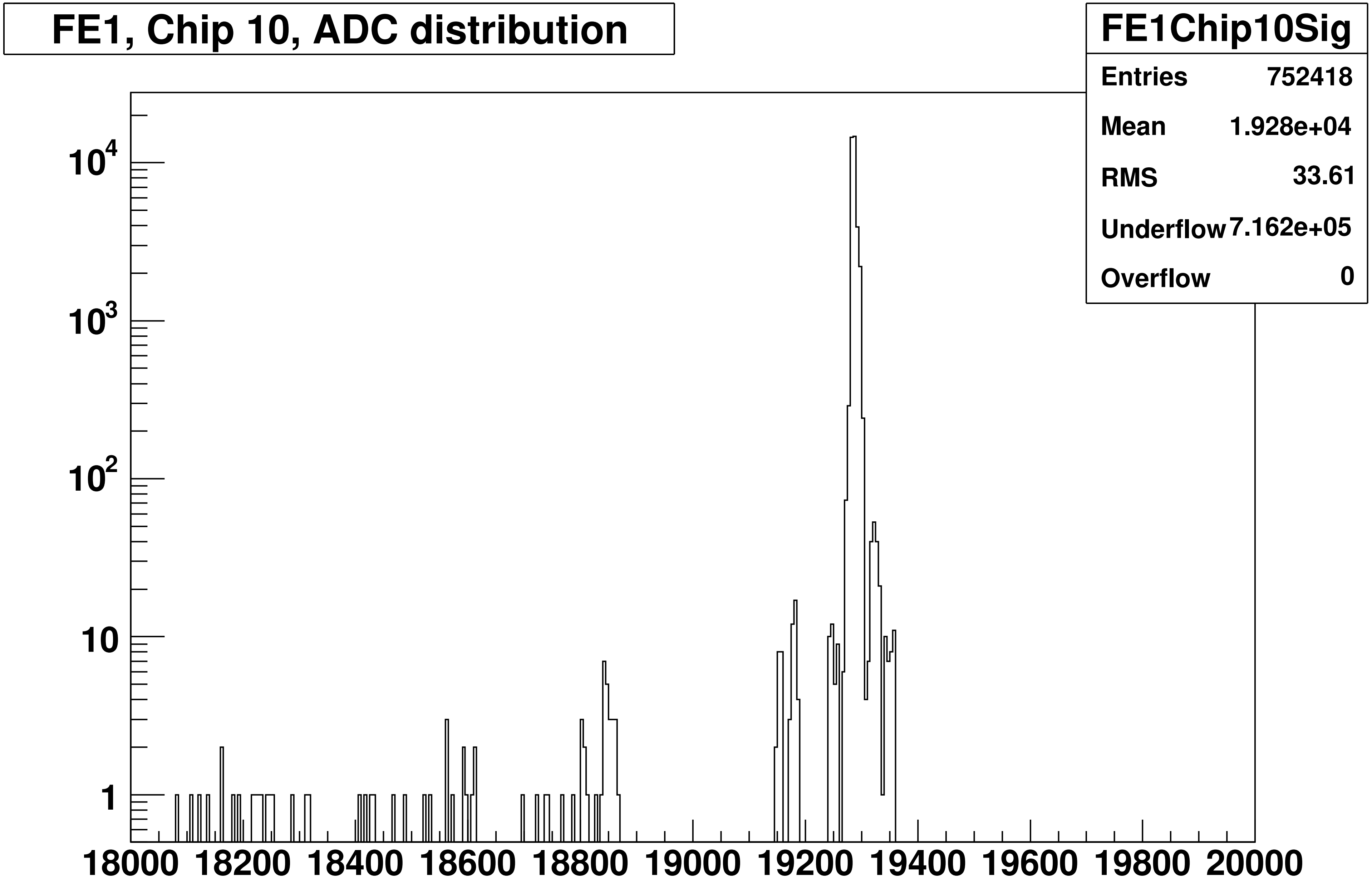
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-200 -150 -100 -50 0 50 100 150 200



FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 752418
Mean 1.928e+04
RMS 33.61
Underflow 7.162e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
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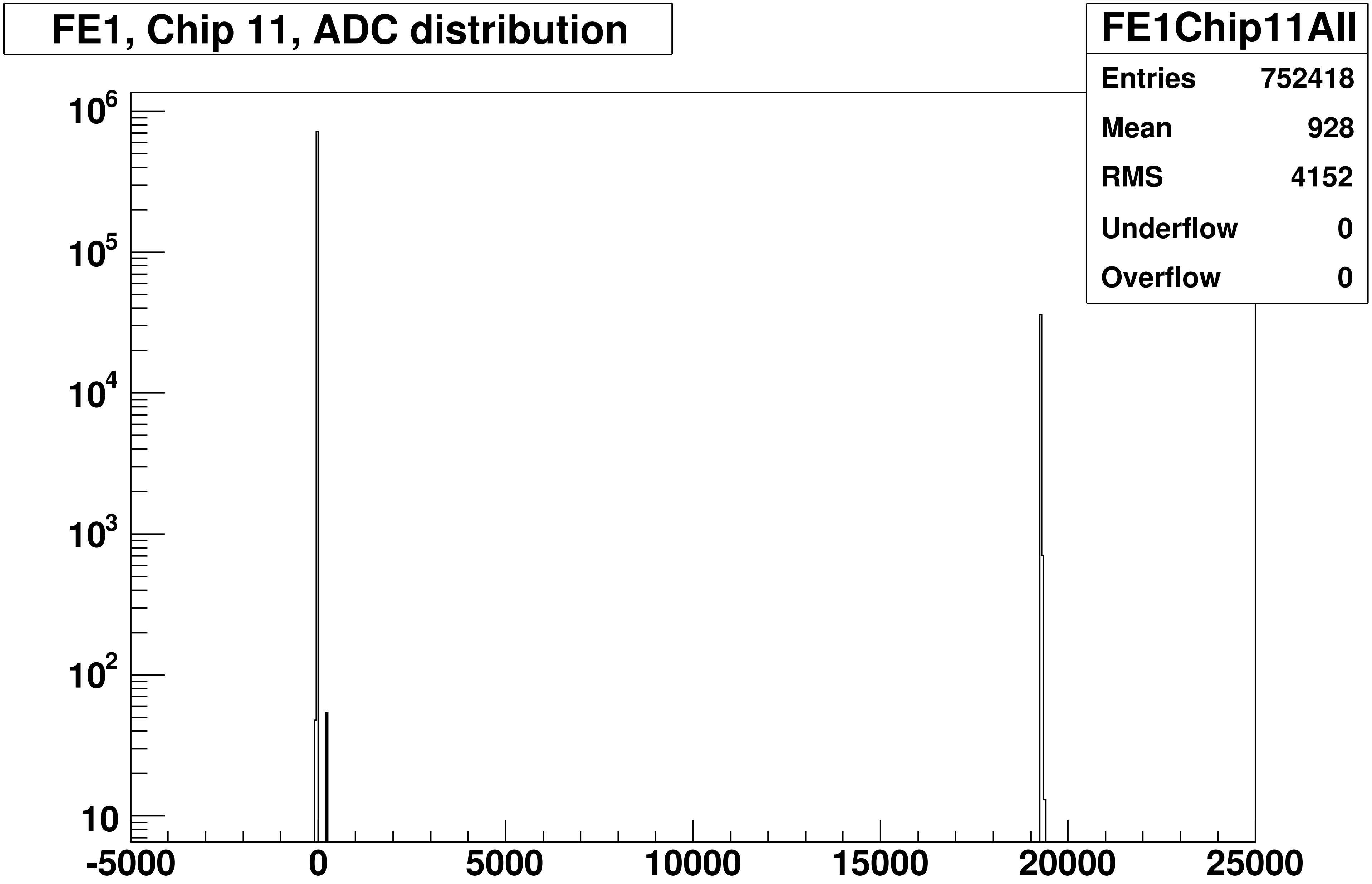
20000



FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 752418
Mean 928
RMS 4152
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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-5000
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5000
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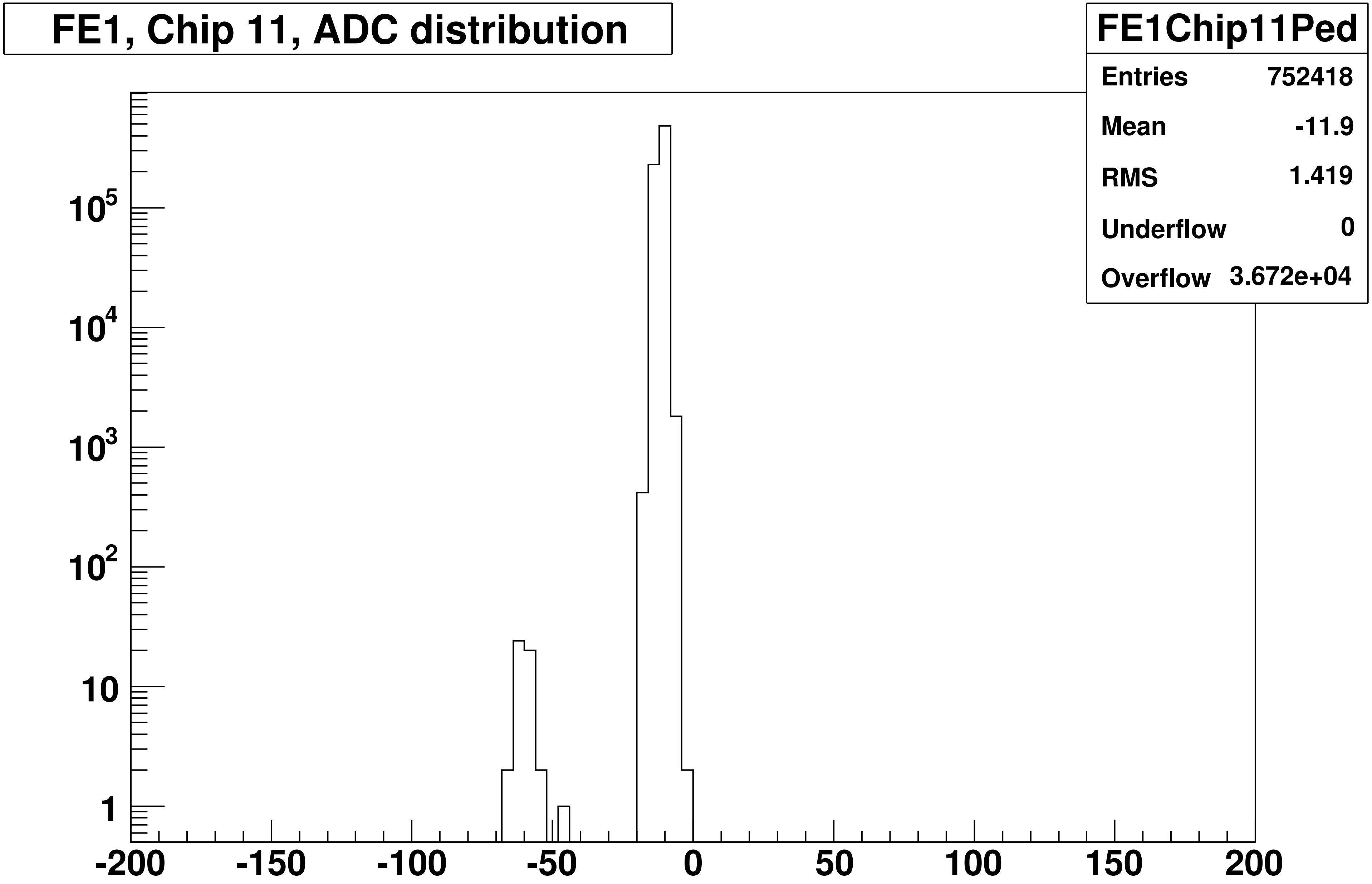
20000
25000



FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 752418
Mean -11.9
RMS 1.419
Underflow 0
Overflow 3.672e+04
$10^5$
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$10^2$
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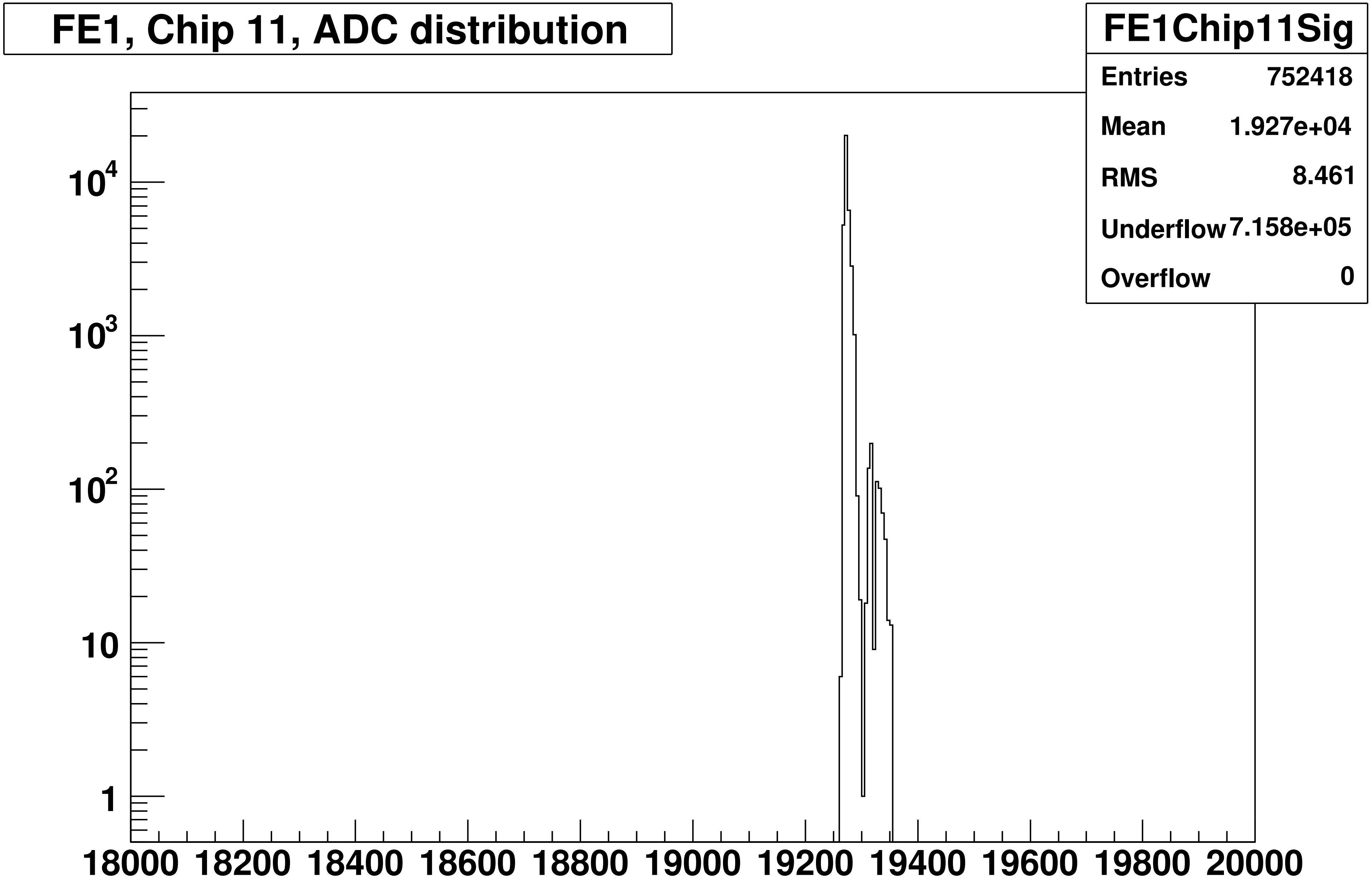
150
200



FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 752418
Mean 1.927e+04
RMS 8.461
Underflow 7.158e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
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18400
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19000
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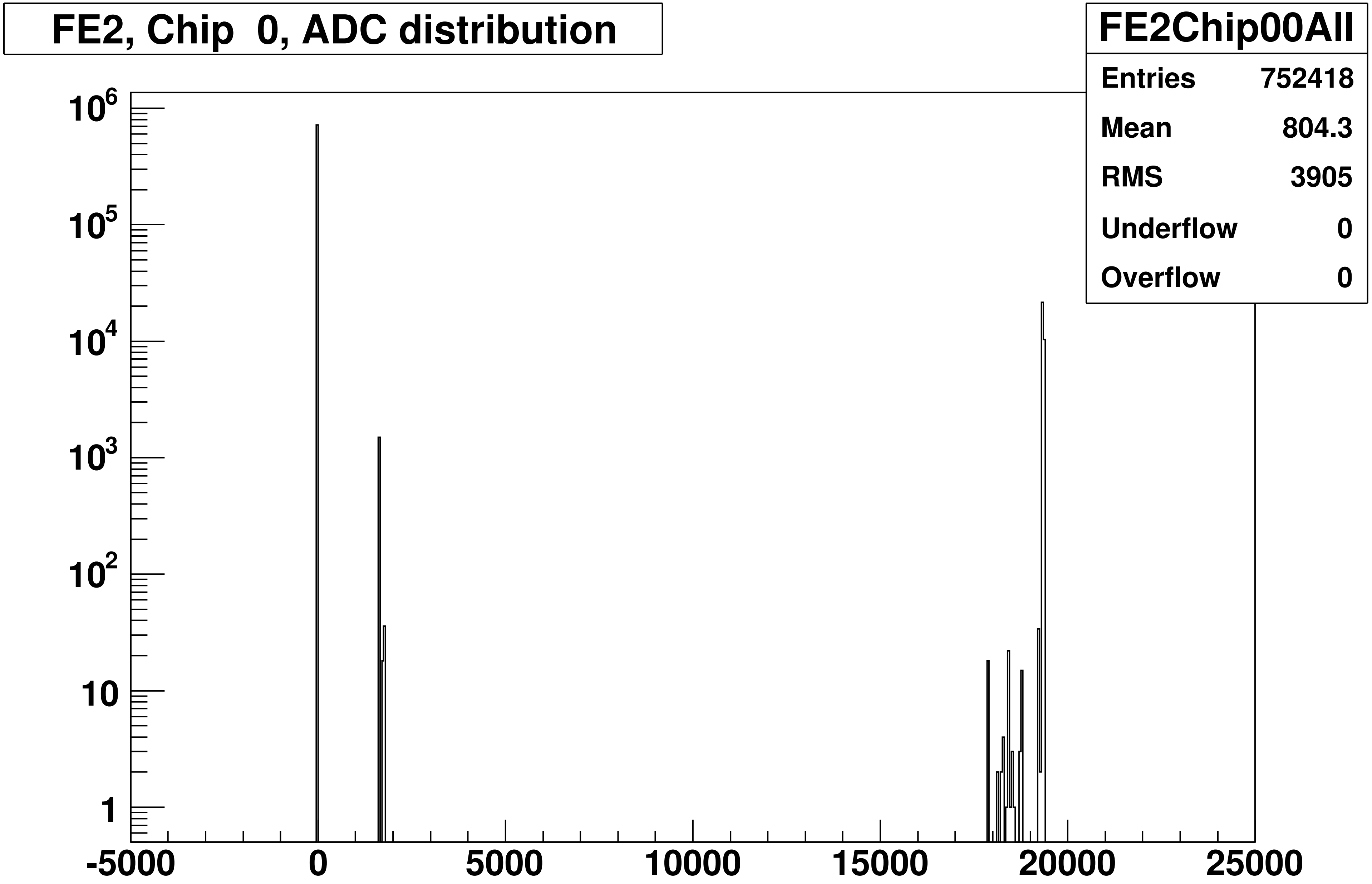
19600
19800
20000



FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 752418
Mean 804.3
RMS 3905
Underflow 0
Overflow 0
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$10^3$
$10^2$
10
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0
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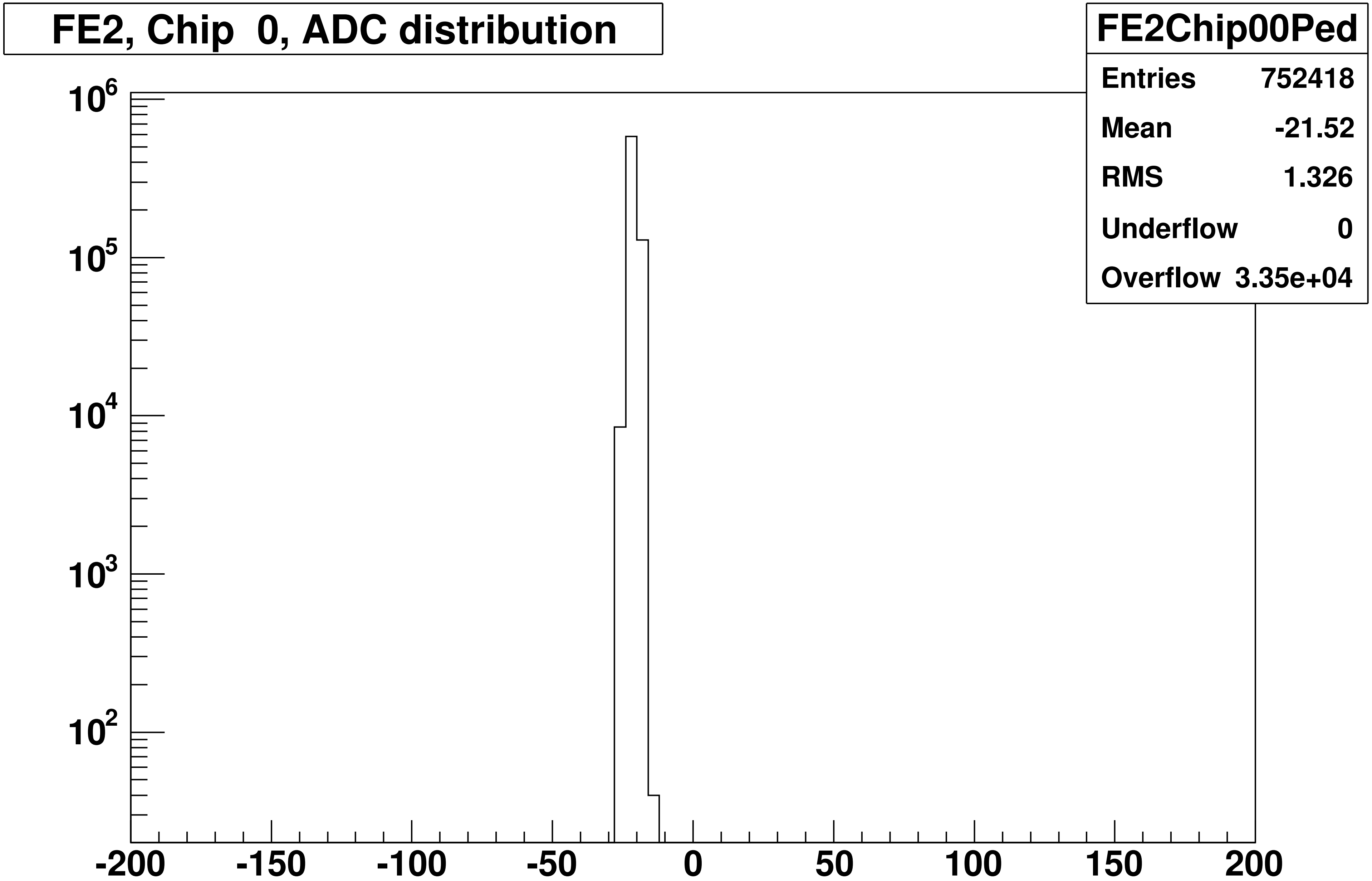
10000
15000
20000
25000


FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 752418
Mean -21.52
RMS 1.326
Underflow 0
Overflow 3.35e+04
$10^6$
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$10^2$
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-100
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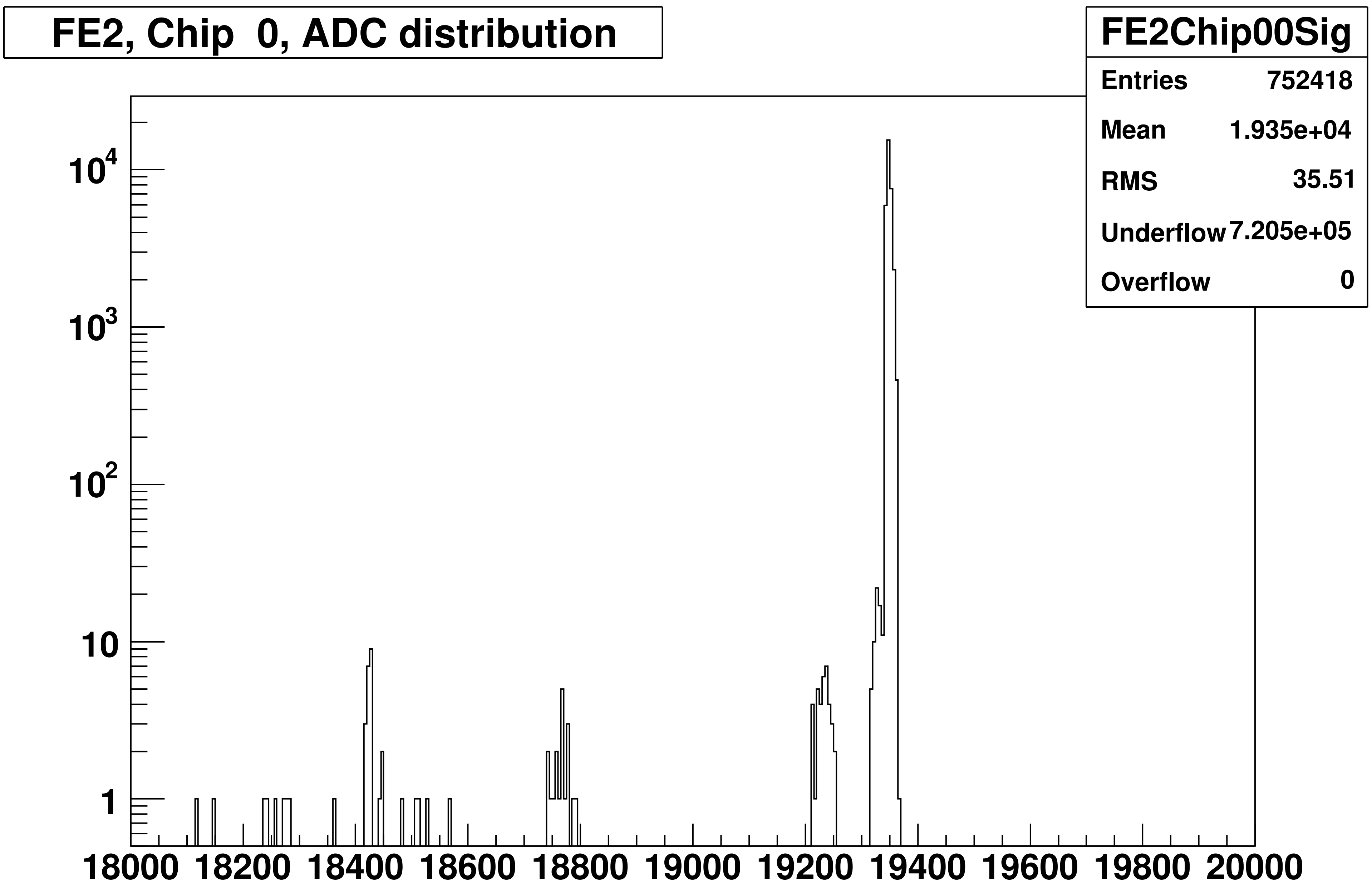
100
150
200


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 752418
Mean 1.935e+04
RMS 35.51
Underflow 7.205e+05
Overflow 0
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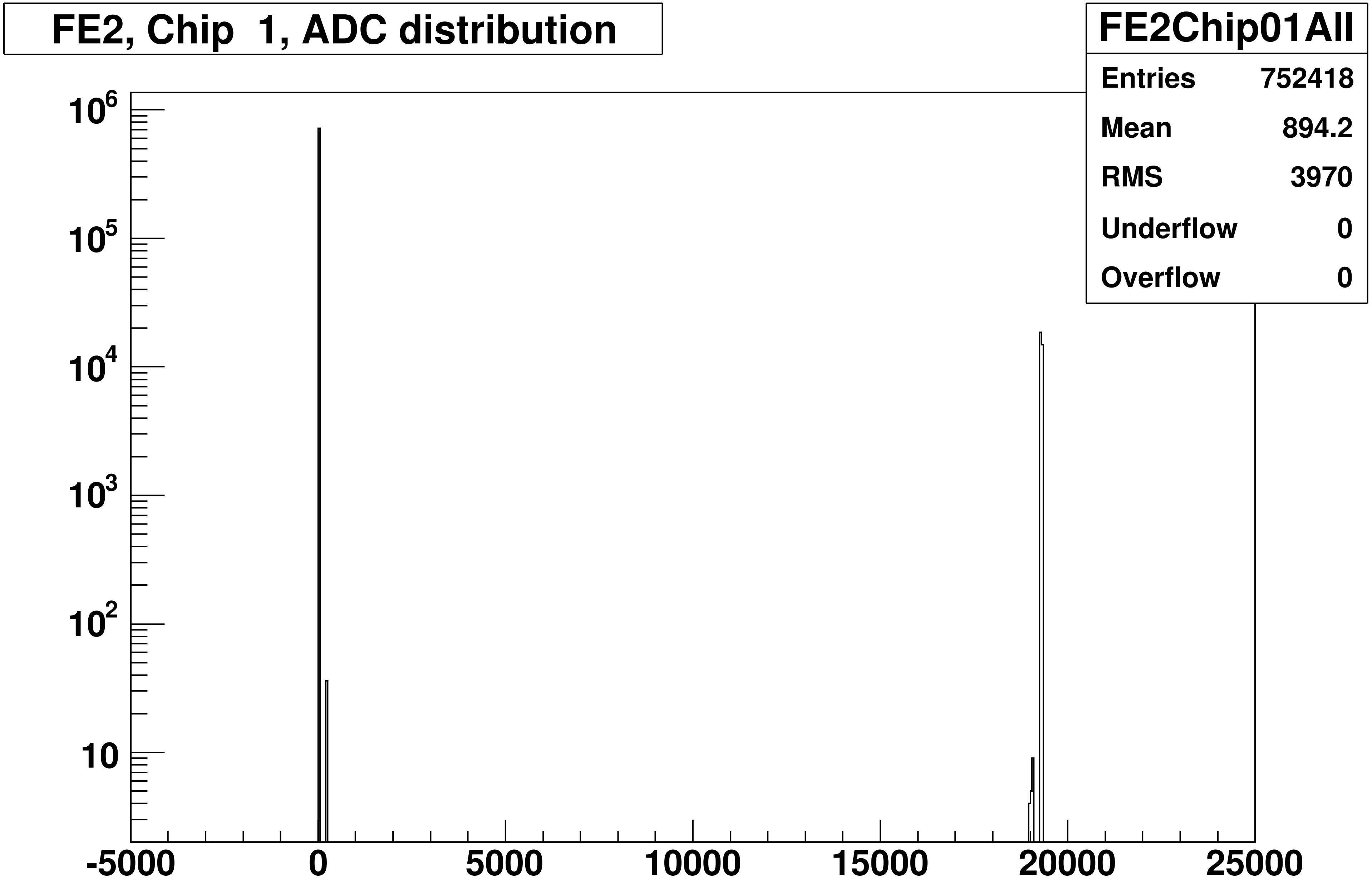
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 752418
Mean 894.2
RMS 3970
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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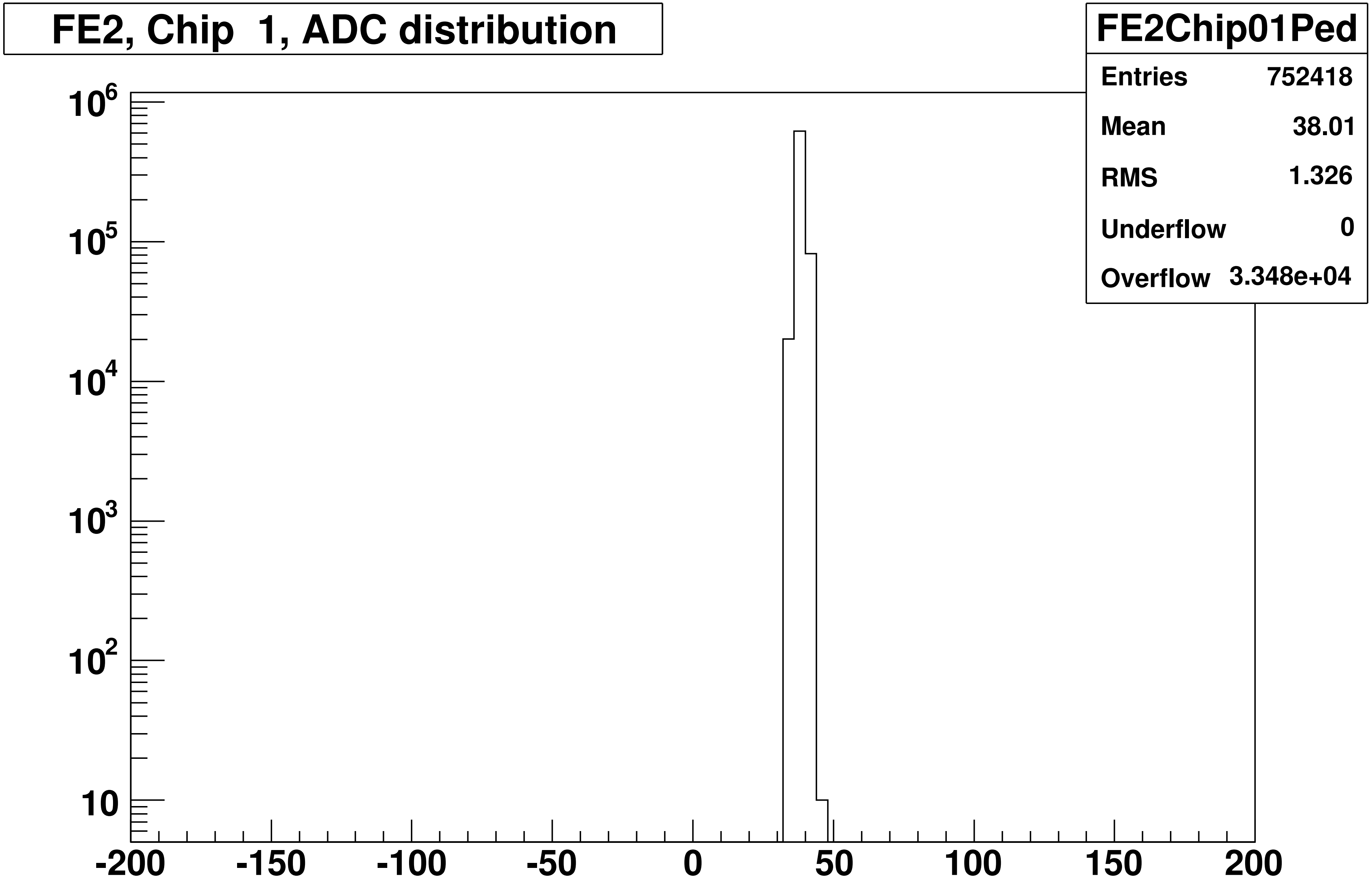
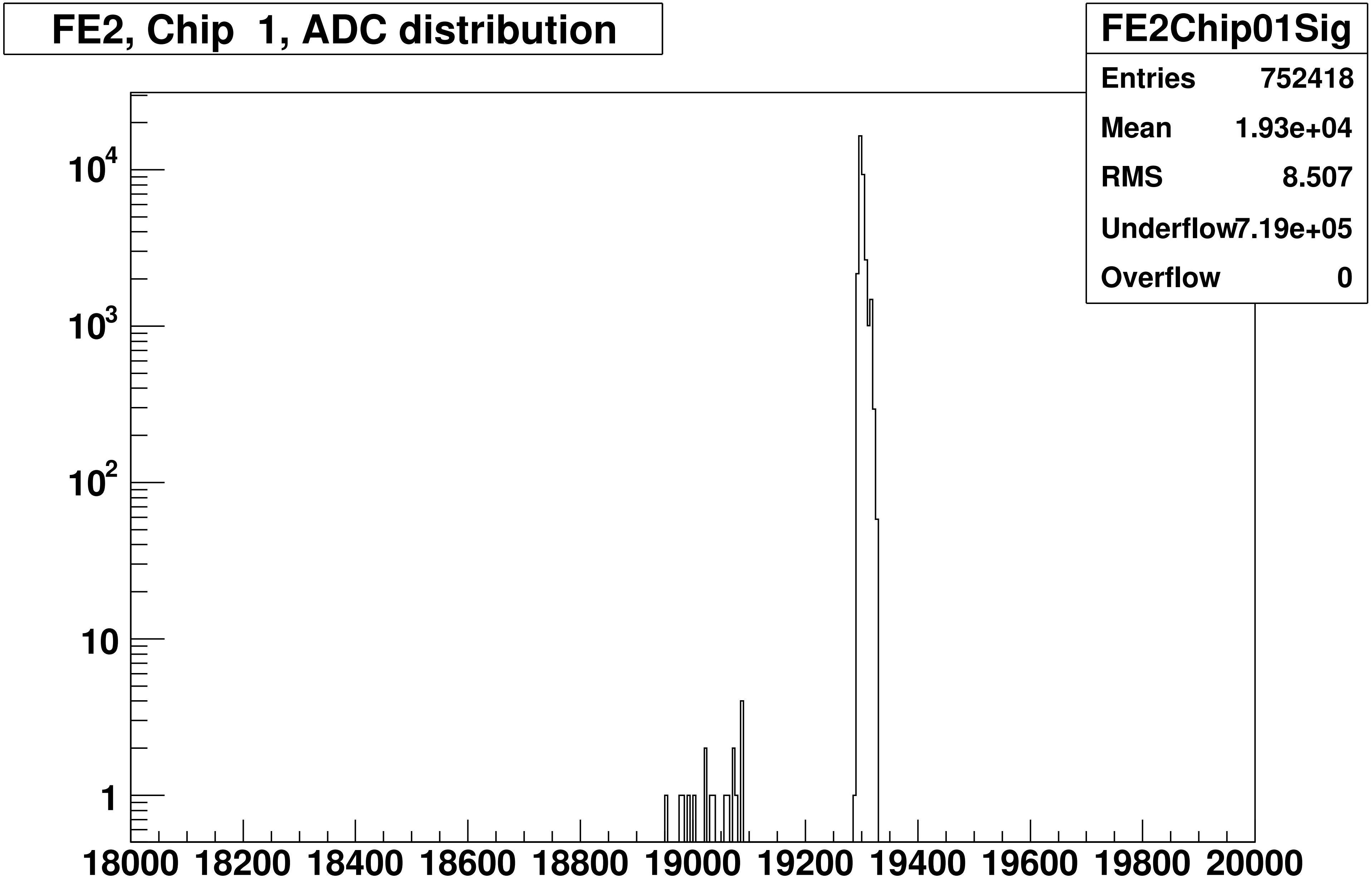
20000
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 752418
Mean 38.01
RMS 1.326
Underflow 0
Overflow 3.348e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
-200
-150
-100
-50
0
50
100
150
200


FE2, Chip  1, ADC distribution
FE2Chip01Sig
Entries 752418
Mean 1.93e+04
RMS 8.507
Underflow 7.19e+05
Overflow 0
$10^4$
$10^3$
$10^2$
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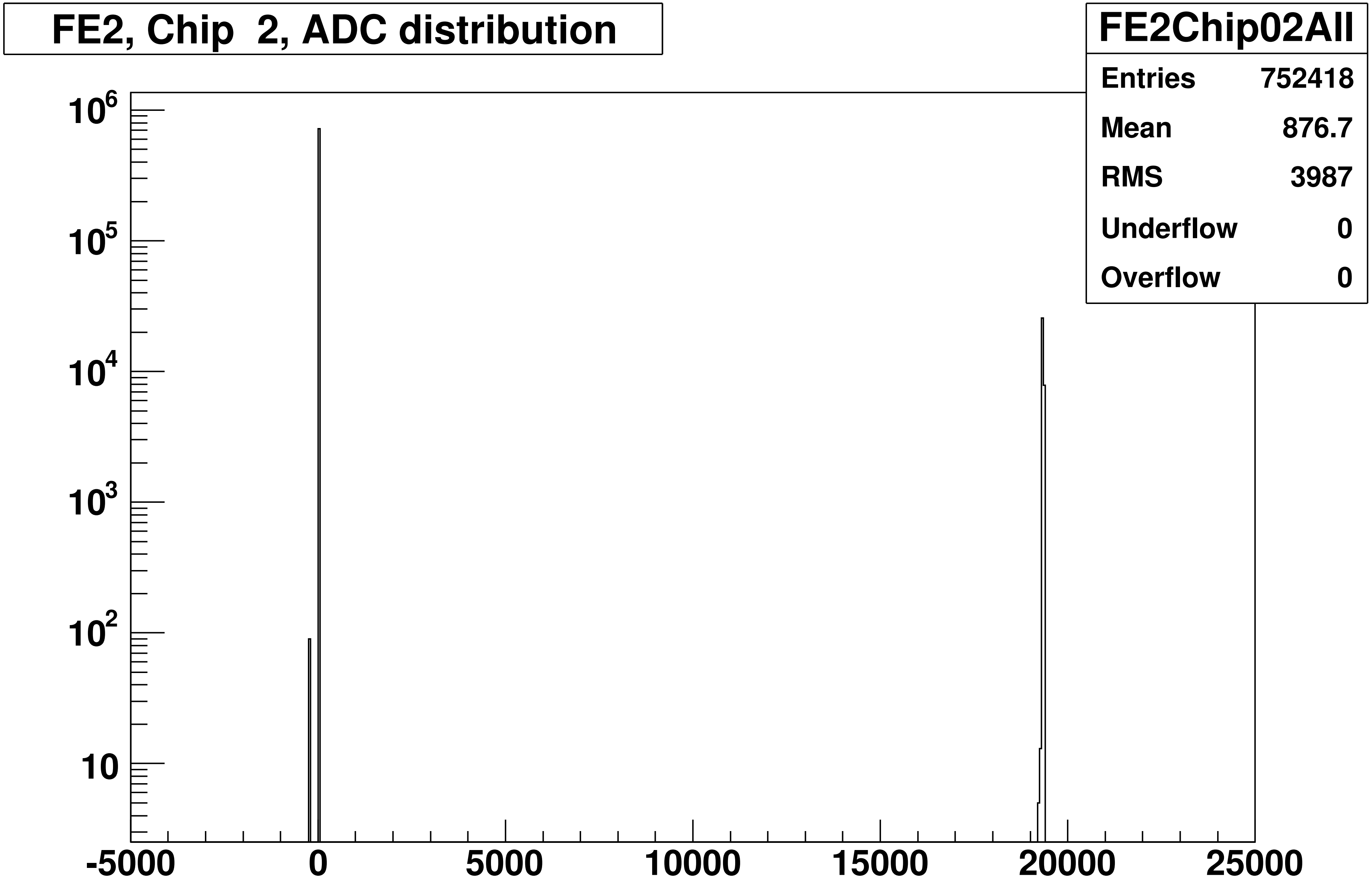
FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 752418
Mean 876.7
RMS 3987
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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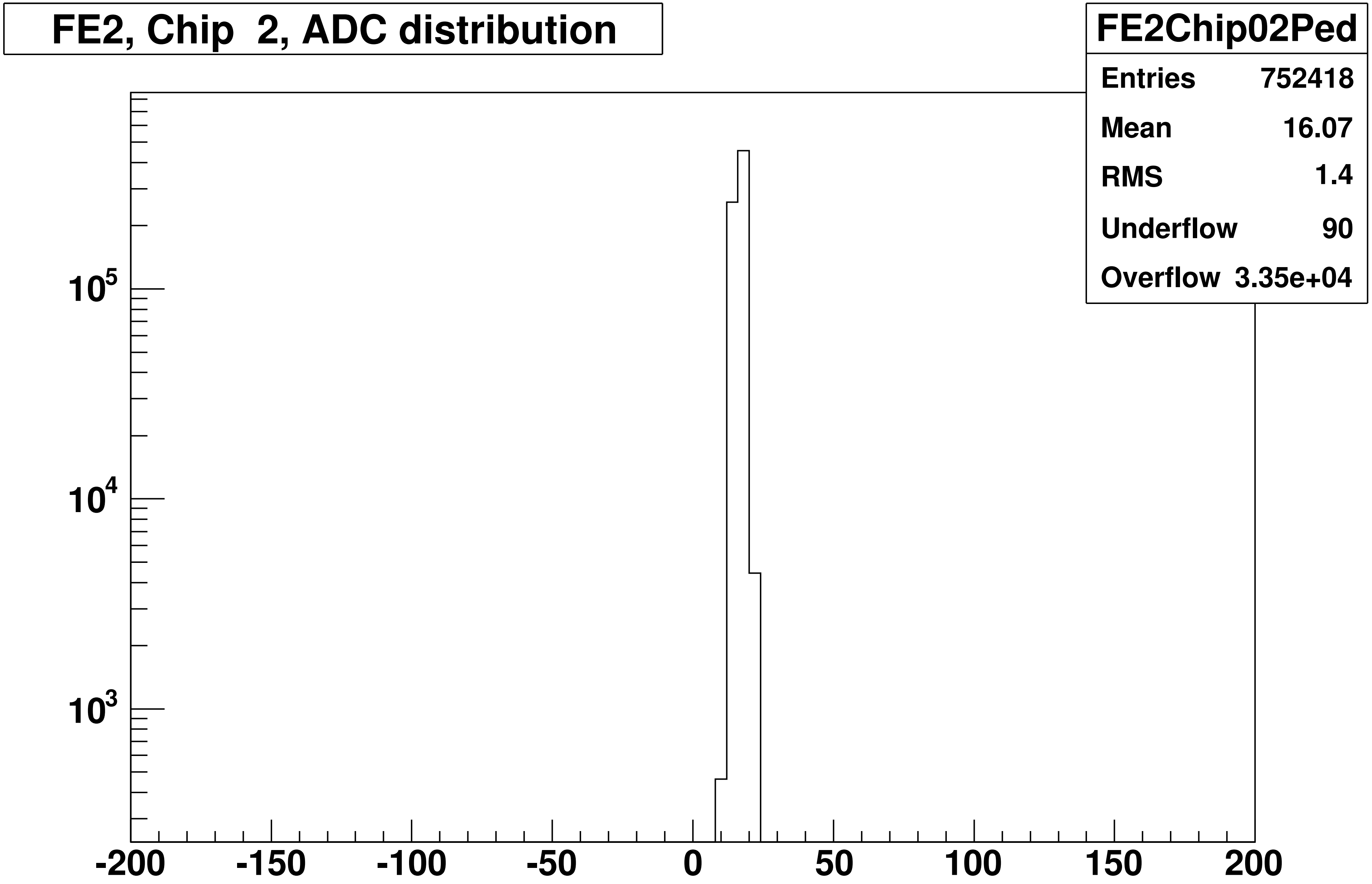

FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 752418
Mean 16.07
RMS 1.4
Underflow 90
Overflow 3.35e+04
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150
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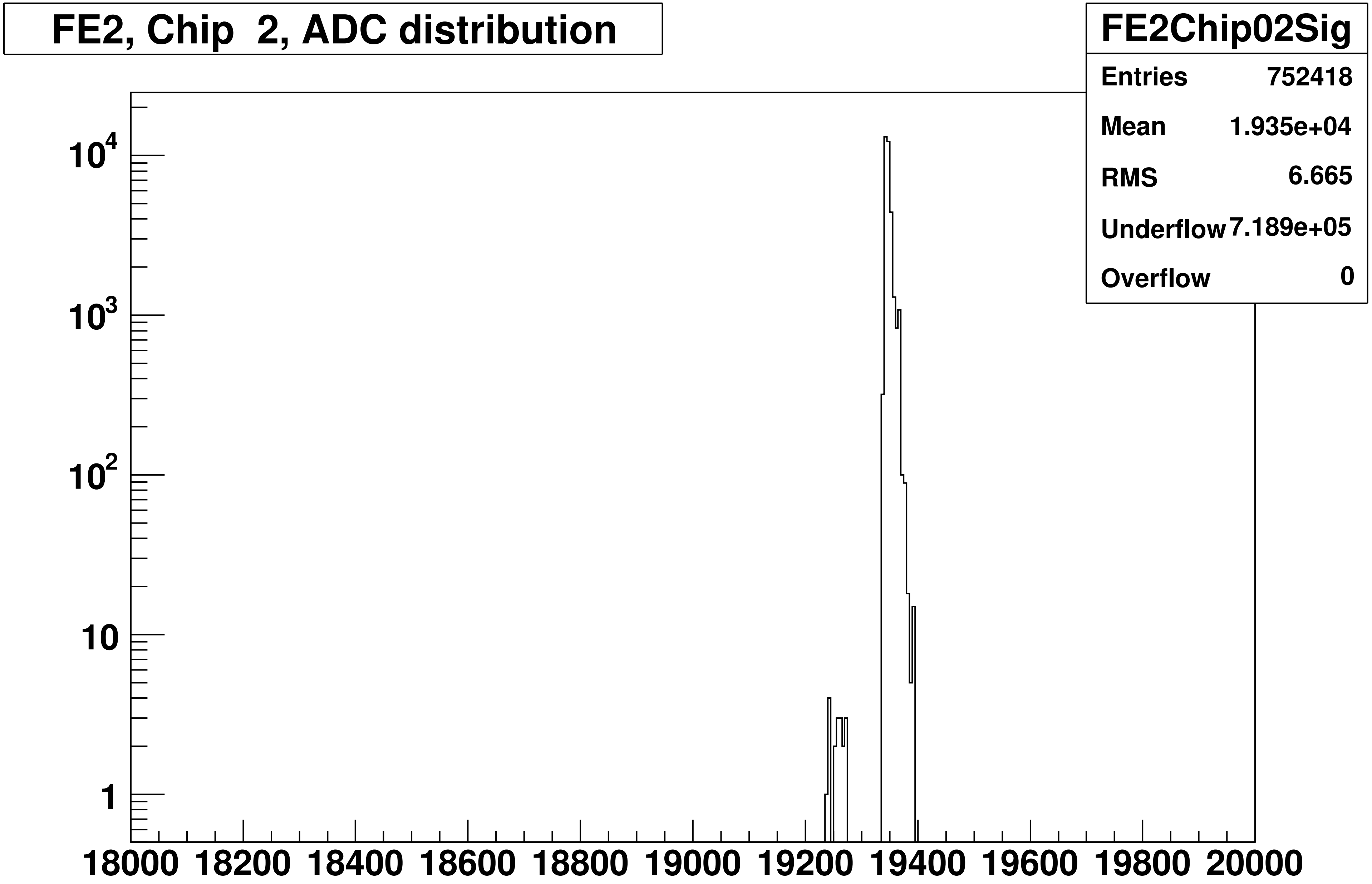
FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 752418
Mean 1.935e+04
RMS 6.665
Underflow 7.189e+05
Overflow 0
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$10^2$
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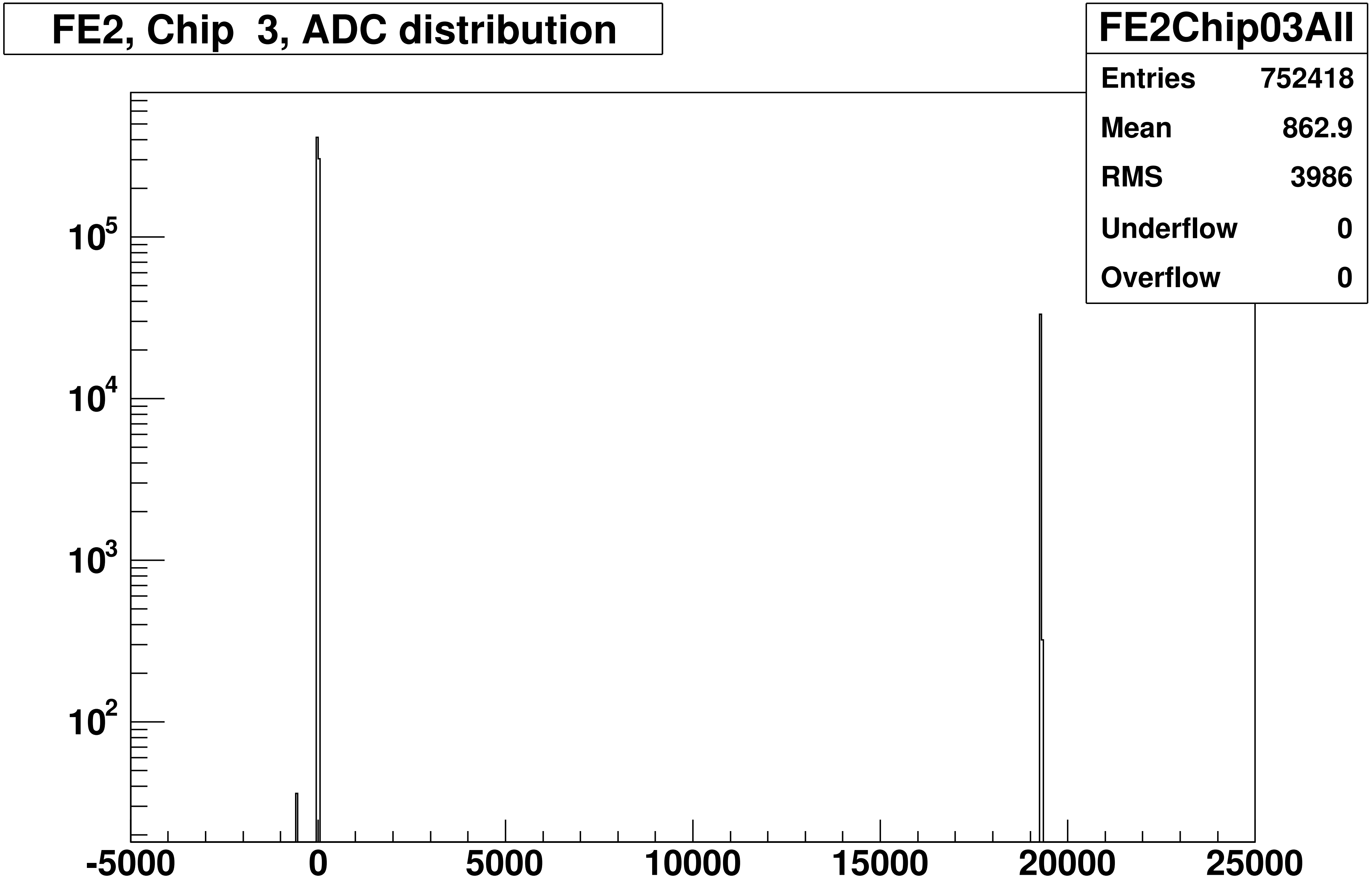

FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 752418
Mean 862.9
RMS 3986
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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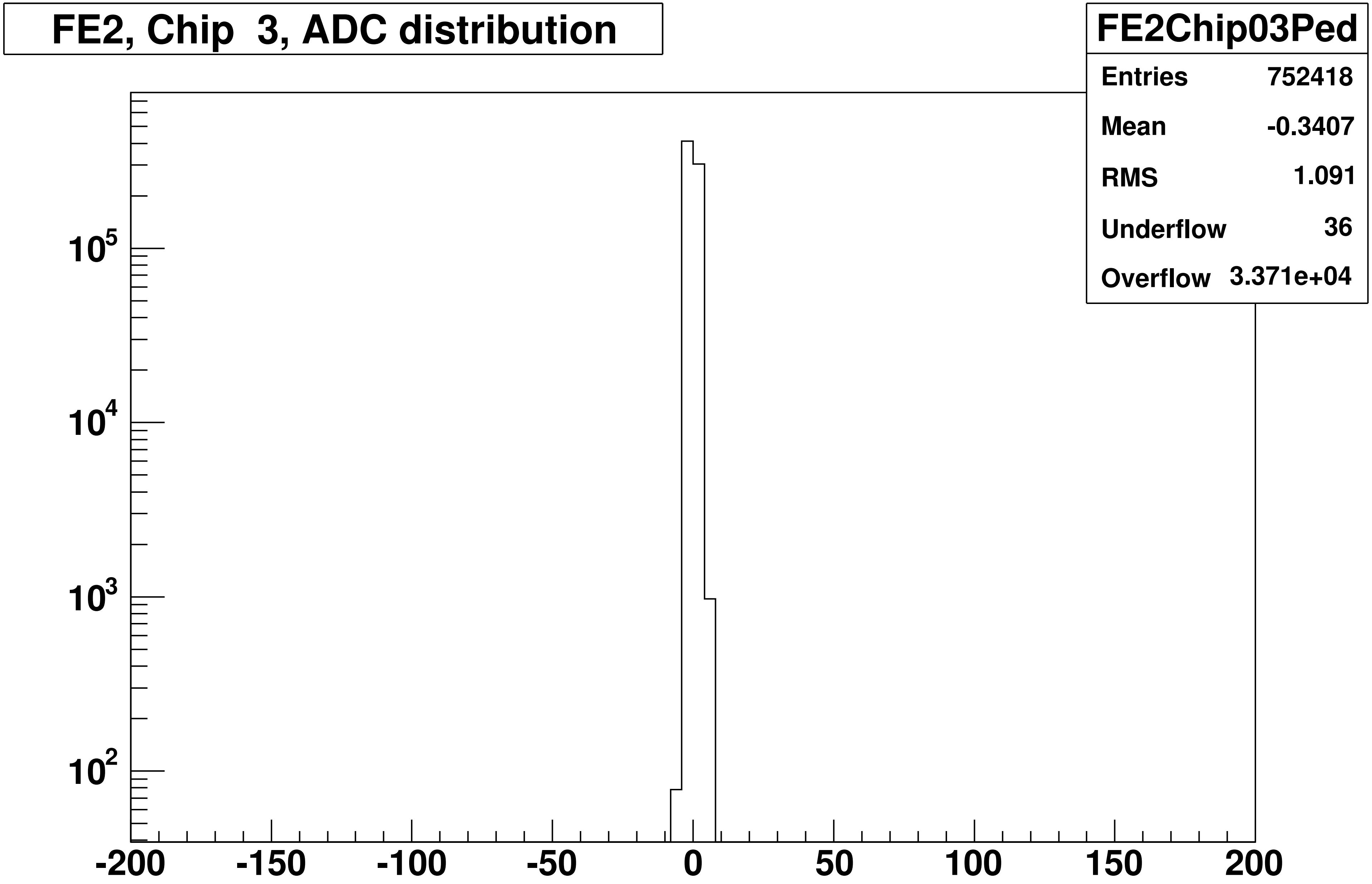
FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 752418
Mean -0.3407
RMS 1.091
Underflow 36
Overflow 3.371e+04
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$10^3$
$10^2$
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150
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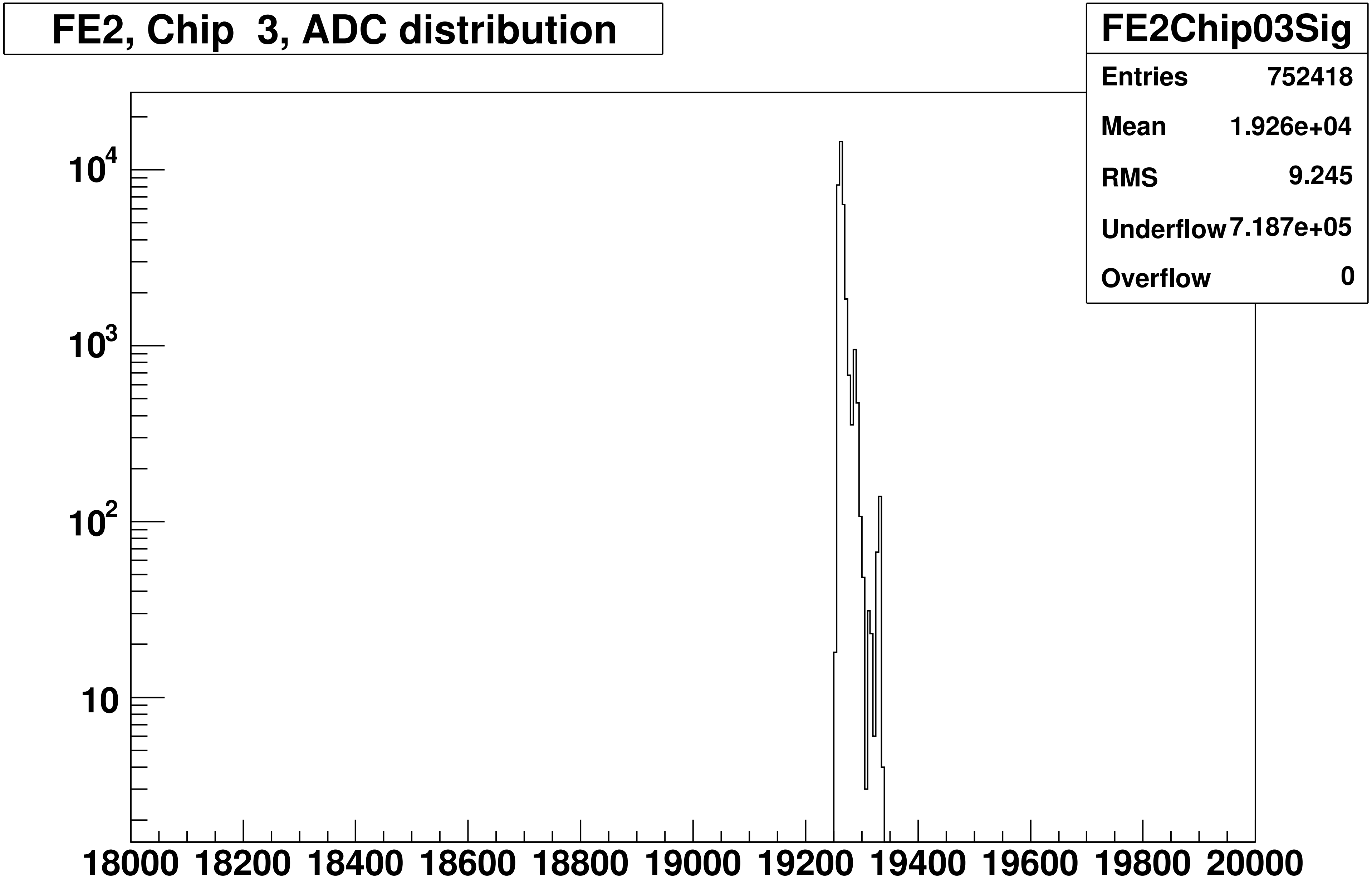
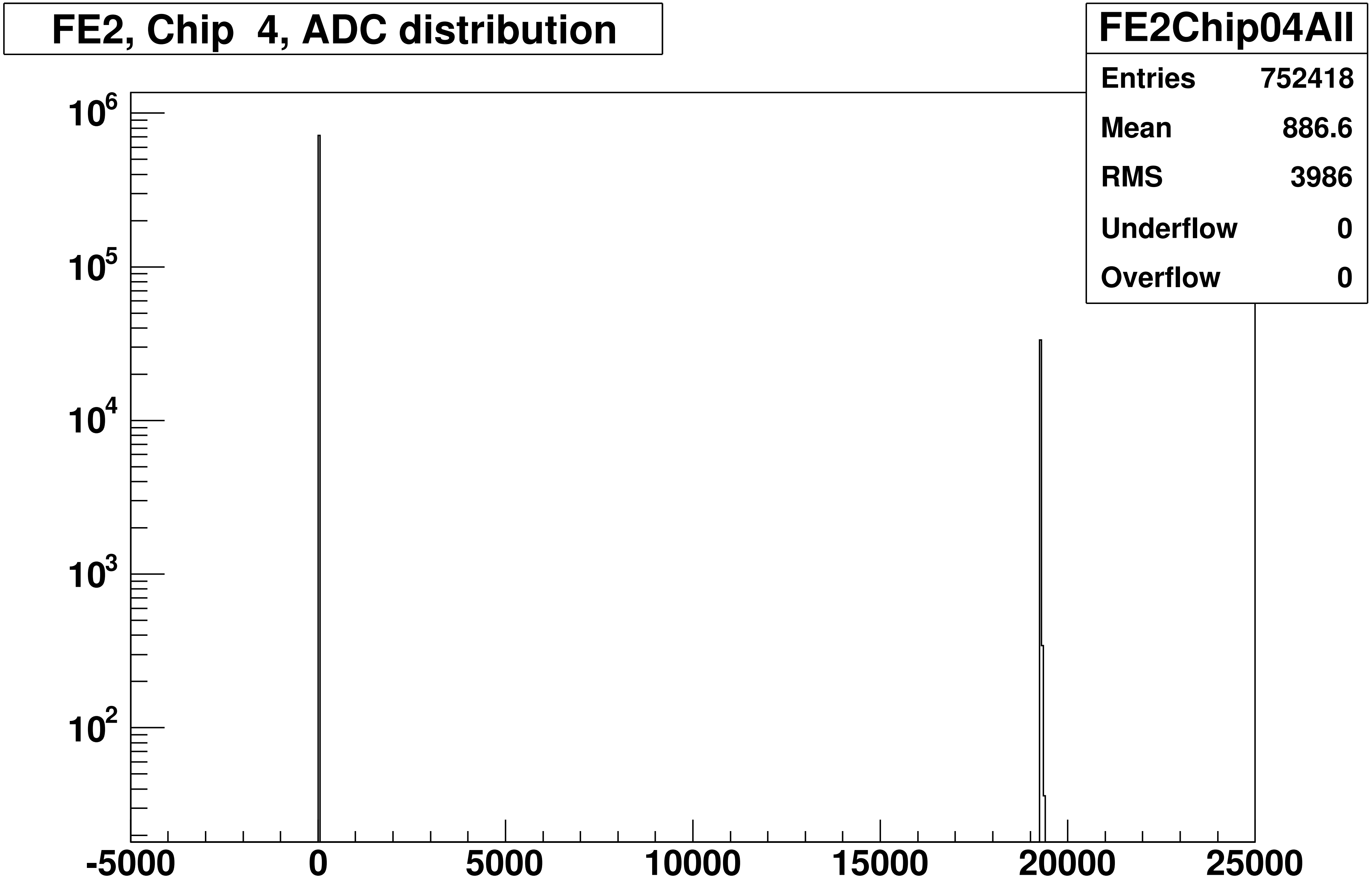


FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 752418
Mean 1.926e+04
RMS 9.245
Underflow 7.187e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 752418
Mean 886.6
RMS 3986
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
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10000
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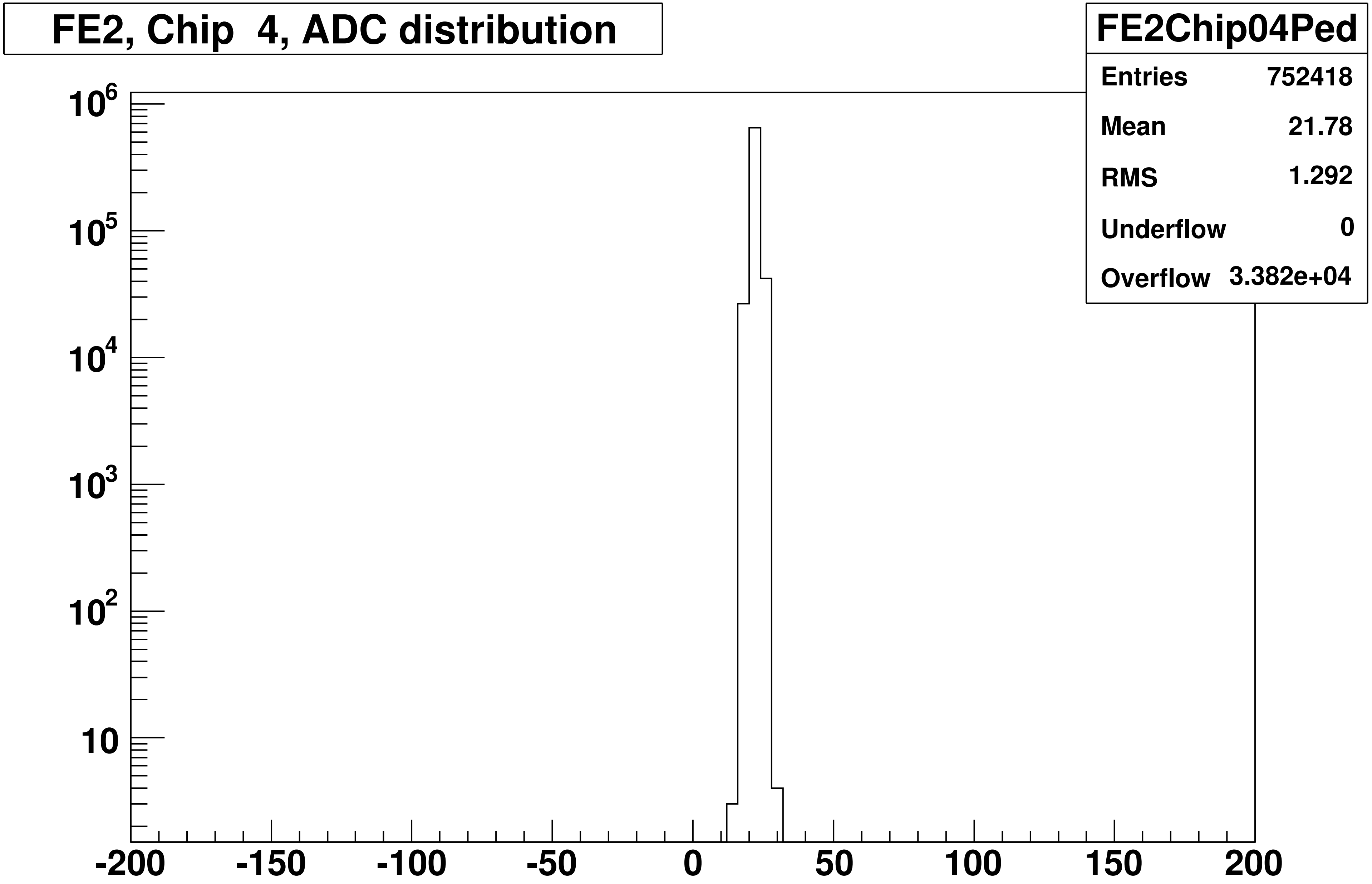
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 752418
Mean 21.78
RMS 1.292
Underflow 0
Overflow 3.382e+04
$10^6$
$10^5$
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$10^3$
$10^2$
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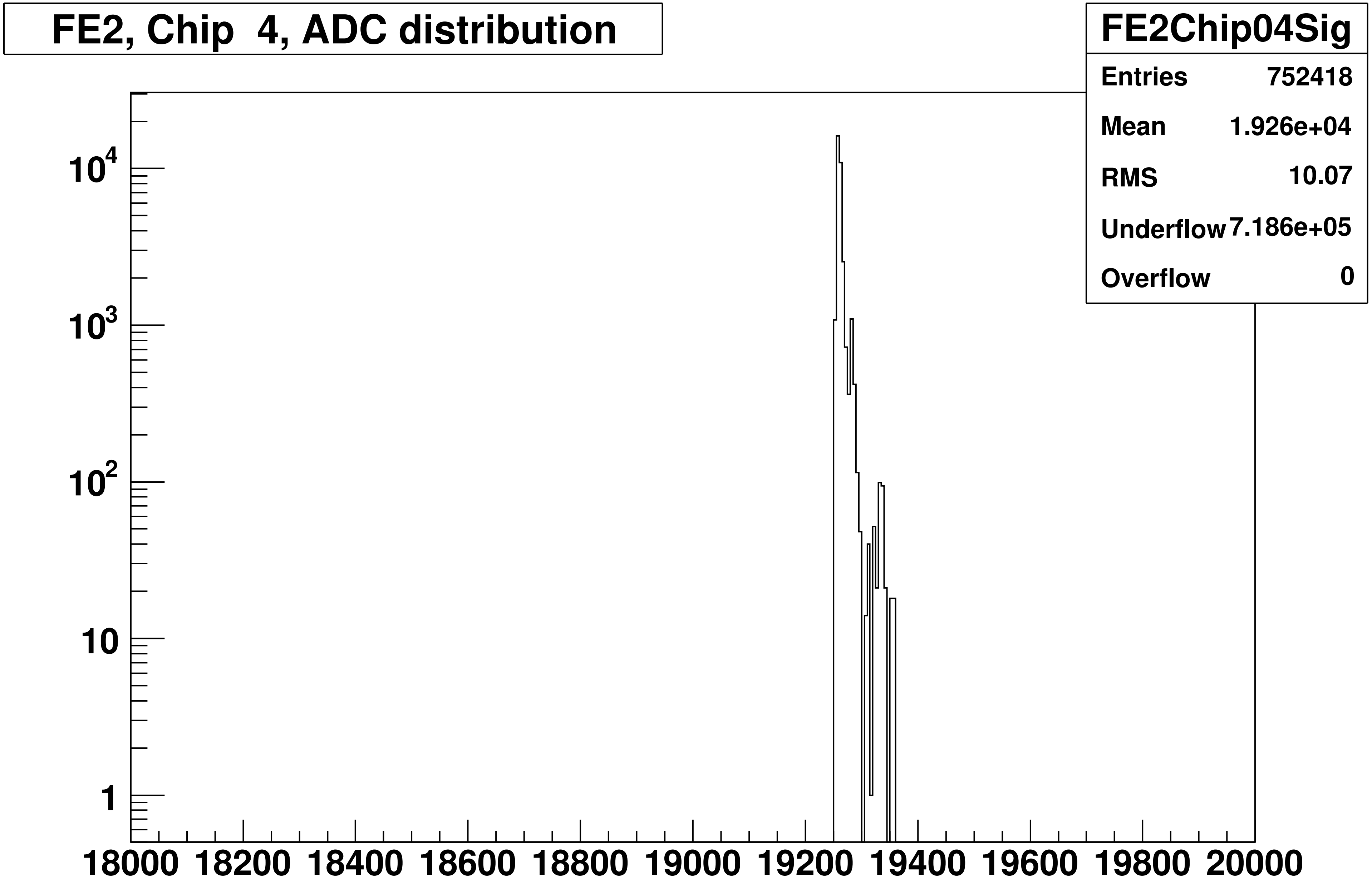
-100
-50
0
50
100
150
200



FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 752418
Mean 1.926e+04
RMS 10.07
Underflow 7.186e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
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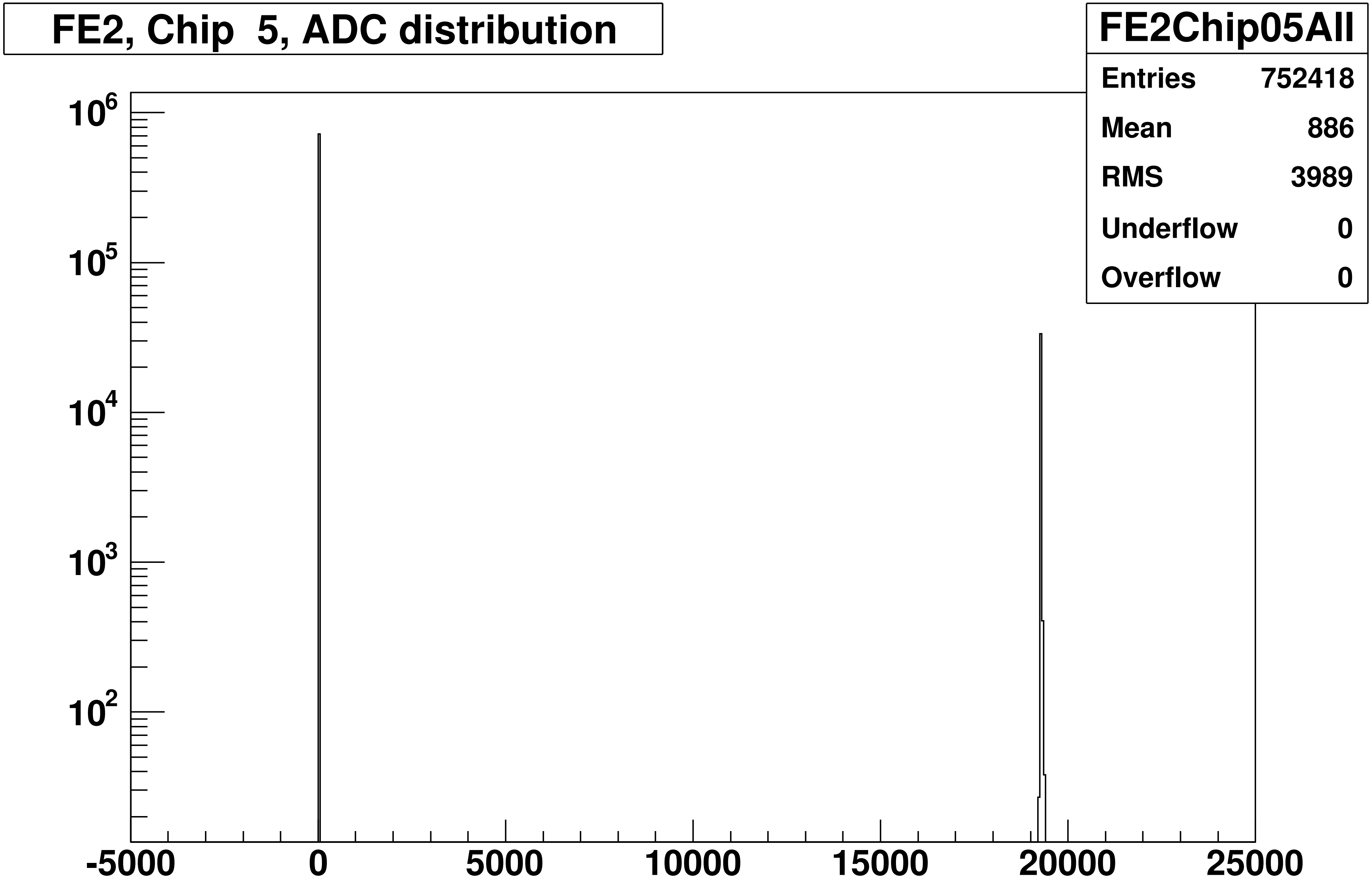
FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 752418
Mean 886
RMS 3989
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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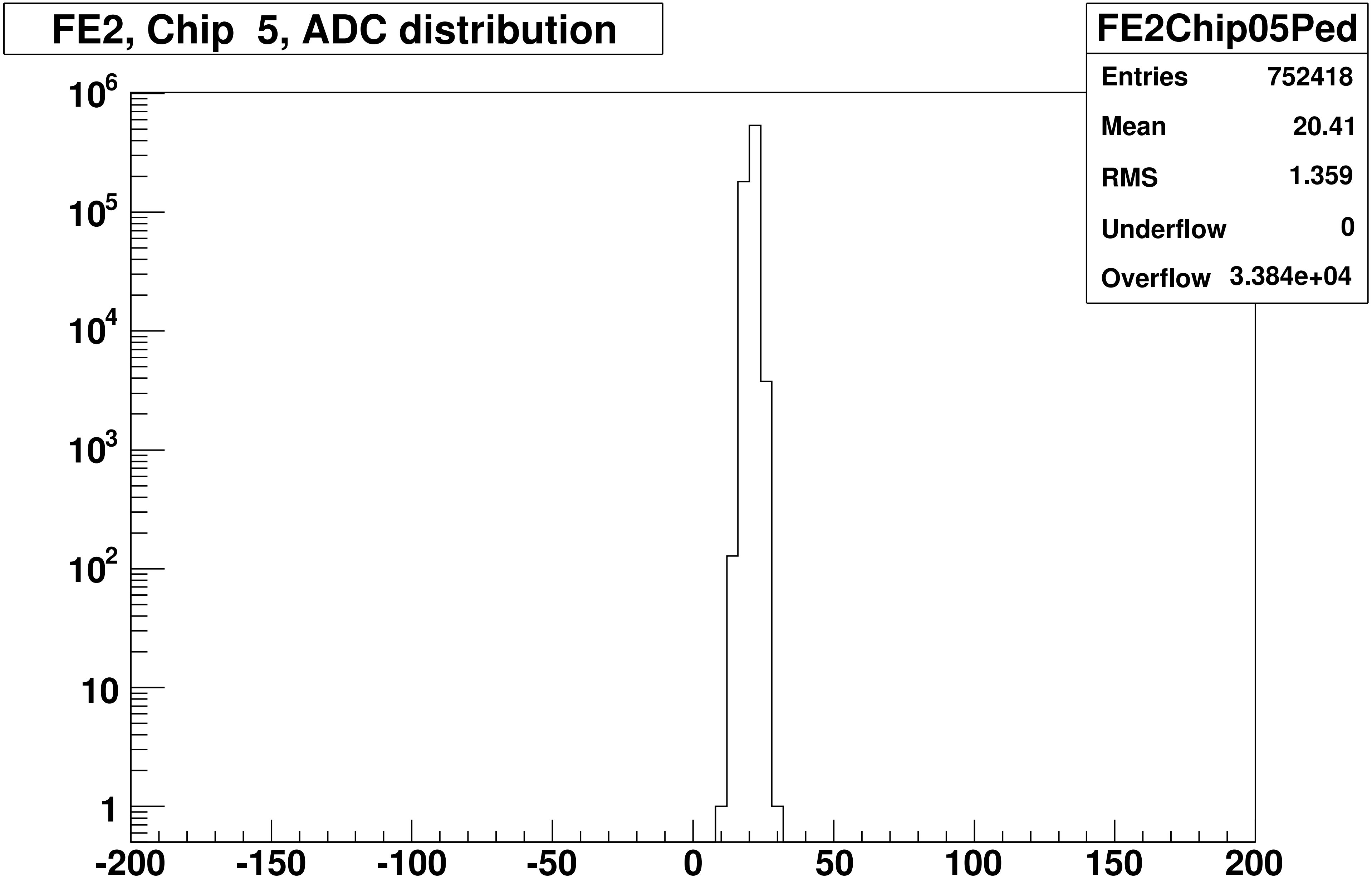
25000



FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 752418
Mean 20.41
RMS 1.359
Underflow 0
Overflow 3.384e+04
$10^6$
$10^5$
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$10^3$
$10^2$
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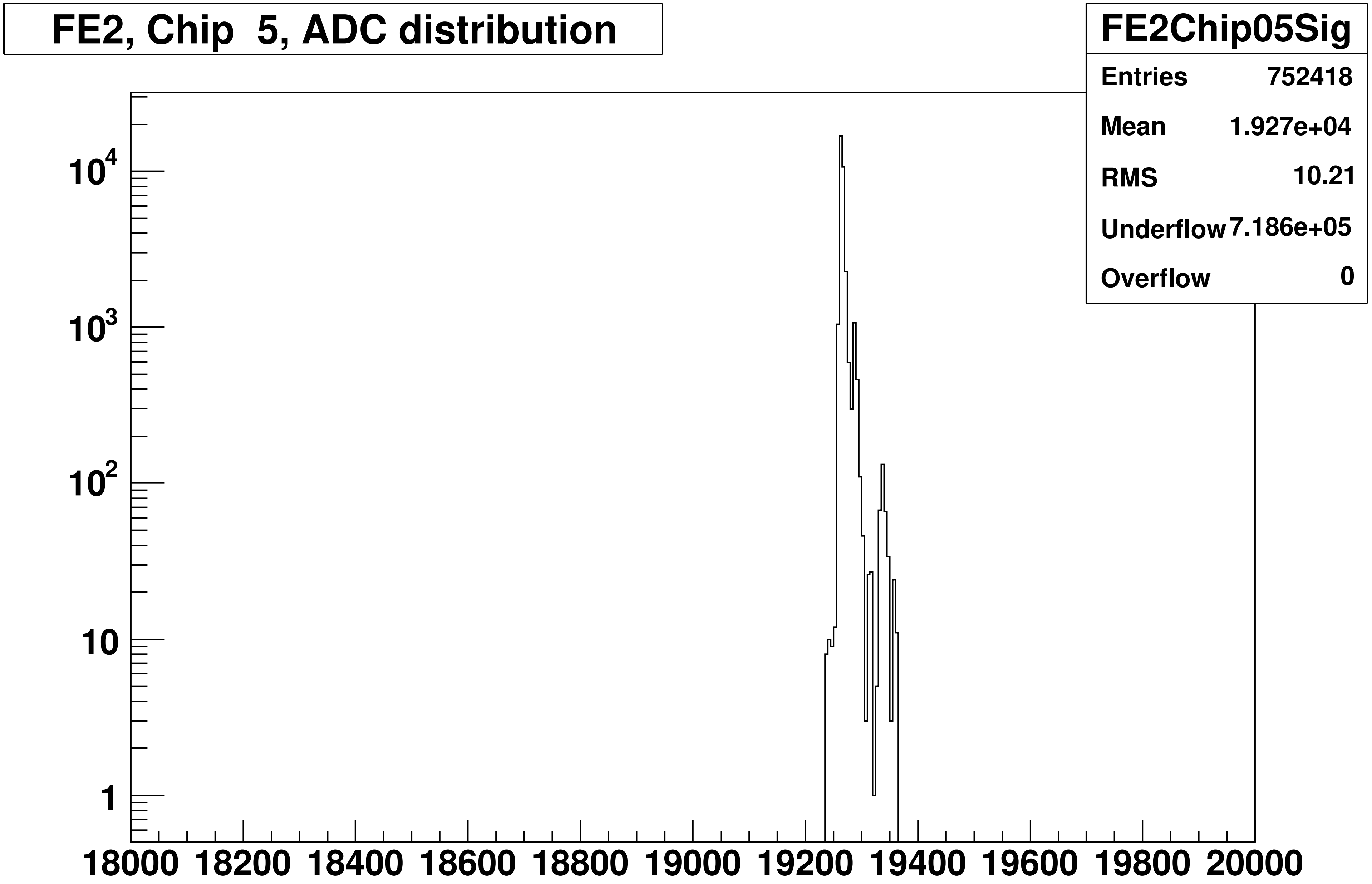
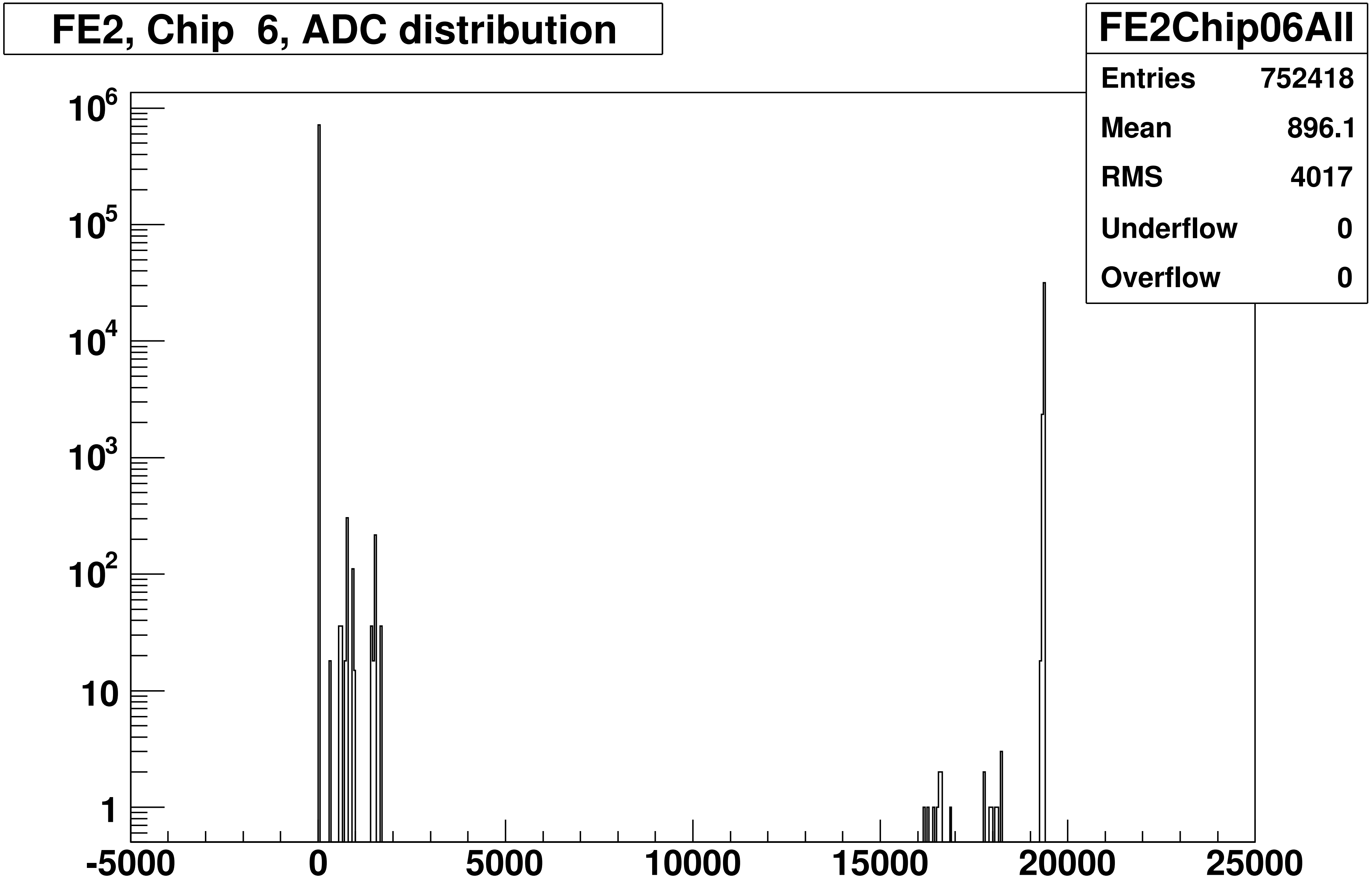
50
100
150
200


FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 752418
Mean 1.927e+04
RMS 10.21
Underflow 7.186e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 752418
Mean 896.1
RMS 4017
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
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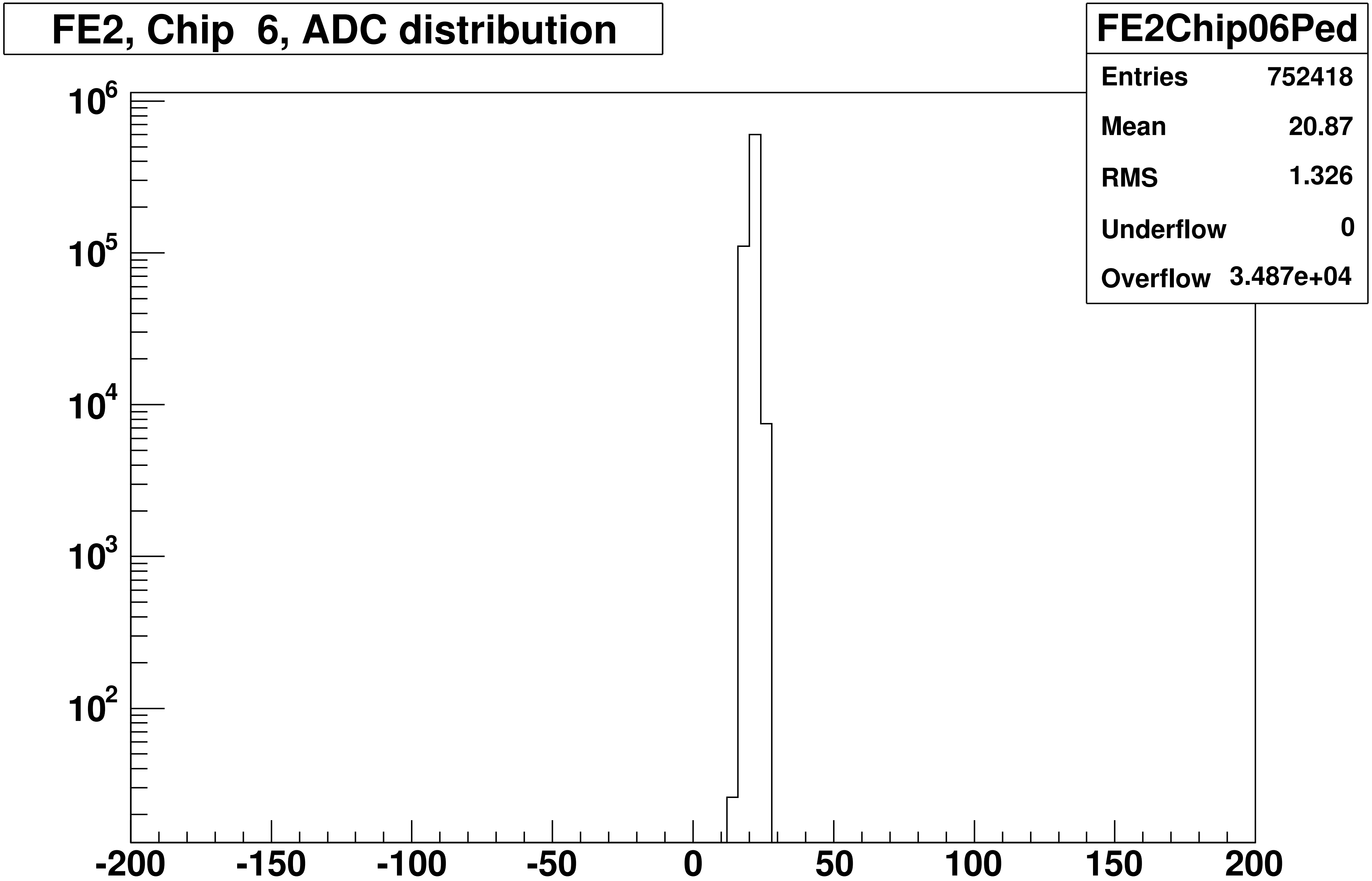
20000
25000



FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 752418
Mean 20.87
RMS 1.326
Underflow 0
Overflow 3.487e+04
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10^5
10^4
10^3
10^2
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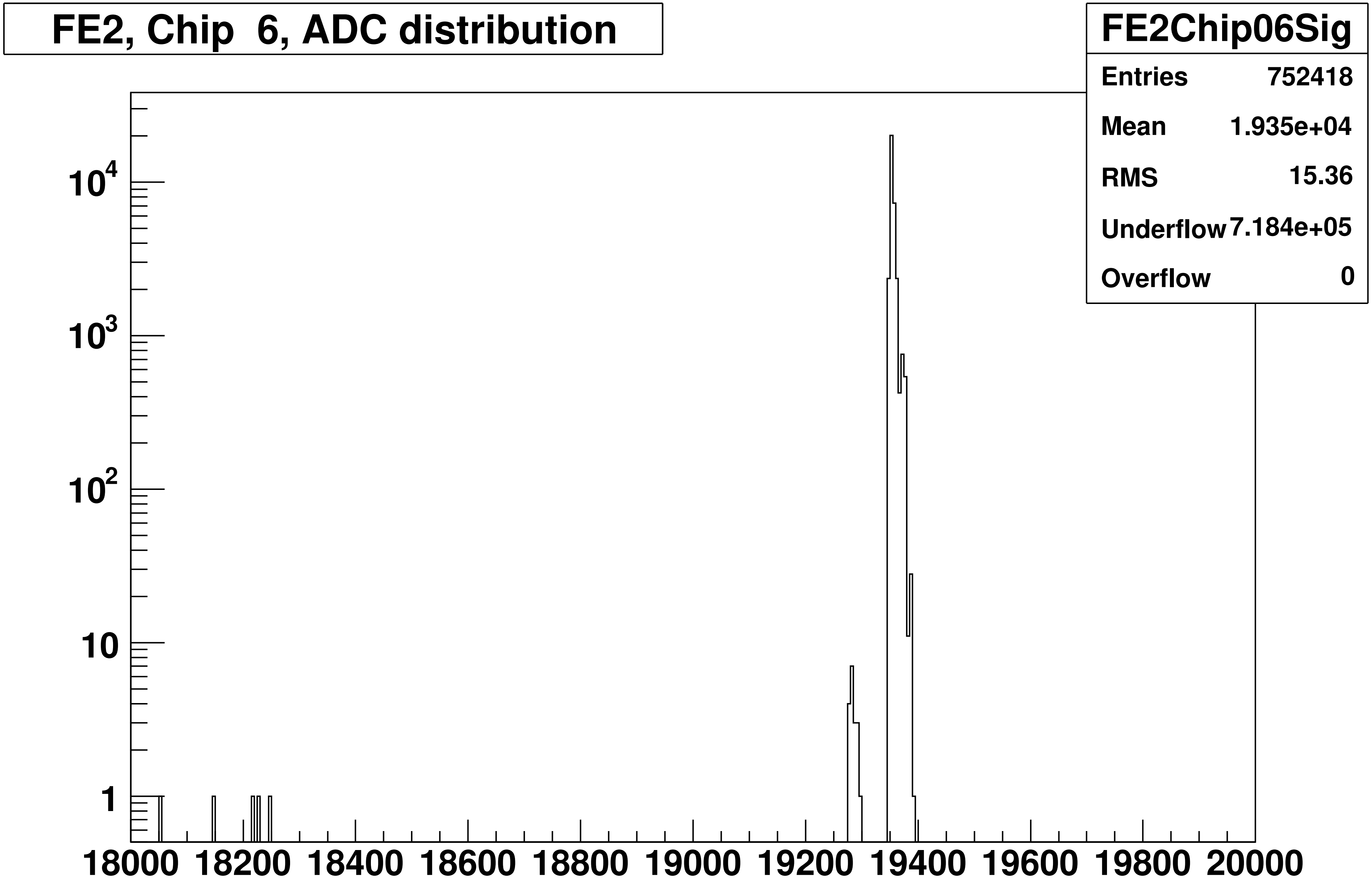
-150
-100
-50
0
50
100
150
200


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 752418
Mean 1.935e+04
RMS 15.36
Underflow 7.184e+05
Overflow 0
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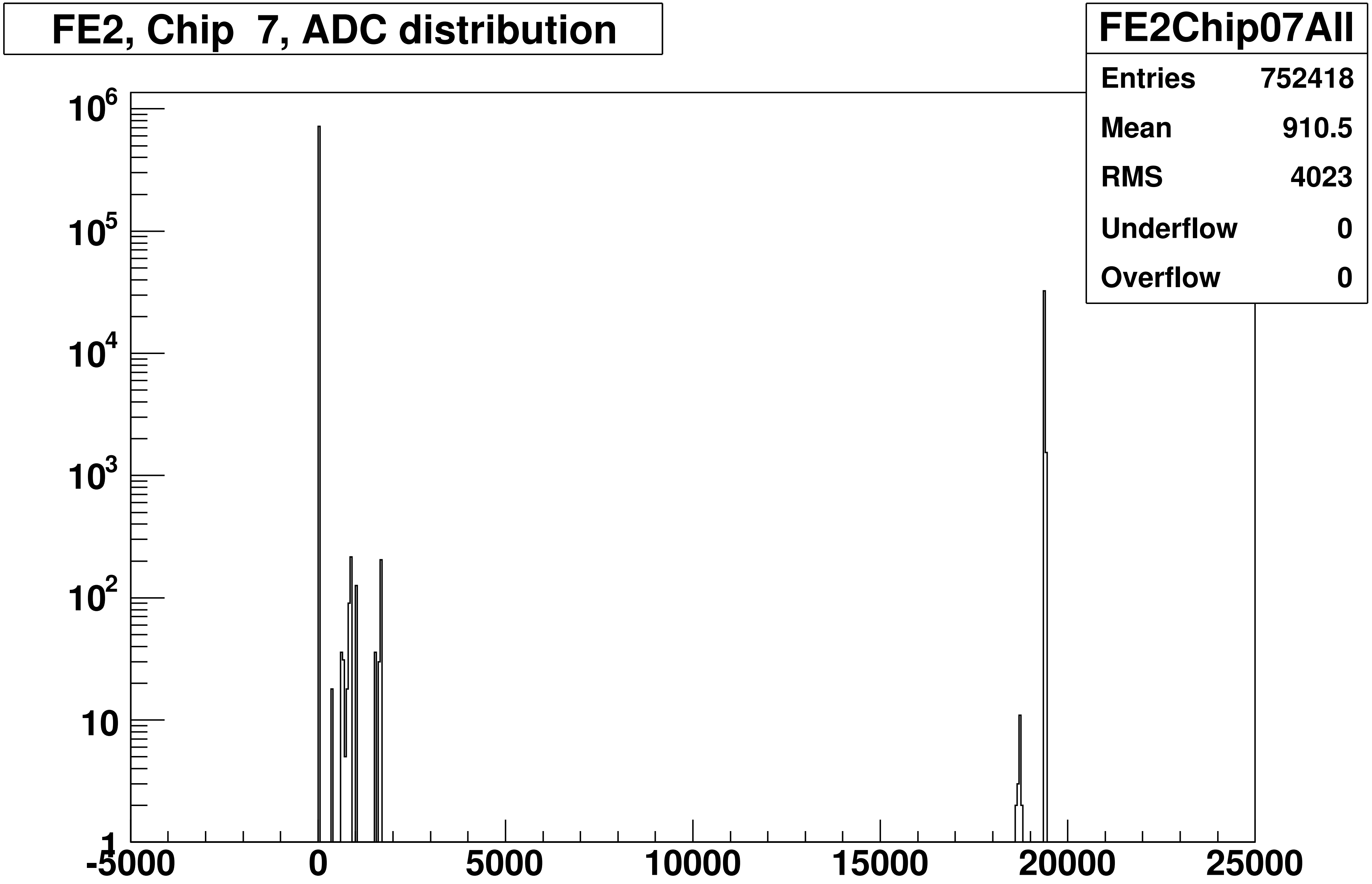
$10^3$
$10^2$
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 752418
Mean 910.5
RMS 4023
Underflow 0
Overflow 0
$10^6$
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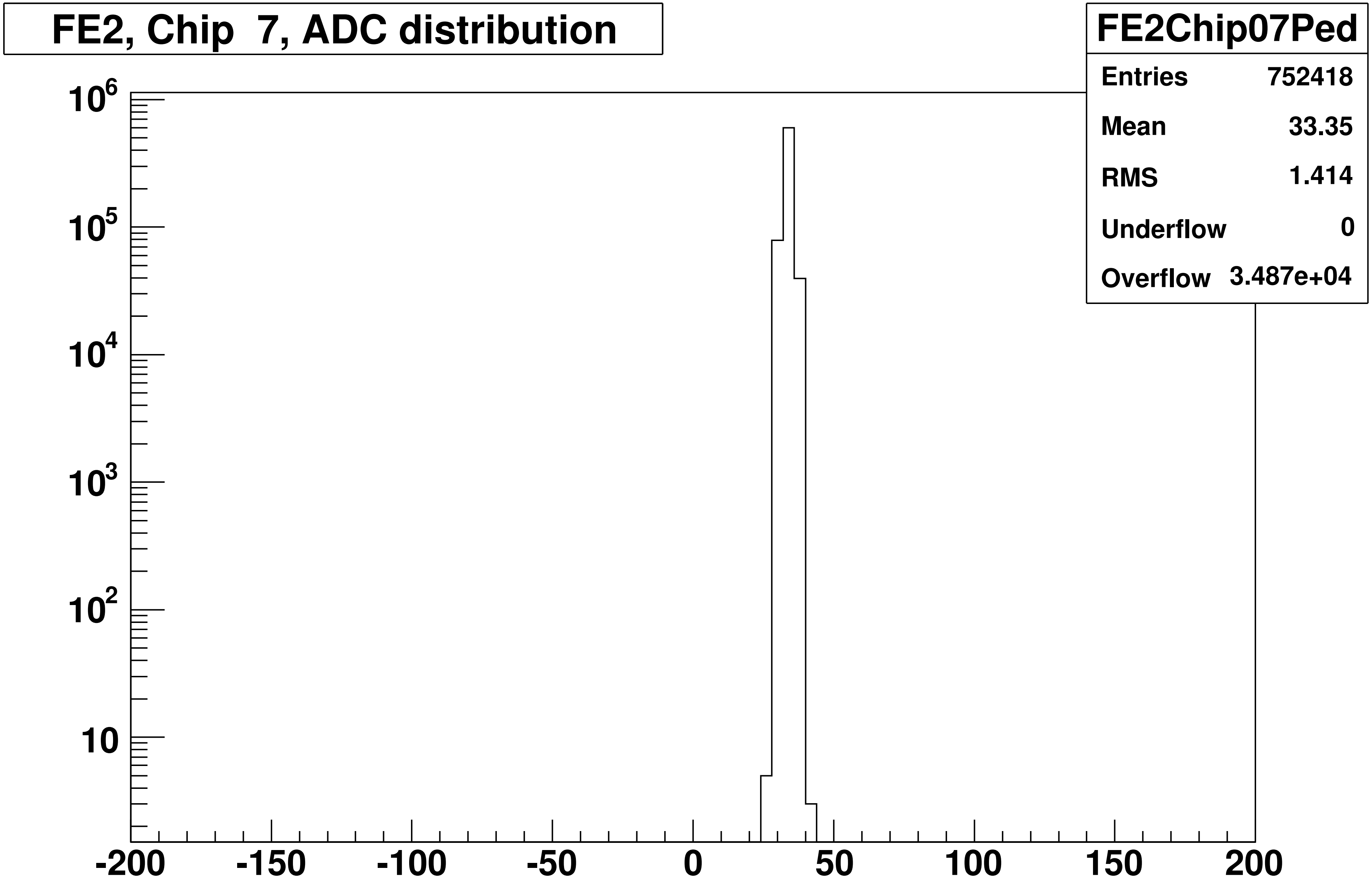
$10^4$
$10^3$
$10^2$
10
1
-5000
0
5000
10000
15000
20000
25000


FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 752418
Mean 33.35
RMS 1.414
Underflow 0
Overflow 3.487e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
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-100
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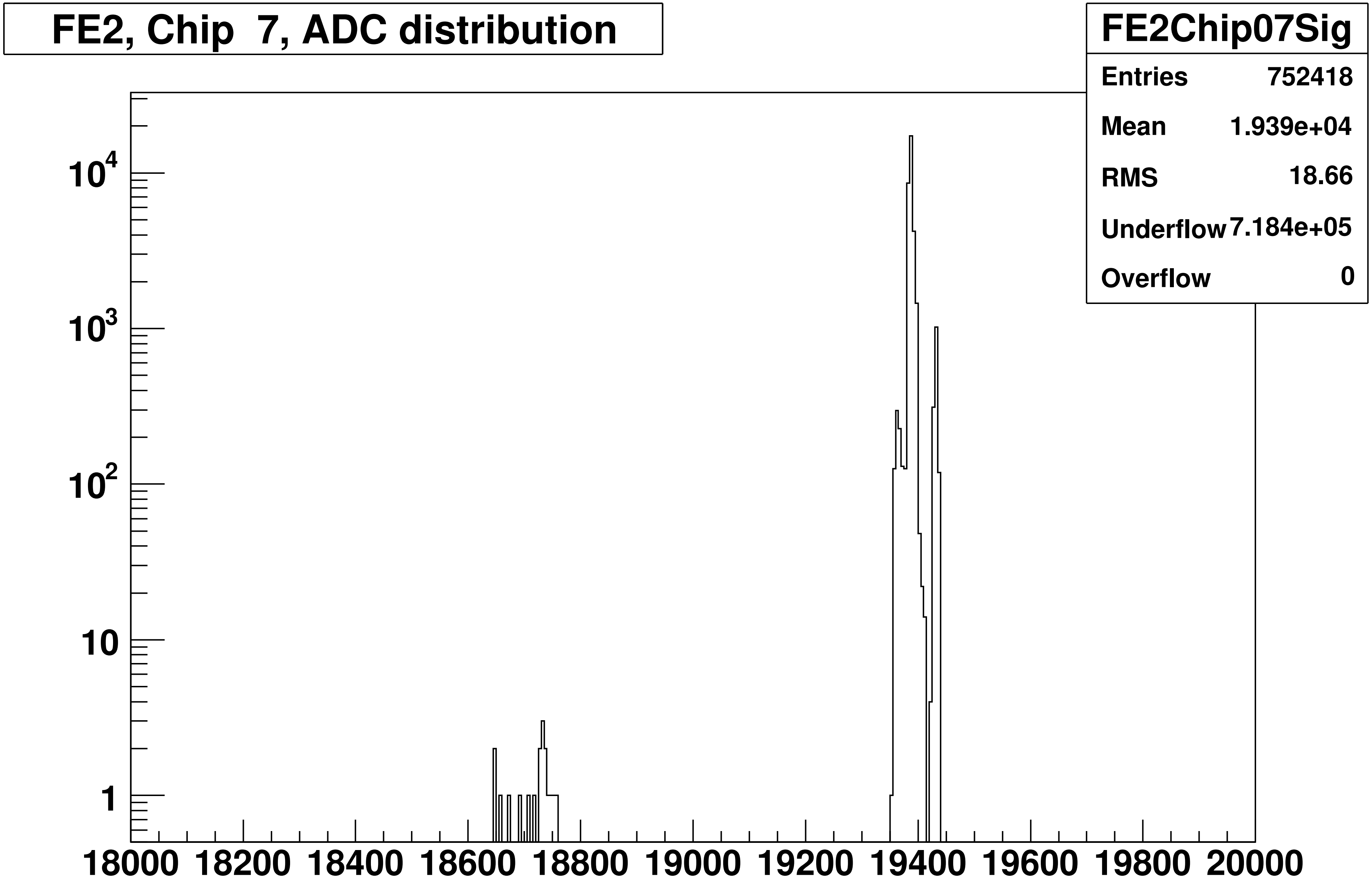
50
100
150
200


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 752418
Mean 1.939e+04
RMS 18.66
Underflow 7.184e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
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19000
19200
19400
19600
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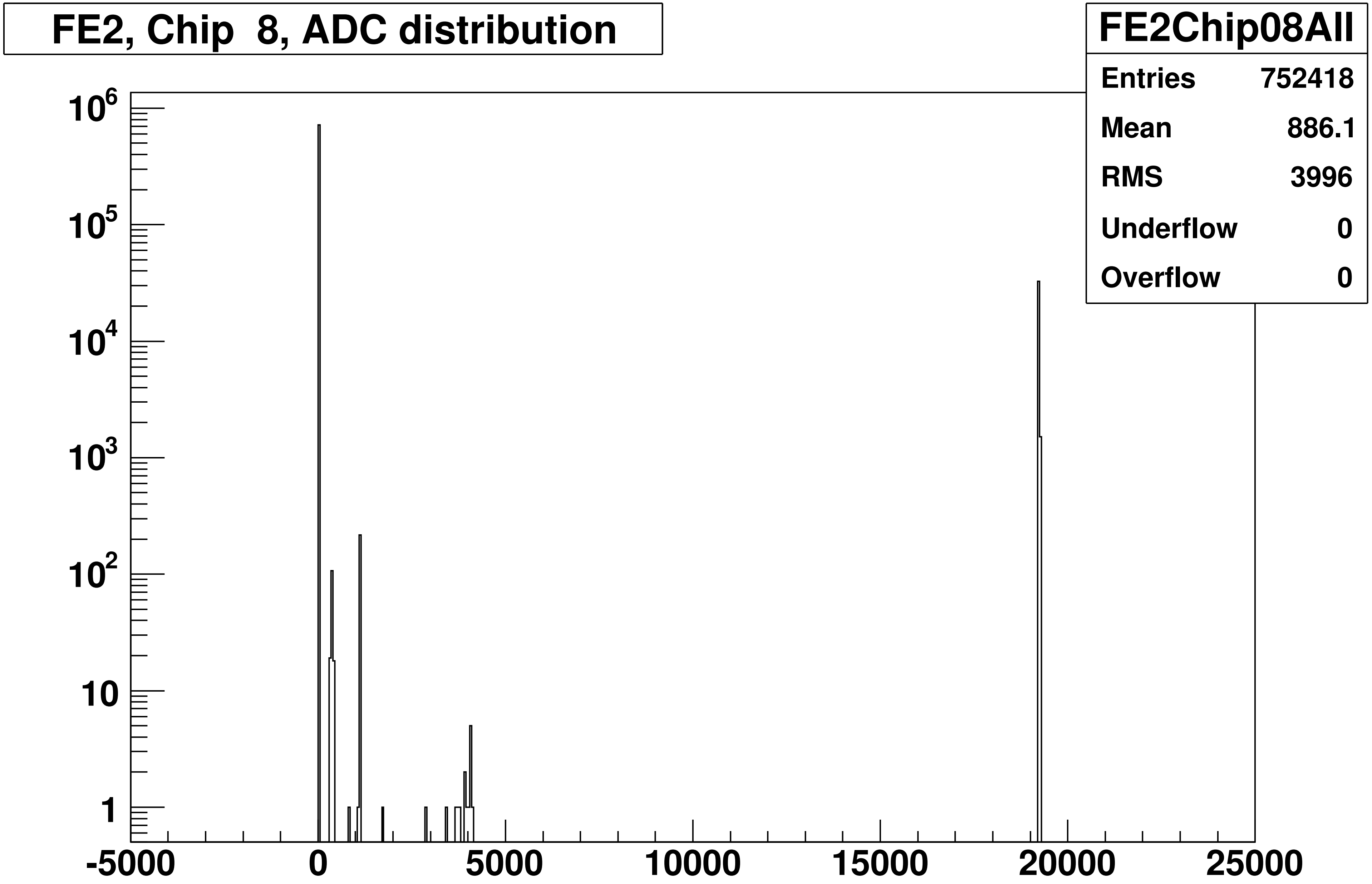
20000


FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 752418
Mean 886.1
RMS 3996
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
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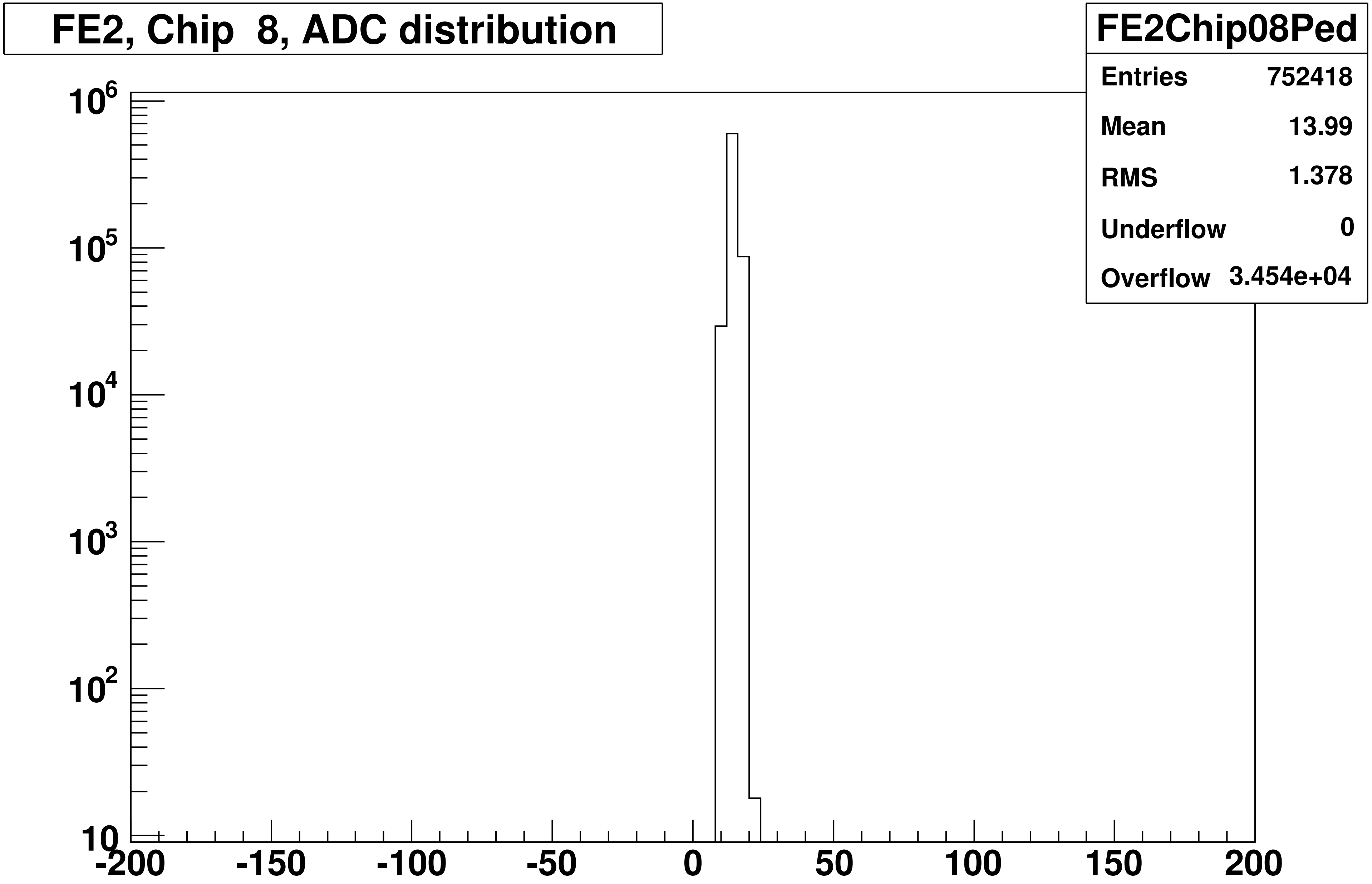
25000



FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 752418
Mean 13.99
RMS 1.378
Underflow 0
Overflow 3.454e+04
10
$10^2$
$10^3$
$10^4$
$10^5$
$10^6$
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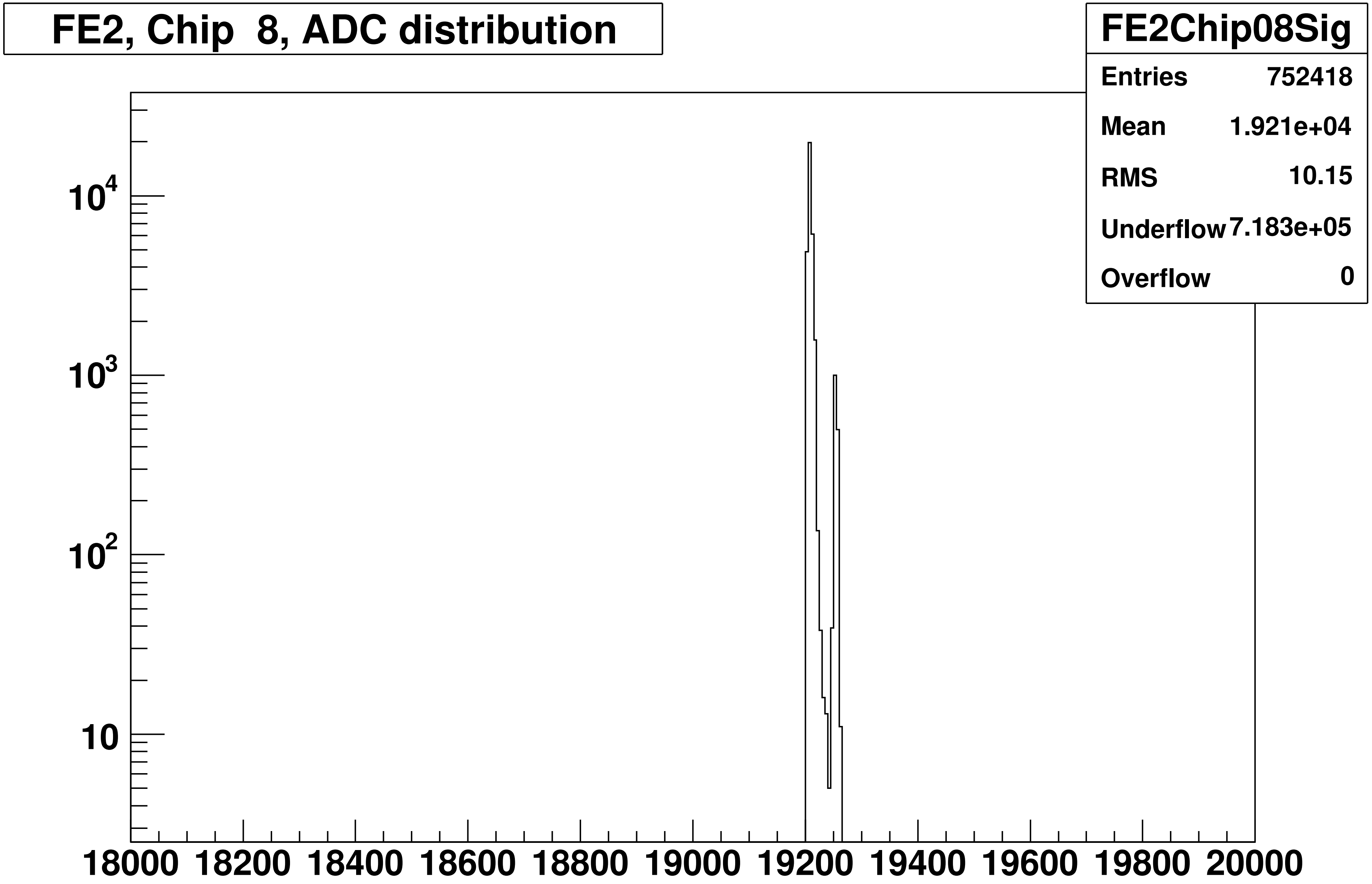
0
50
100
150
200


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 752418
Mean 1.921e+04
RMS 10.15
Underflow 7.183e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000
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18400
18600
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19000
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19800
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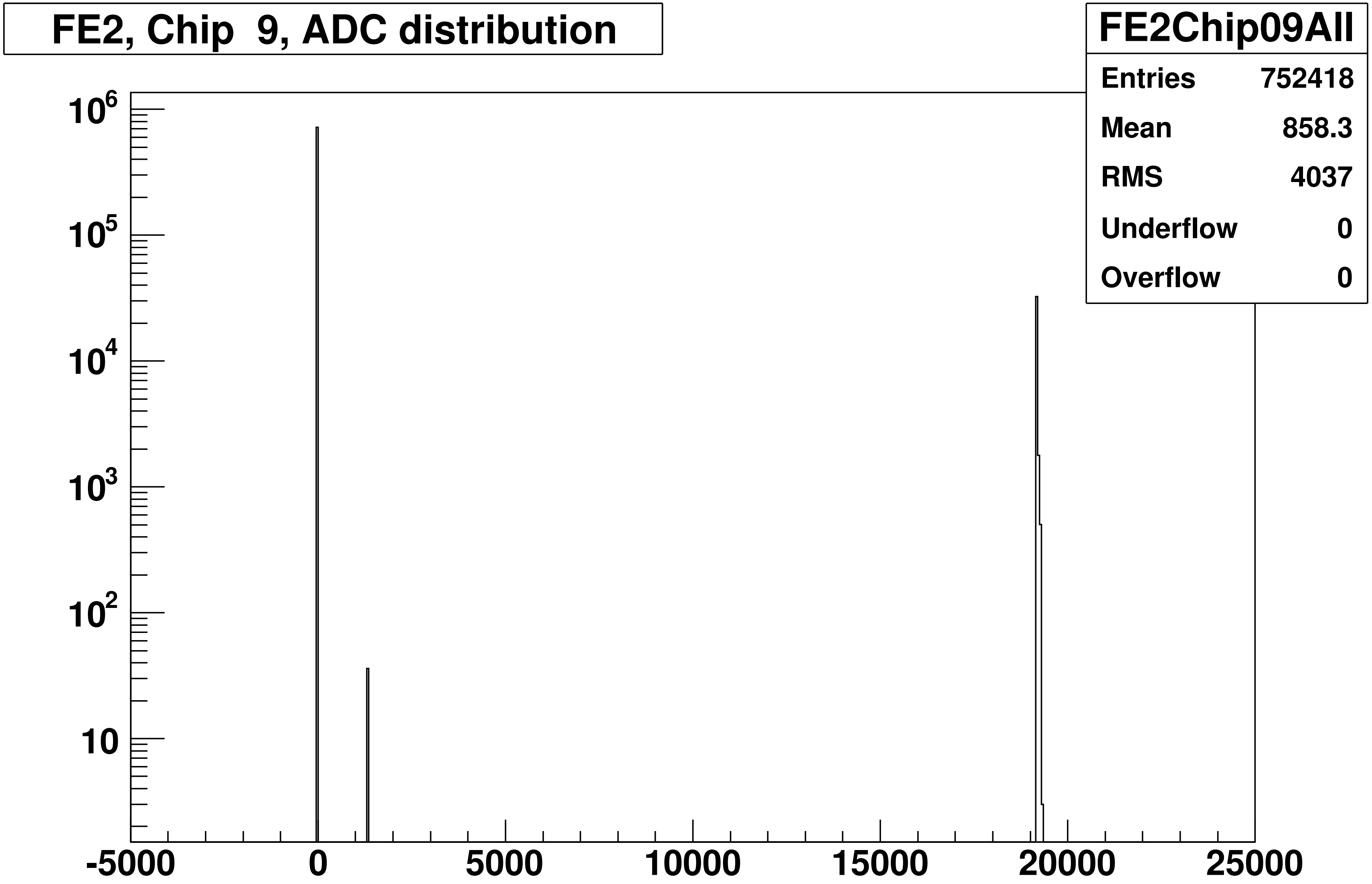

FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 752418
Mean 858.3
RMS 4037
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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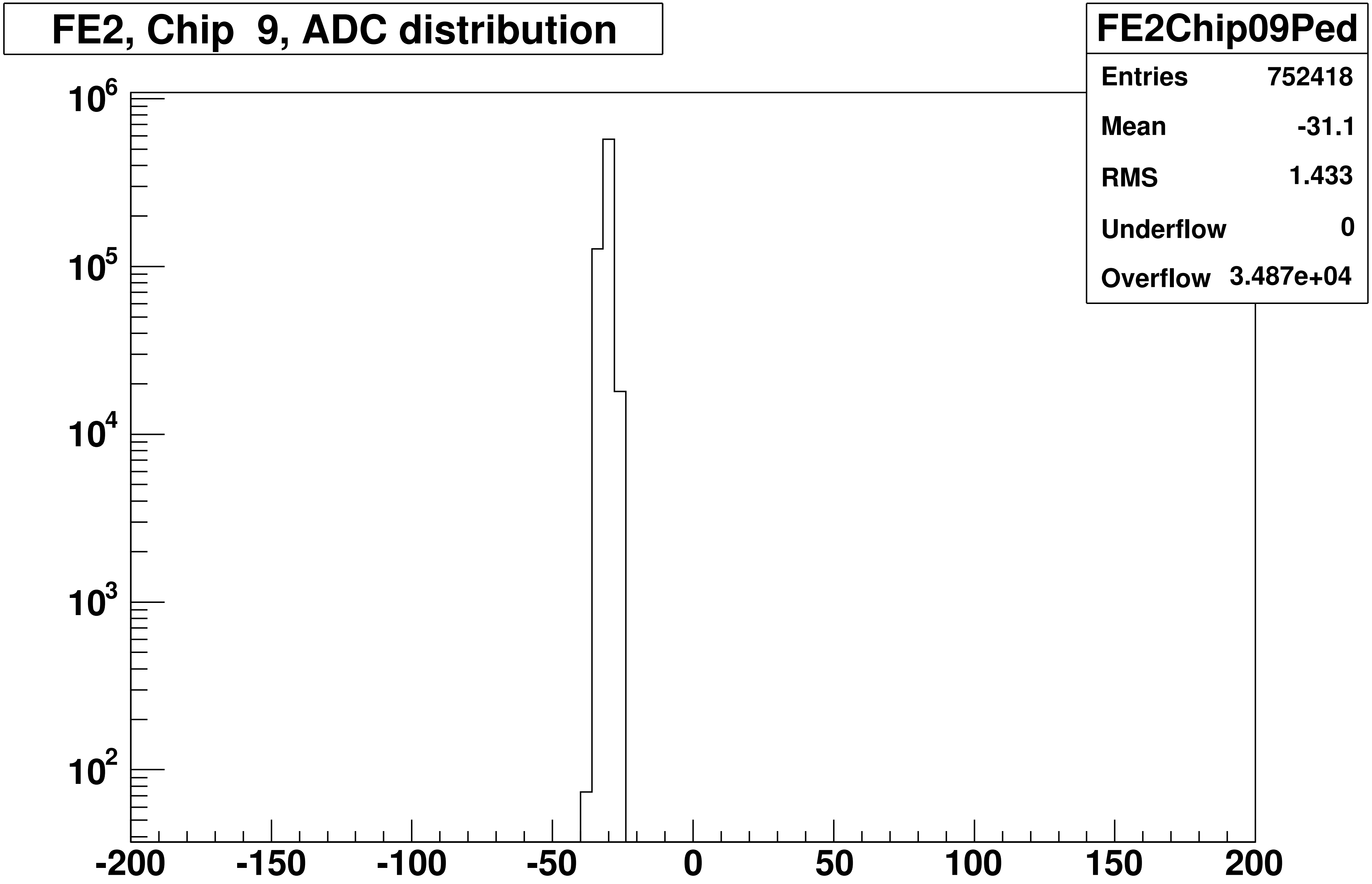
25000



FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 752418
Mean -31.1
RMS 1.433
Underflow 0
Overflow 3.487e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
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-100
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50
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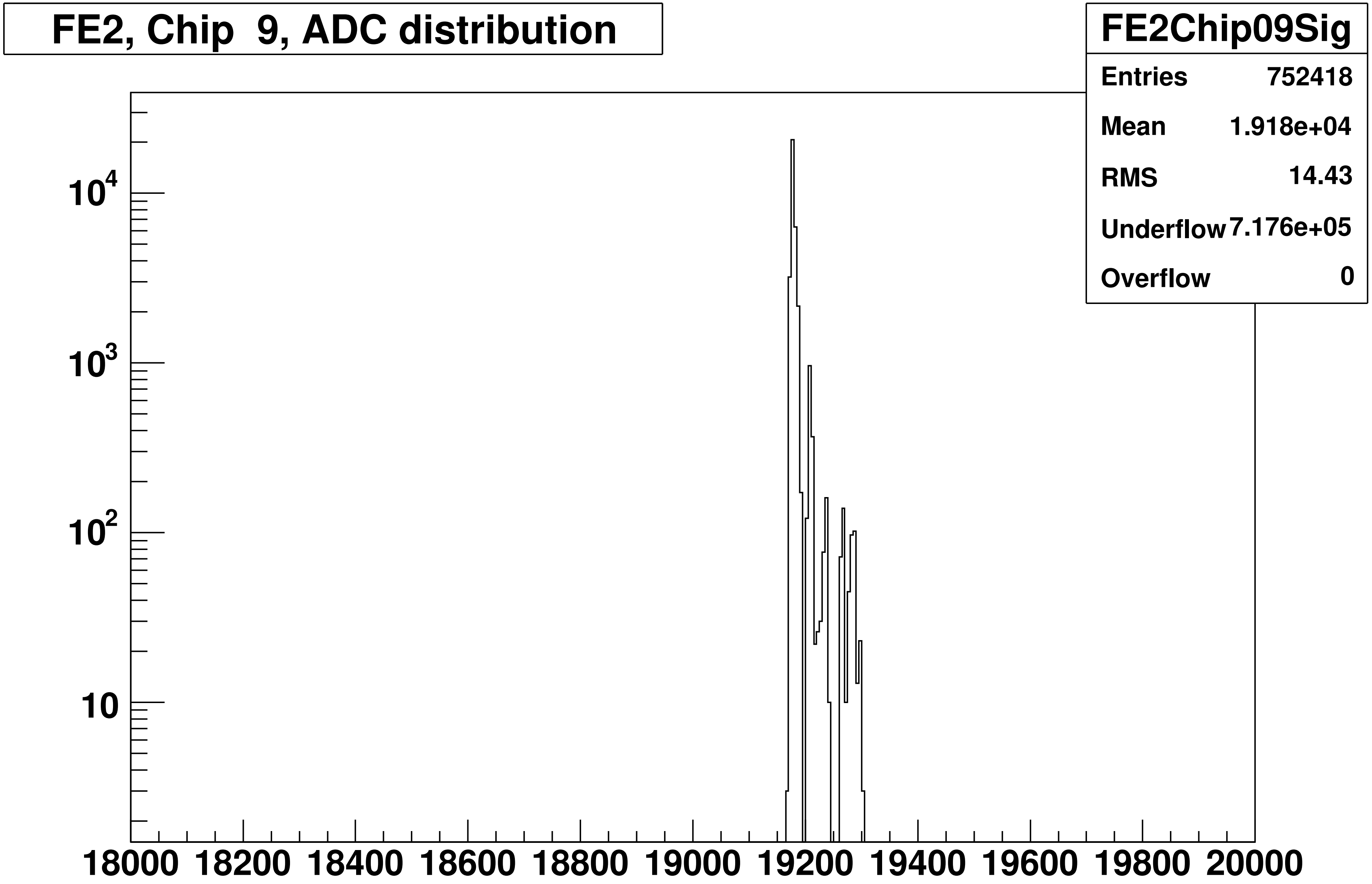
150
200


FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 752418
Mean 1.918e+04
RMS 14.43
Underflow 7.176e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
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19000
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19600
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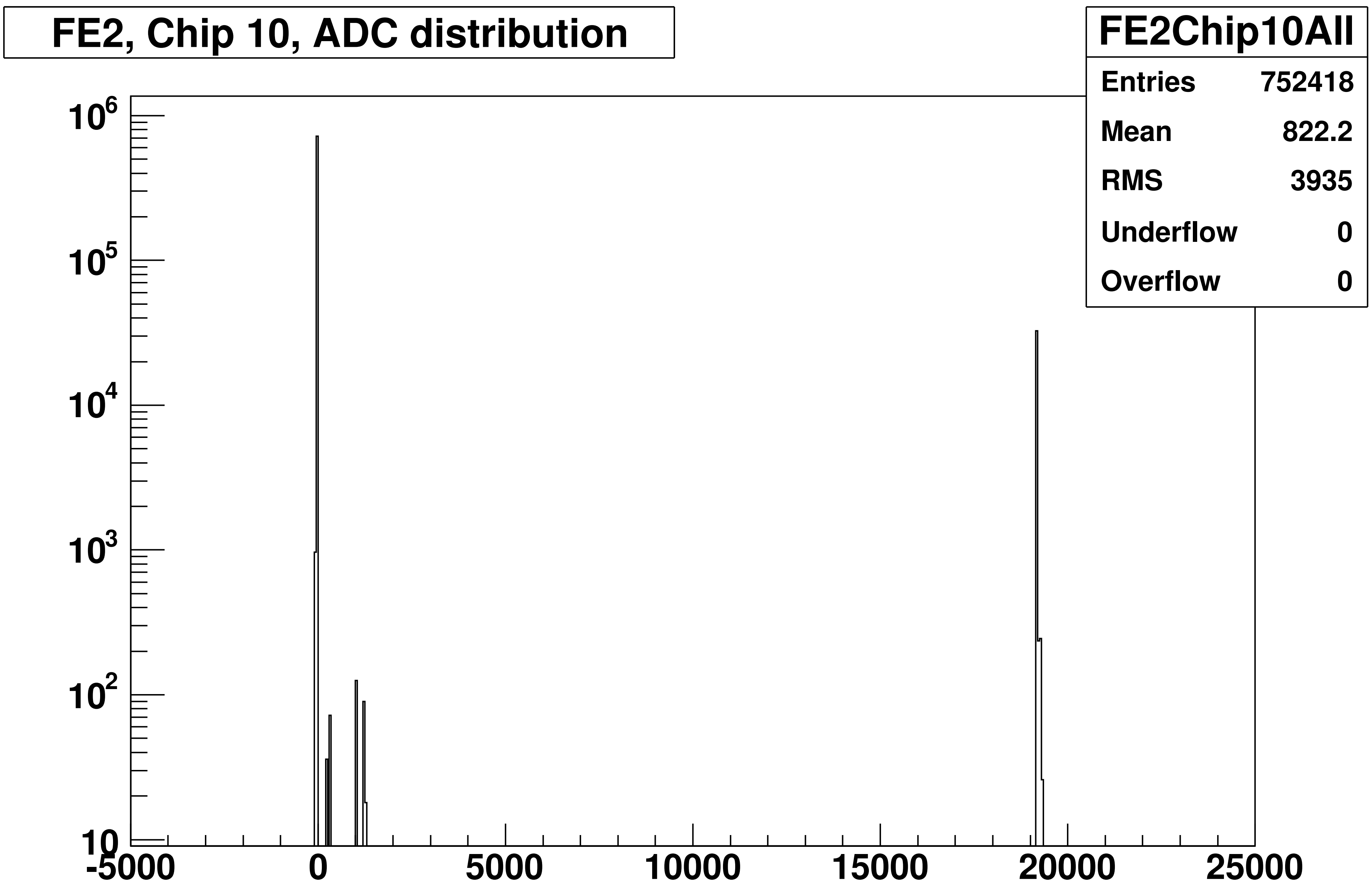
20000



FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 752418
Mean 822.2
RMS 3935
Underflow 0
Overflow 0
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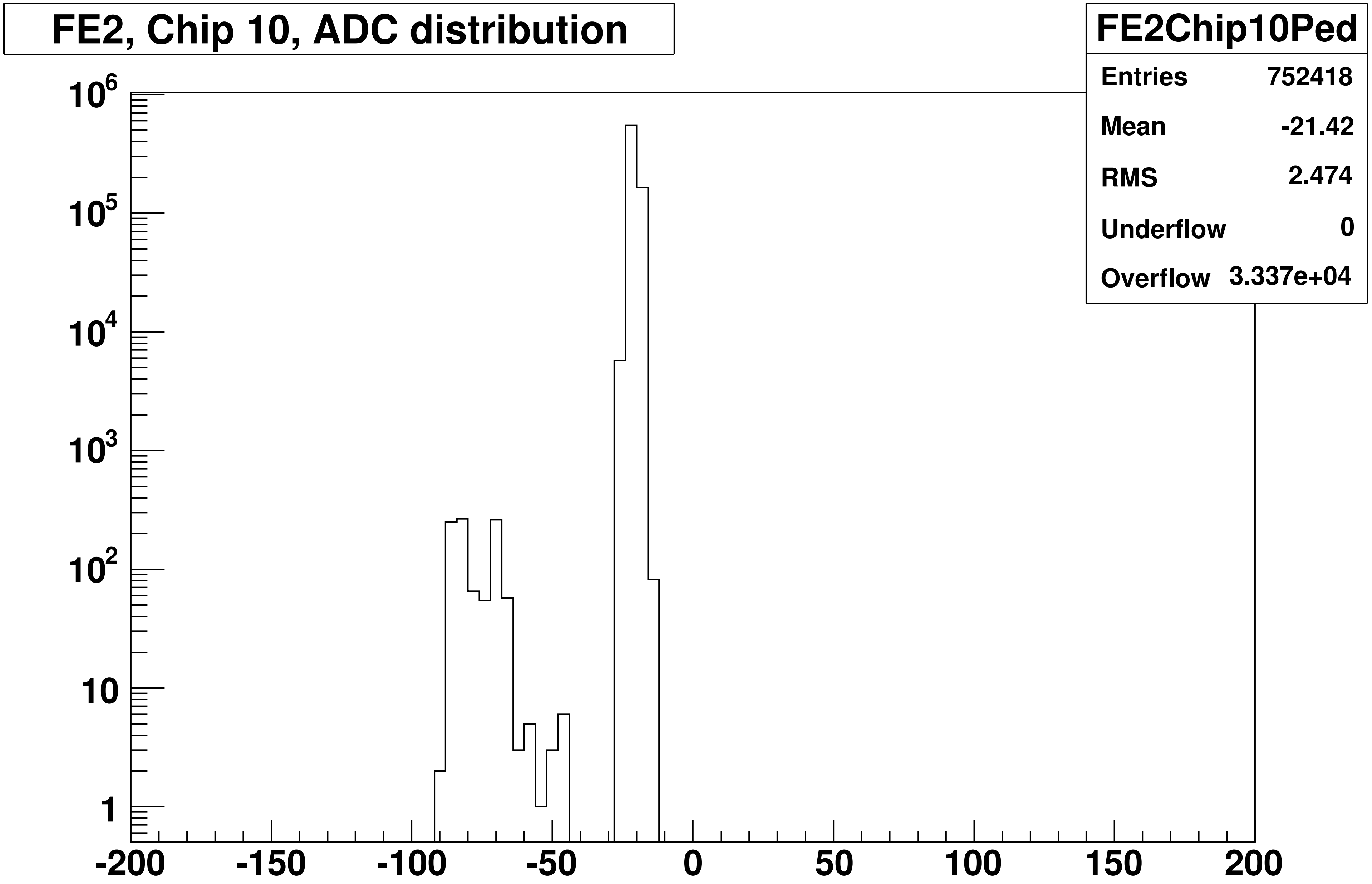
$10^5$
$10^4$
$10^3$
$10^2$
10
-5000
0
5000
10000
15000
20000
25000



FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 752418
Mean -21.42
RMS 2.474
Underflow 0
Overflow 3.337e+04
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10^5
10^4
10^3
10^2
10
1
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-100
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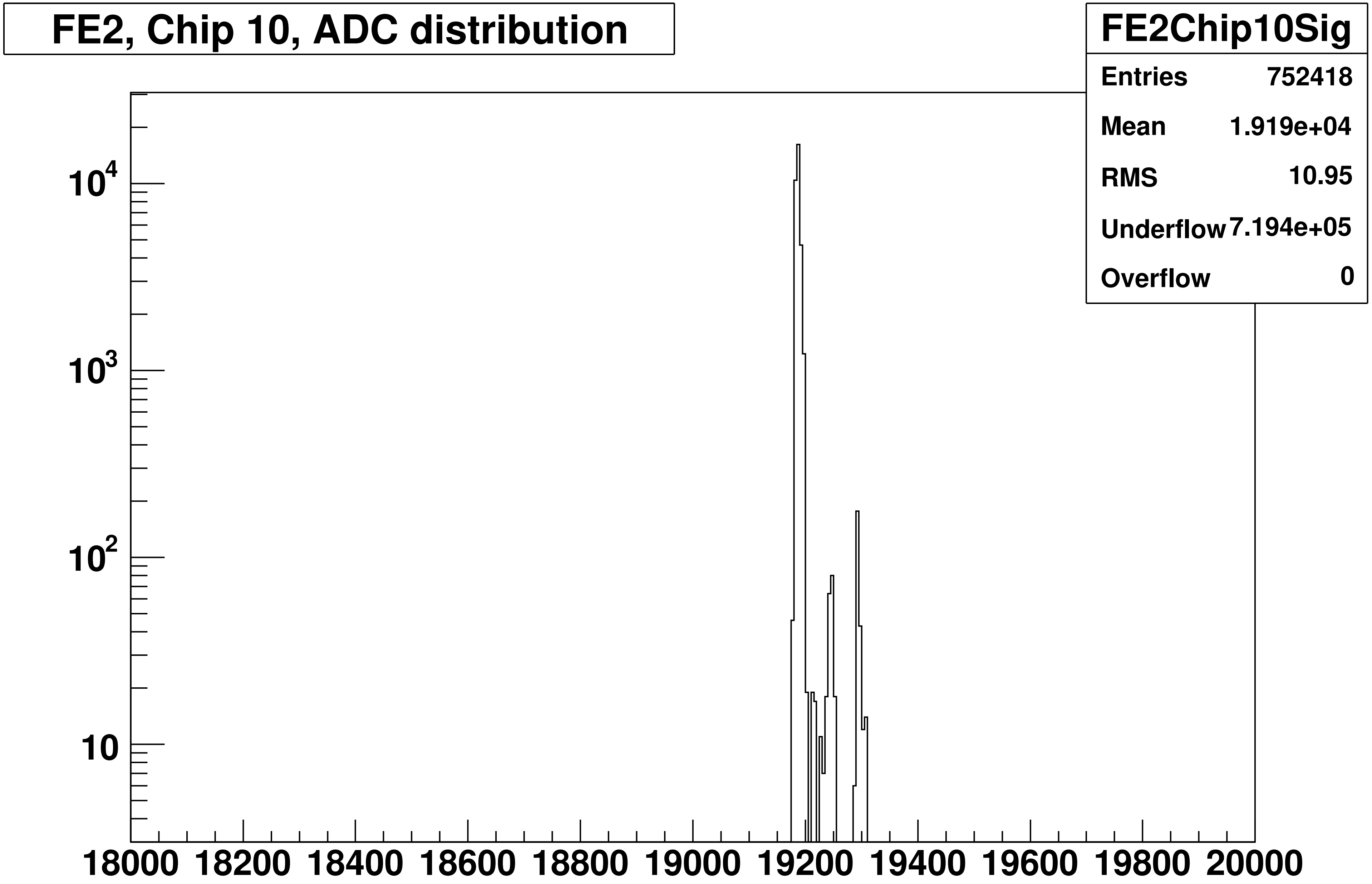
0
50
100
150
200


FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 752418
Mean 1.919e+04
RMS 10.95
Underflow 7.194e+05
Overflow 0
10
$10^2$
$10^3$
$10^4$
18000
18200
18400
18600
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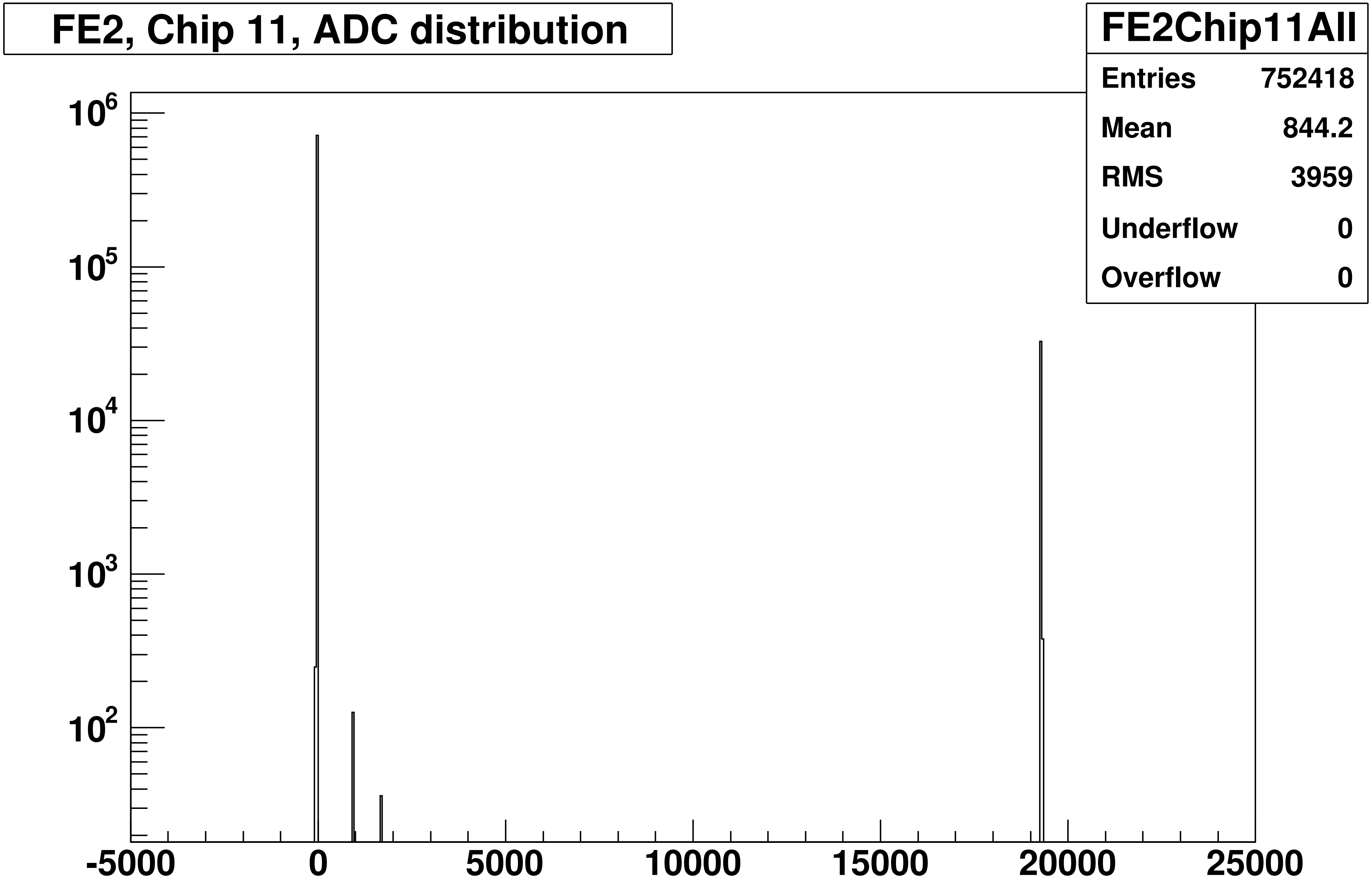
19000
19200
19400
19600
19800
20000



FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 752418
Mean 844.2
RMS 3959
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
-5000
0
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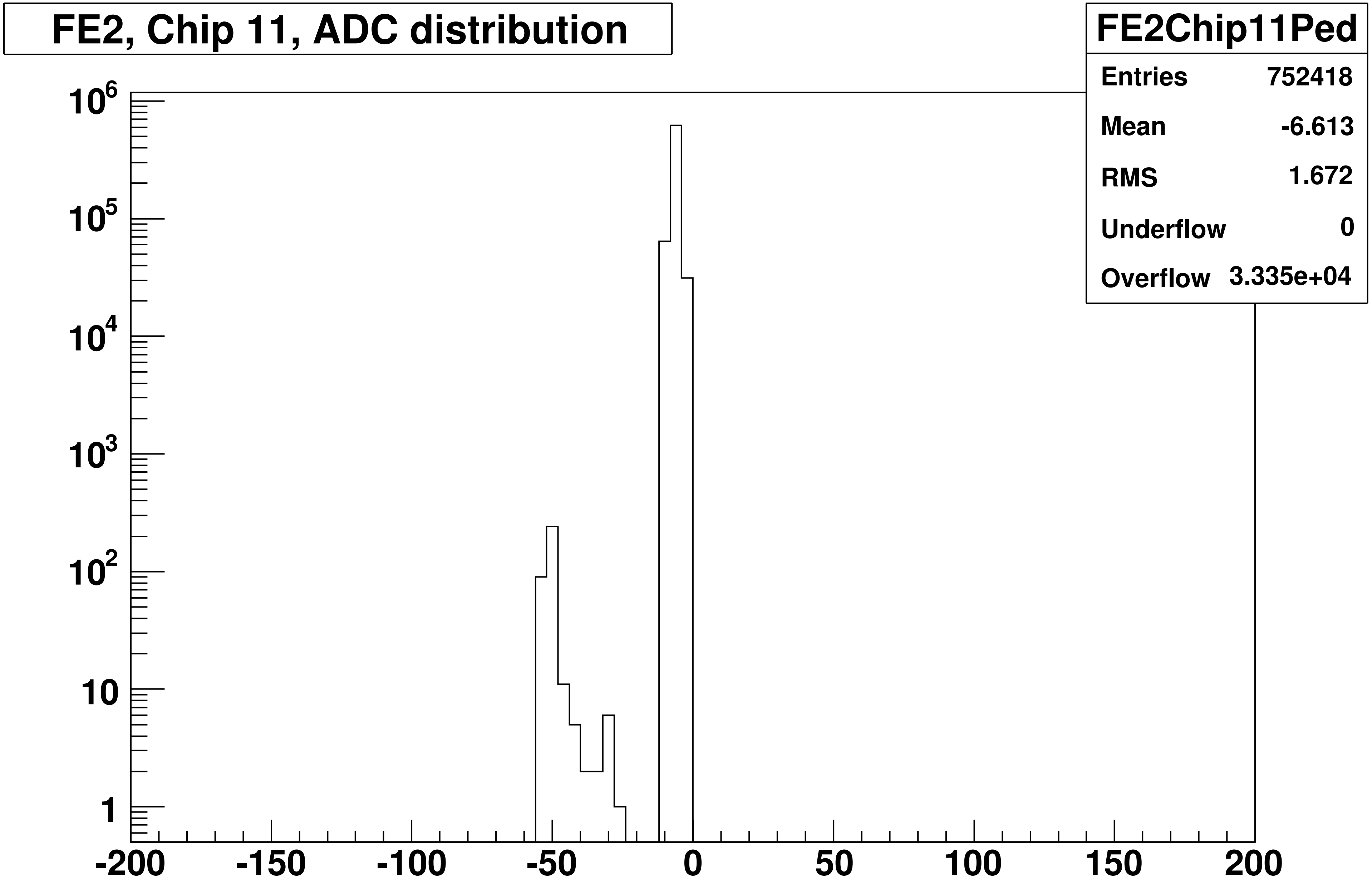
10000
15000
20000
25000


FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 752418
Mean -6.613
RMS 1.672
Underflow 0
Overflow 3.335e+04
$10^6$
$10^5$
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$10^2$
10
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-100
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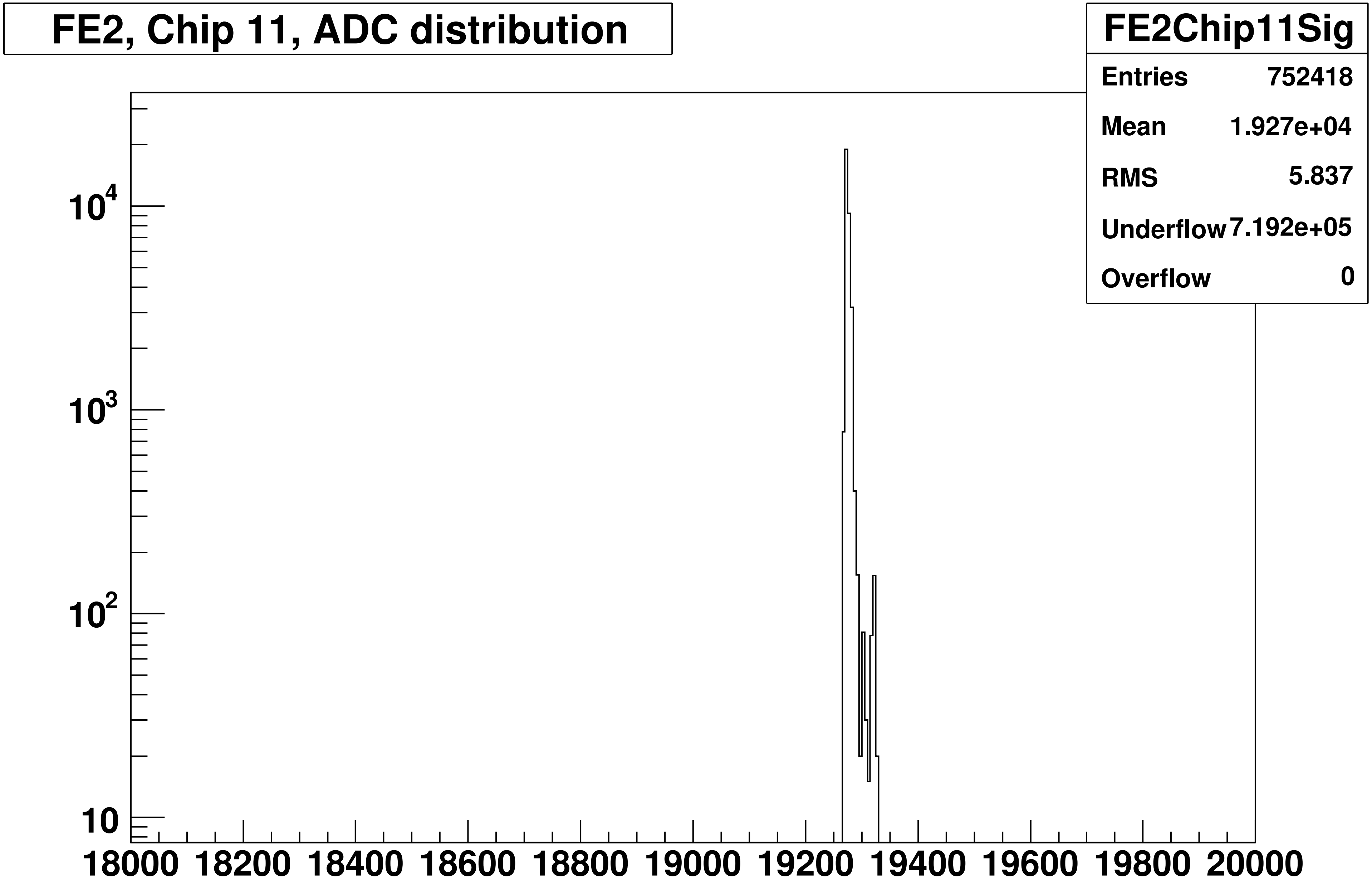
100
150
200



FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 752418
Mean 1.927e+04
RMS 5.837
Underflow 7.192e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000
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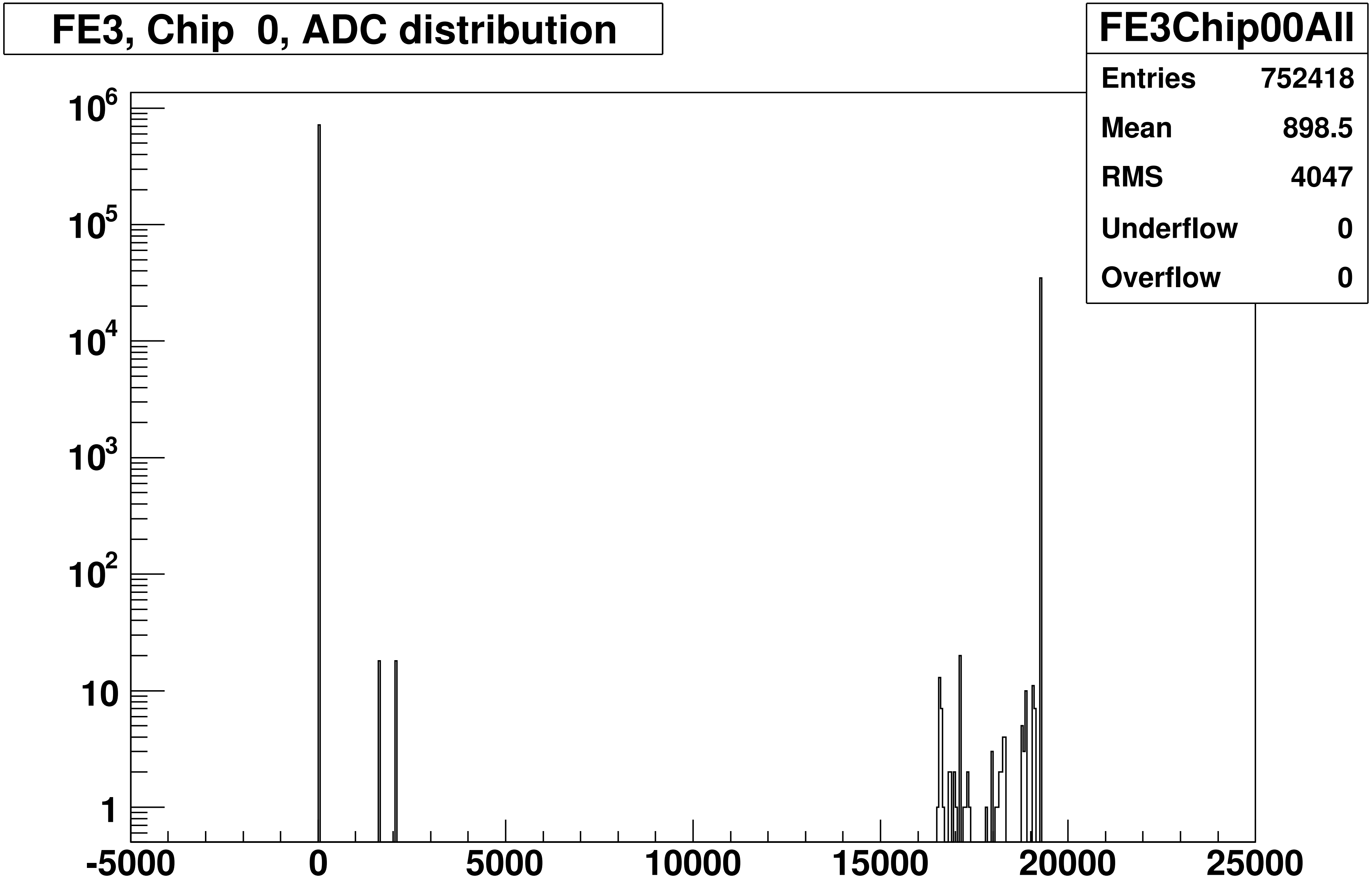
FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 752418
Mean 898.5
RMS 4047
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
10
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-5000
0
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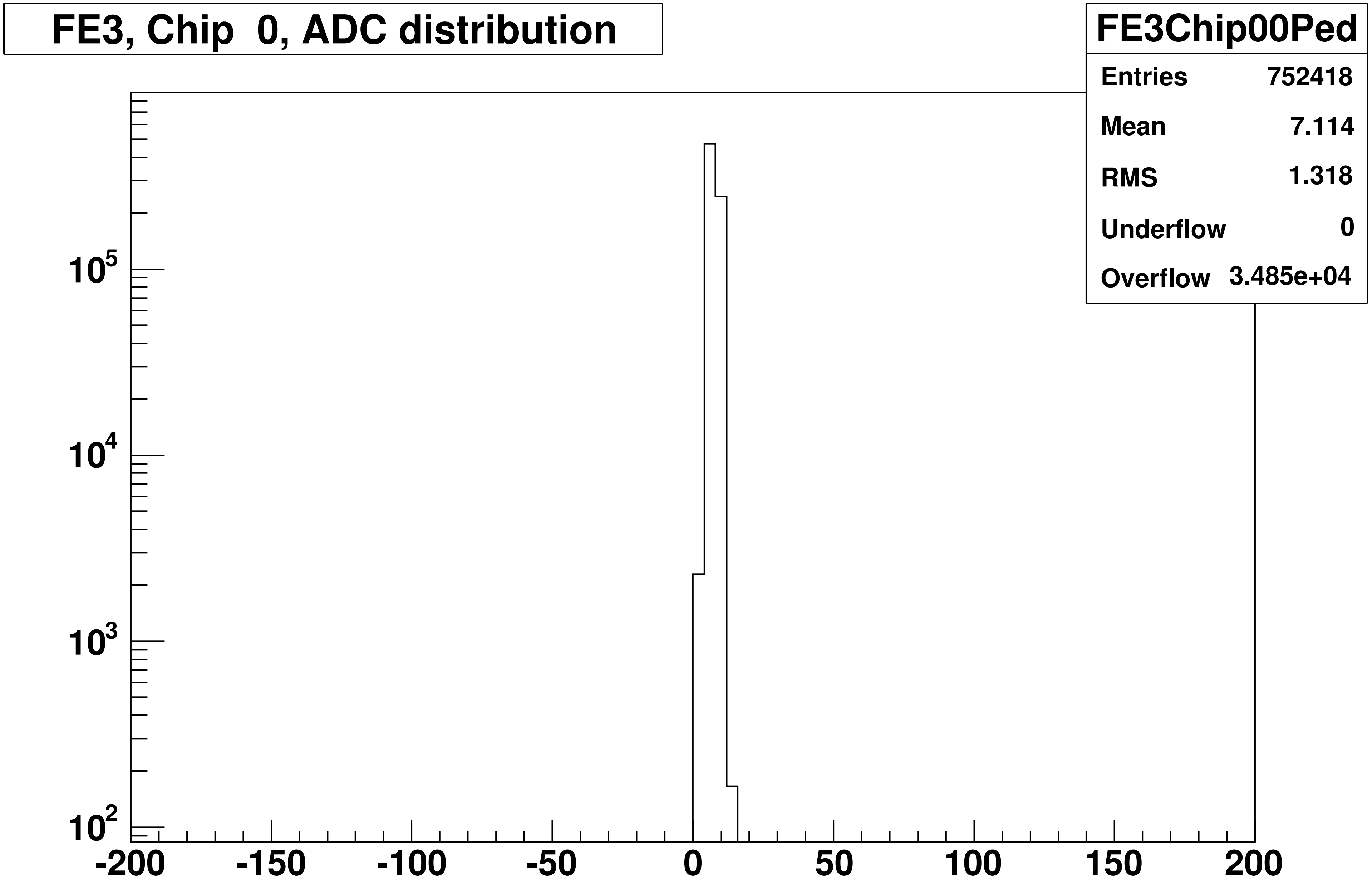
25000



FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 752418
Mean 7.114
RMS 1.318
Underflow 0
Overflow 3.485e+04
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10^4
10^3
10^2
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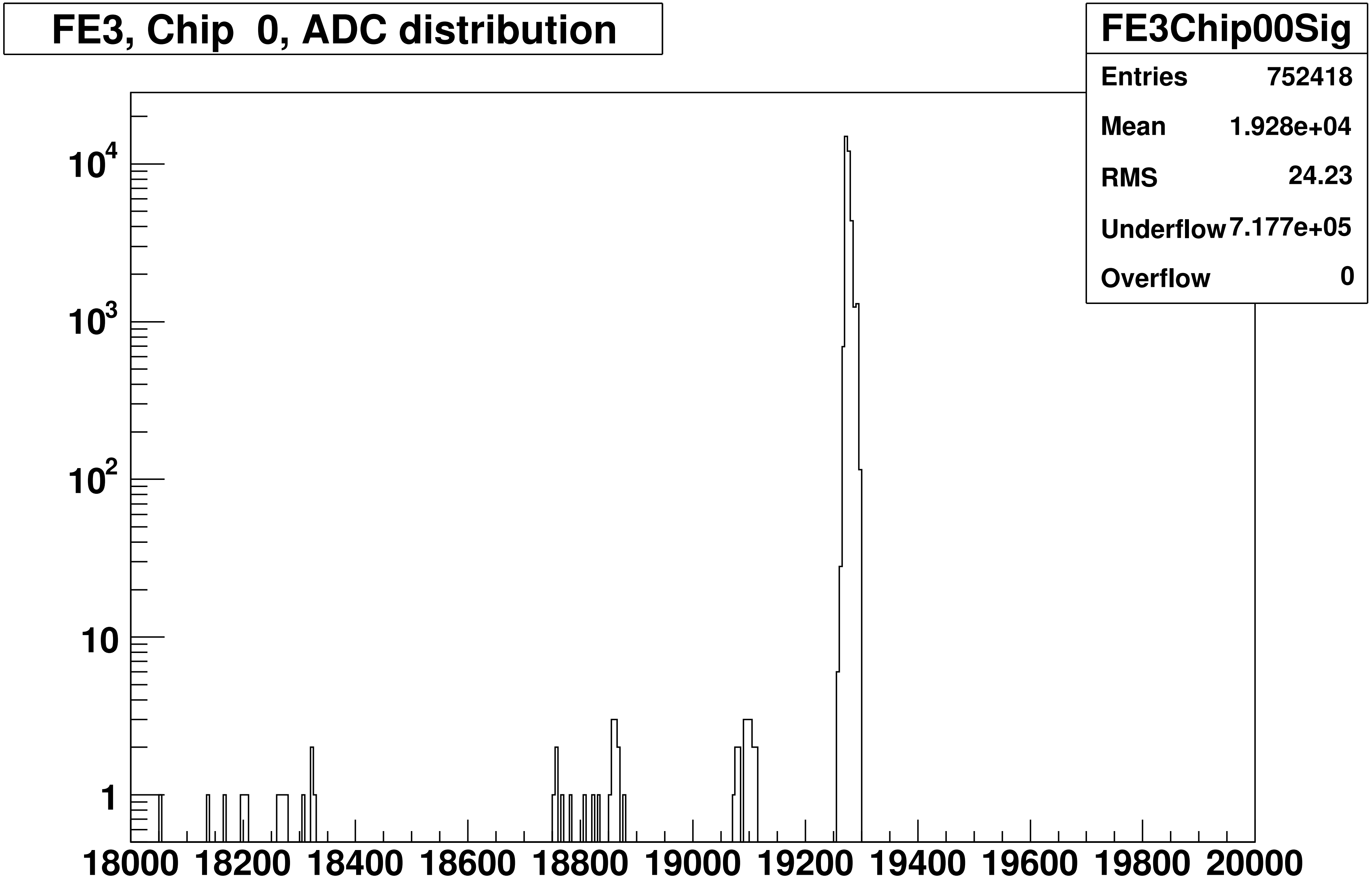
-100
-50
0
50
100
150
200



FE3, Chip  0, ADC distribution
FE3Chip00Sig
Entries 752418
Mean 1.928e+04
RMS 24.23
Underflow 7.177e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
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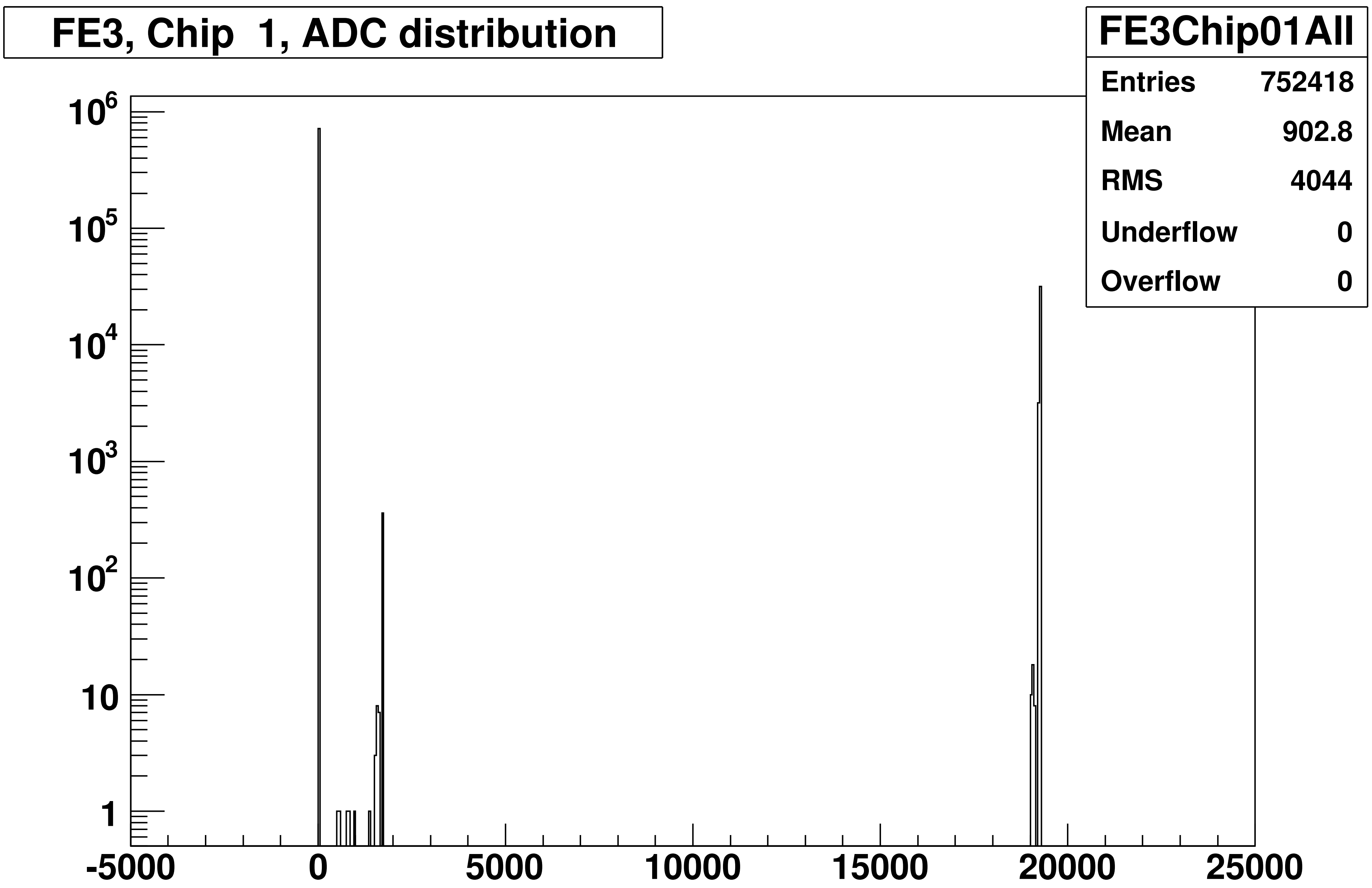
20000



FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 752418
Mean 902.8
RMS 4044
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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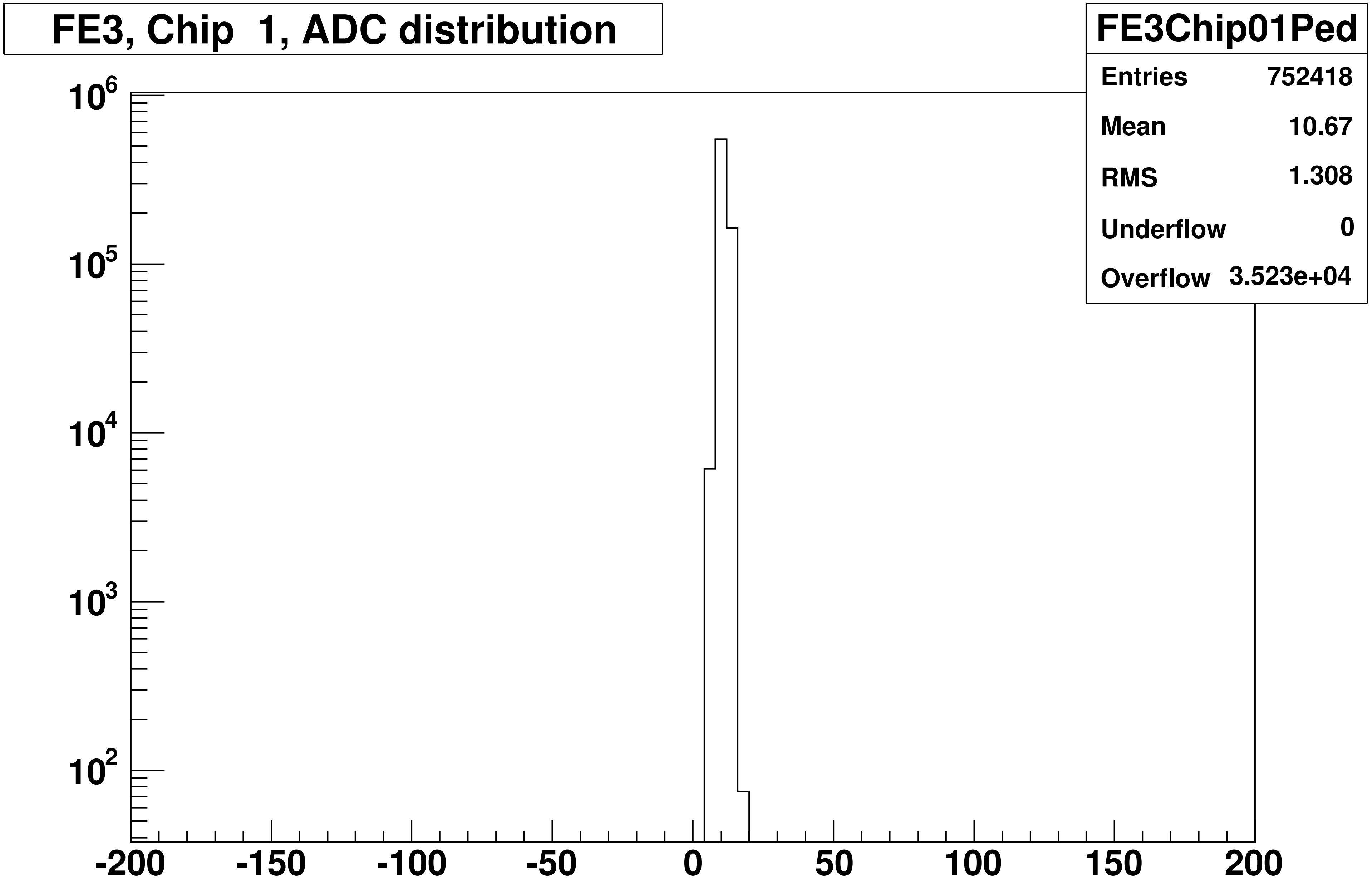
25000


FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 752418
Mean 10.67
RMS 1.308
Underflow 0
Overflow 3.523e+04
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10^5
10^4
10^3
10^2
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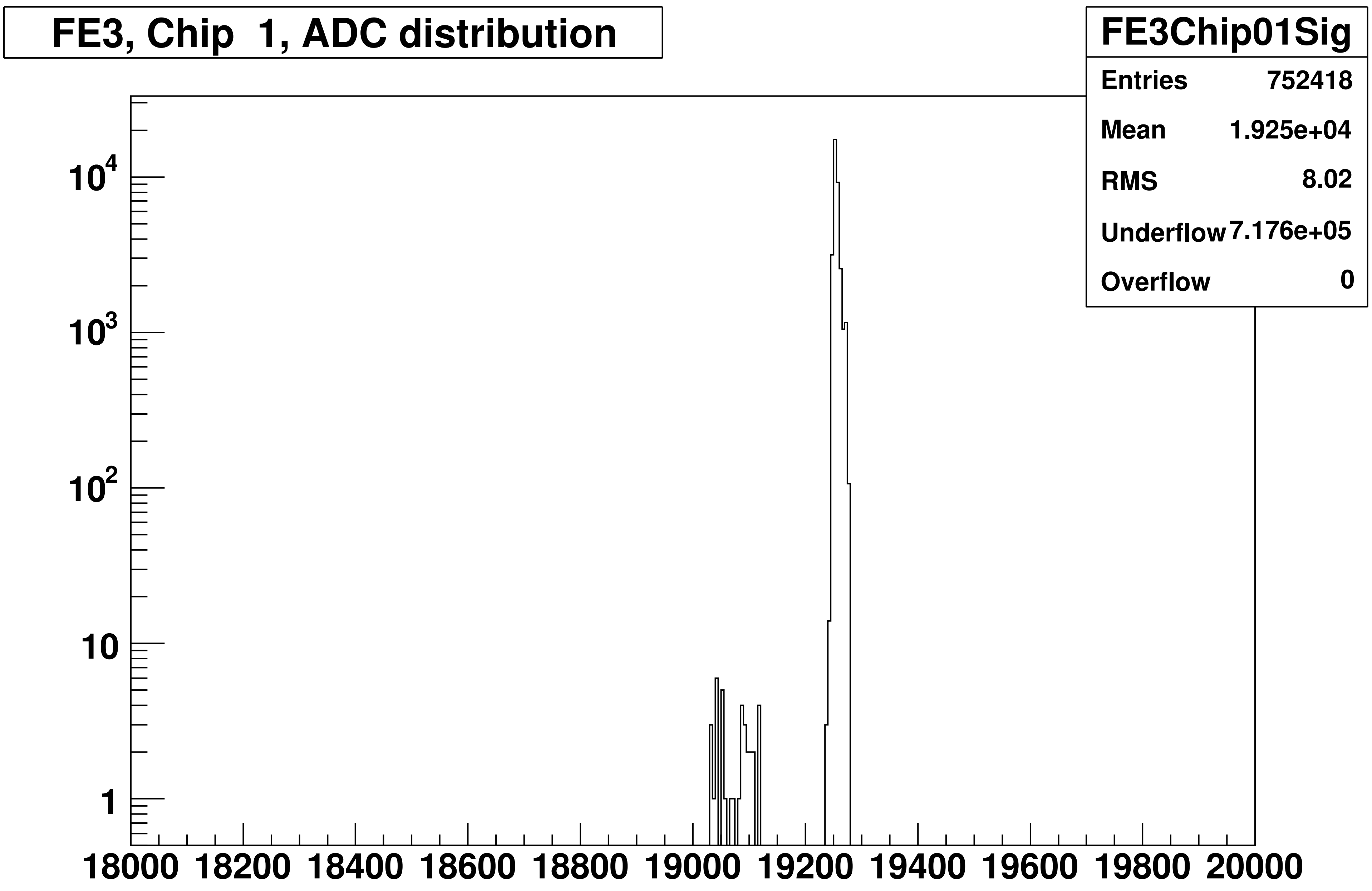
0
50
100
150
200


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 752418
Mean 1.925e+04
RMS 8.02
Underflow 7.176e+05
Overflow 0
$10^4$
$10^3$
$10^2$
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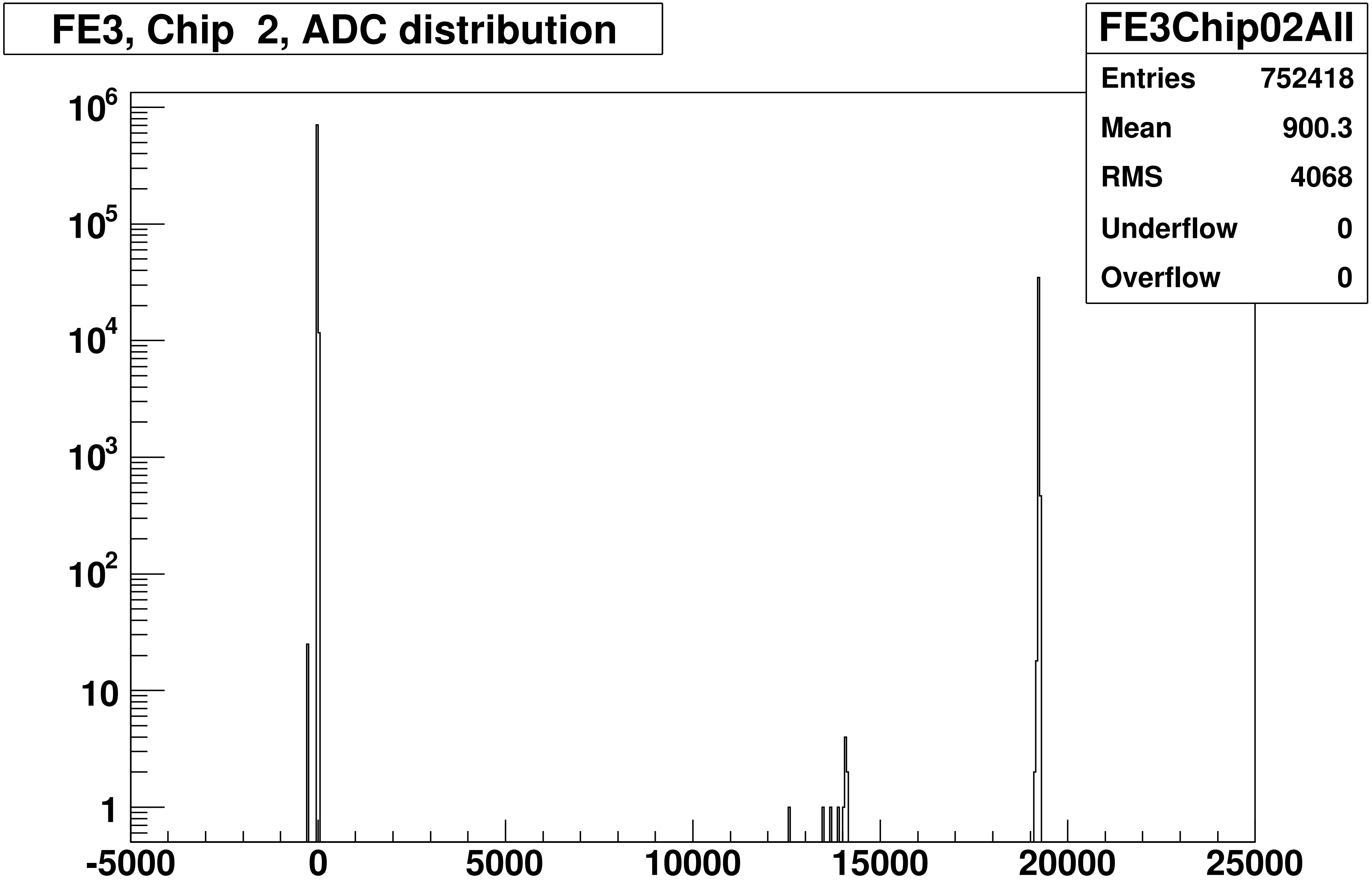

FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 752418
Mean 900.3
RMS 4068
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
10
1
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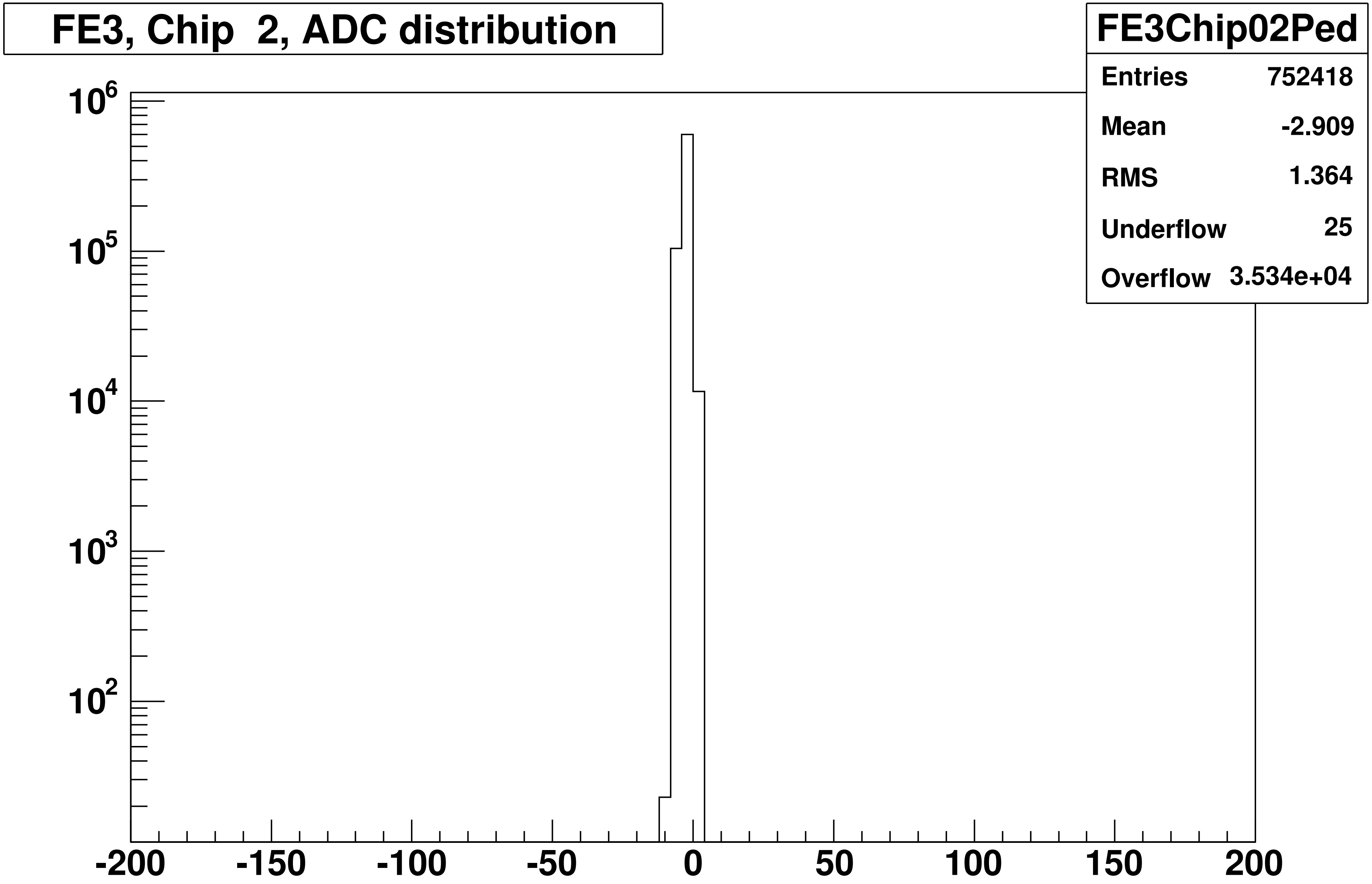
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 752418
Mean -2.909
RMS 1.364
Underflow 25
Overflow 3.534e+04
$10^6$
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$10^3$
$10^2$
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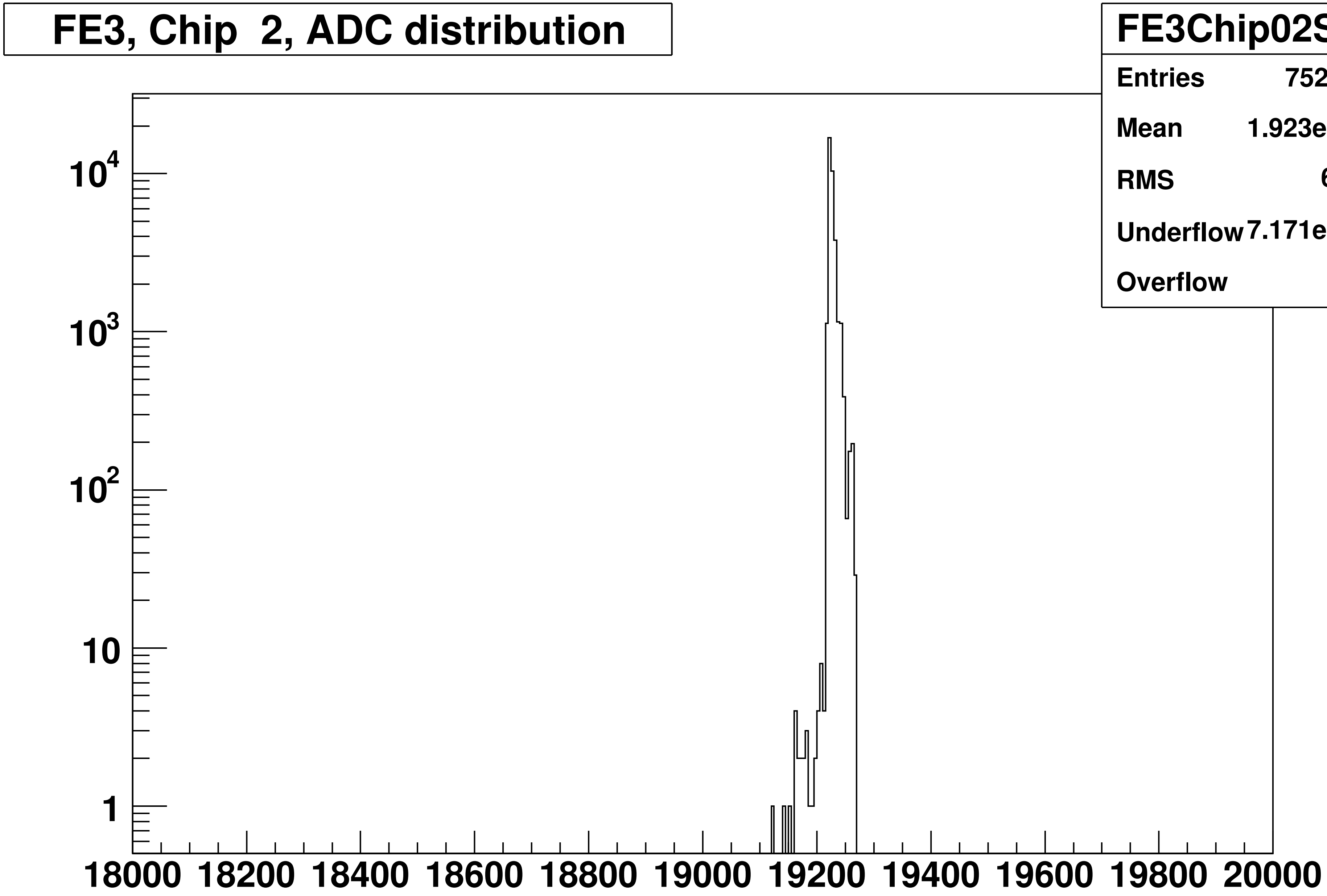
FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 752418
Mean 1.923e+04
RMS 6.78
Underflow 7.171e+05
Overflow 0
$10^4$
$10^3$
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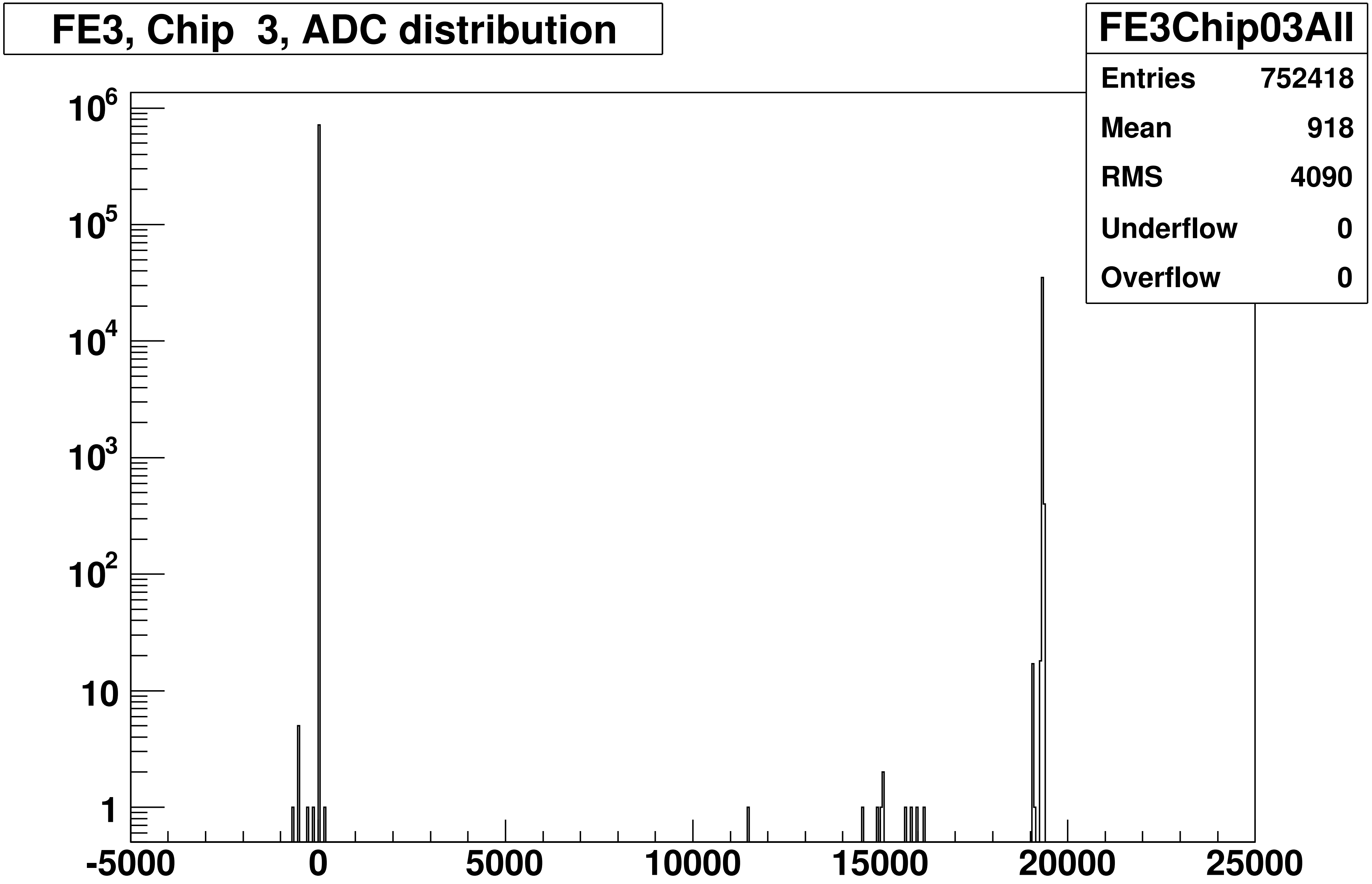
FE3, Chip  3, ADC distribution
FE3Chip03All
Entries 752418
Mean 918
RMS 4090
Underflow 0
Overflow 0
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$10^3$
$10^2$
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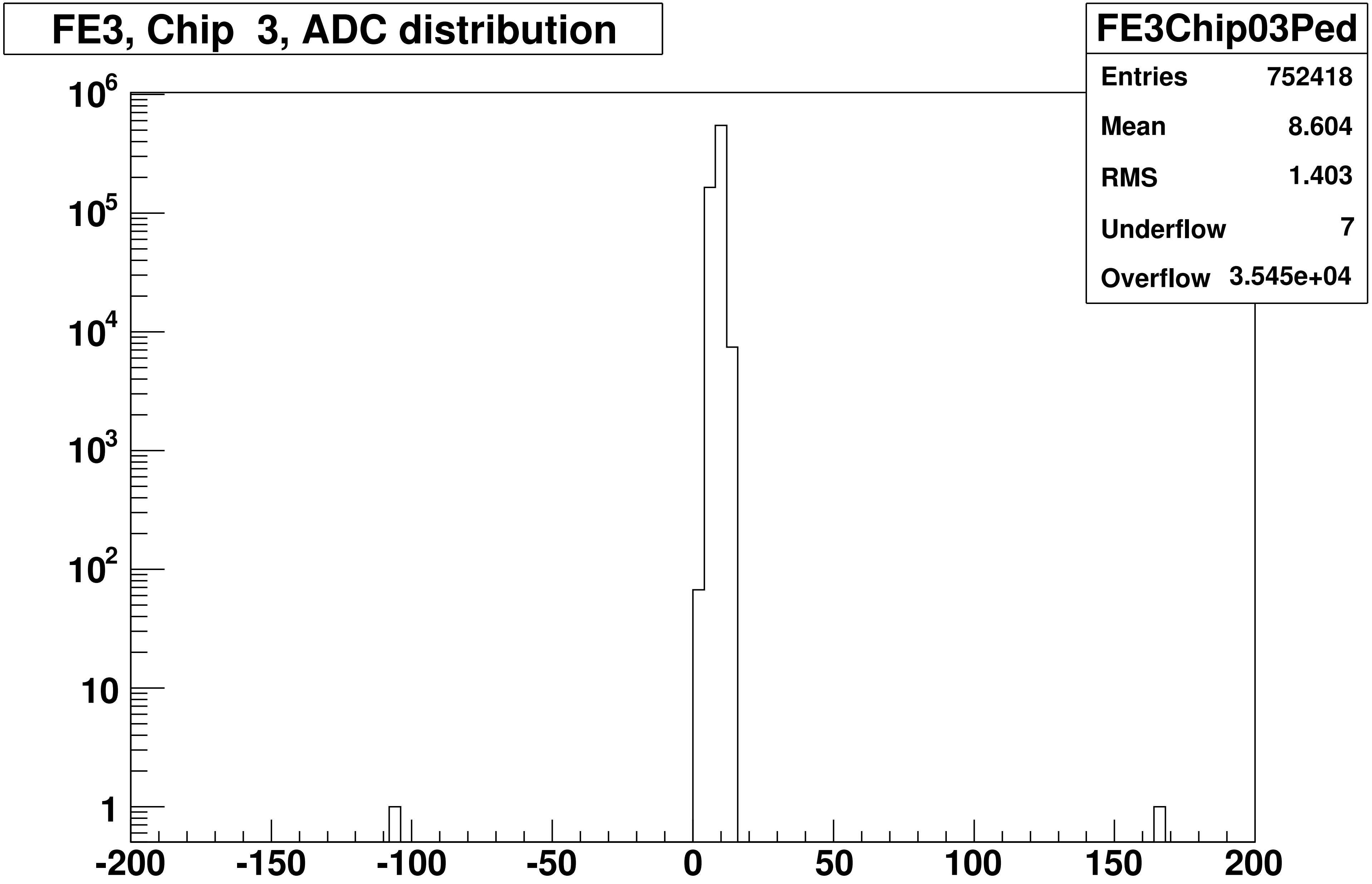
FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 752418
Mean 8.604
RMS 1.403
Underflow 7
Overflow 3.545e+04
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10^5
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10^3
10^2
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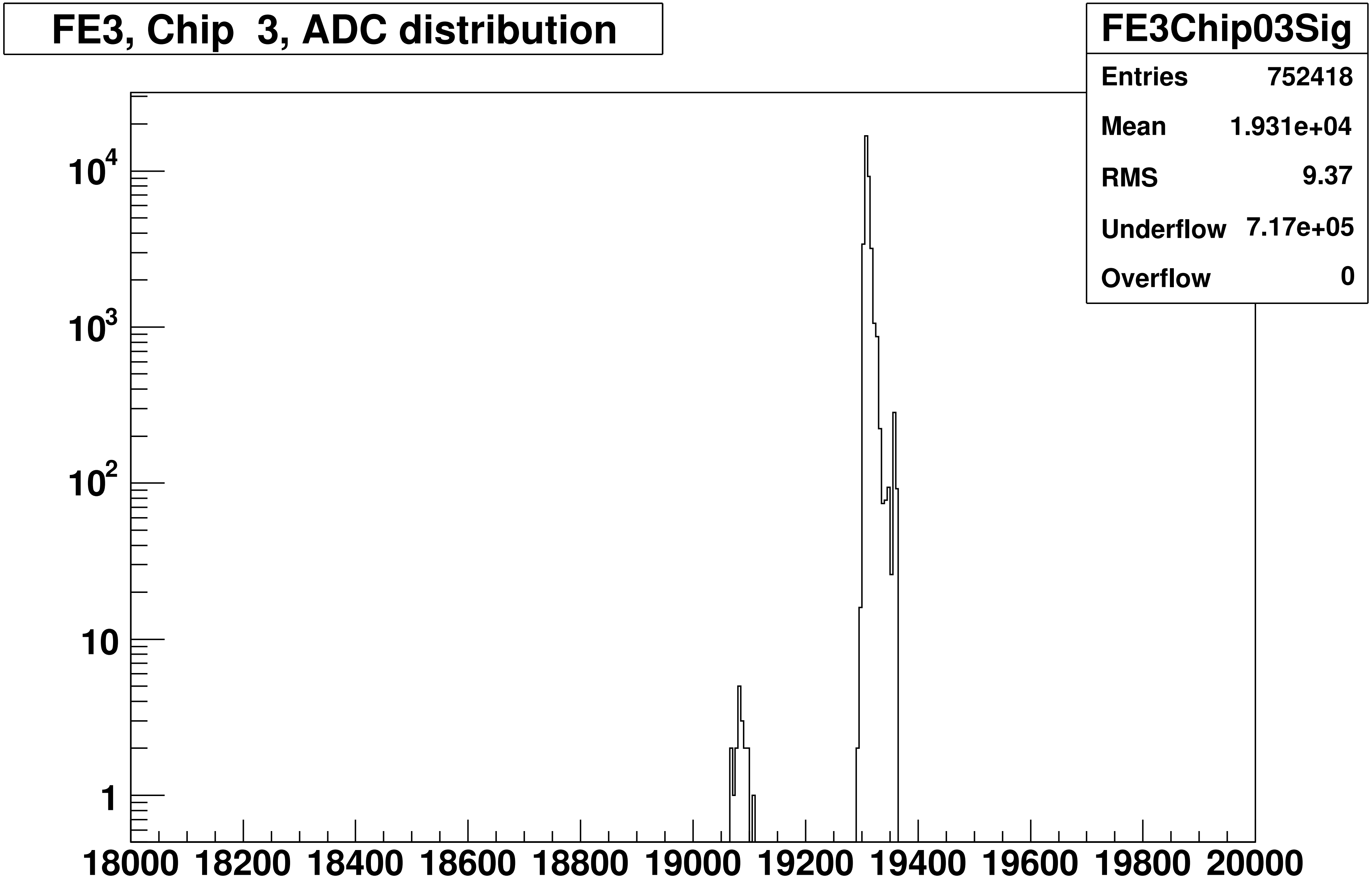
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 752418
Mean 1.931e+04
RMS 9.37
Underflow 7.17e+05
Overflow 0
10^4
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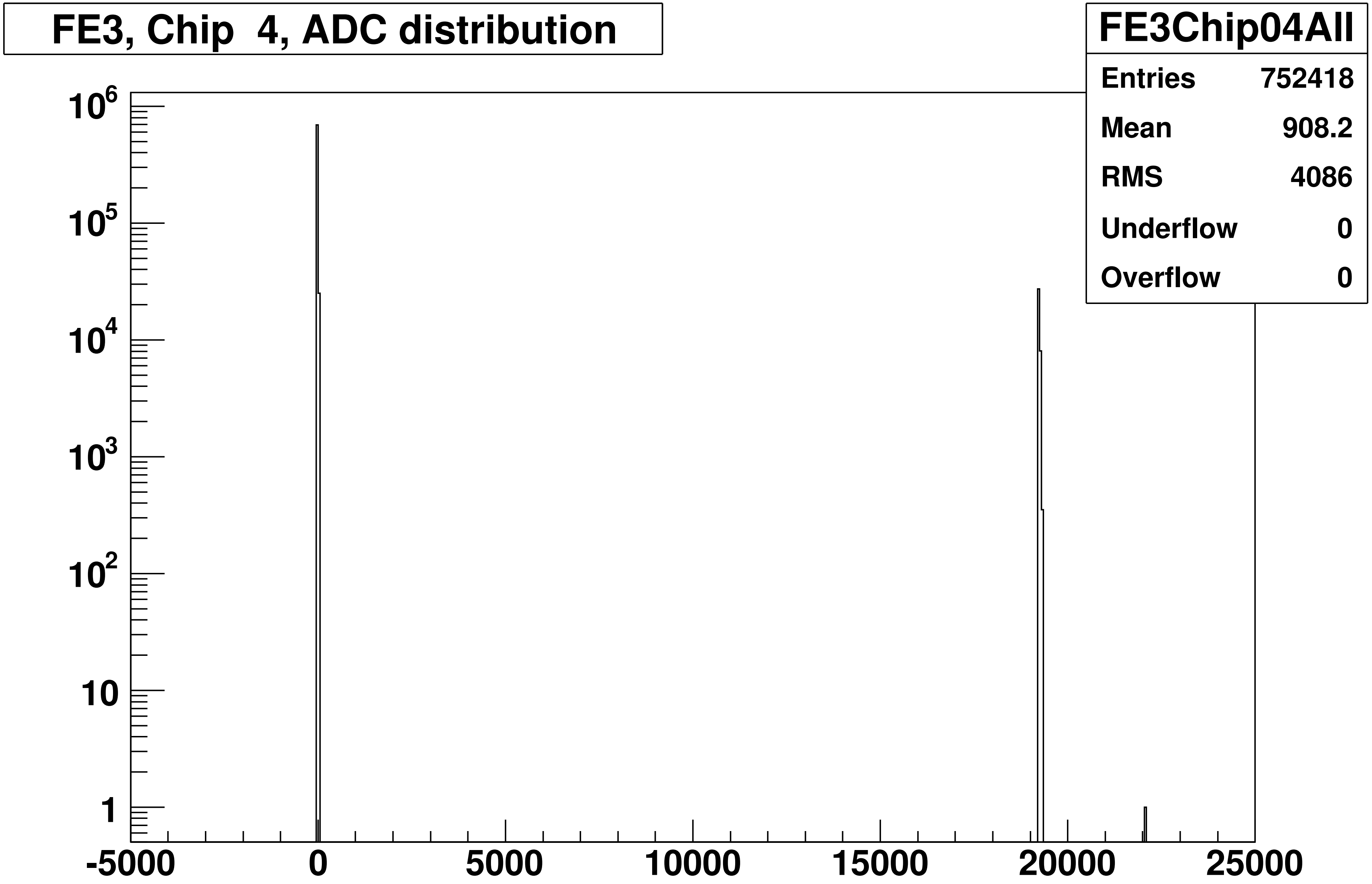

FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 752418
Mean 908.2
RMS 4086
Underflow 0
Overflow 0
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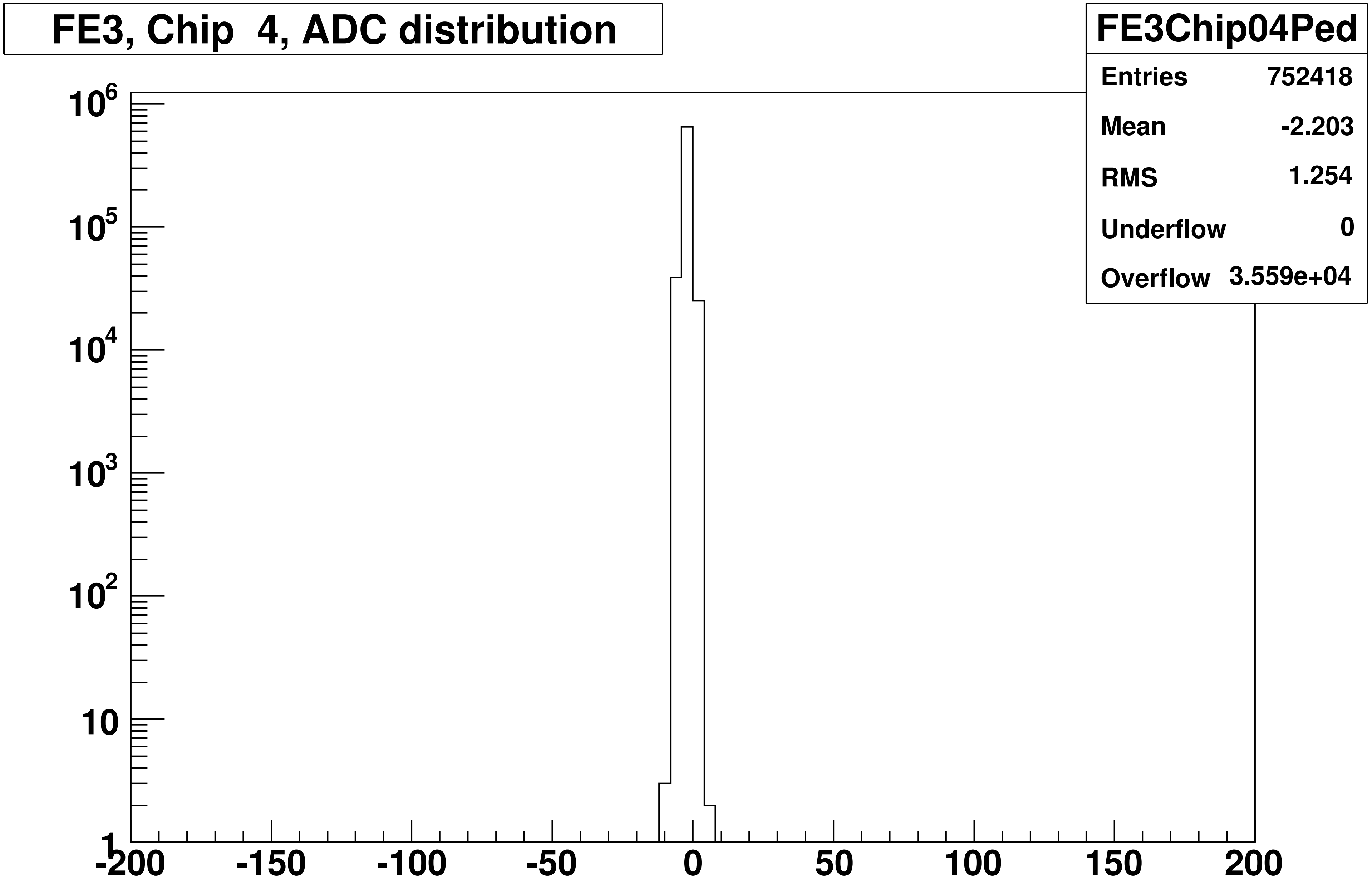
FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 752418
Mean -2.203
RMS 1.254
Underflow 0
Overflow 3.559e+04
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$10^5$
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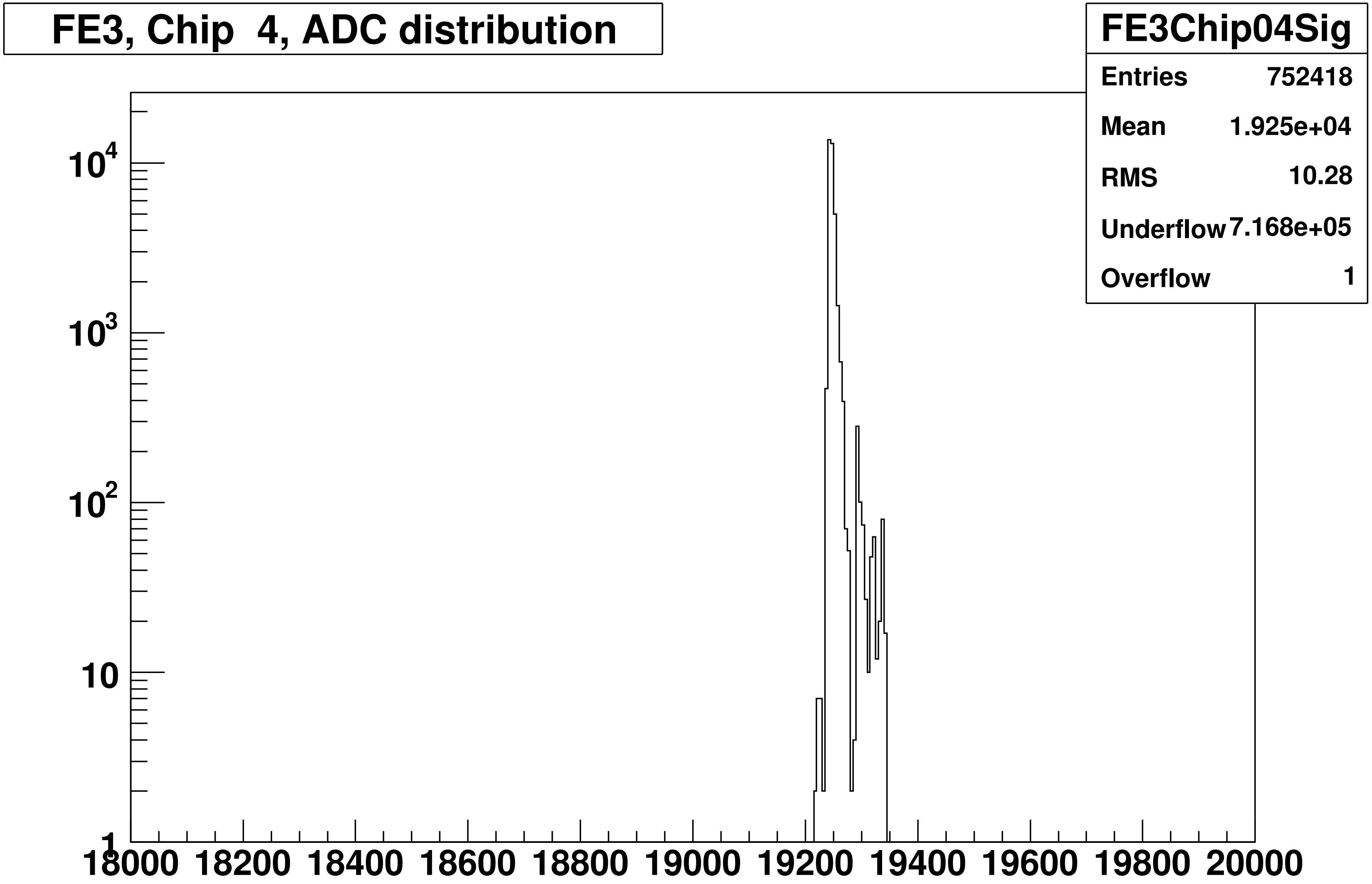

FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 752418
Mean 1.925e+04
RMS 10.28
Underflow 7.168e+05
Overflow 1
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10^3
10^2
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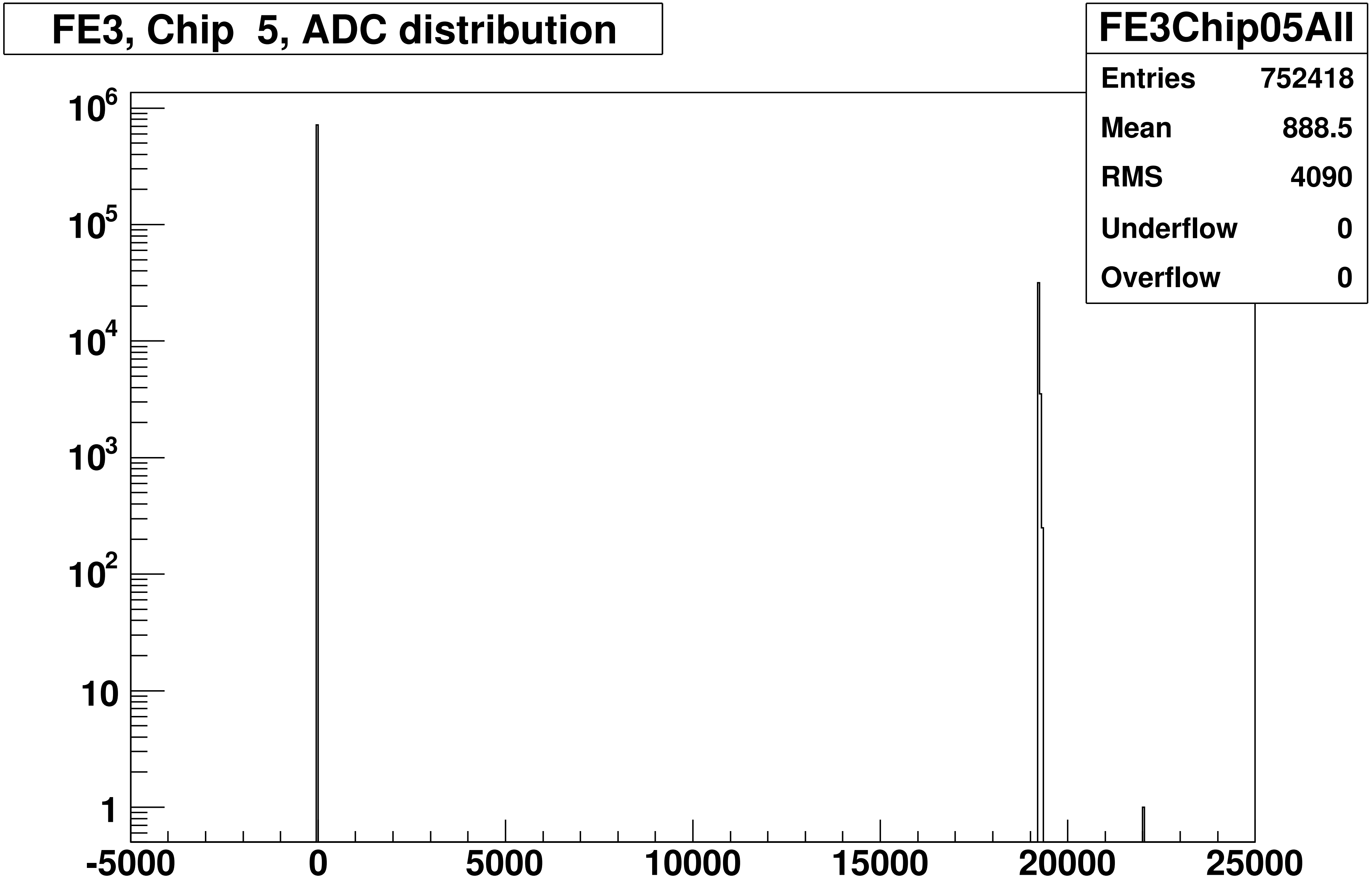
FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 752418
Mean 888.5
RMS 4090
Underflow 0
Overflow 0
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$10^3$
$10^2$
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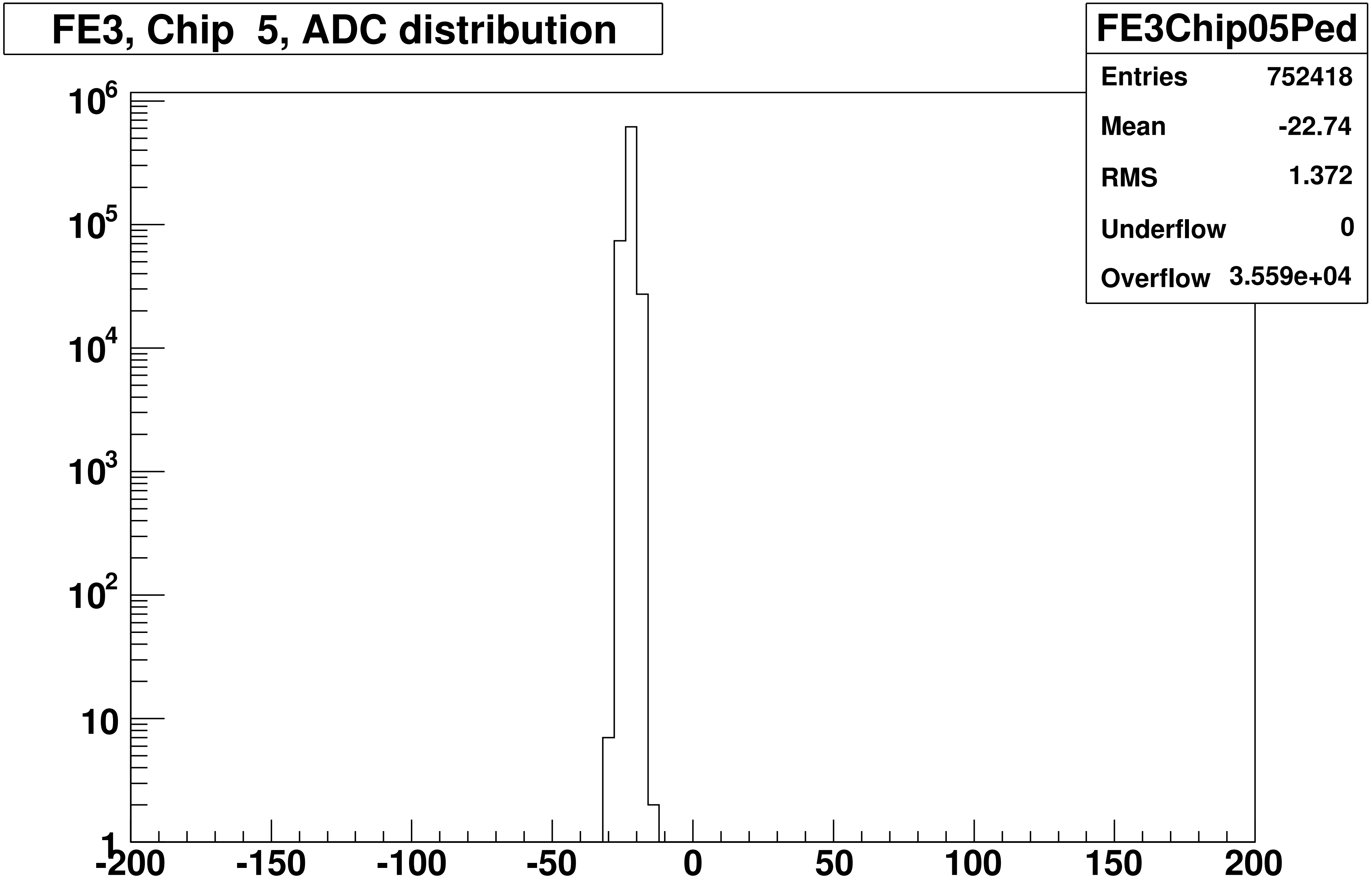
FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 752418
Mean -22.74
RMS 1.372
Underflow 0
Overflow 3.559e+04
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$10^5$
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$10^2$
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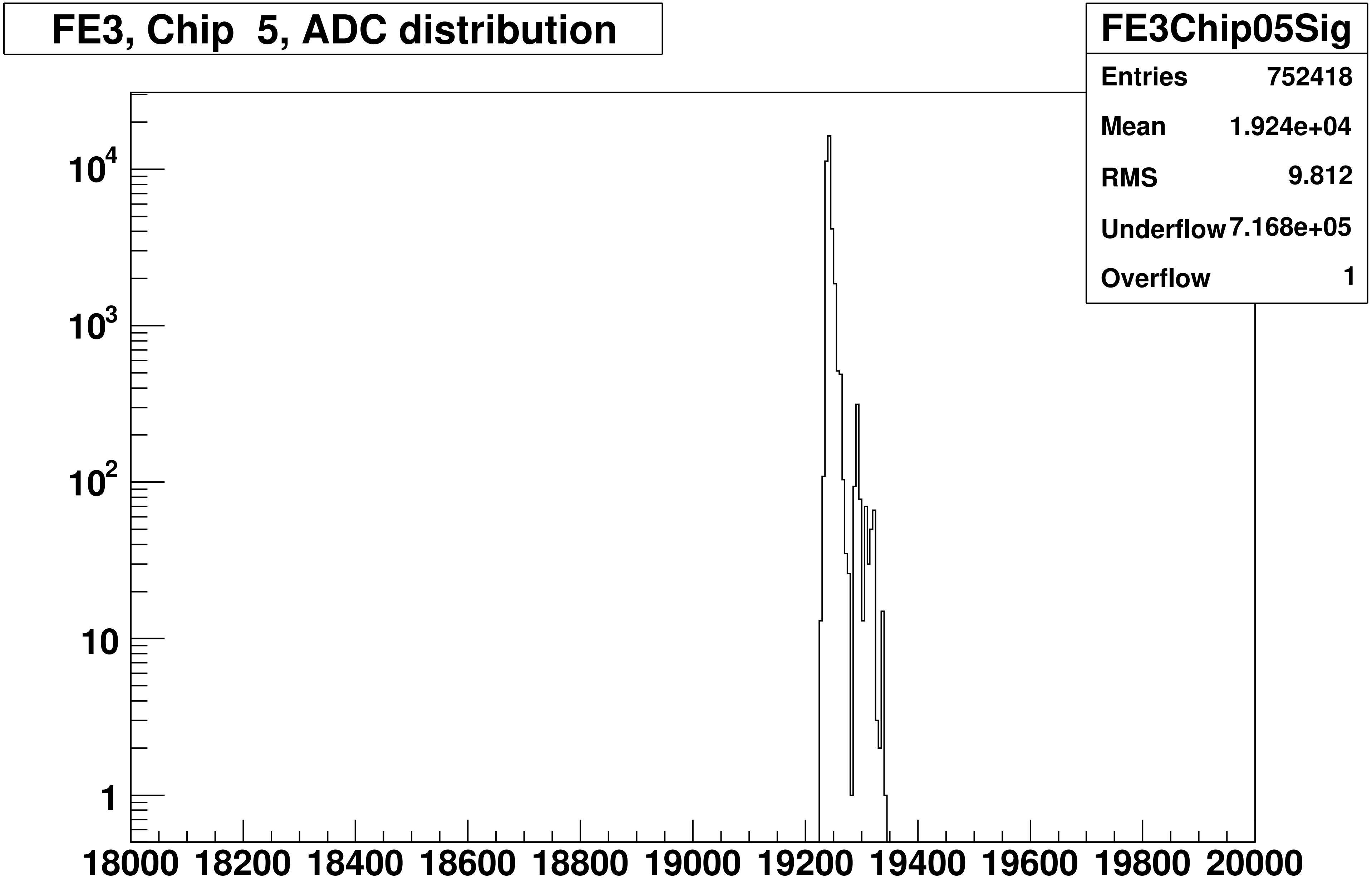

FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 752418
Mean 1.924e+04
RMS 9.812
Underflow 7.168e+05
Overflow 1
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$10^3$
$10^2$
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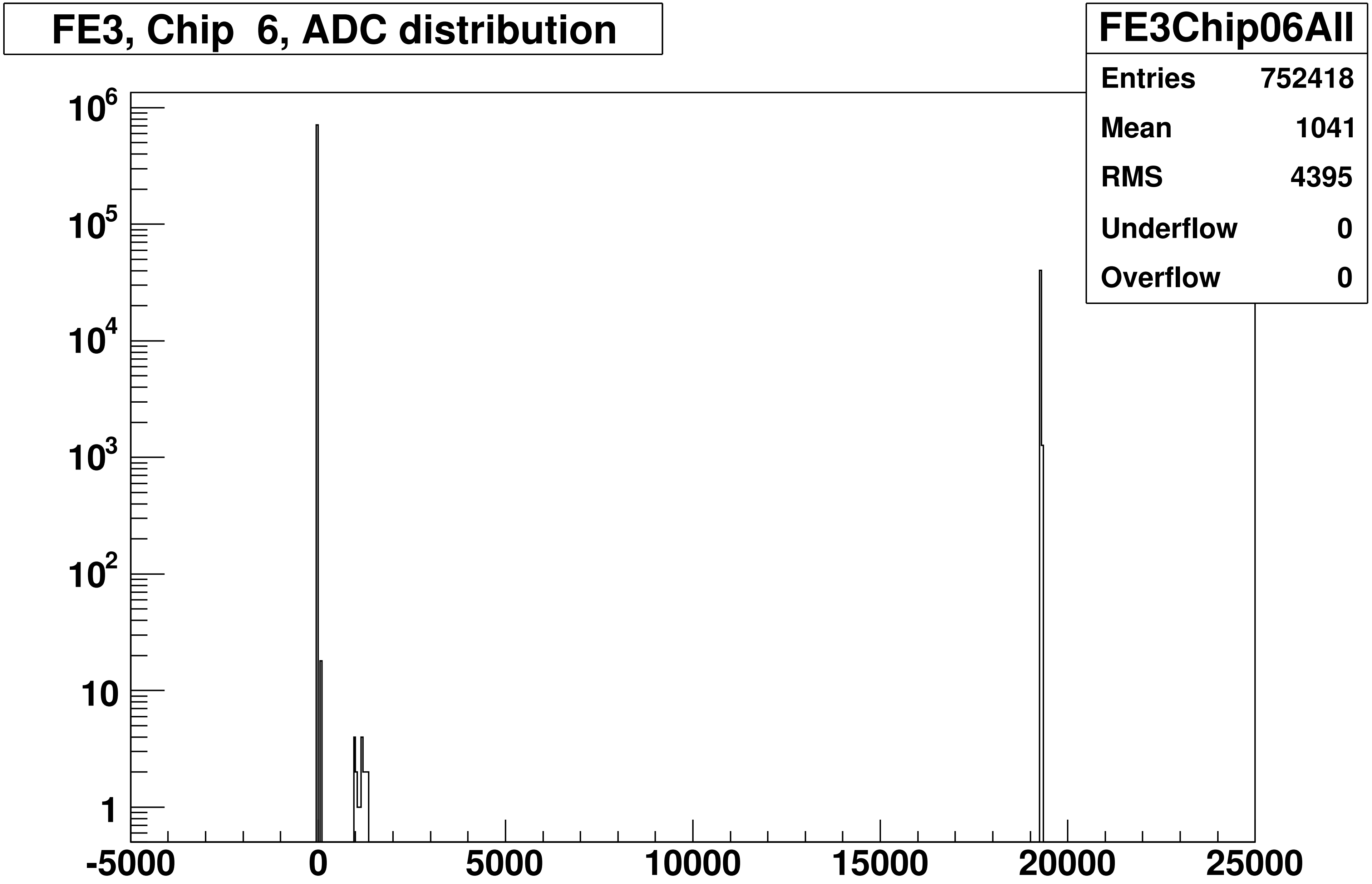
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 752418
Mean 1041
RMS 4395
Underflow 0
Overflow 0
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$10^3$
$10^2$
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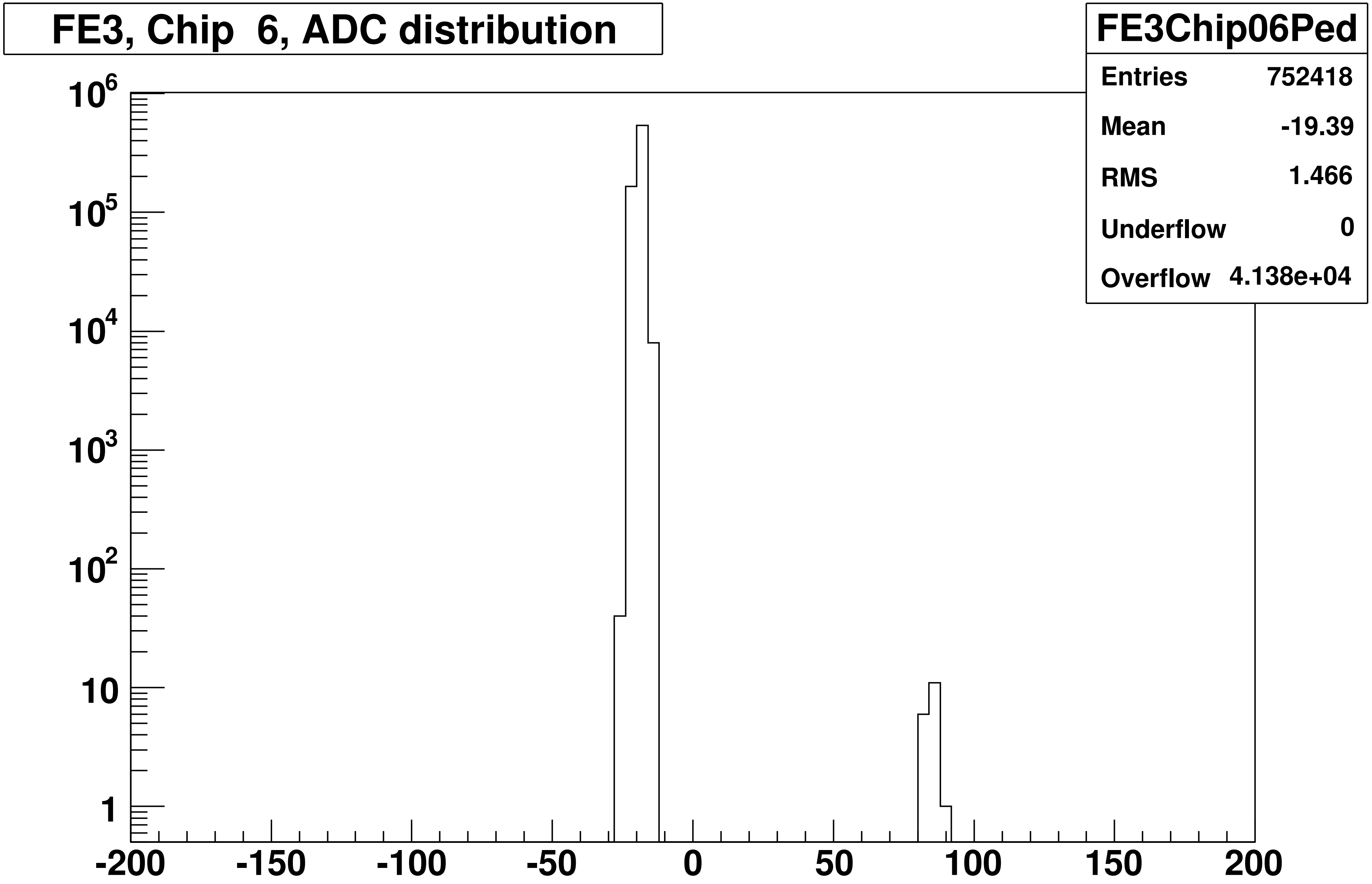
20000
25000


FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 752418
Mean -19.39
RMS 1.466
Underflow 0
Overflow 4.138e+04
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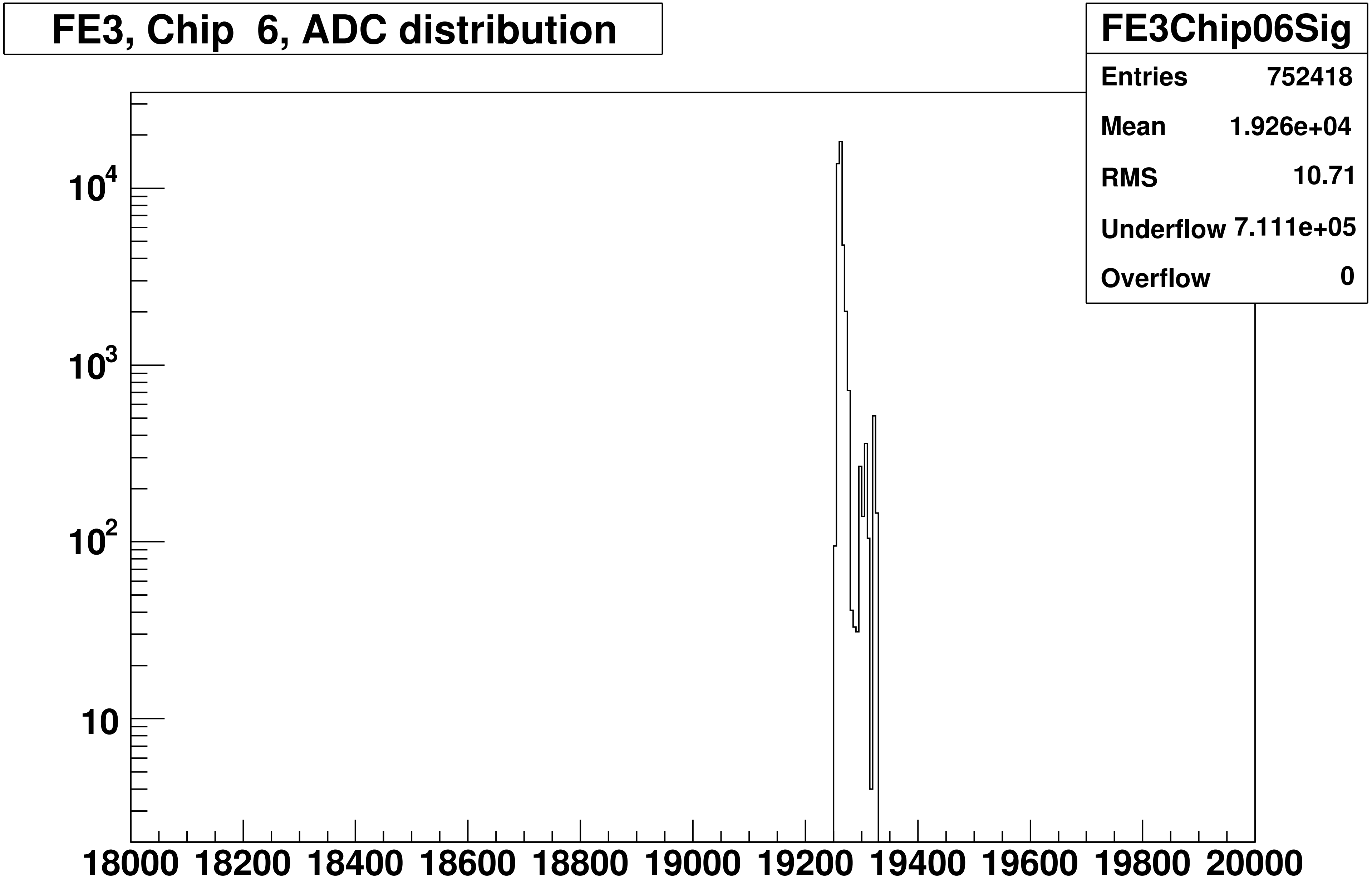
FE3, Chip  6, ADC distribution
FE3Chip06Sig
Entries 752418
Mean 1.926e+04
RMS 10.71
Underflow 7.111e+05
Overflow 0
$10^4$
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$10^2$
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19000
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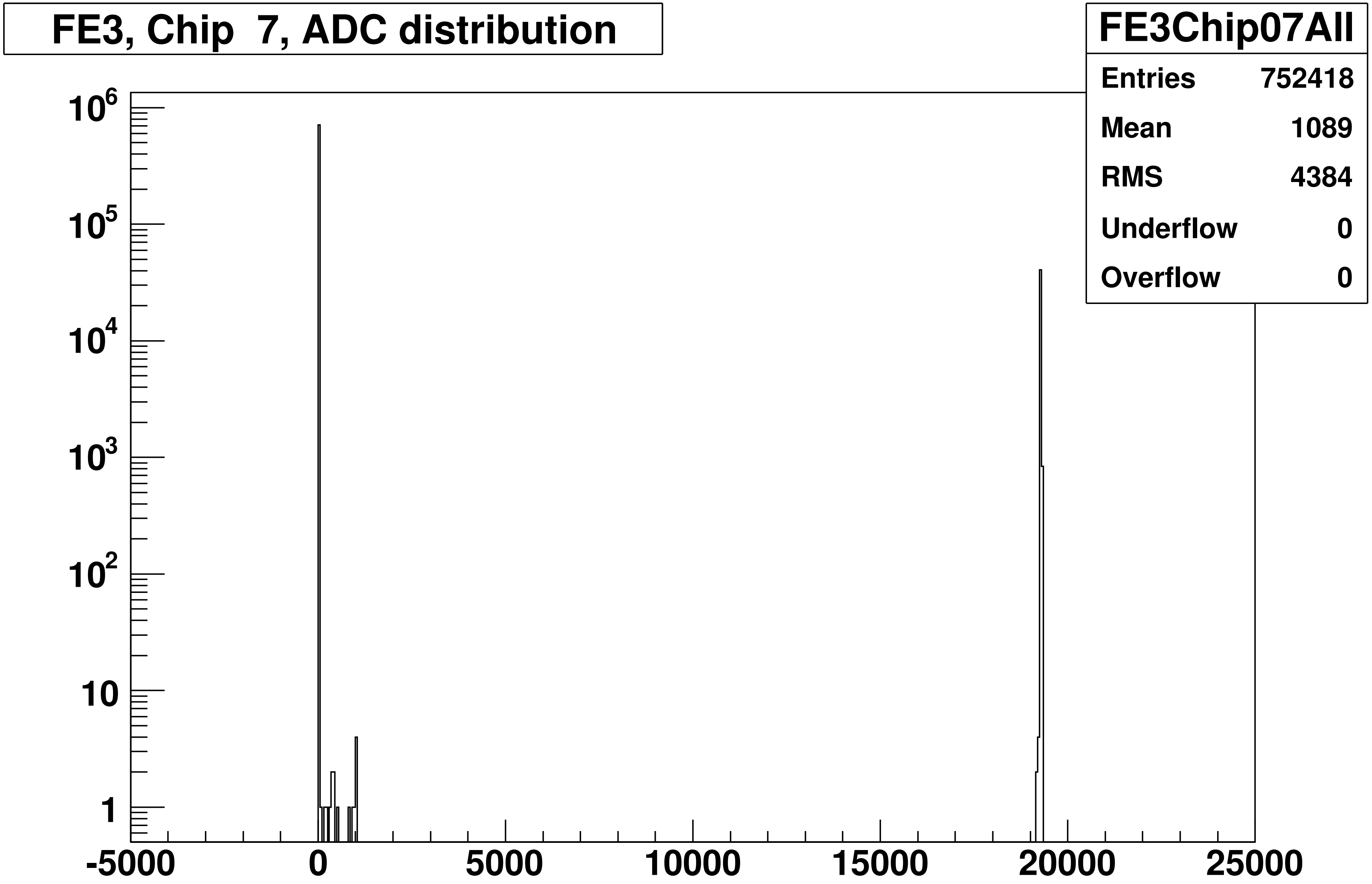
19400
19600
19800
20000


FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 752418
Mean 1089
RMS 4384
Underflow 0
Overflow 0
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$10^3$
$10^2$
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20000
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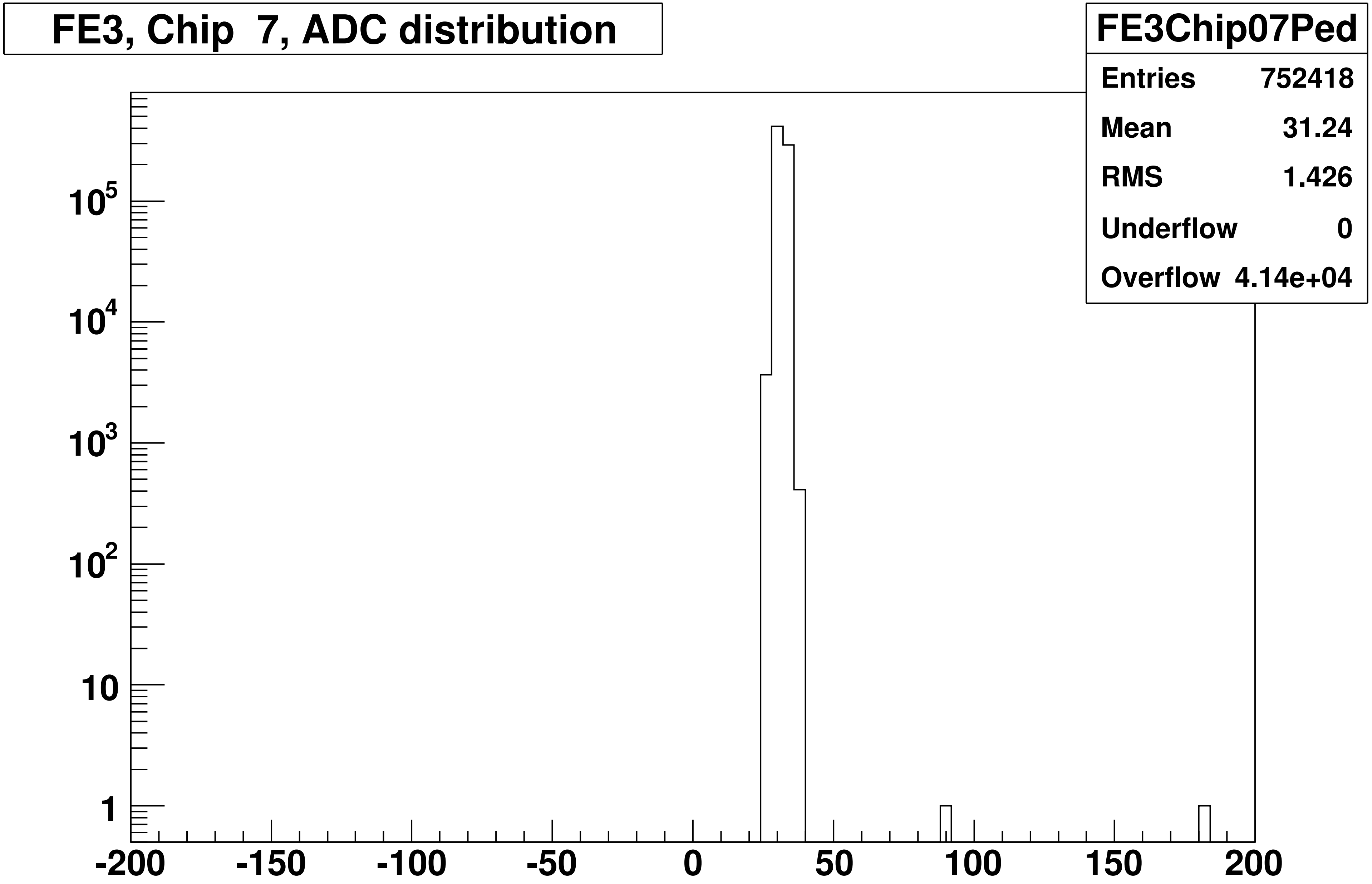
FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 752418
Mean 31.24
RMS 1.426
Underflow 0
Overflow 4.14e+04
$10^5$
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$10^2$
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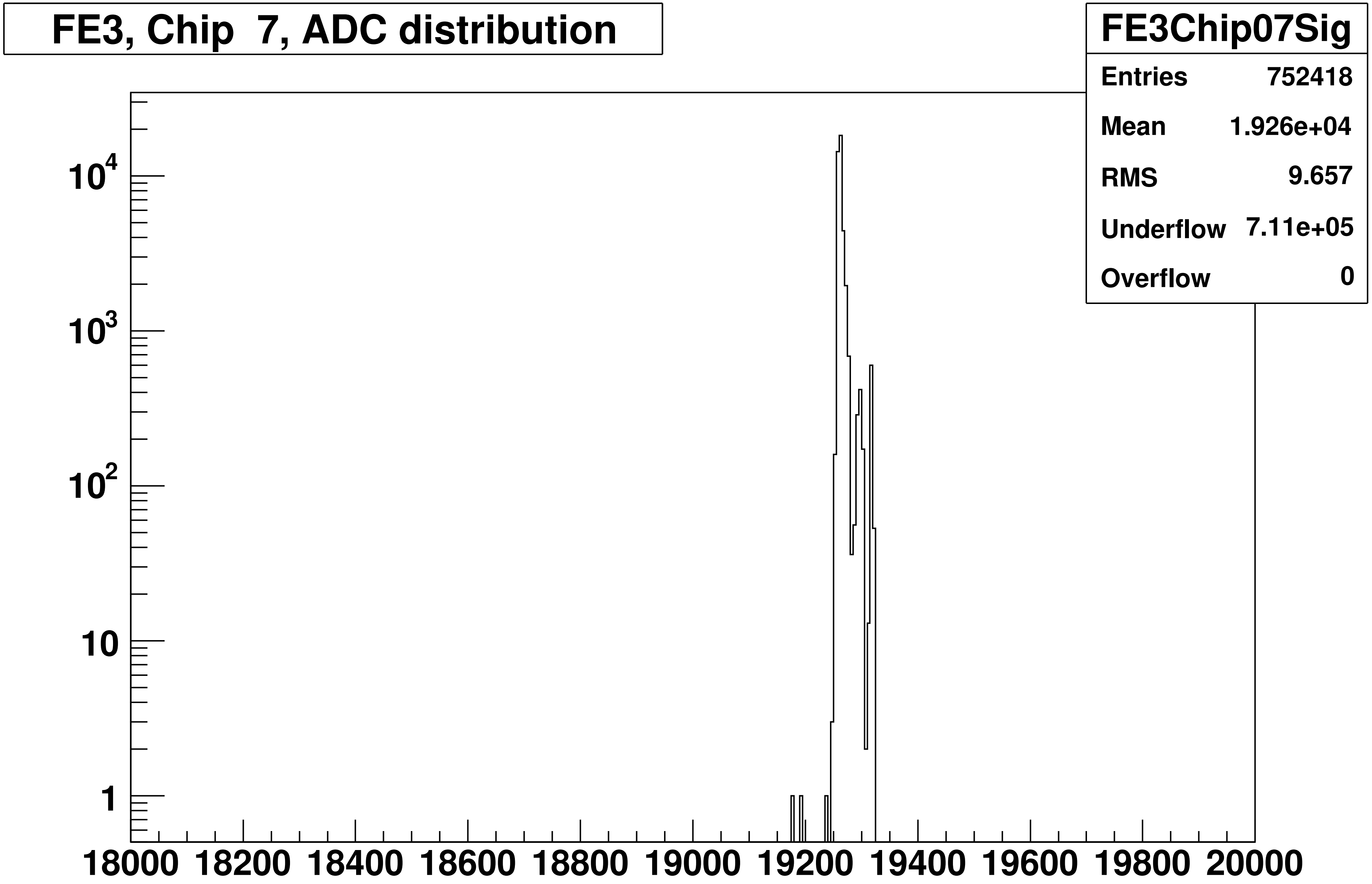
FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 752418
Mean 1.926e+04
RMS 9.657
Underflow 7.11e+05
Overflow 0
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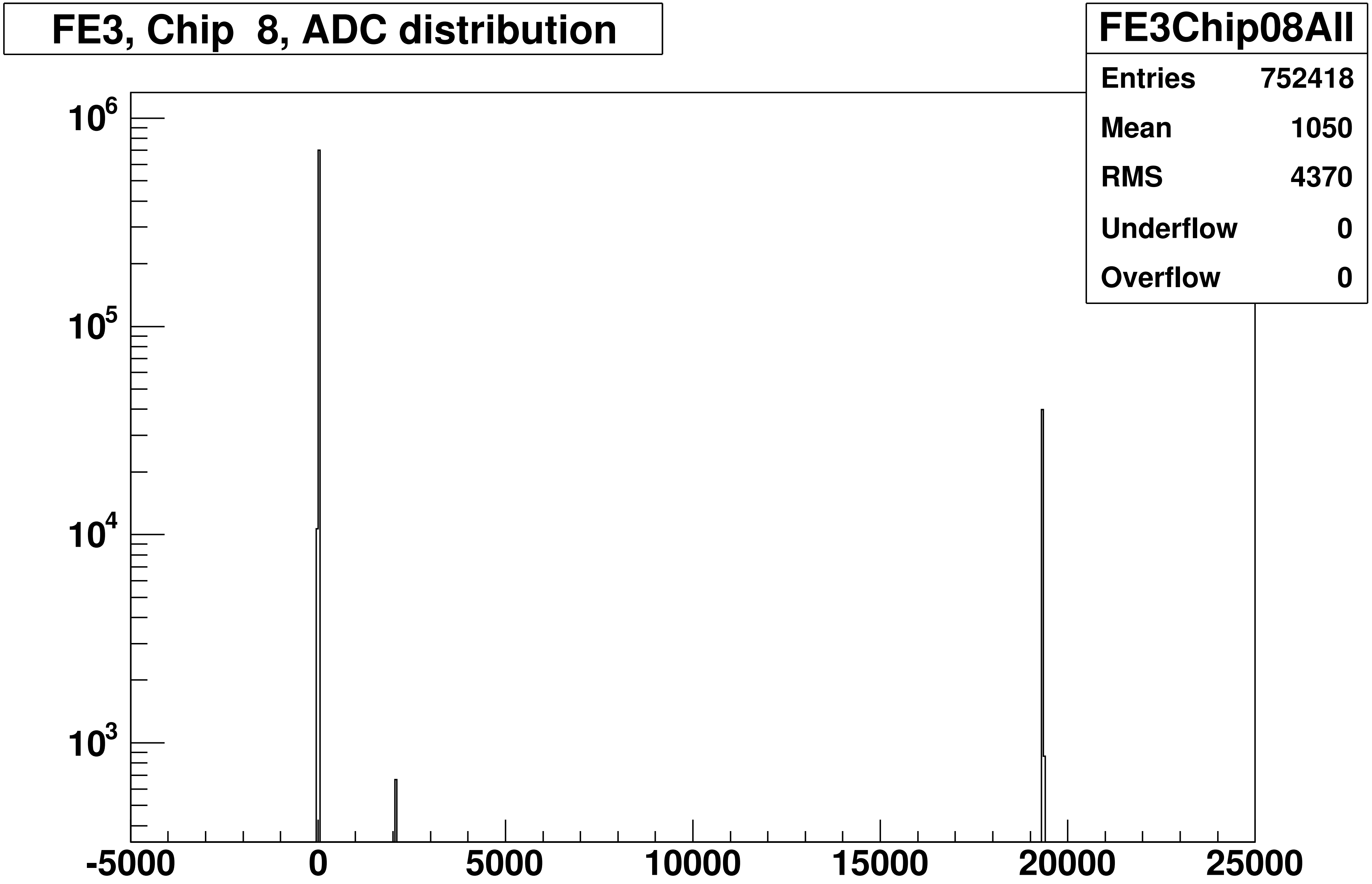
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 752418
Mean 1050
RMS 4370
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
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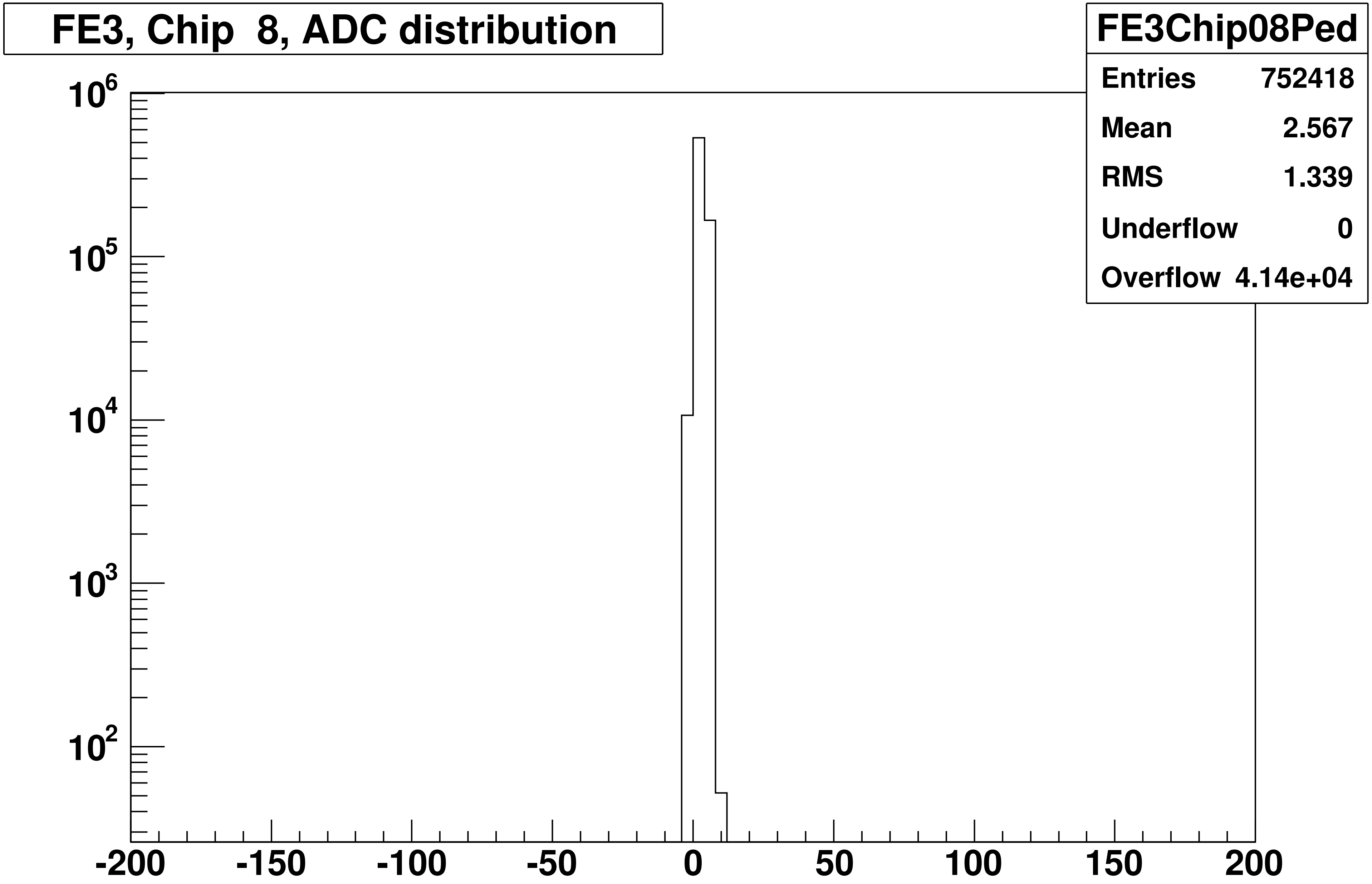
20000
25000


FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 752418
Mean 2.567
RMS 1.339
Underflow 0
Overflow 4.14e+04
$10^6$
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$10^3$
$10^2$
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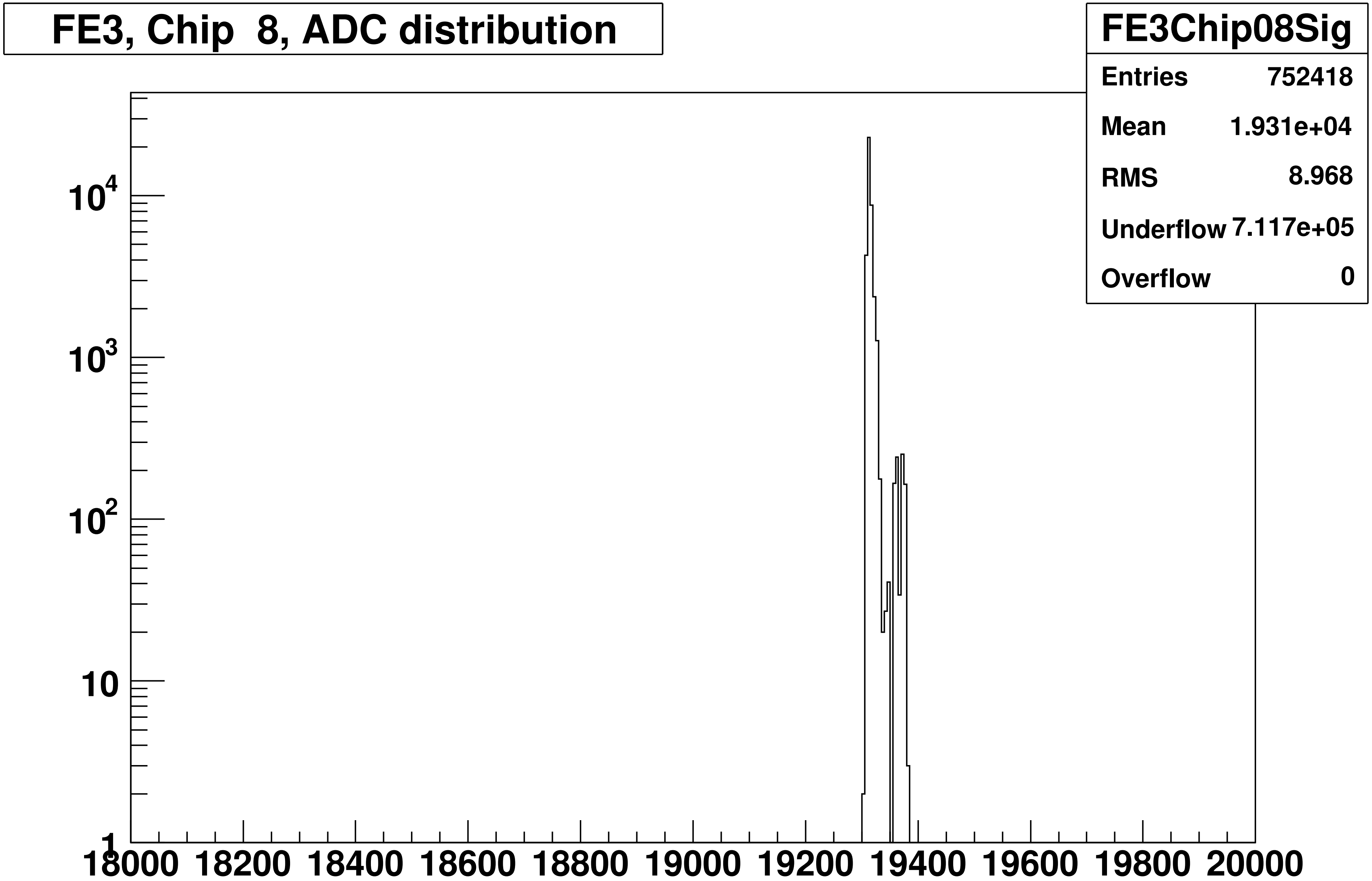
150
200


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 752418
Mean 1.931e+04
RMS 8.968
Underflow 7.117e+05
Overflow 0
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$10^3$
$10^2$
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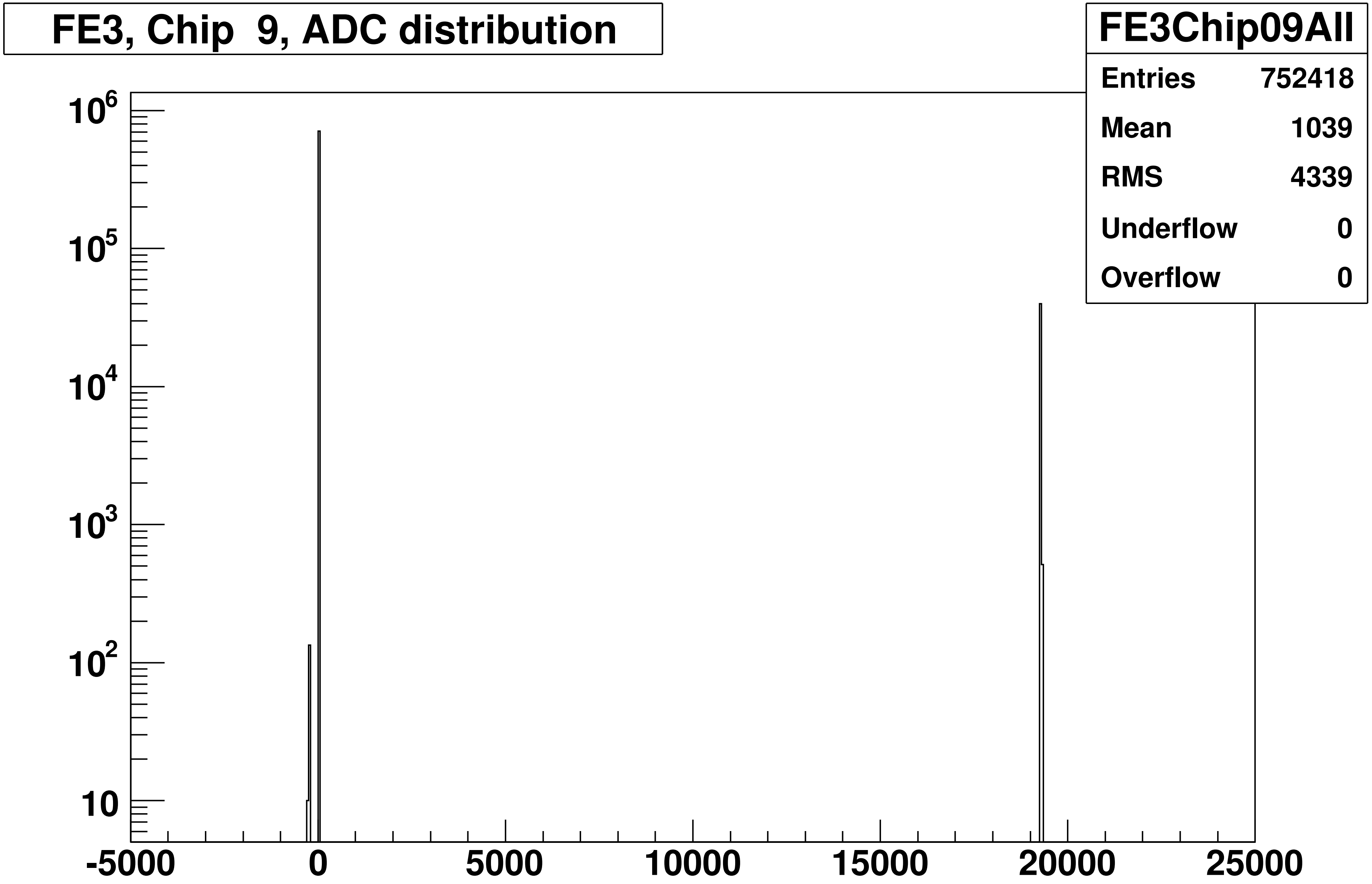
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 752418
Mean 1039
RMS 4339
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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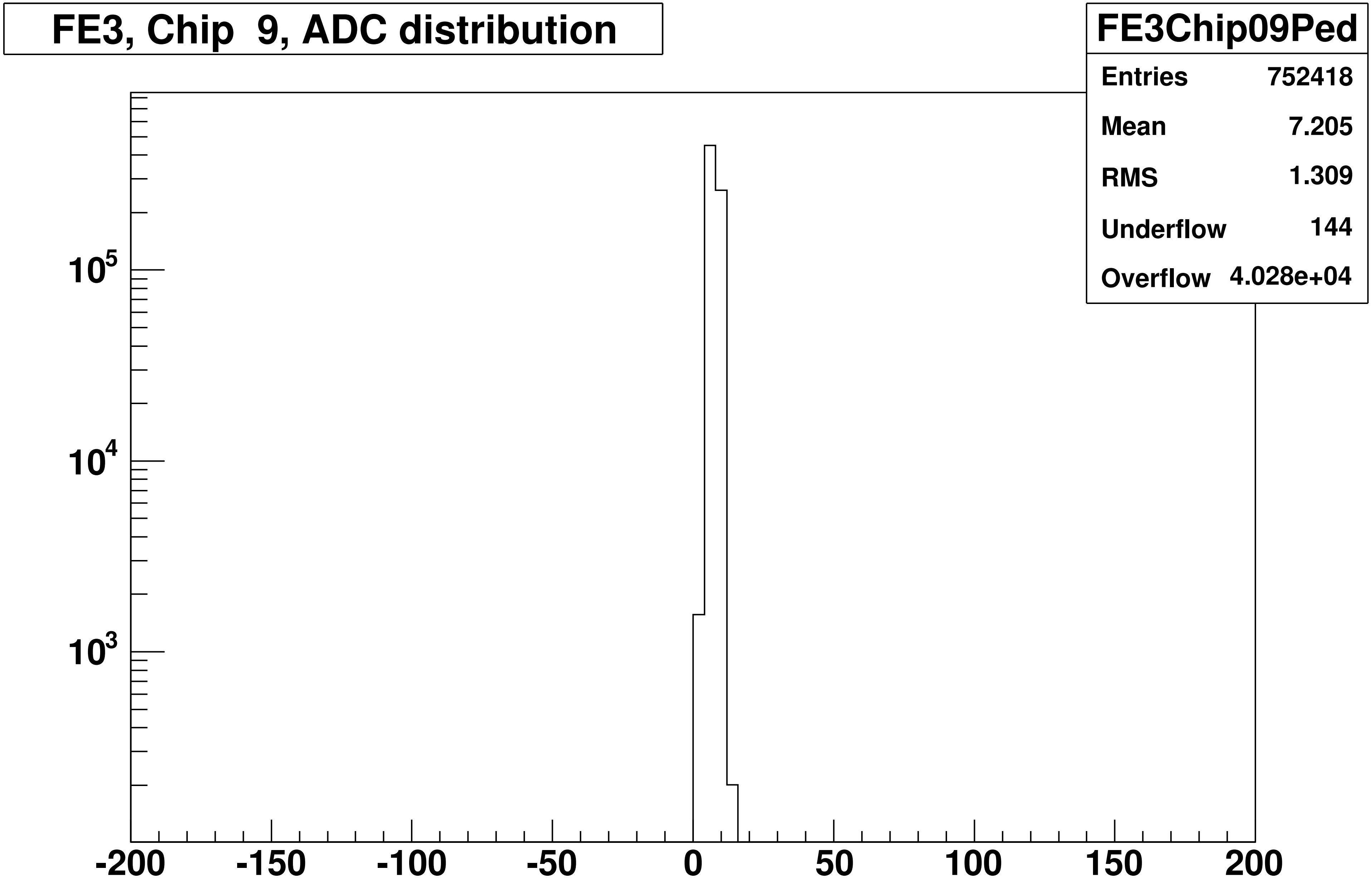
20000
25000


FE3, Chip  9, ADC distribution
FE3Chip09Ped
Entries 752418
Mean 7.205
RMS 1.309
Underflow 144
Overflow 4.028e+04
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$10^4$
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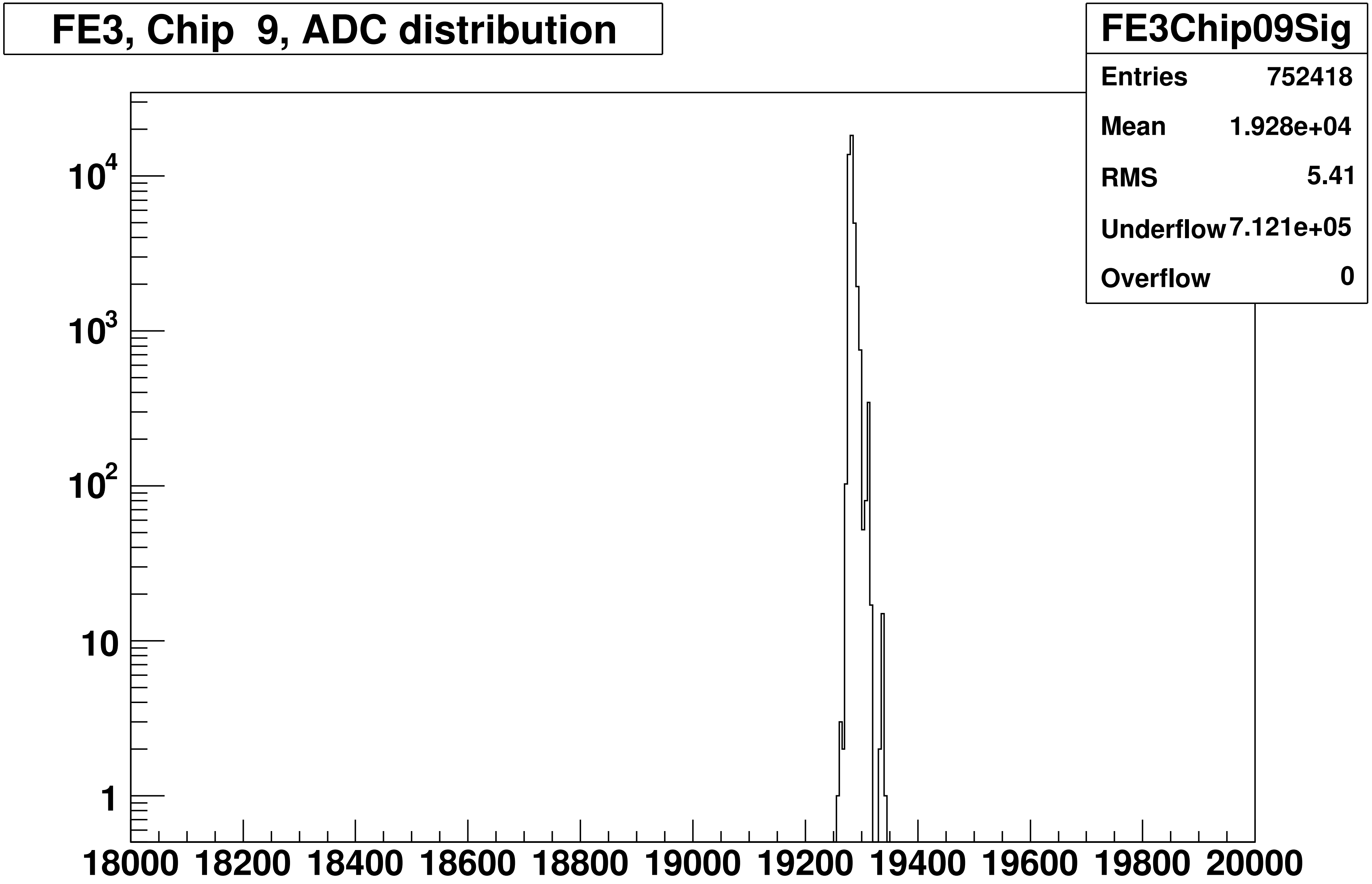
200



FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 752418
Mean 1.928e+04
RMS 5.41
Underflow 7.121e+05
Overflow 0
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$10^3$
$10^2$
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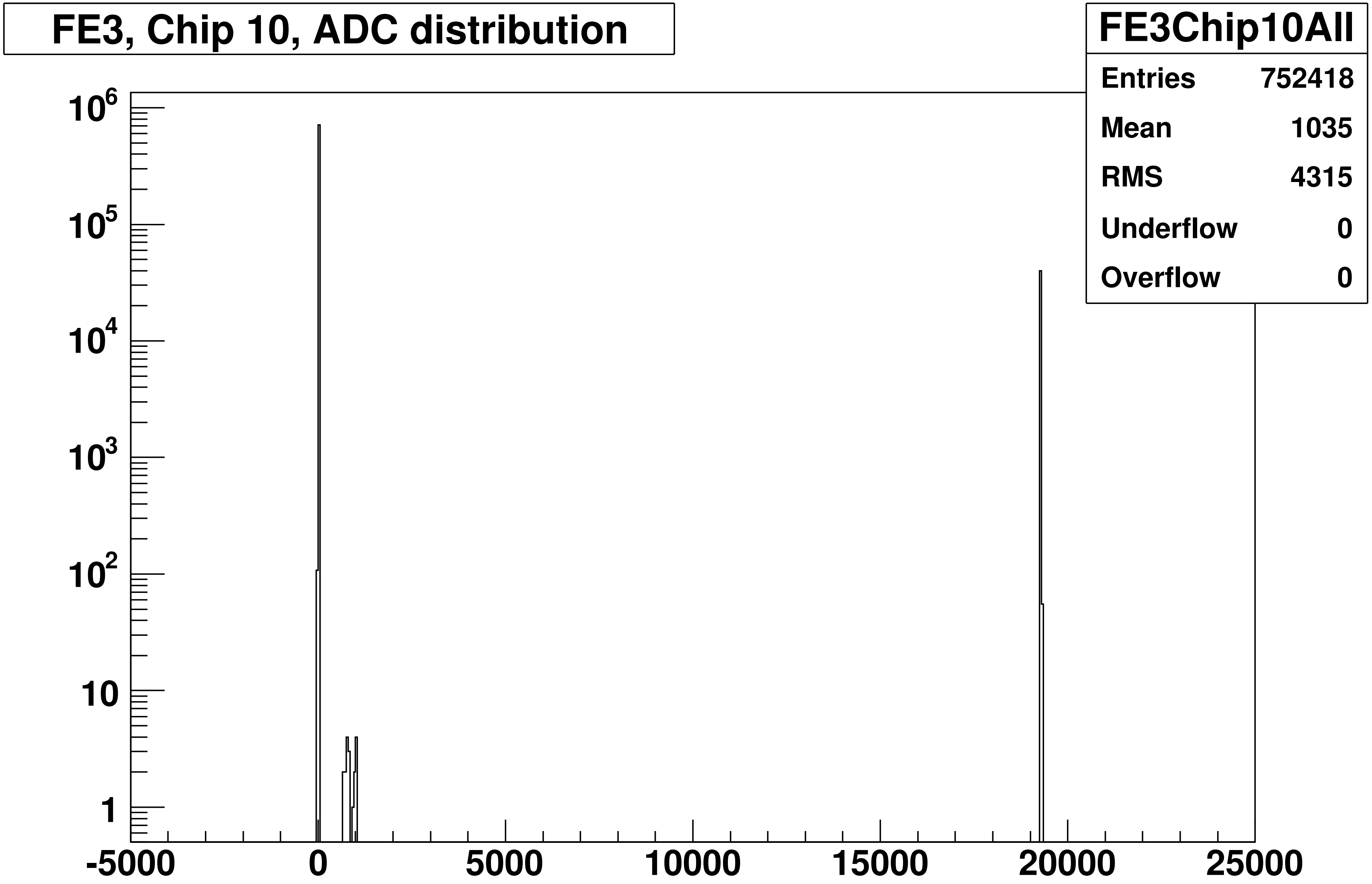
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 752418
Mean 1035
RMS 4315
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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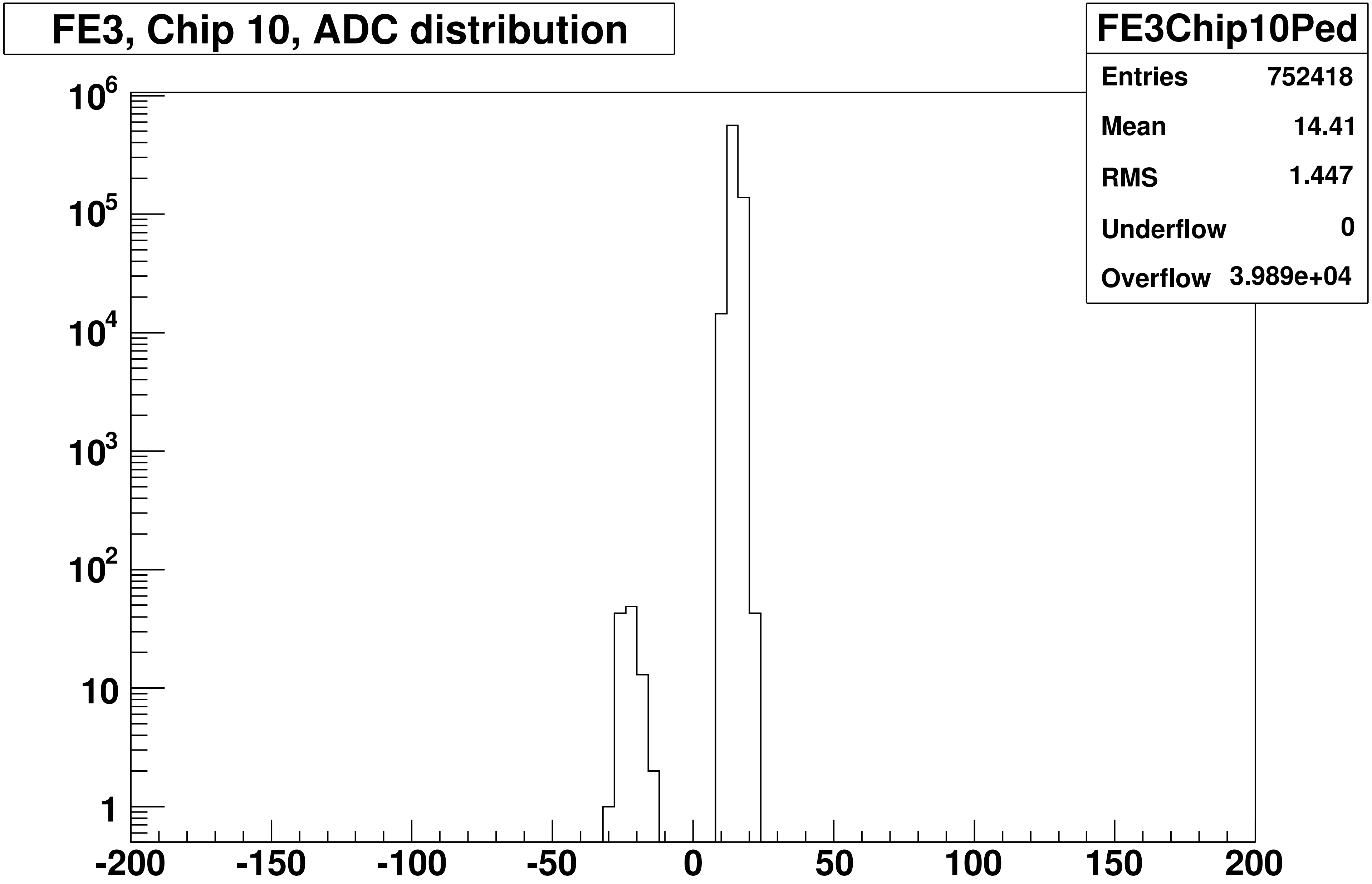
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 752418
Mean 14.41
RMS 1.447
Underflow 0
Overflow 3.989e+04
$10^6$
$10^5$
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$10^2$
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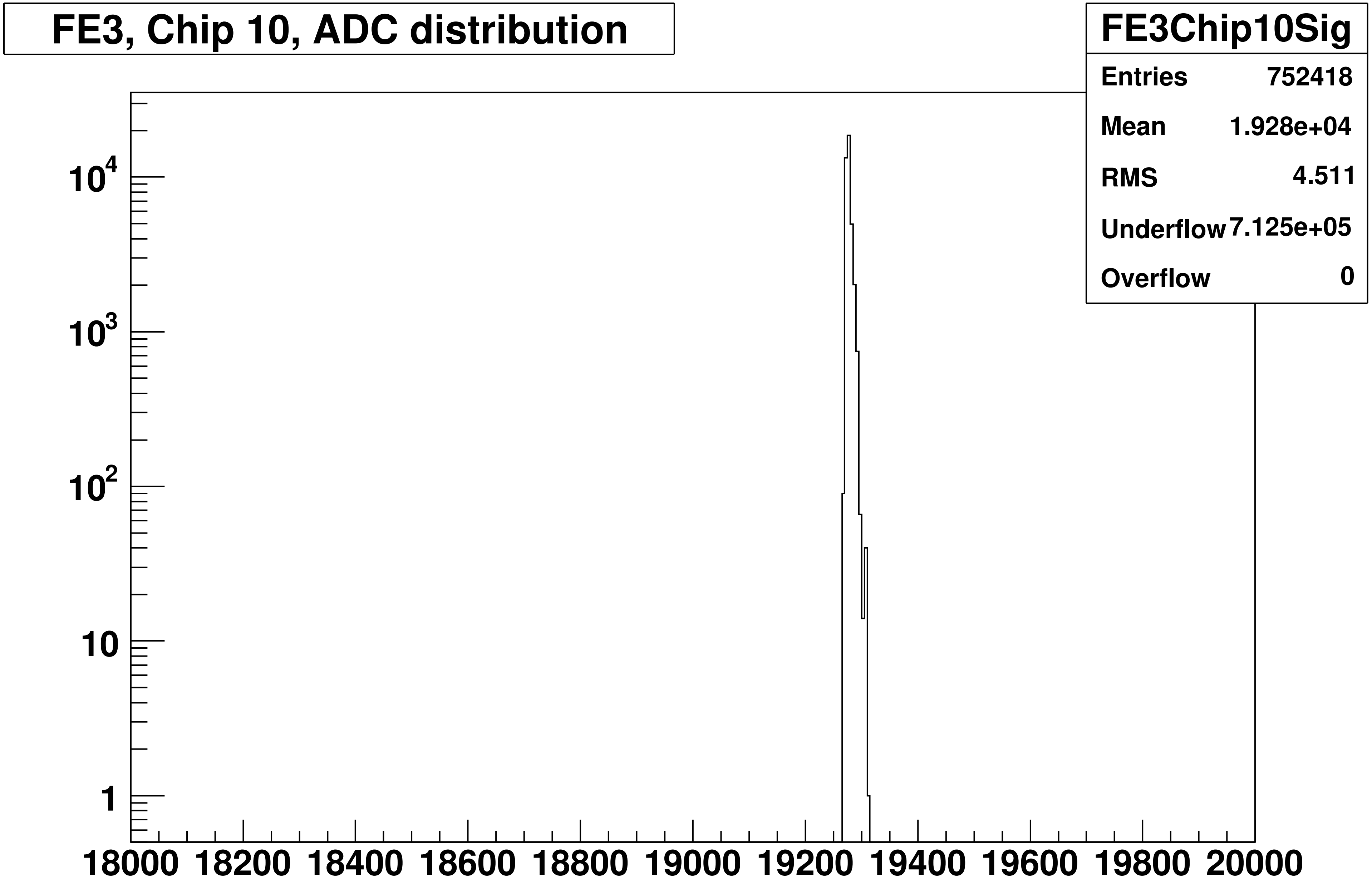

FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 752418
Mean 1.928e+04
RMS 4.511
Underflow 7.125e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
18200
18400
18600
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19000
19200
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19600
19800
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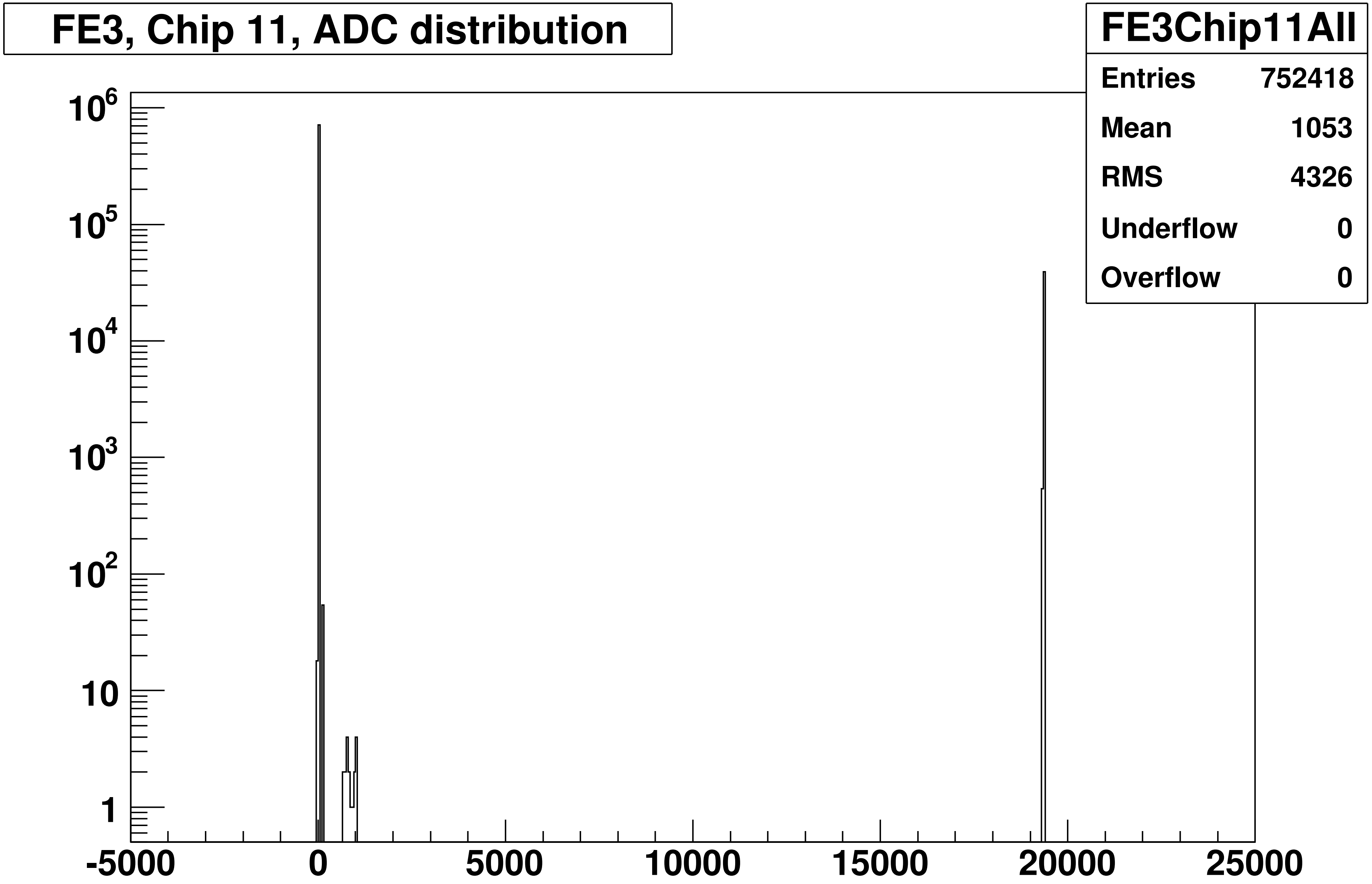
FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 752418
Mean 1053
RMS 4326
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
10
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0
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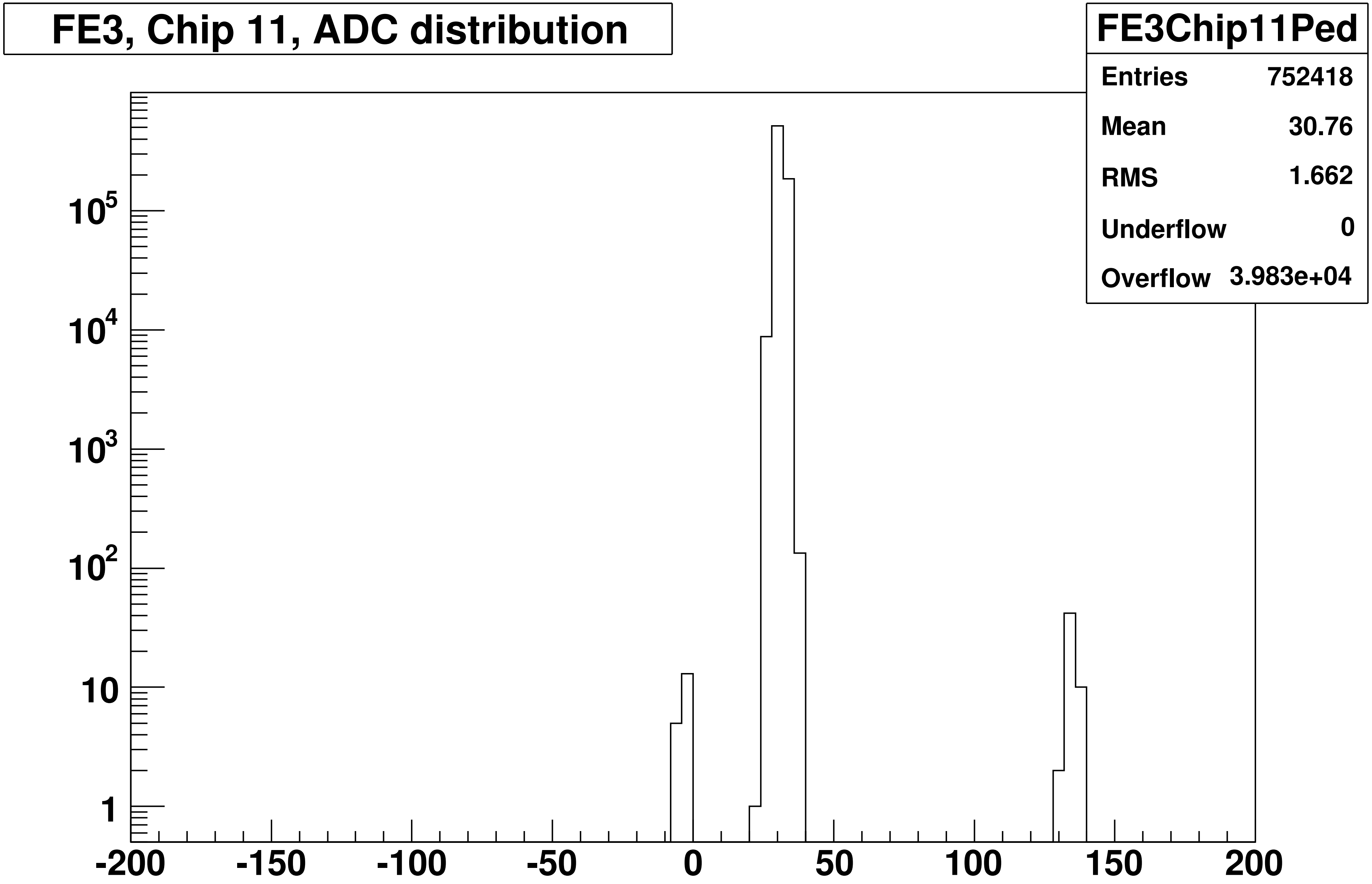
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 752418
Mean 30.76
RMS 1.662
Underflow 0
Overflow 3.983e+04
$10^5$
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$10^3$
$10^2$
10
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-100
-50
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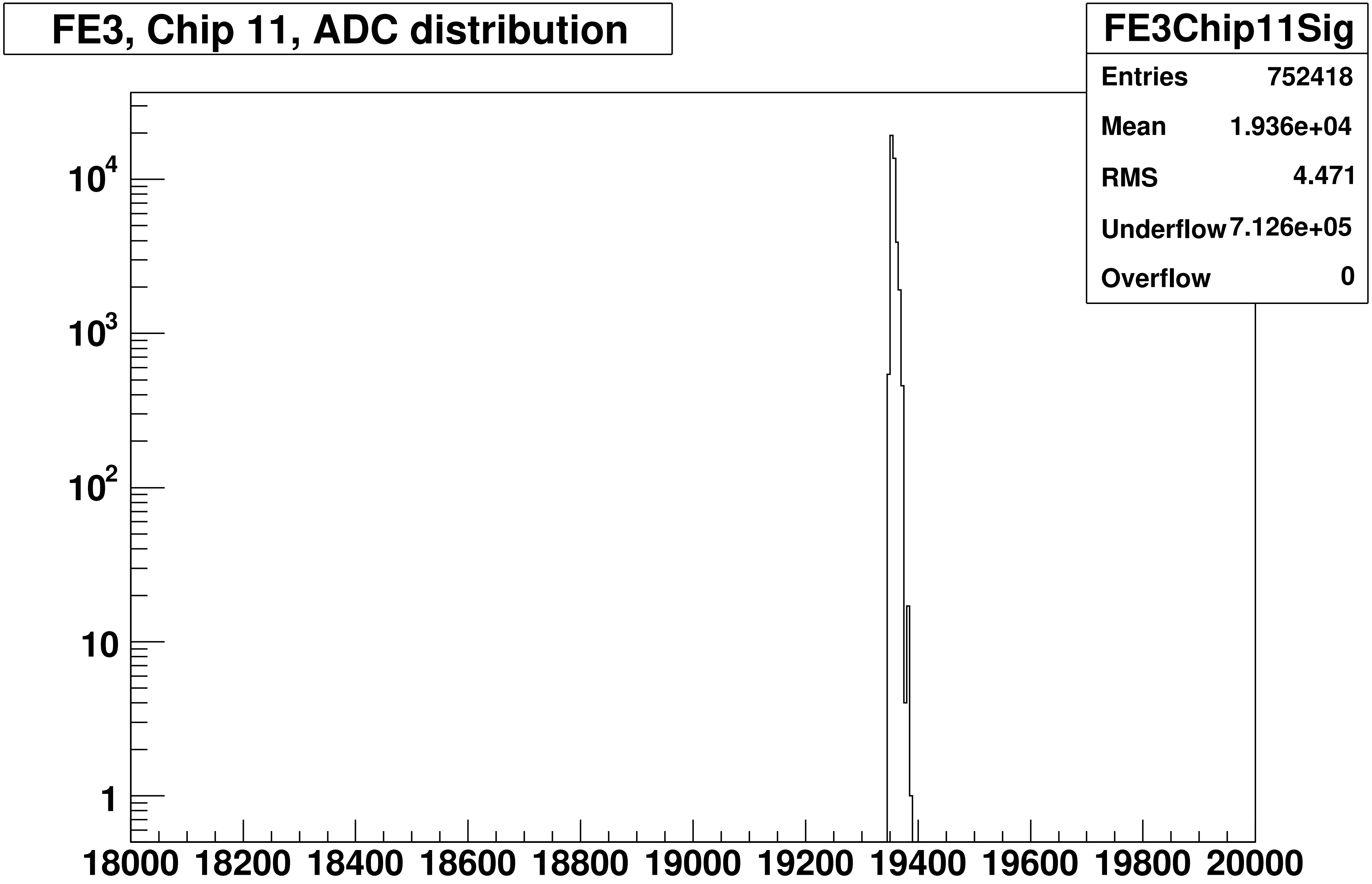
50
100
150
200



FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 752418
Mean 1.936e+04
RMS 4.471
Underflow 7.126e+05
Overflow 0
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$10^3$
$10^2$
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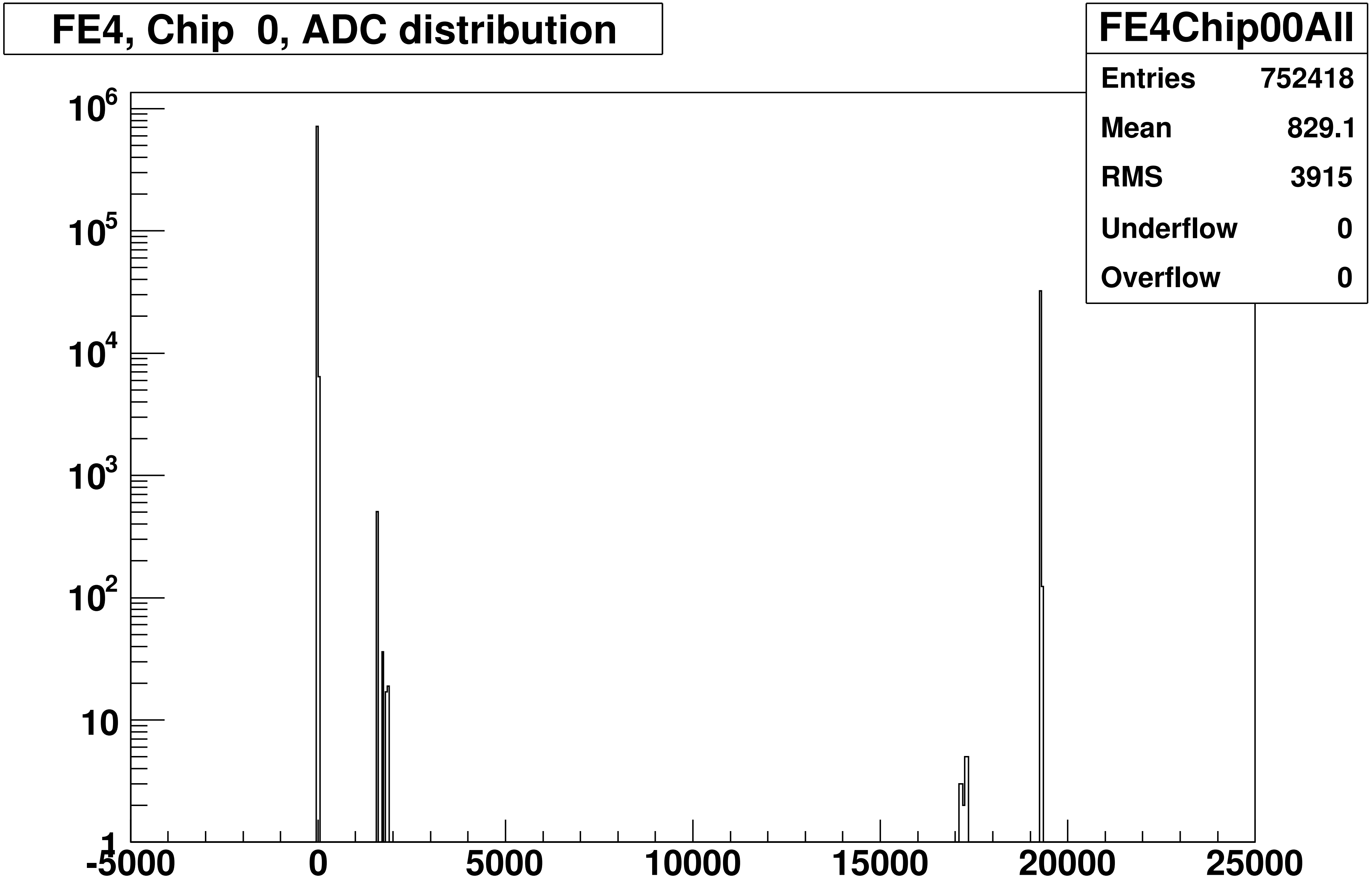

FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 752418
Mean 829.1
RMS 3915
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
10
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0
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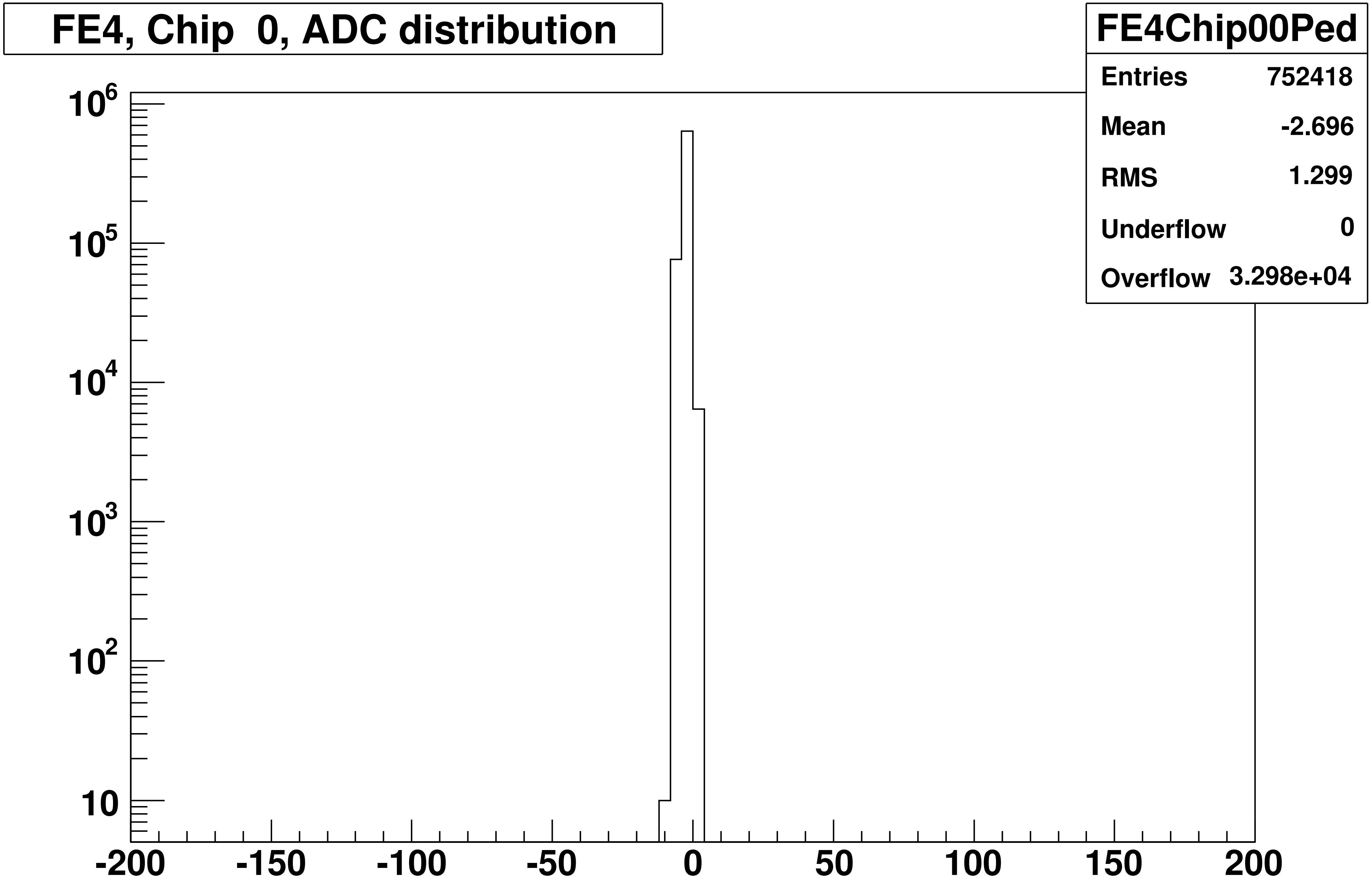
20000
25000


FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 752418
Mean -2.696
RMS 1.299
Underflow 0
Overflow 3.298e+04
$10^6$
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$10^3$
$10^2$
10
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-100
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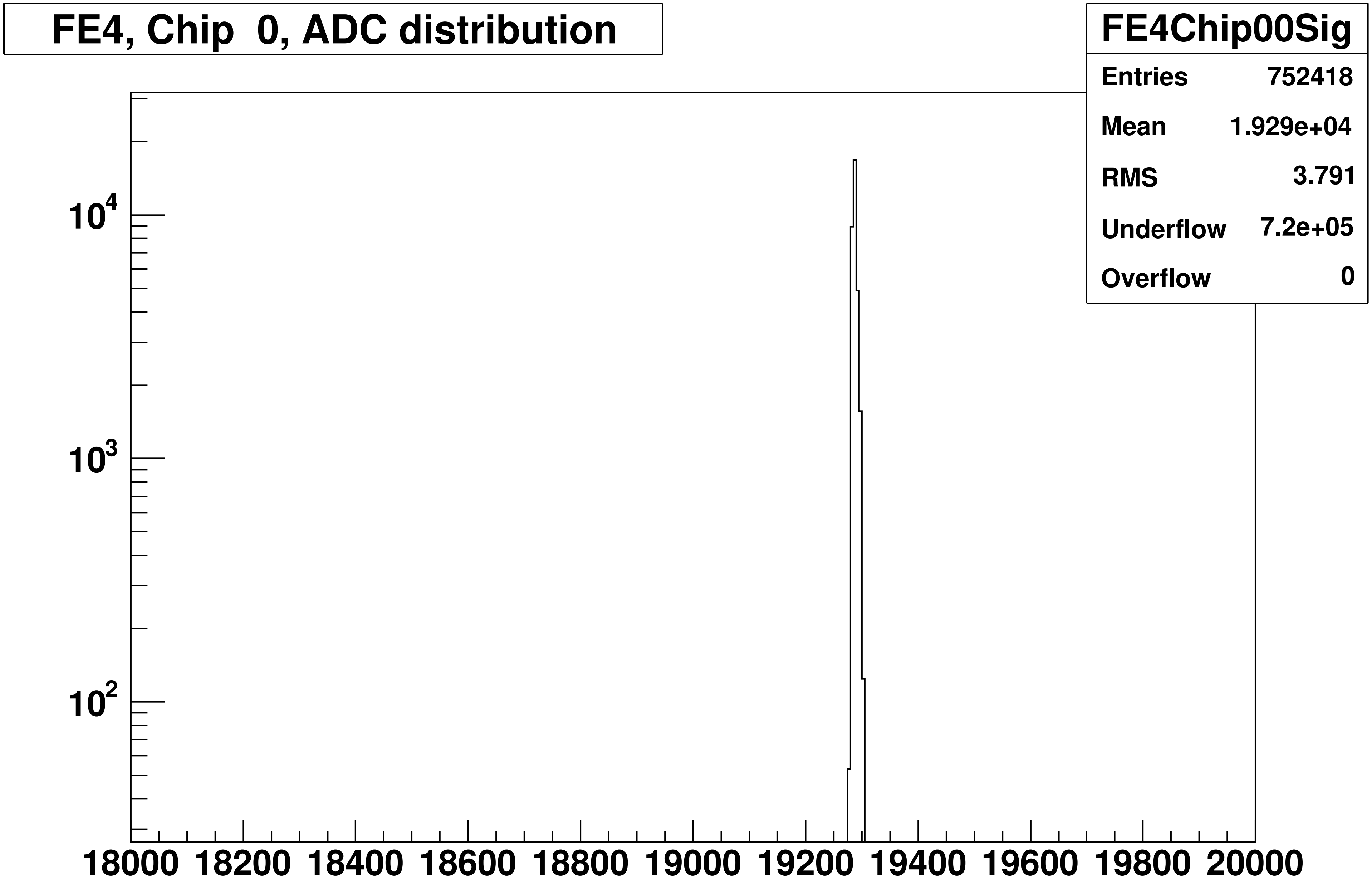
150
200


FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 752418
Mean 1.929e+04
RMS 3.791
Underflow 7.2e+05
Overflow 0
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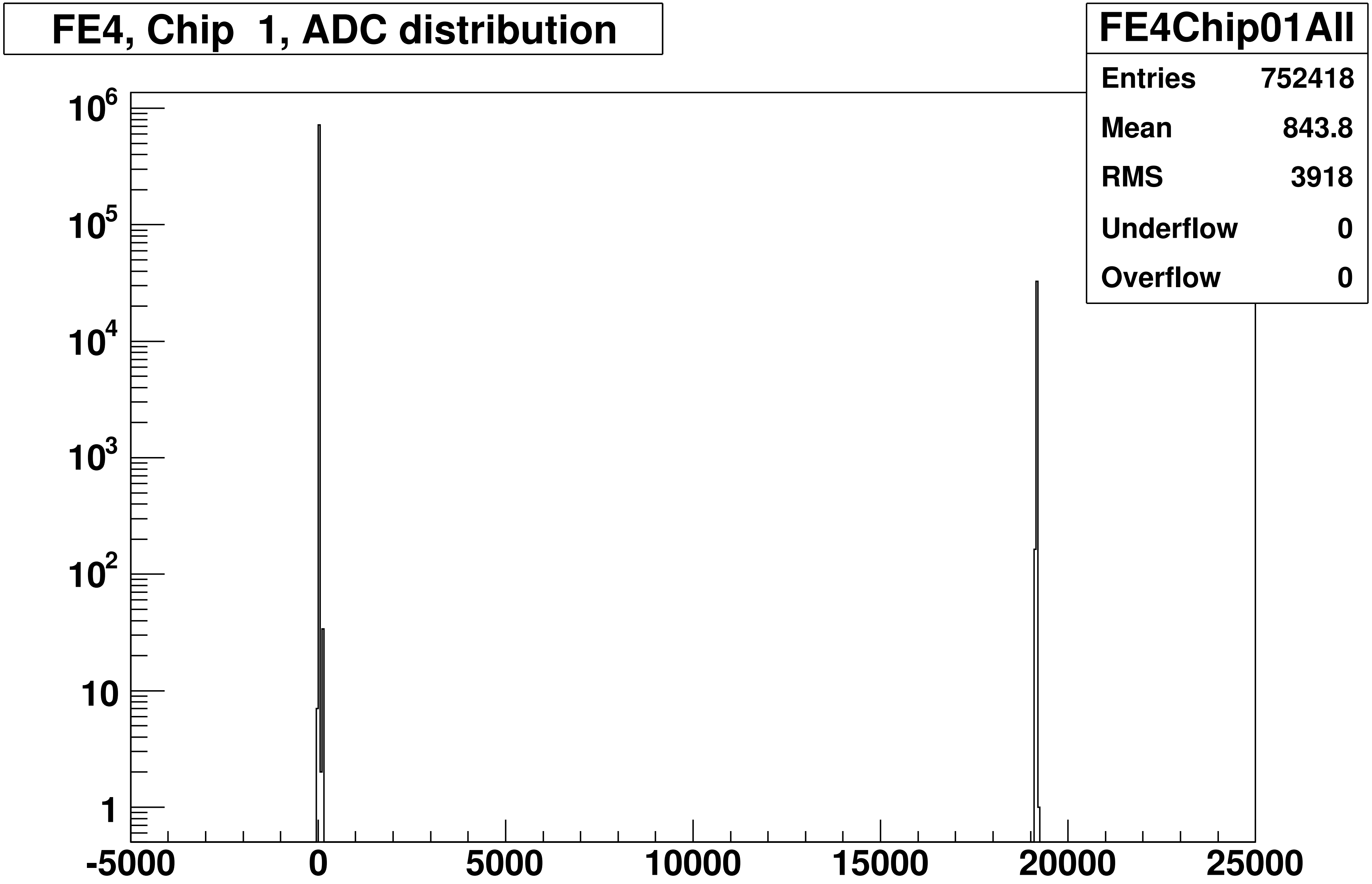
10^3
10^2
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 752418
Mean 843.8
RMS 3918
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
10
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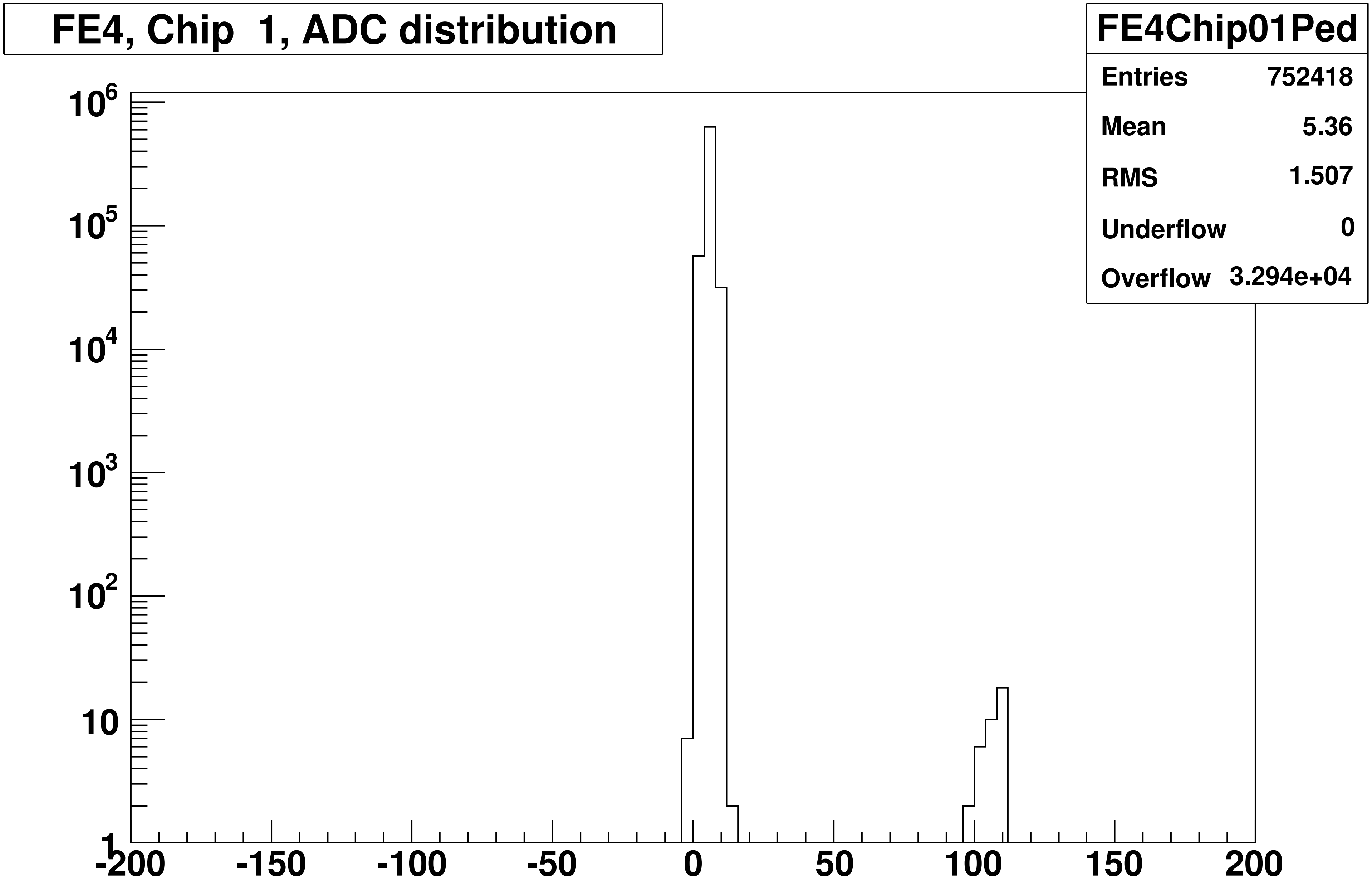
20000
25000


FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 752418
Mean 5.36
RMS 1.507
Underflow 0
Overflow 3.294e+04
$10^6$
$10^5$
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$10^2$
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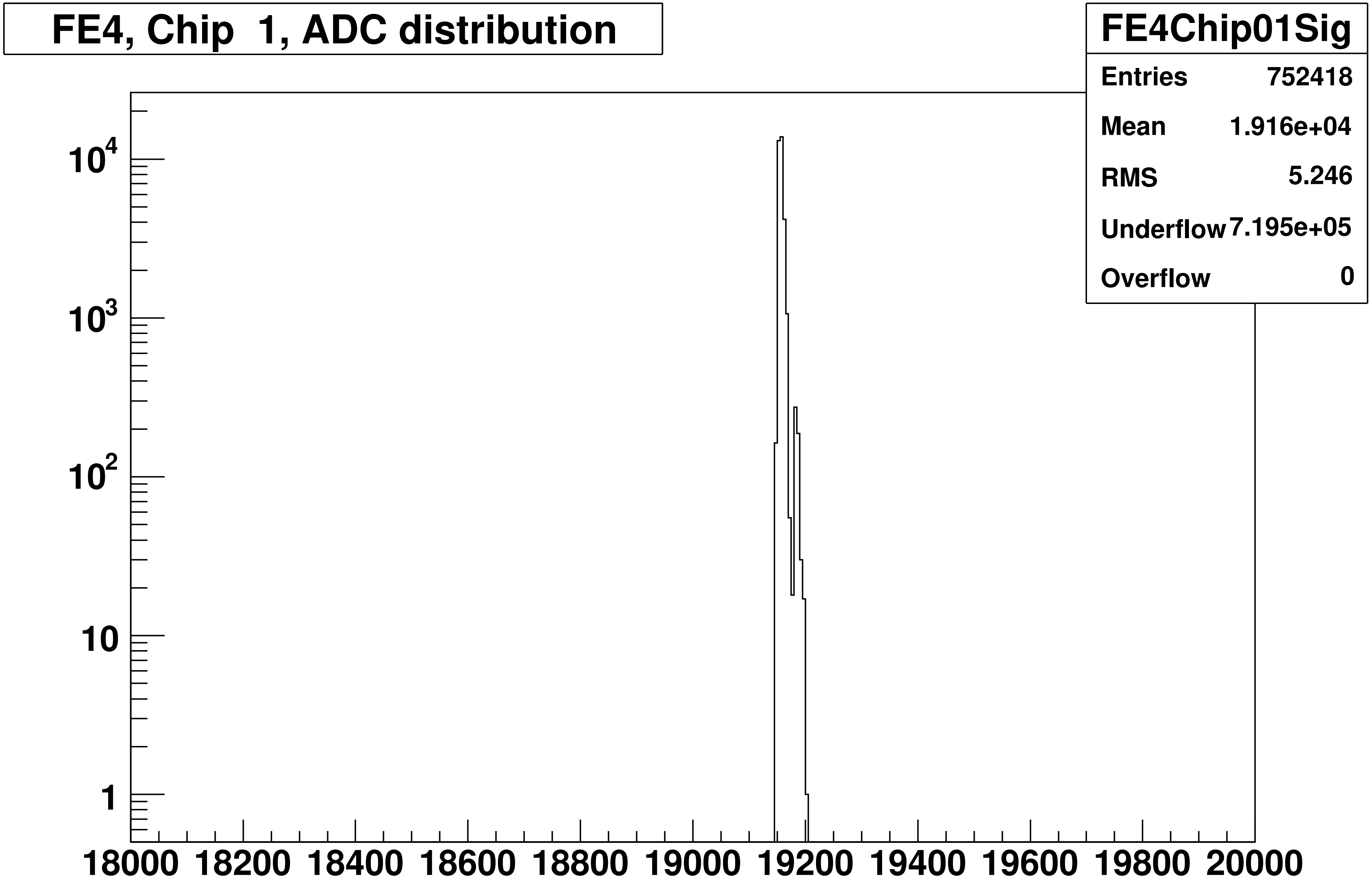
100
150
200


FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 752418
Mean 1.916e+04
RMS 5.246
Underflow 7.195e+05
Overflow 0
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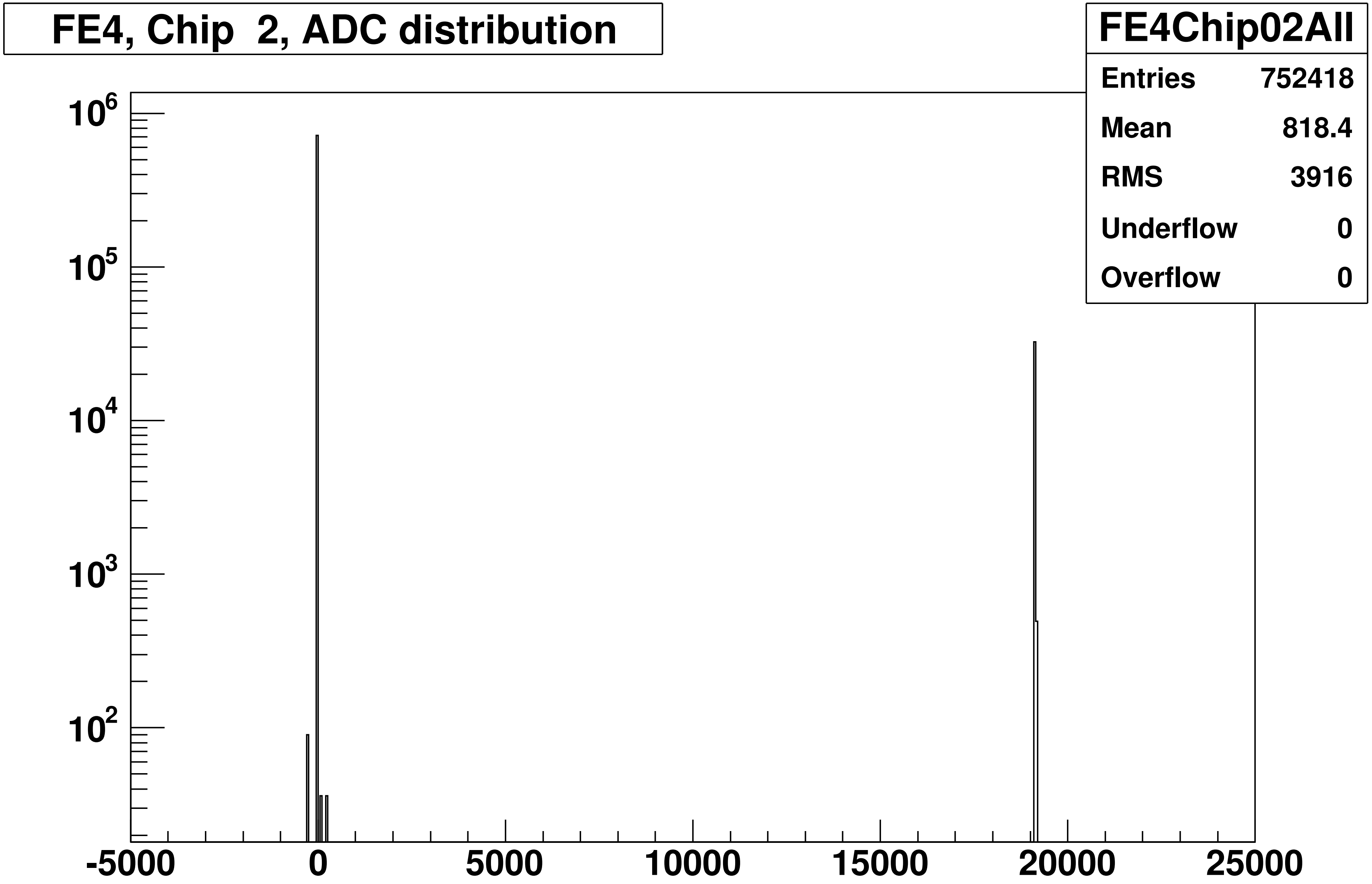
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 752418
Mean 818.4
RMS 3916
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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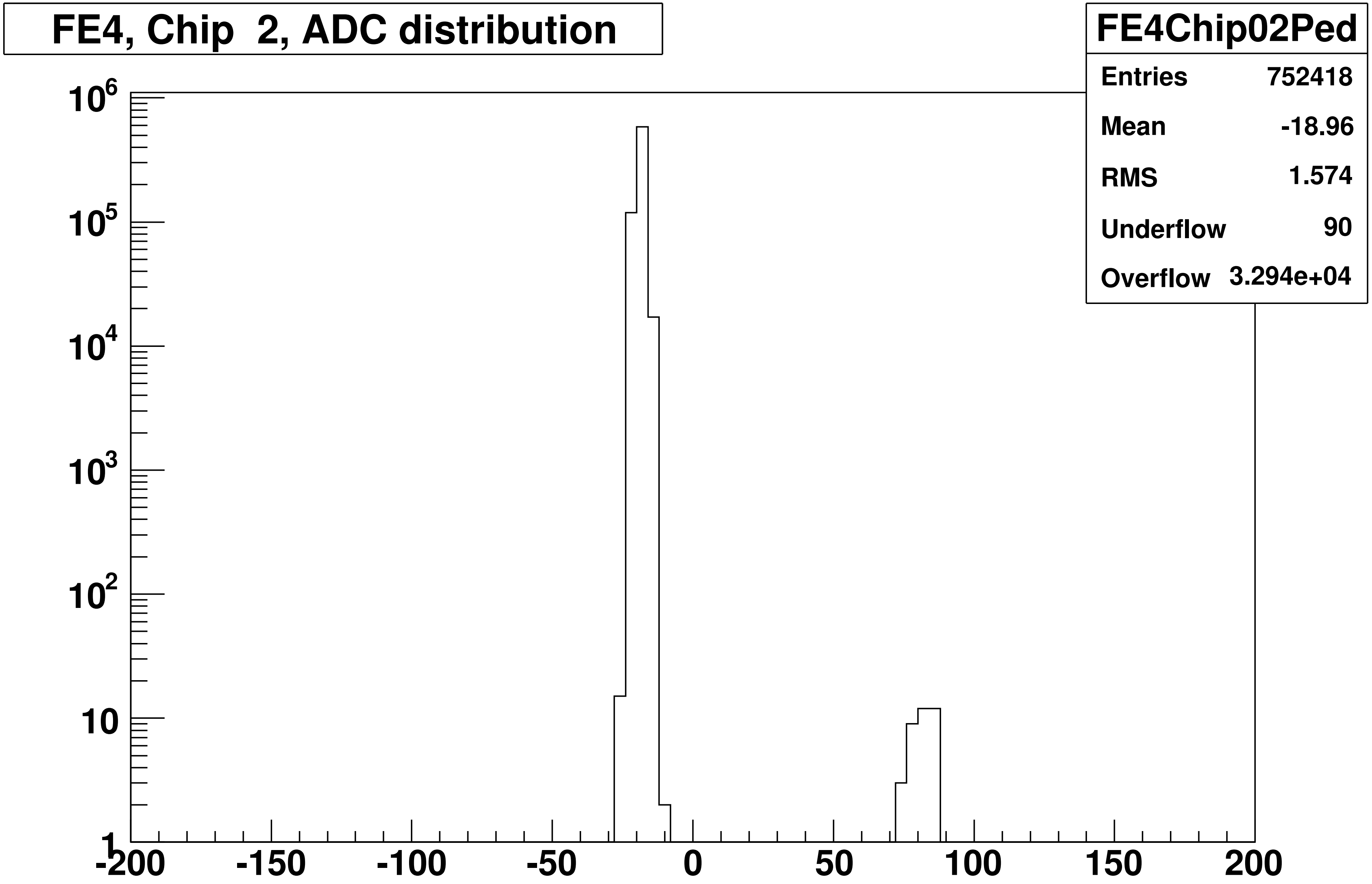
25000



FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 752418
Mean -18.96
RMS 1.574
Underflow 90
Overflow 3.294e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
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-100
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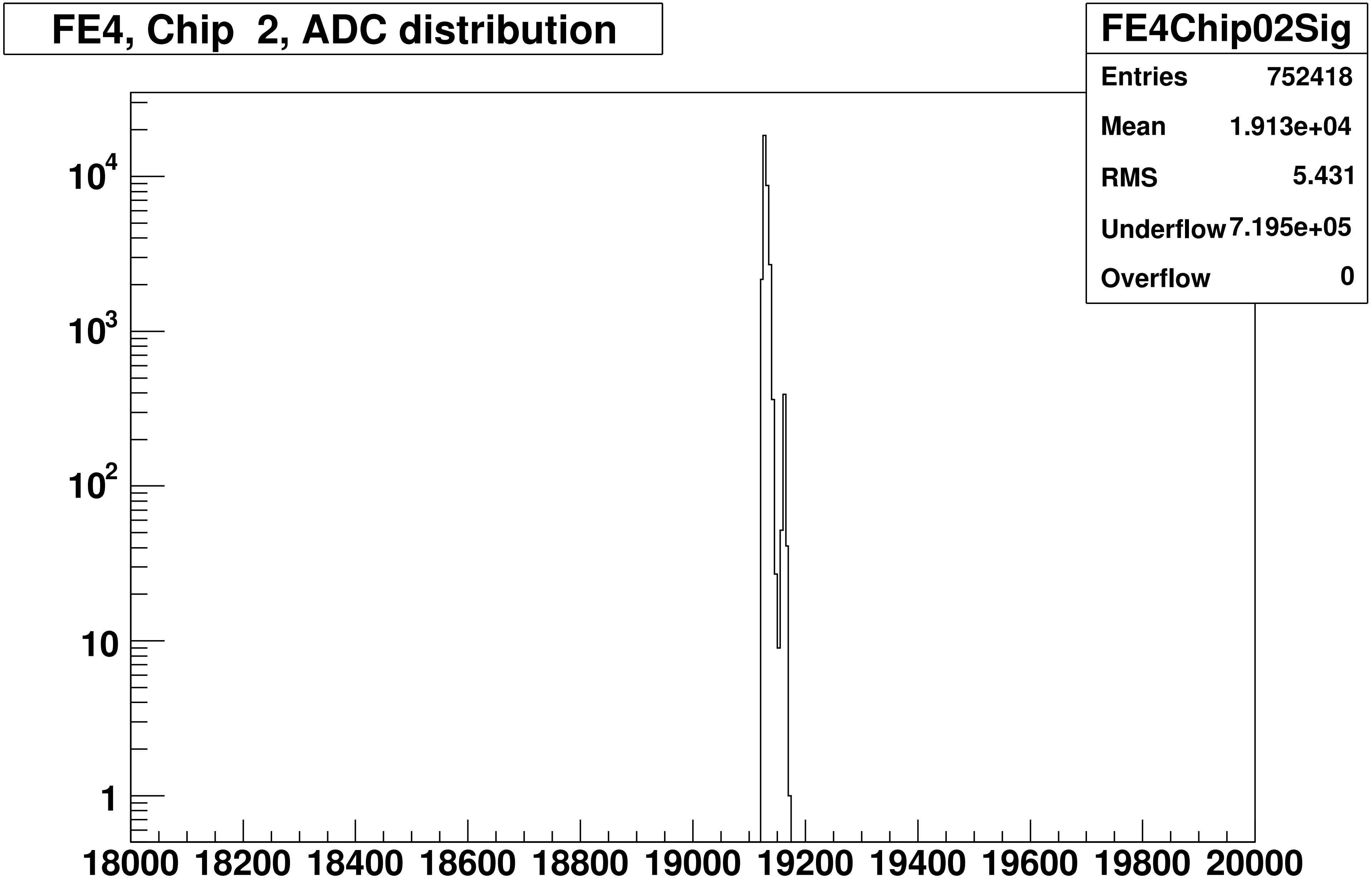
50
100
150
200


FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 752418
Mean 1.913e+04
RMS 5.431
Underflow 7.195e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
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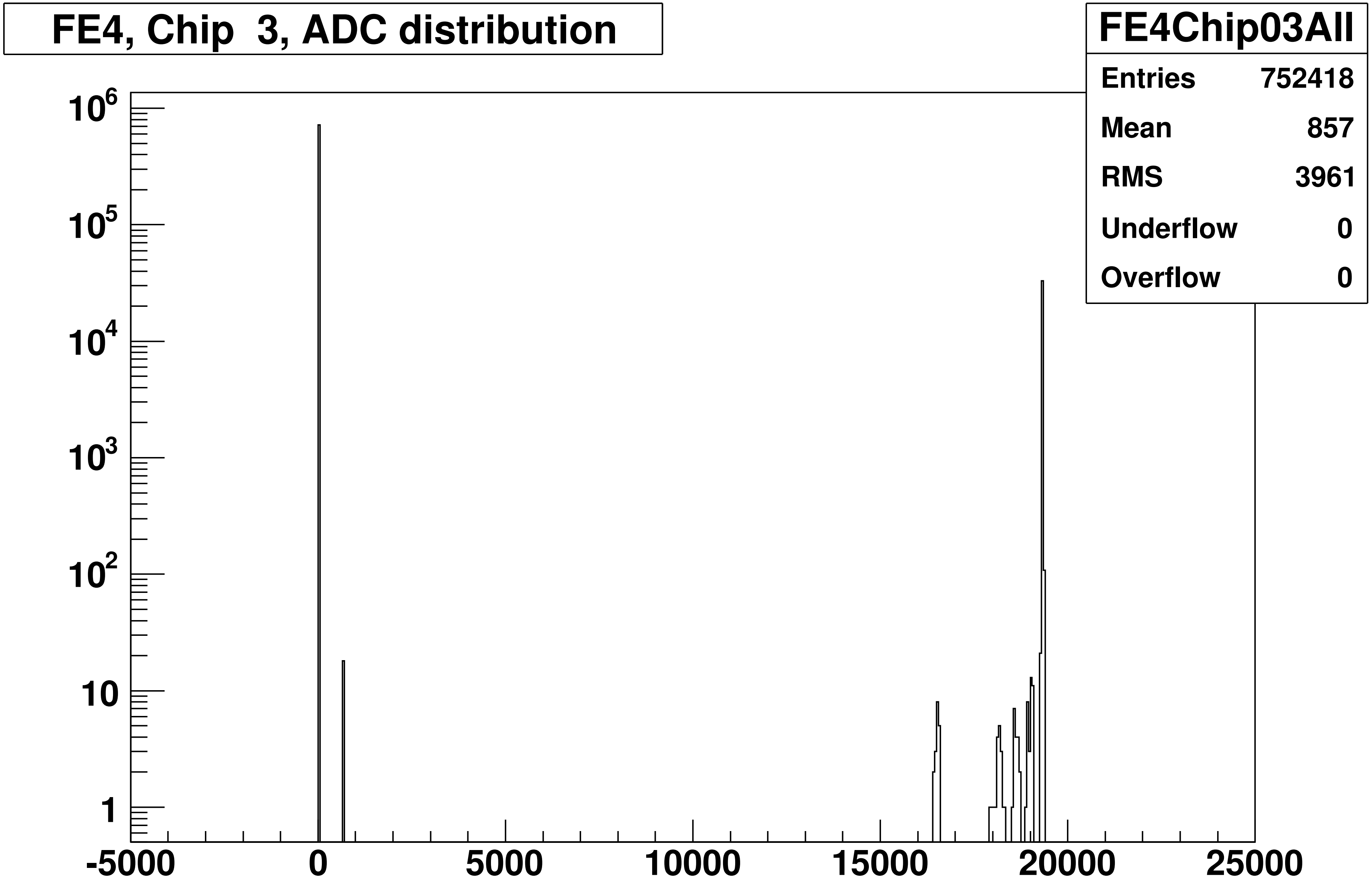
FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 752418
Mean 857
RMS 3961
Underflow 0
Overflow 0
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$10^2$
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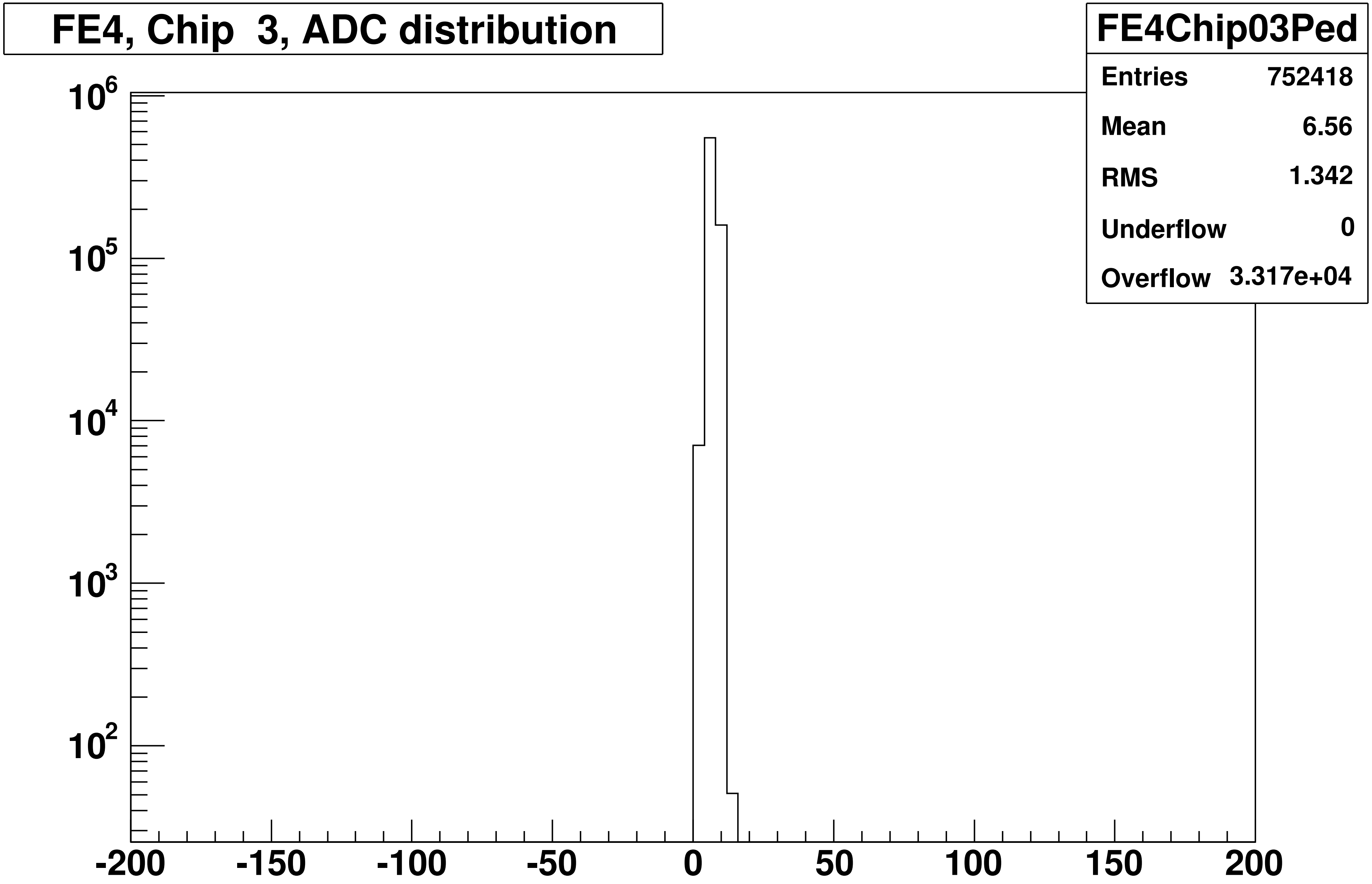

FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 752418
Mean 6.56
RMS 1.342
Underflow 0
Overflow 3.317e+04
$10^6$
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$10^3$
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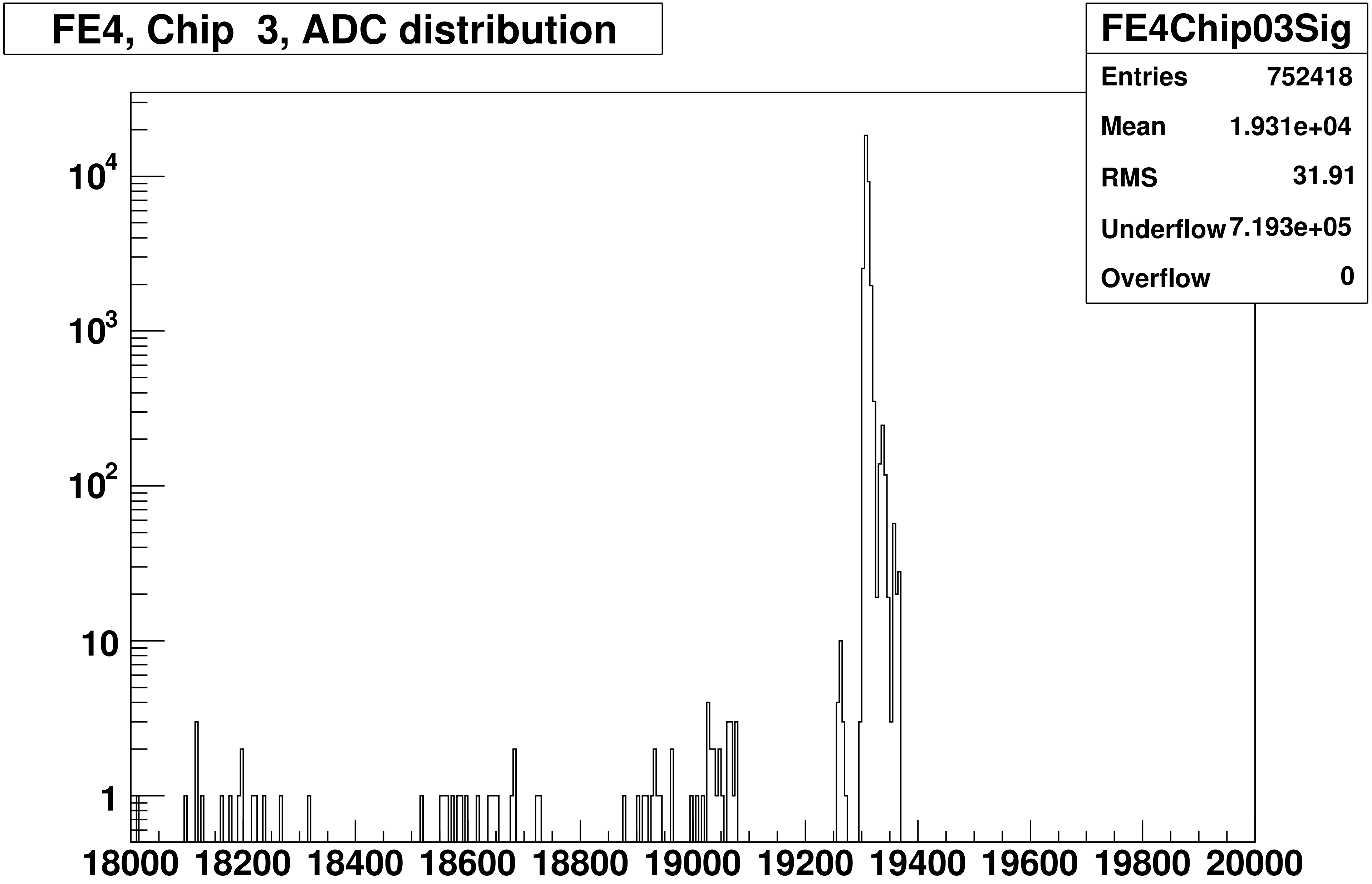
FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 752418
Mean 1.931e+04
RMS 31.91
Underflow 7.193e+05
Overflow 0
$10^4$
$10^3$
$10^2$
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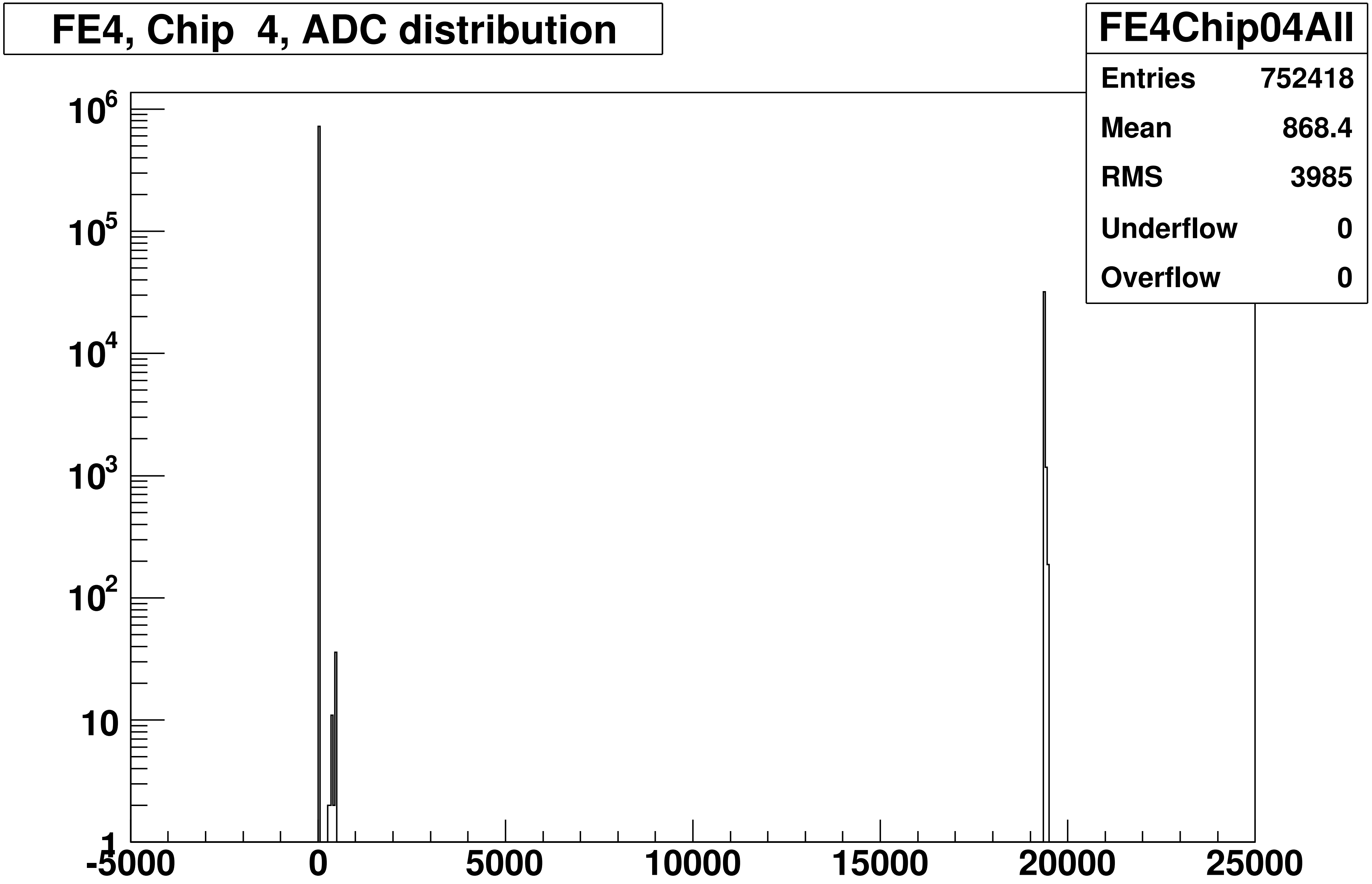

FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 752418
Mean 868.4
RMS 3985
Underflow 0
Overflow 0
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$10^3$
$10^2$
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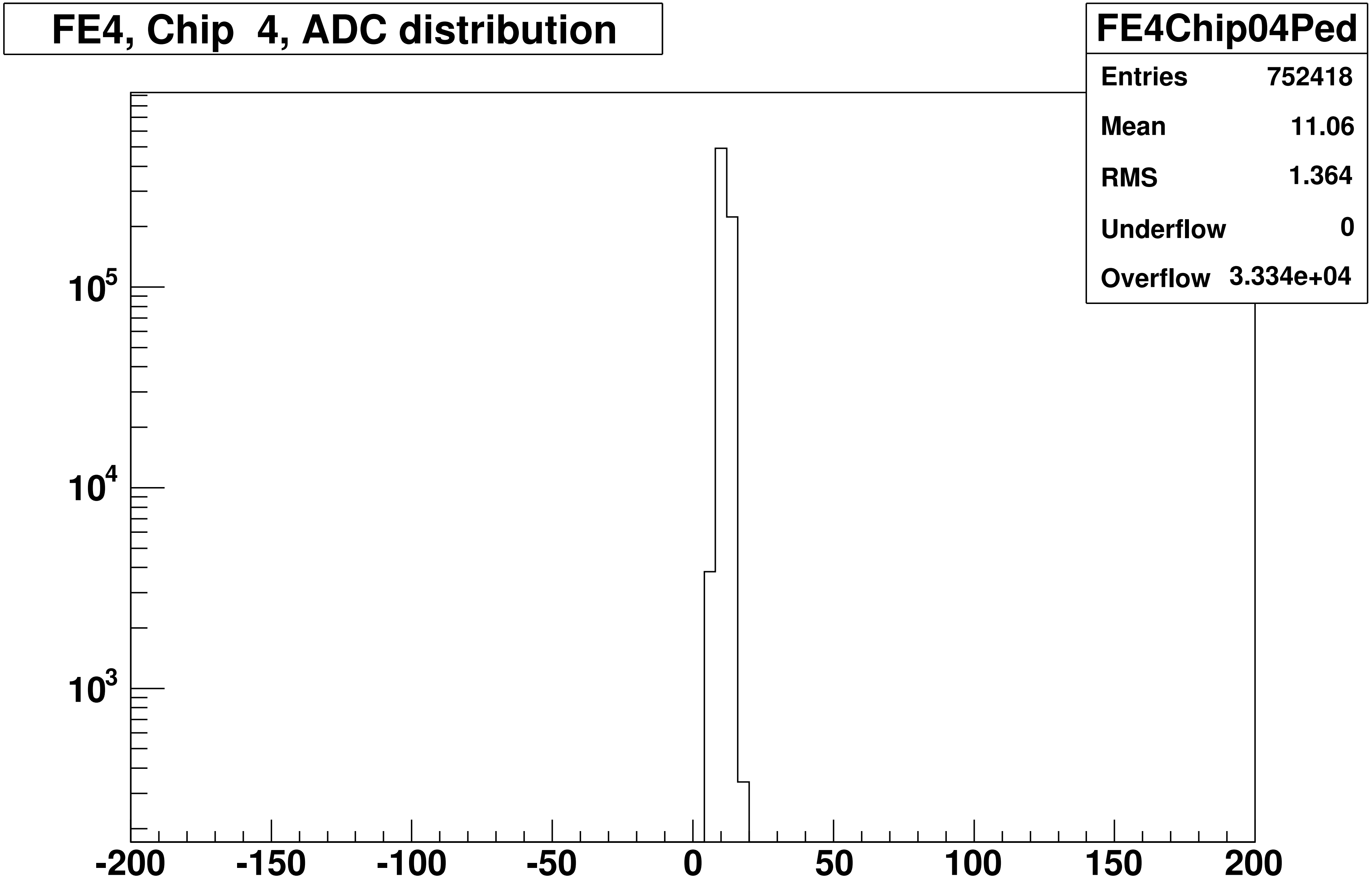

FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 752418
Mean 11.06
RMS 1.364
Underflow 0
Overflow 3.334e+04
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10^4
10^3
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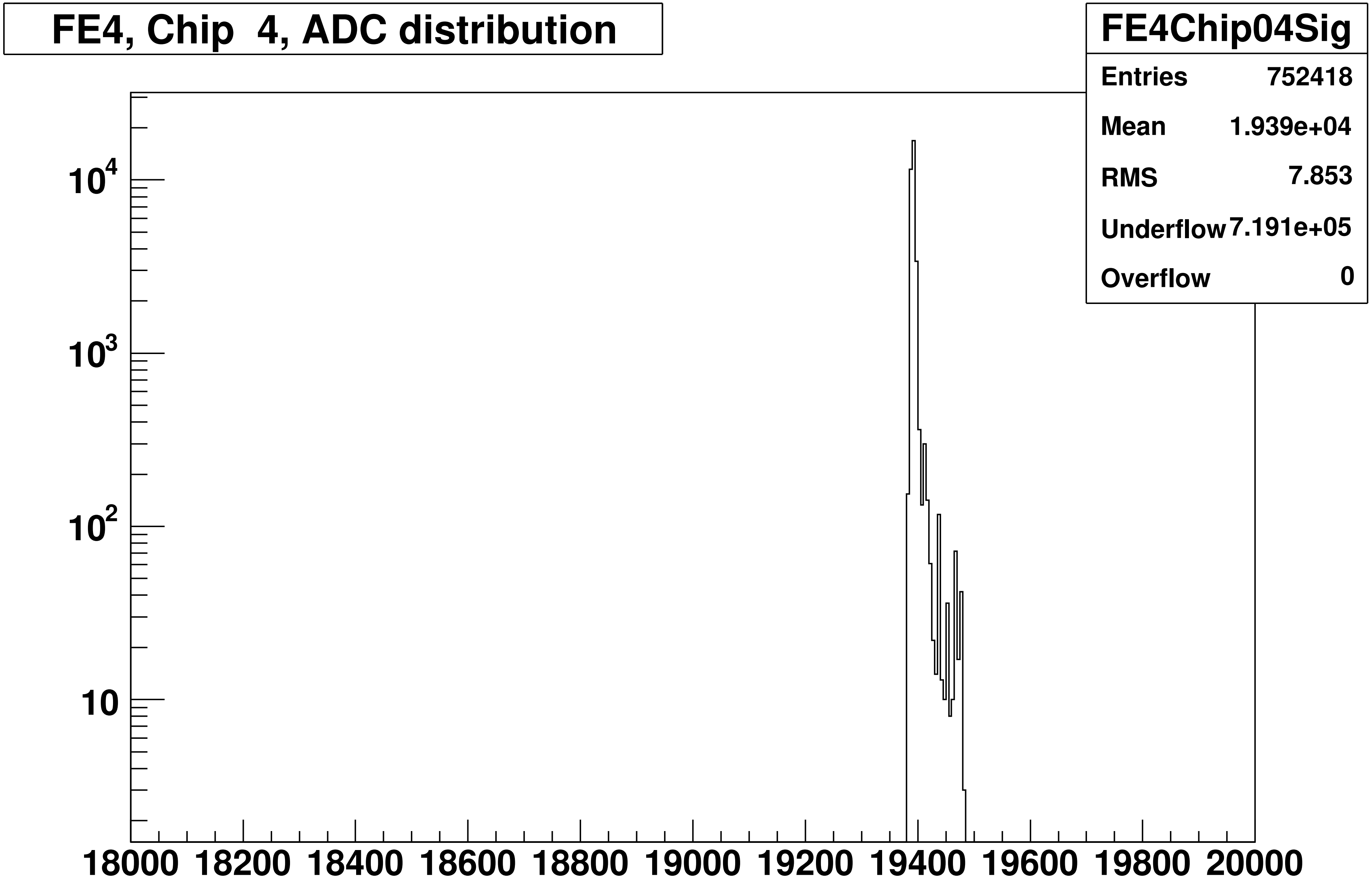

FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 752418
Mean 1.939e+04
RMS 7.853
Underflow 7.191e+05
Overflow 0
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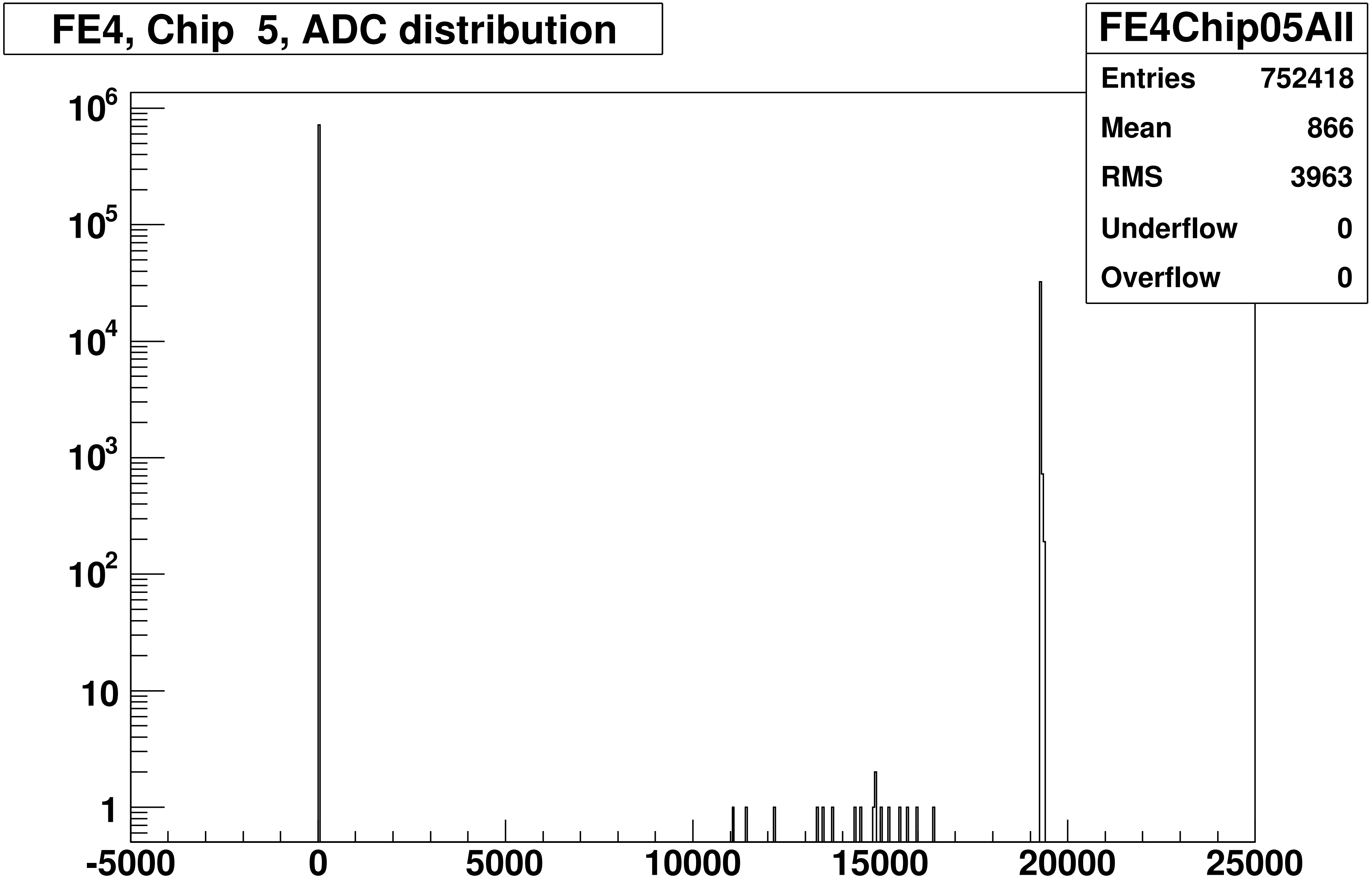
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 752418
Mean 866
RMS 3963
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
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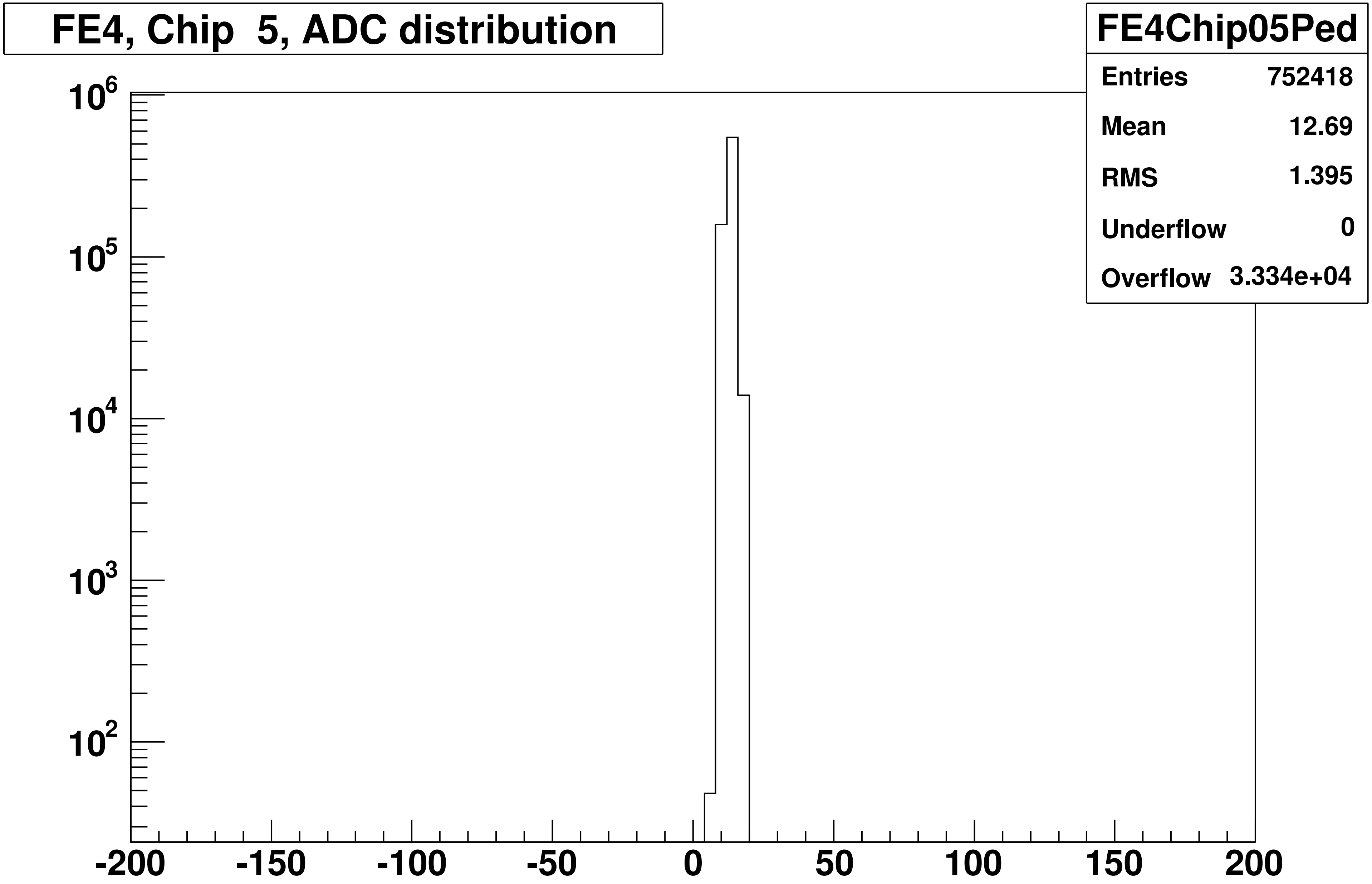
25000


FE4, Chip  5, ADC distribution
FE4Chip05Ped
Entries 752418
Mean 12.69
RMS 1.395
Underflow 0
Overflow 3.334e+04
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10^4
10^3
10^2
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150
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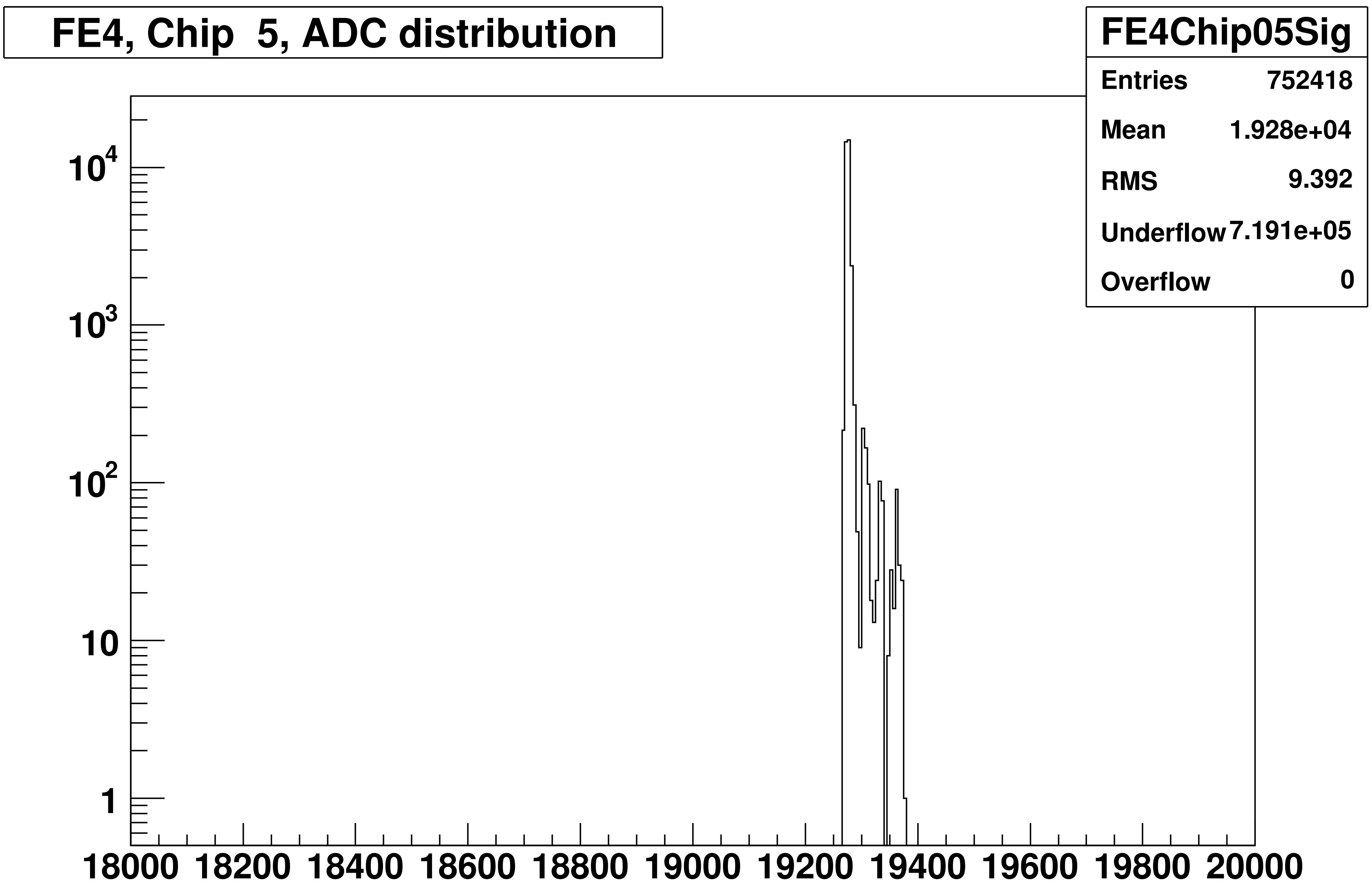
FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 752418
Mean 1.928e+04
RMS 9.392
Underflow 7.191e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
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19400
19600
19800
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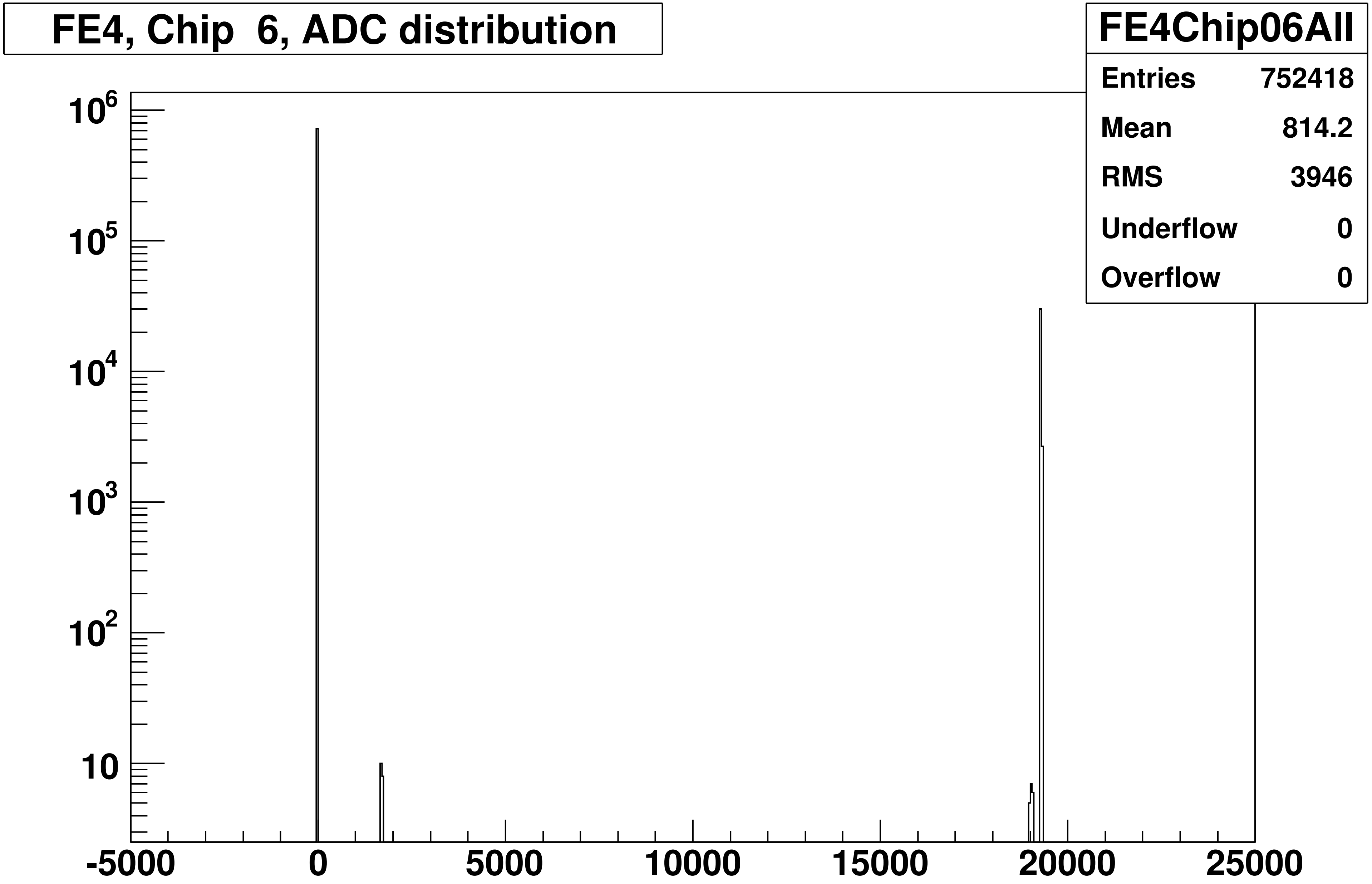
FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 752418
Mean 814.2
RMS 3946
Underflow 0
Overflow 0
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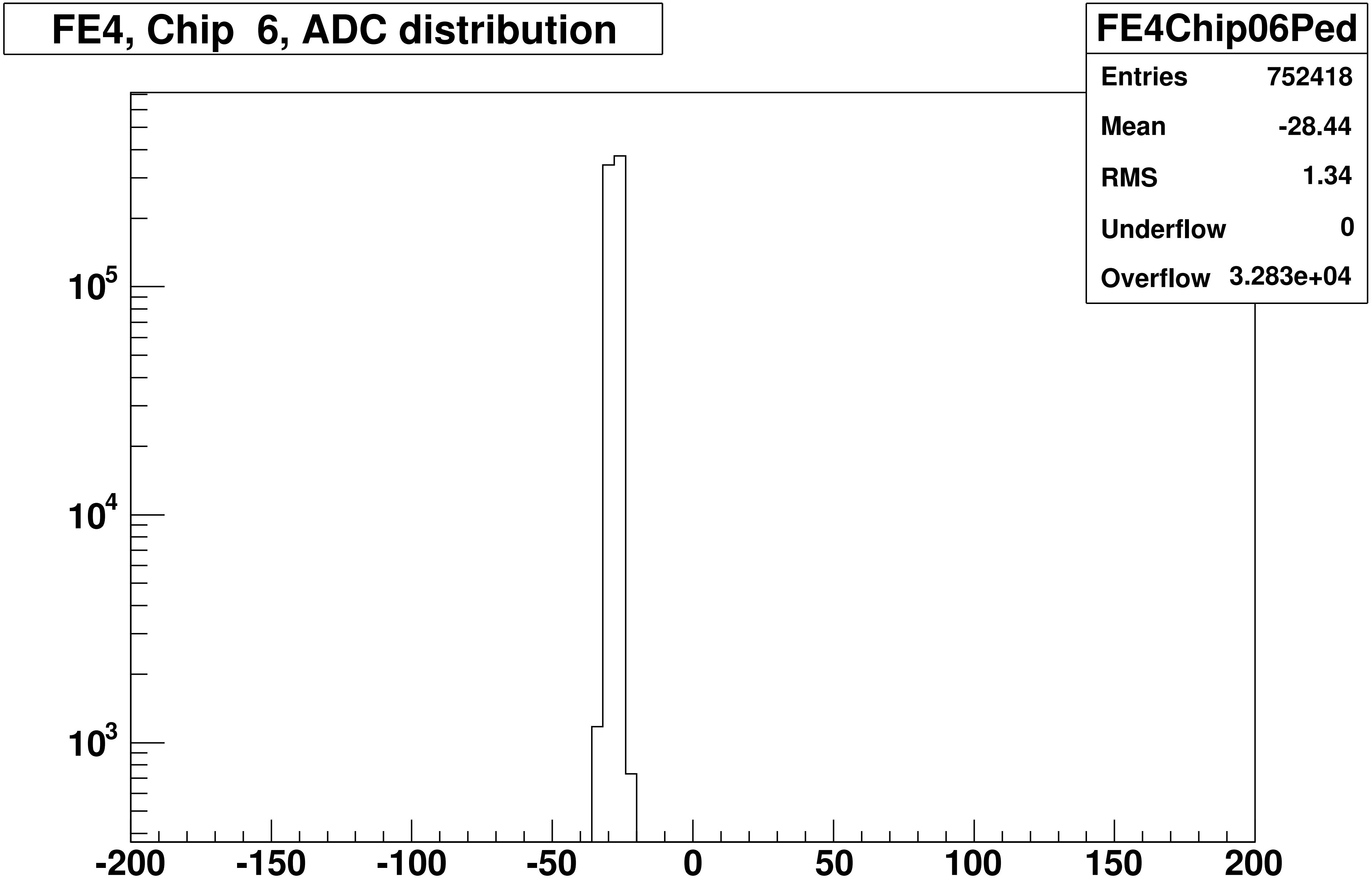

FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 752418
Mean -28.44
RMS 1.34
Underflow 0
Overflow 3.283e+04
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$10^4$
$10^3$
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-100
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0
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100
150
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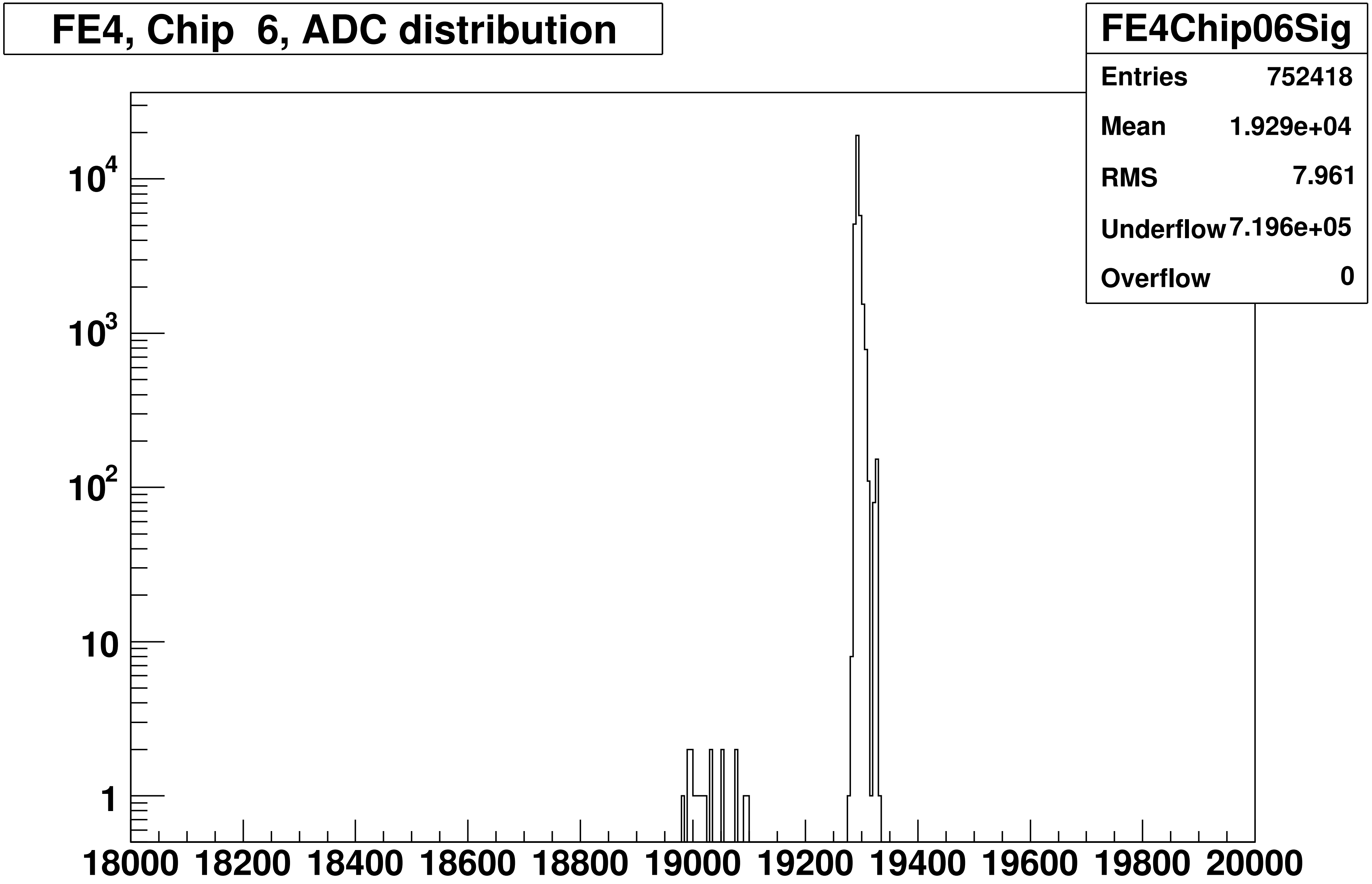
FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 752418
Mean 1.929e+04
RMS 7.961
Underflow 7.196e+05
Overflow 0
$10^4$
$10^3$
$10^2$
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18000
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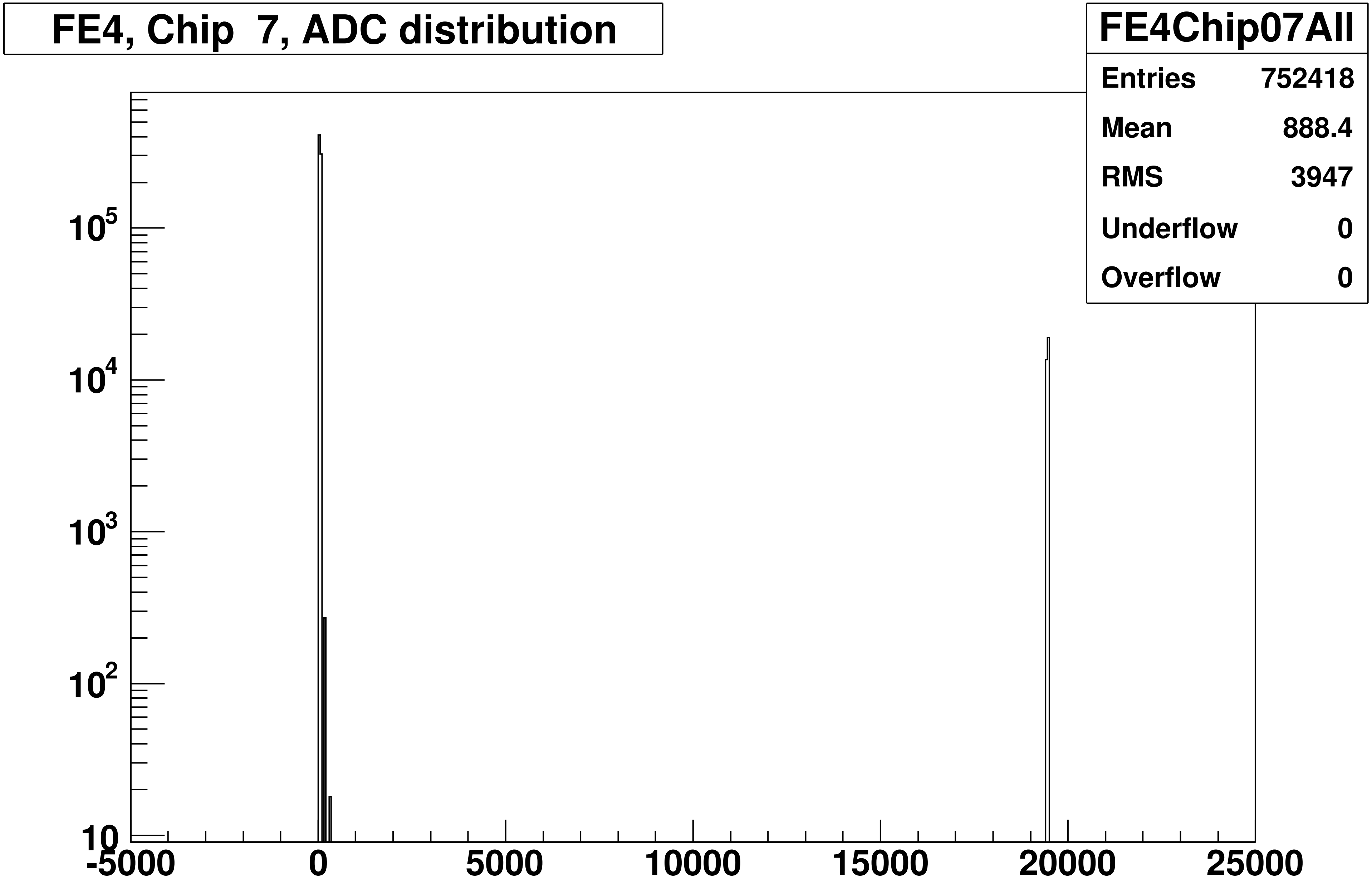
19600
19800
20000



FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 752418
Mean 888.4
RMS 3947
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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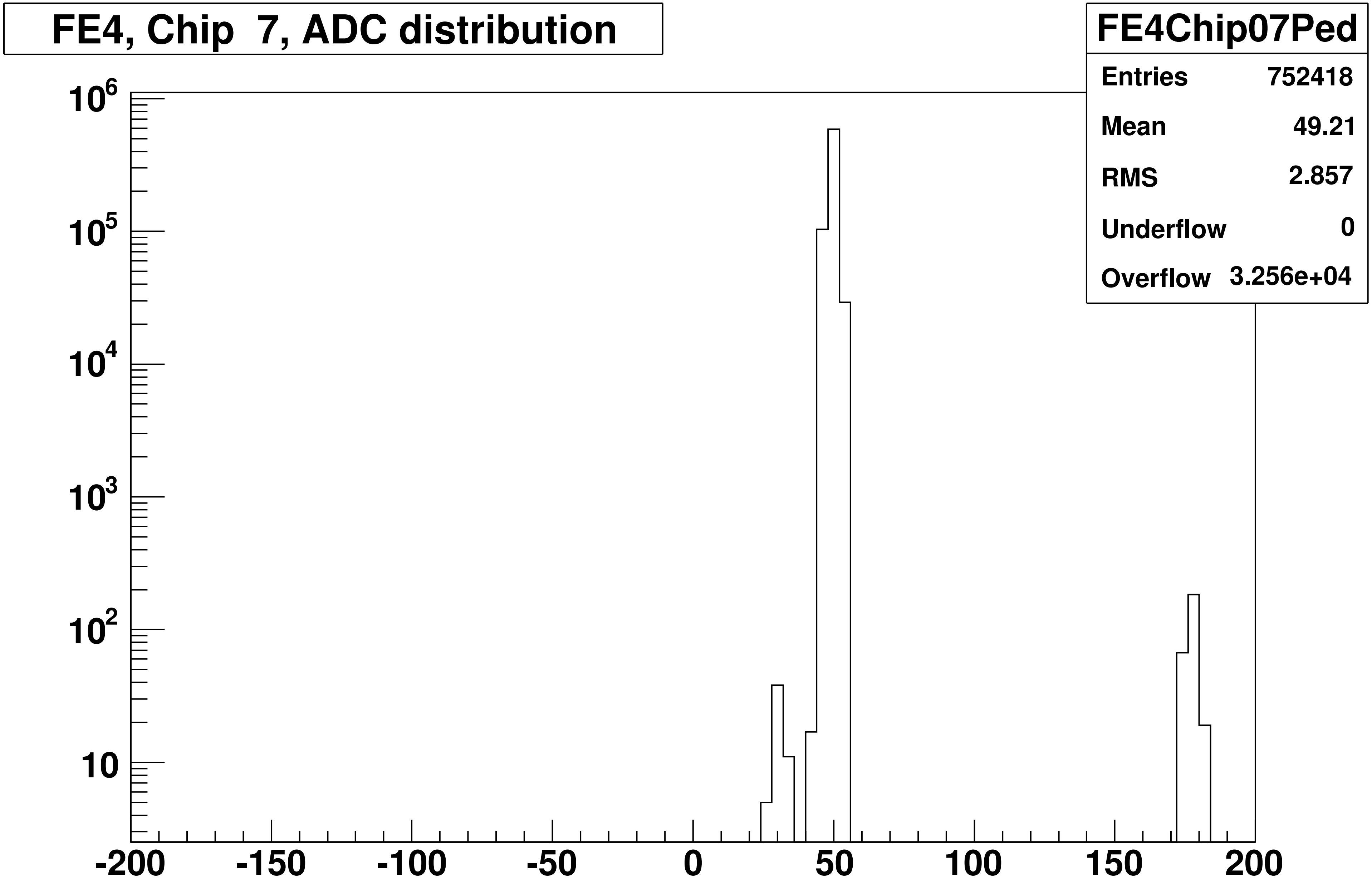
25000


FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 752418
Mean 49.21
RMS 2.857
Underflow 0
Overflow 3.256e+04
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$10^5$
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$10^3$
$10^2$
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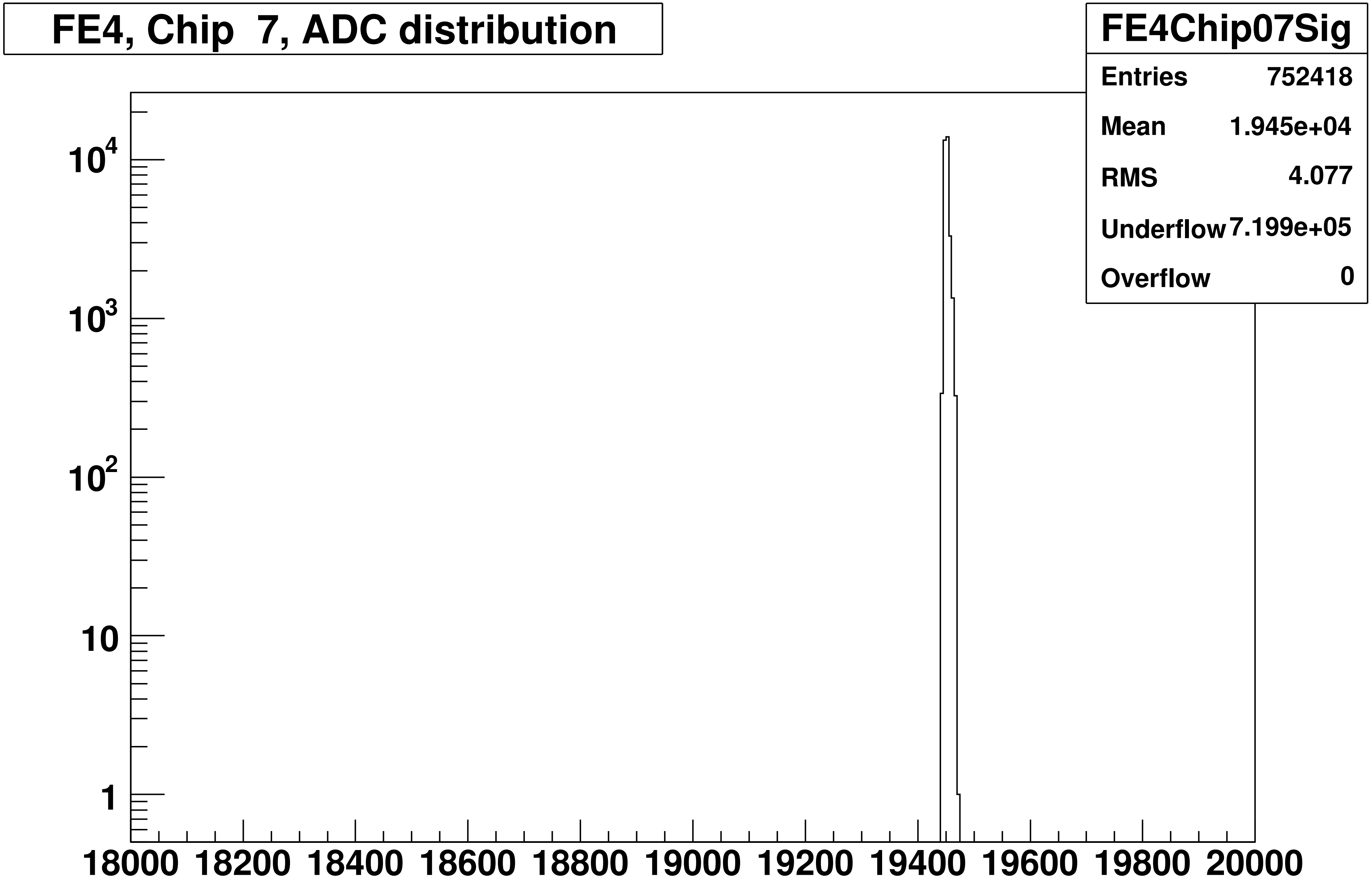
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 752418
Mean 1.945e+04
RMS 4.077
Underflow 7.199e+05
Overflow 0
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$10^3$
$10^2$
10
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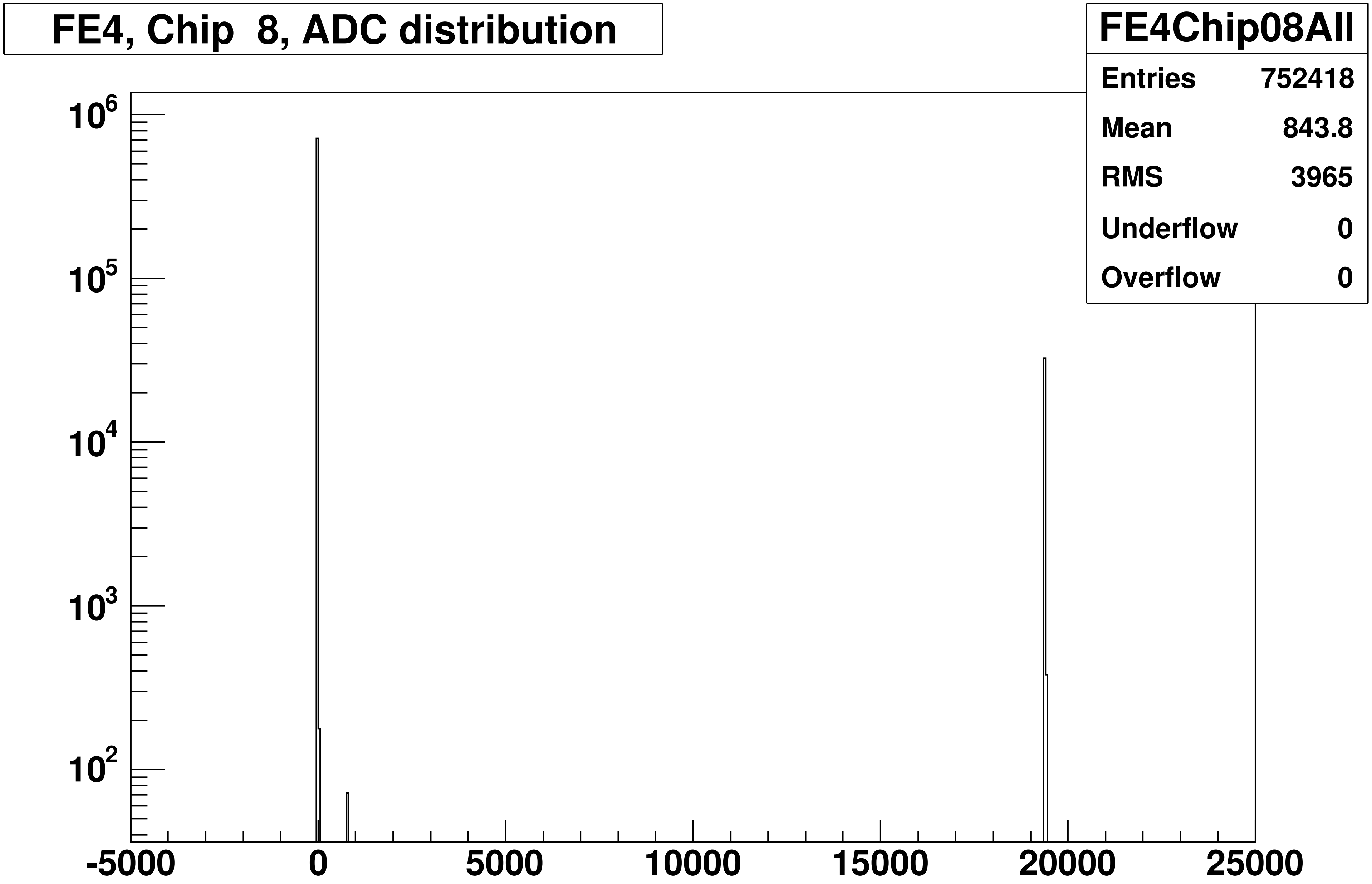
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 752418
Mean 843.8
RMS 3965
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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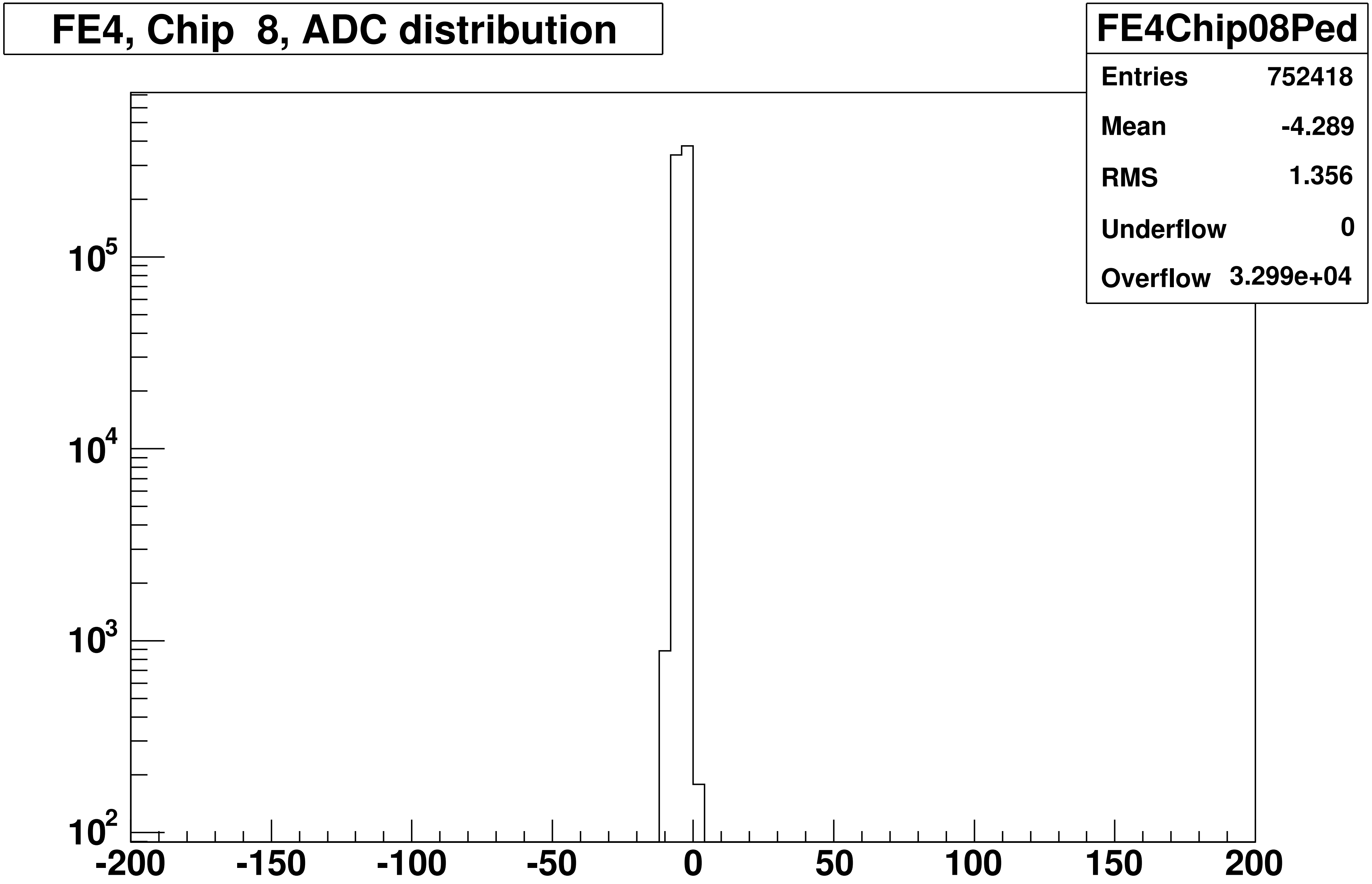
10000
15000
20000
25000



FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 752418
Mean -4.289
RMS 1.356
Underflow 0
Overflow 3.299e+04
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$10^3$
$10^2$
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0
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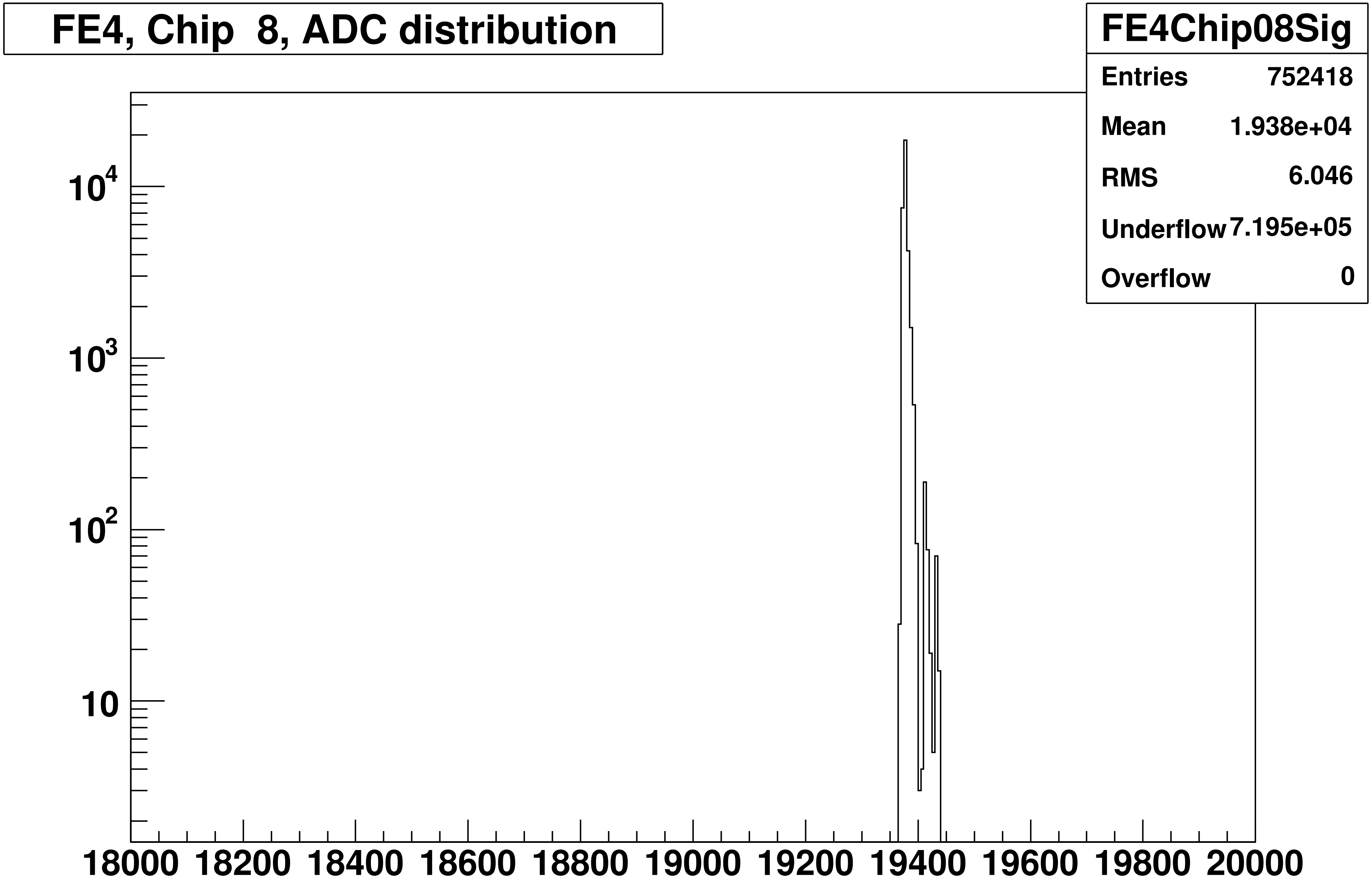
100
150
200


FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 752418
Mean 1.938e+04
RMS 6.046
Underflow 7.195e+05
Overflow 0
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10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
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19600
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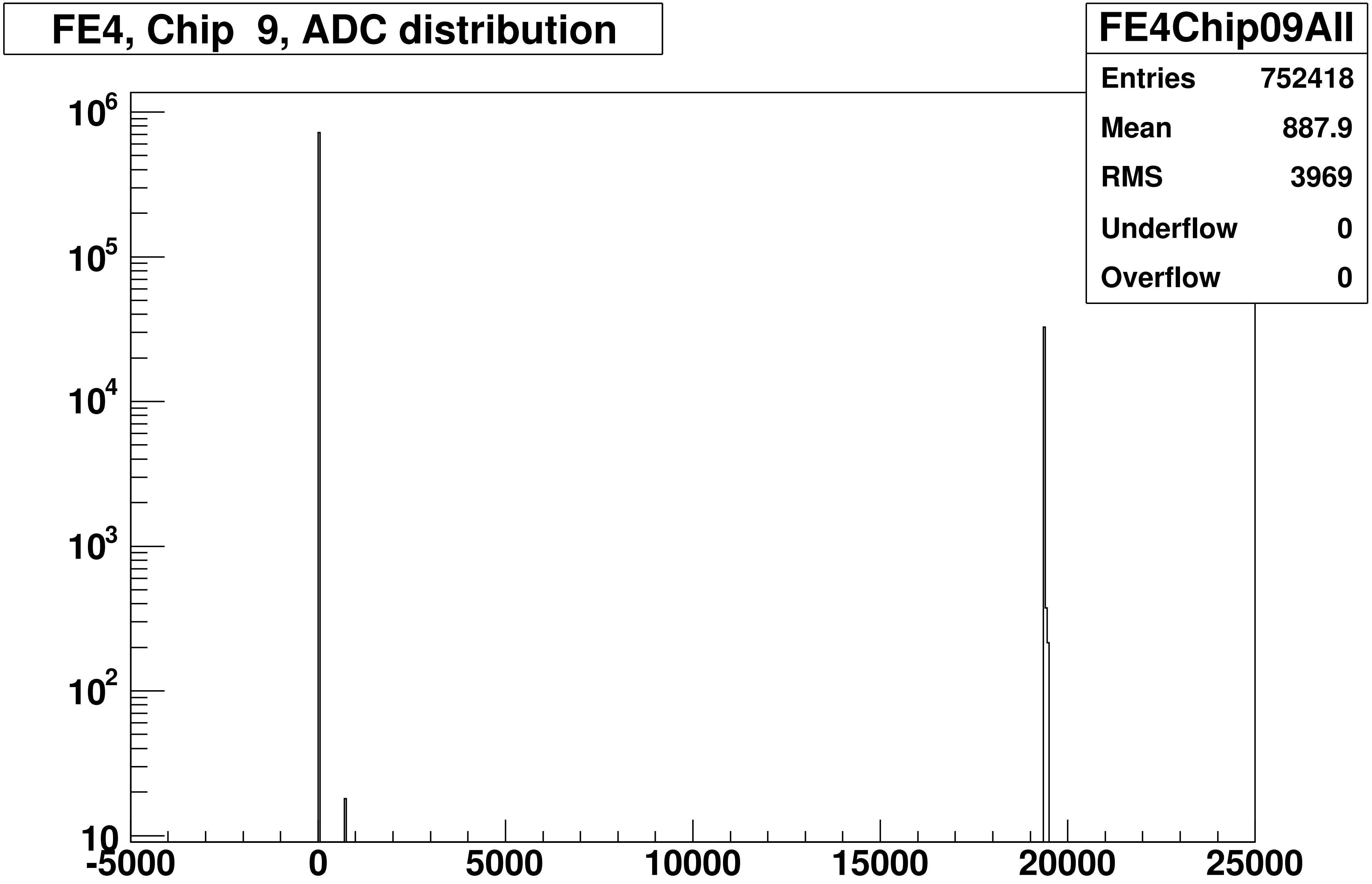
20000


FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 752418
Mean 887.9
RMS 3969
Underflow 0
Overflow 0
10
$10^2$
$10^3$
$10^4$
$10^5$
$10^6$
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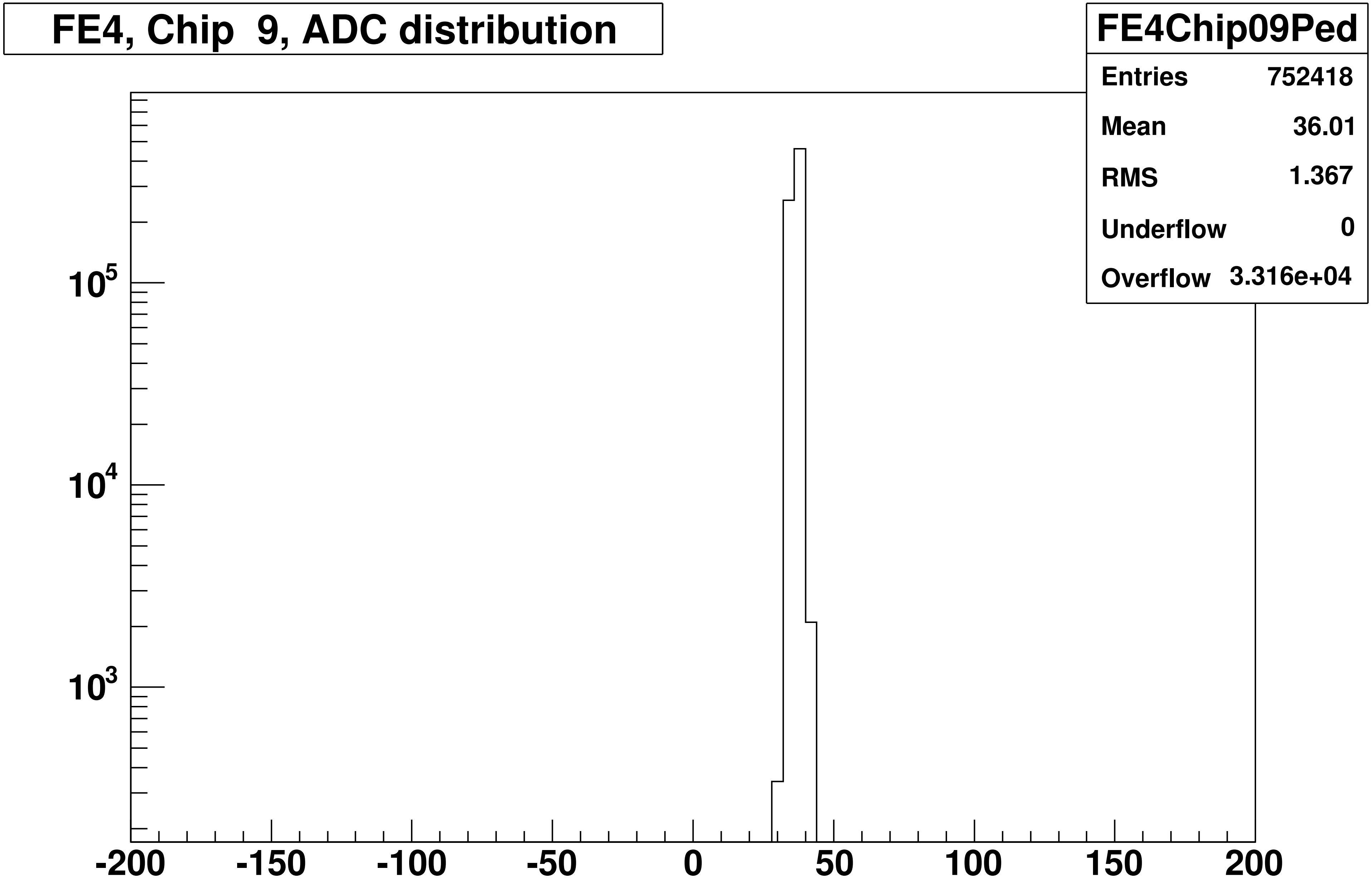
10000
15000
20000
25000


FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 752418
Mean 36.01
RMS 1.367
Underflow 0
Overflow 3.316e+04
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$10^4$
$10^3$
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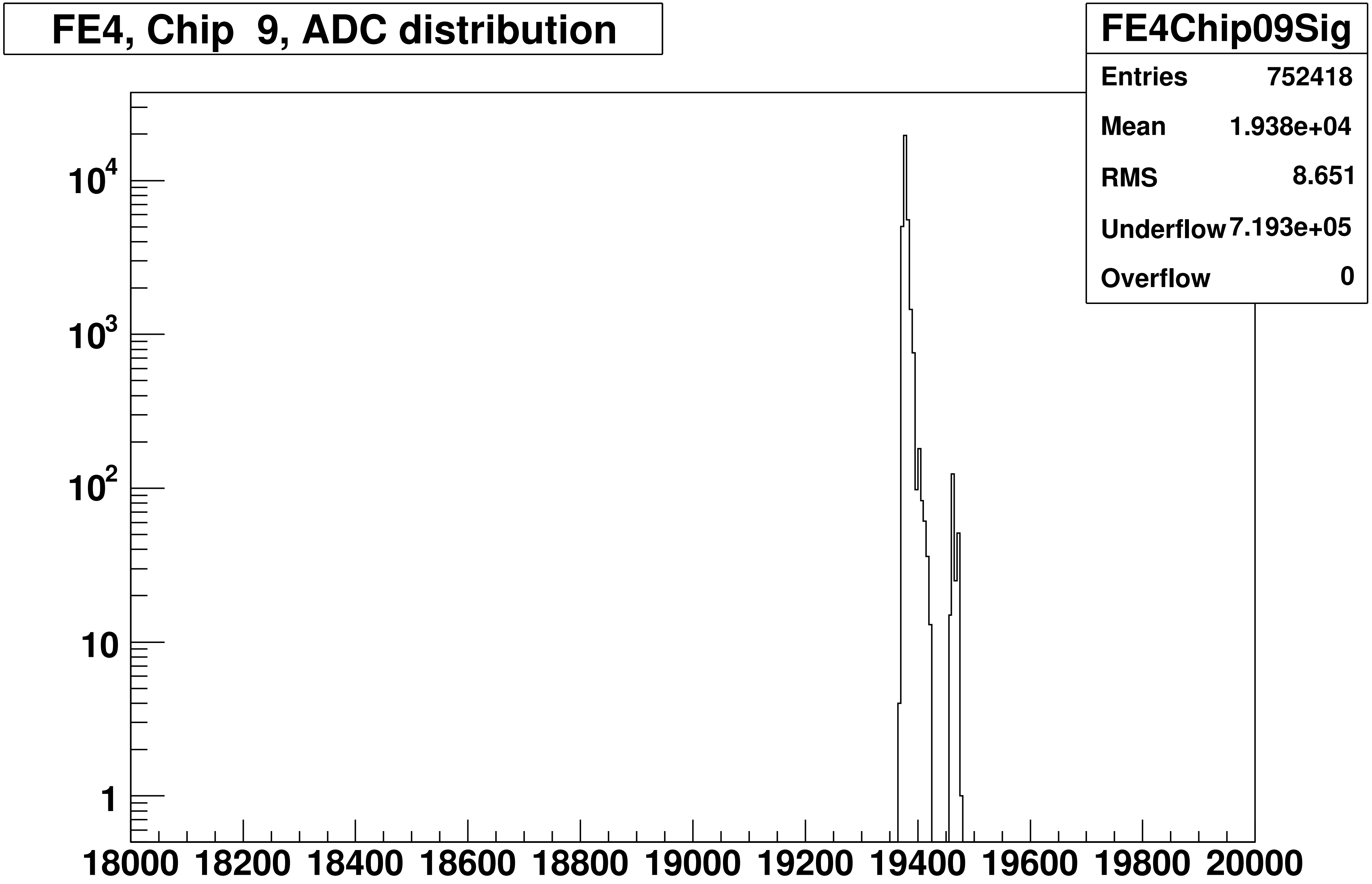
100
150
200



FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 752418
Mean 1.938e+04
RMS 8.651
Underflow 7.193e+05
Overflow 0
10^4
10^3
10^2
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1
18000
18200
18400
18600
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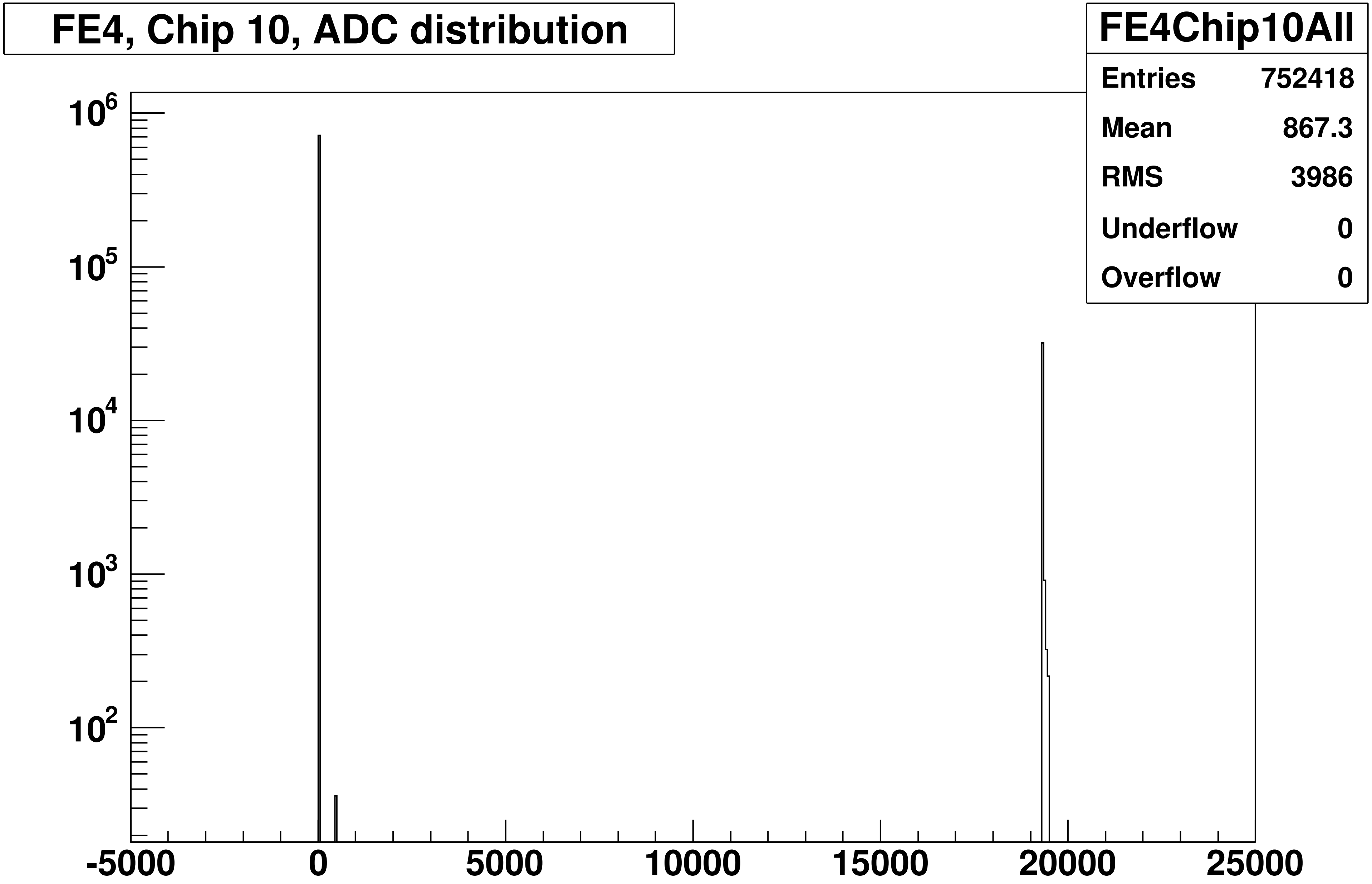
19800
20000


FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 752418
Mean 867.3
RMS 3986
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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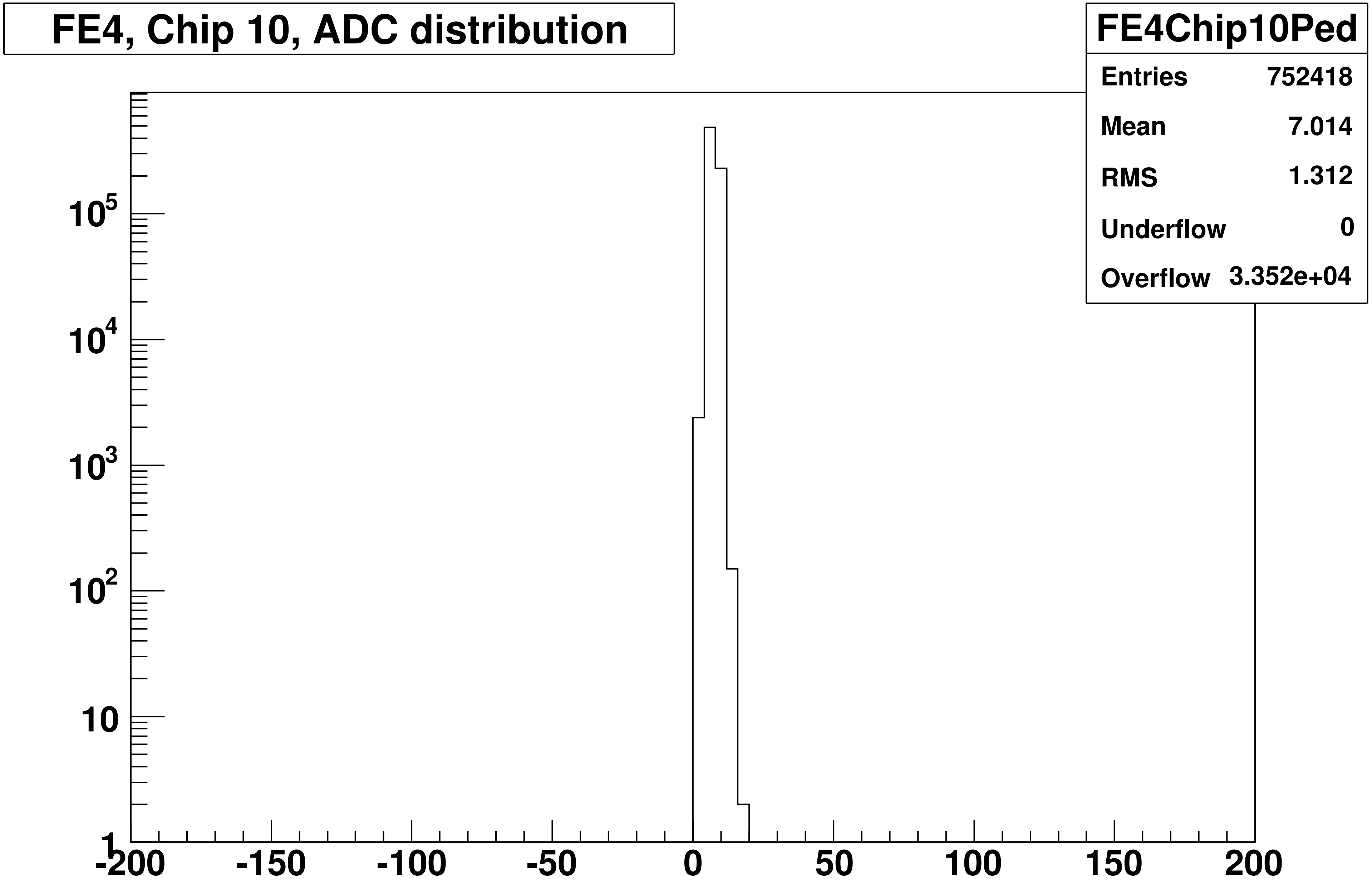
FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 752418
Mean 7.014
RMS 1.312
Underflow 0
Overflow 3.352e+04
$10^5$
$10^4$
$10^3$
$10^2$
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-100
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0
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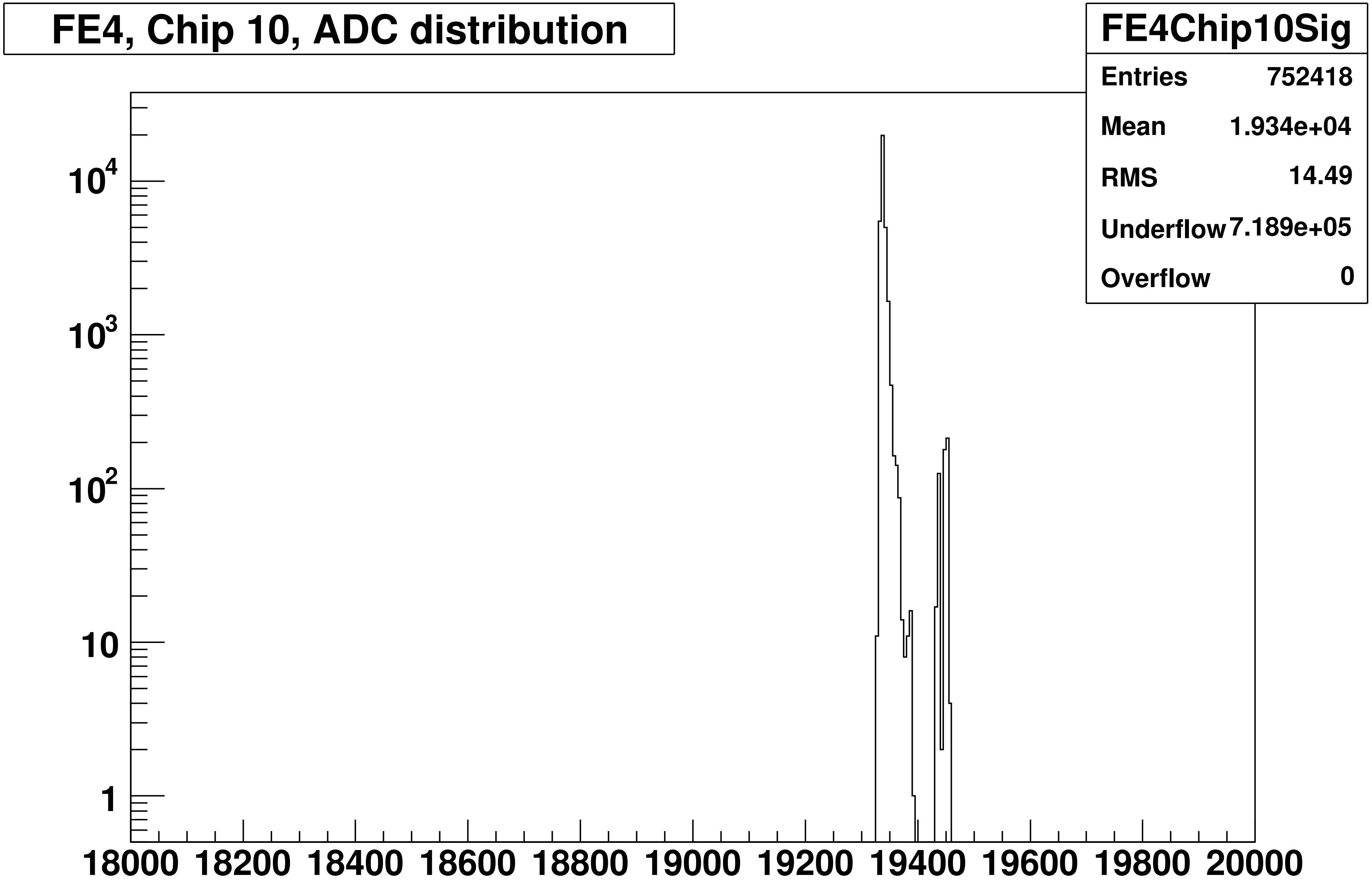
150
200


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 752418
Mean 1.934e+04
RMS 14.49
Underflow 7.189e+05
Overflow 0
1
10
10^2
10^3
10^4
18000
18200
18400
18600
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19000
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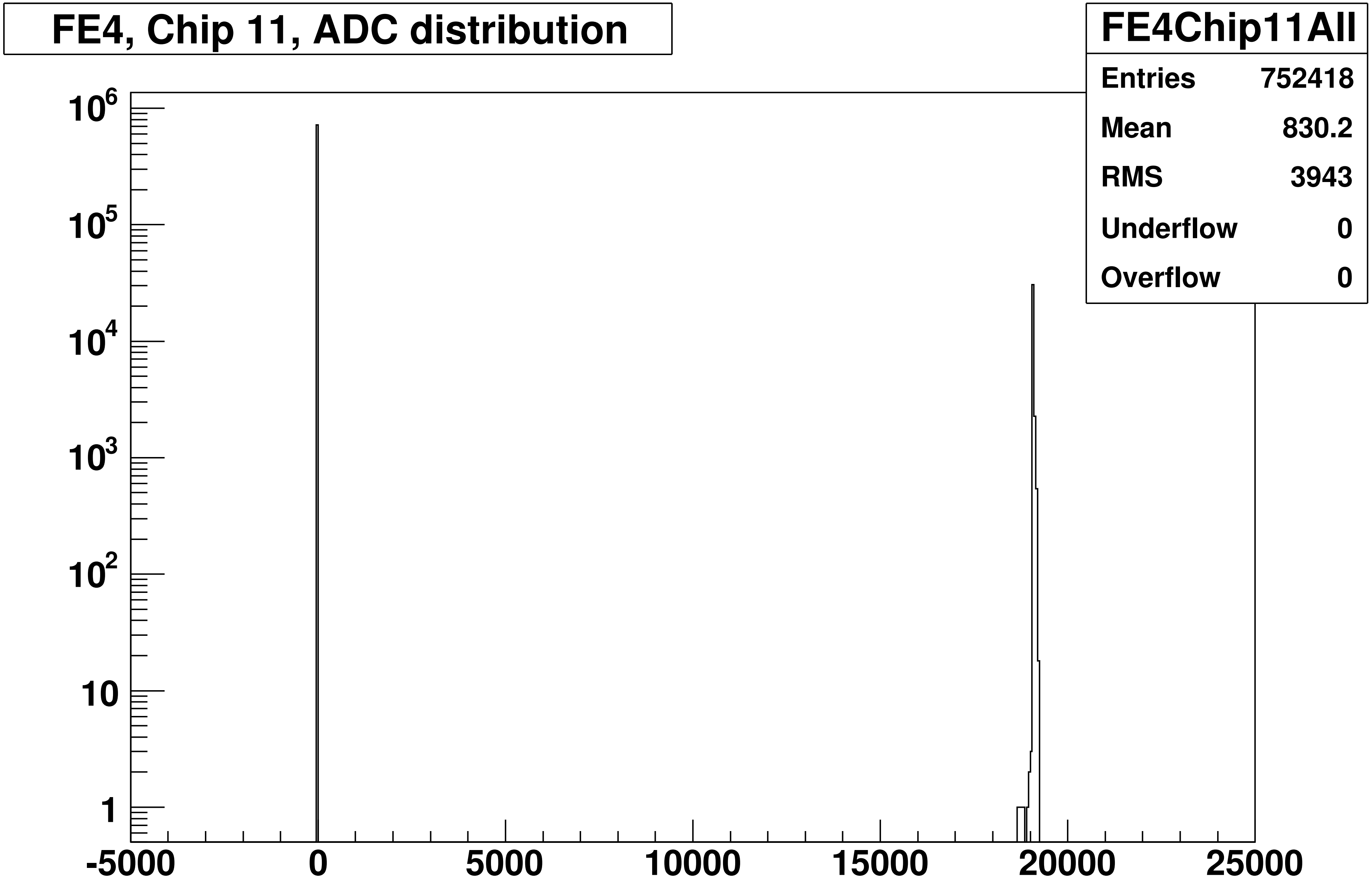
19600
19800
20000



FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 752418
Mean 830.2
RMS 3943
Underflow 0
Overflow 0
$10^6$
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$10^4$
$10^3$
$10^2$
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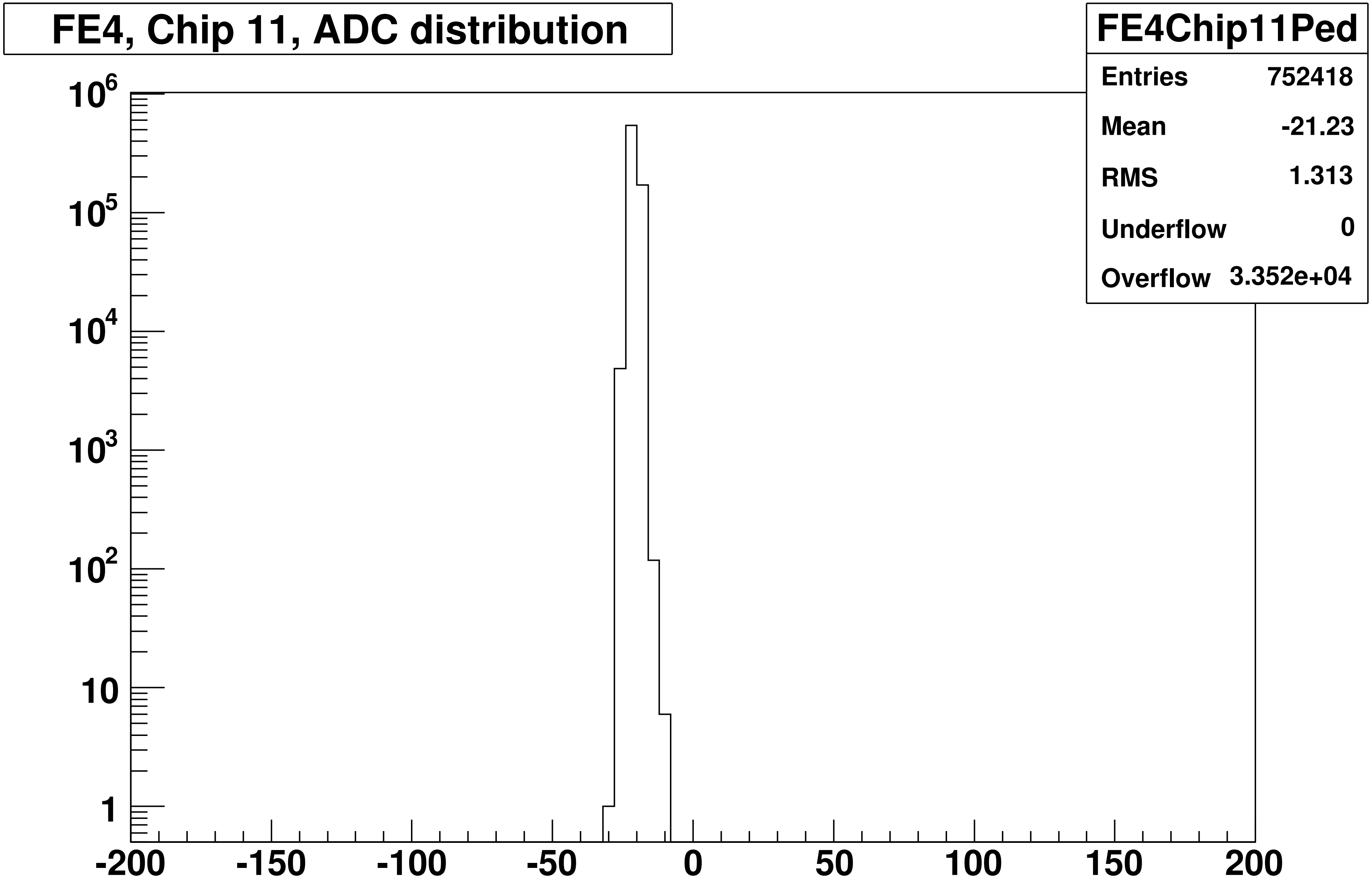
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 752418
Mean -21.23
RMS 1.313
Underflow 0
Overflow 3.352e+04
$10^6$
$10^5$
$10^4$
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$10^2$
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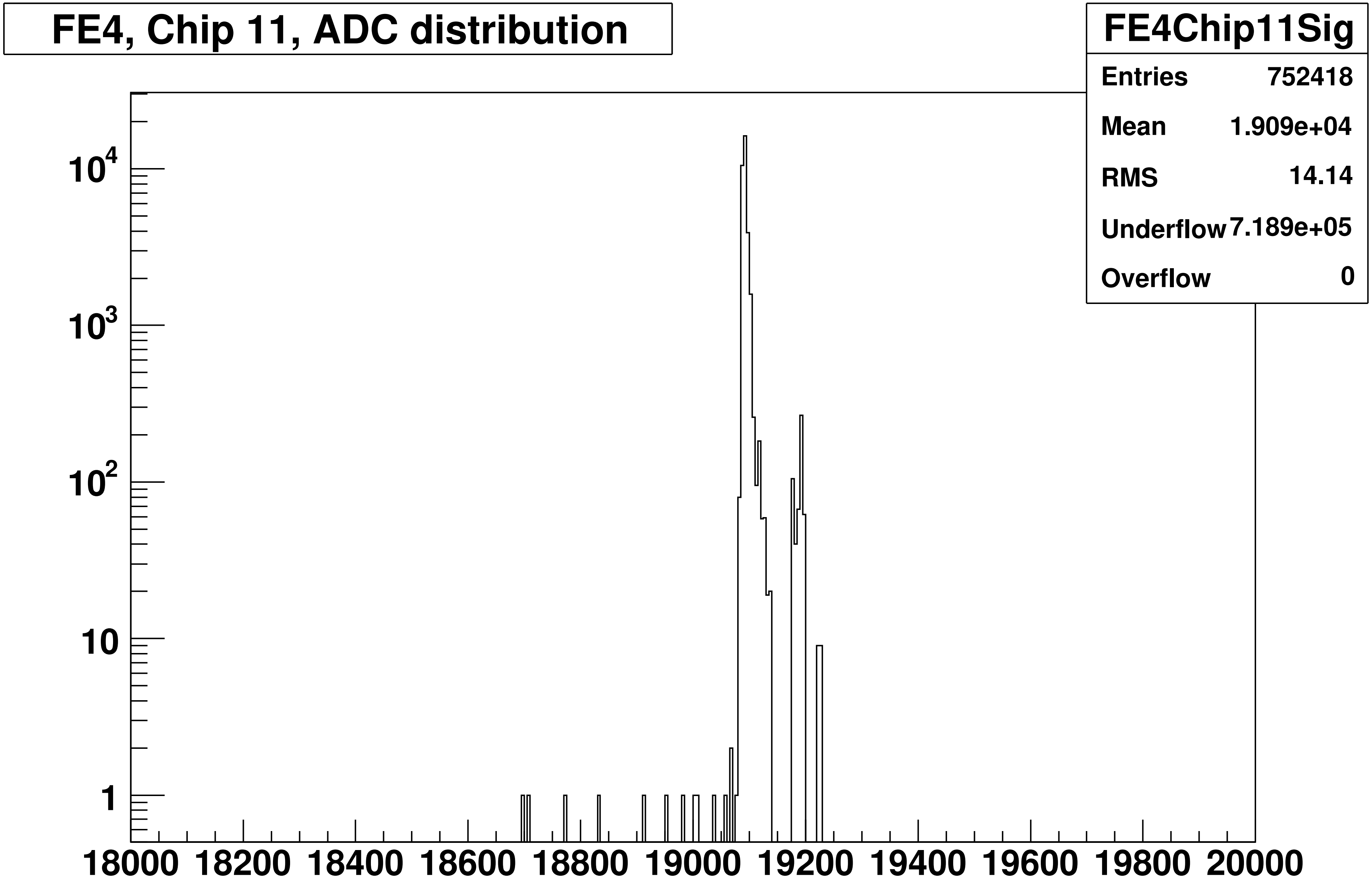
-100
-50
0
50
100
150
200


FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 752418
Mean 1.909e+04
RMS 14.14
Underflow 7.189e+05
Overflow 0
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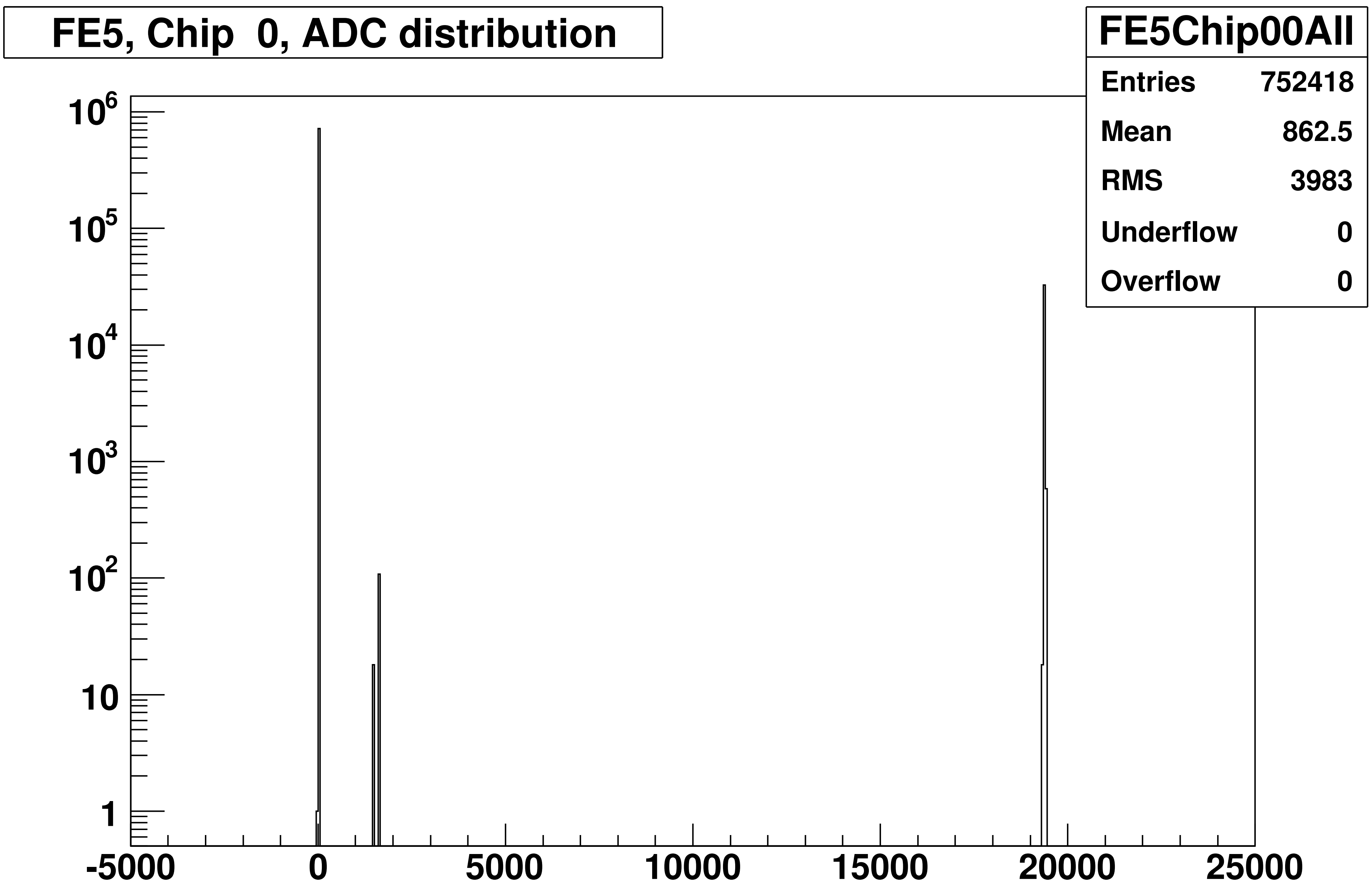
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 752418
Mean 862.5
RMS 3983
Underflow 0
Overflow 0
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$10^3$
$10^2$
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0
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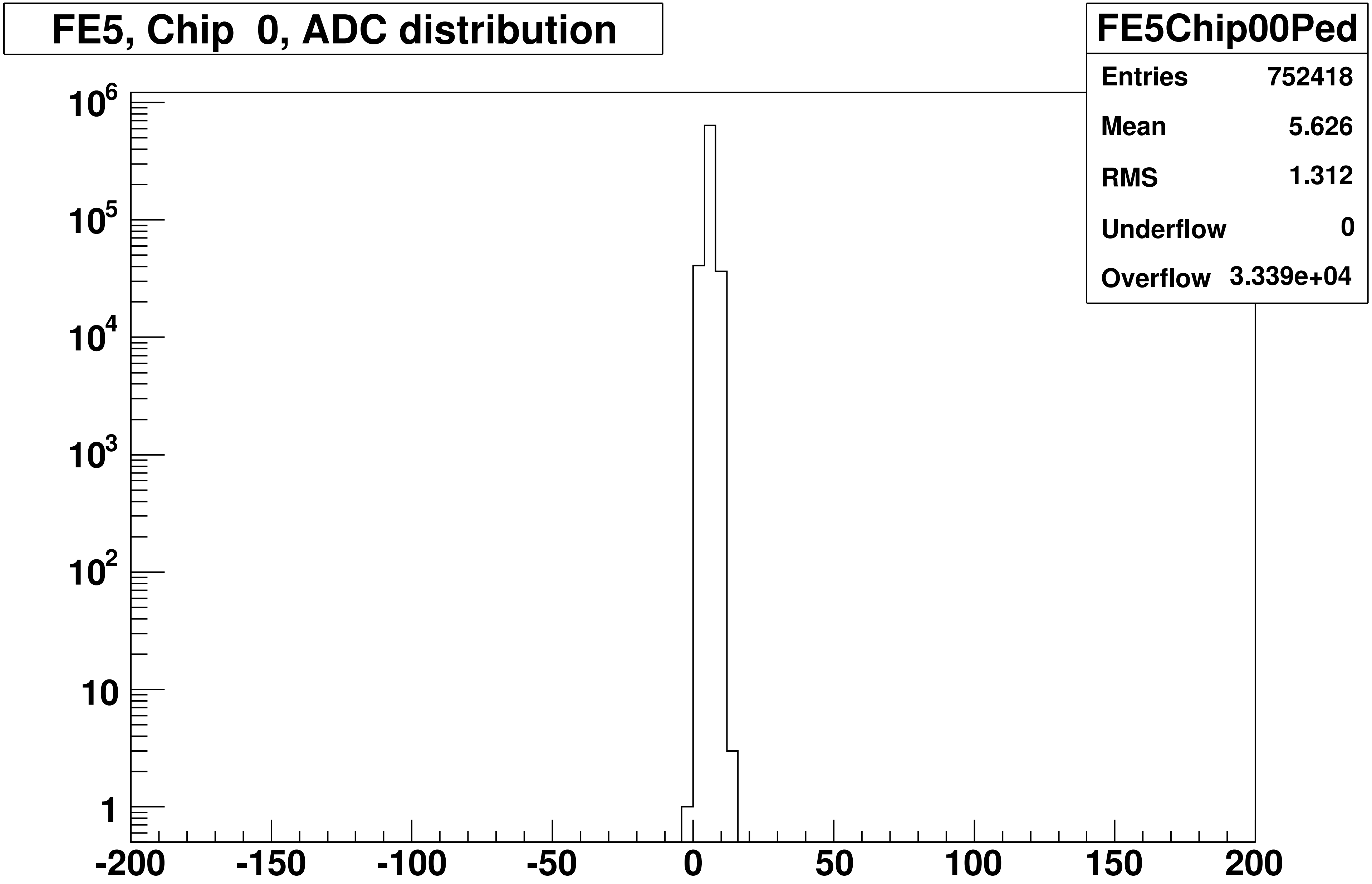
10000
15000
20000
25000


FE5, Chip  0, ADC distribution
FE5Chip00Ped
Entries 752418
Mean 5.626
RMS 1.312
Underflow 0
Overflow 3.339e+04
$10^6$
$10^5$
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$10^3$
$10^2$
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-100
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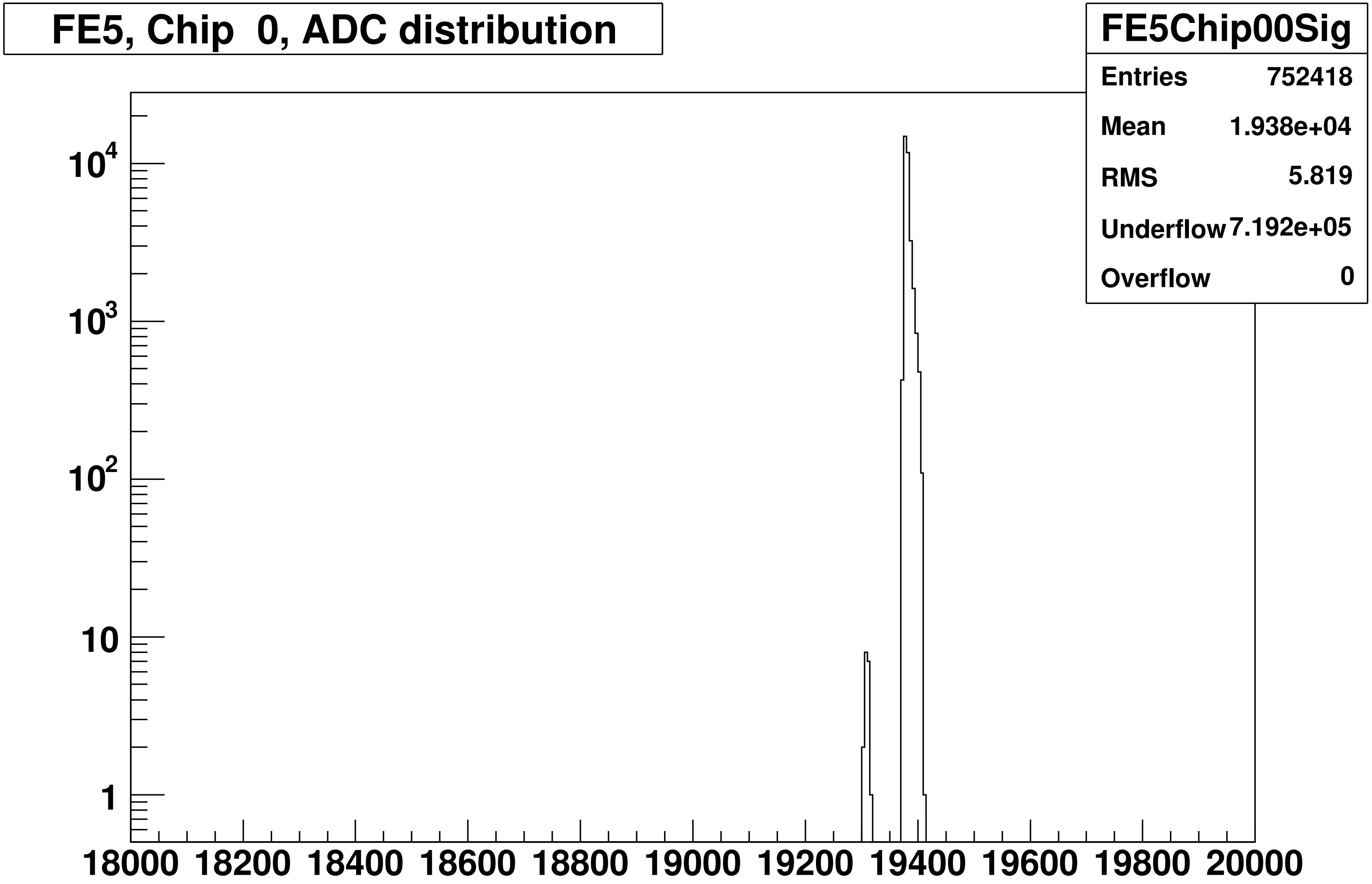
50
100
150
200



FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 752418
Mean 1.938e+04
RMS 5.819
Underflow 7.192e+05
Overflow 0
10^4
10^3
10^2
10
1
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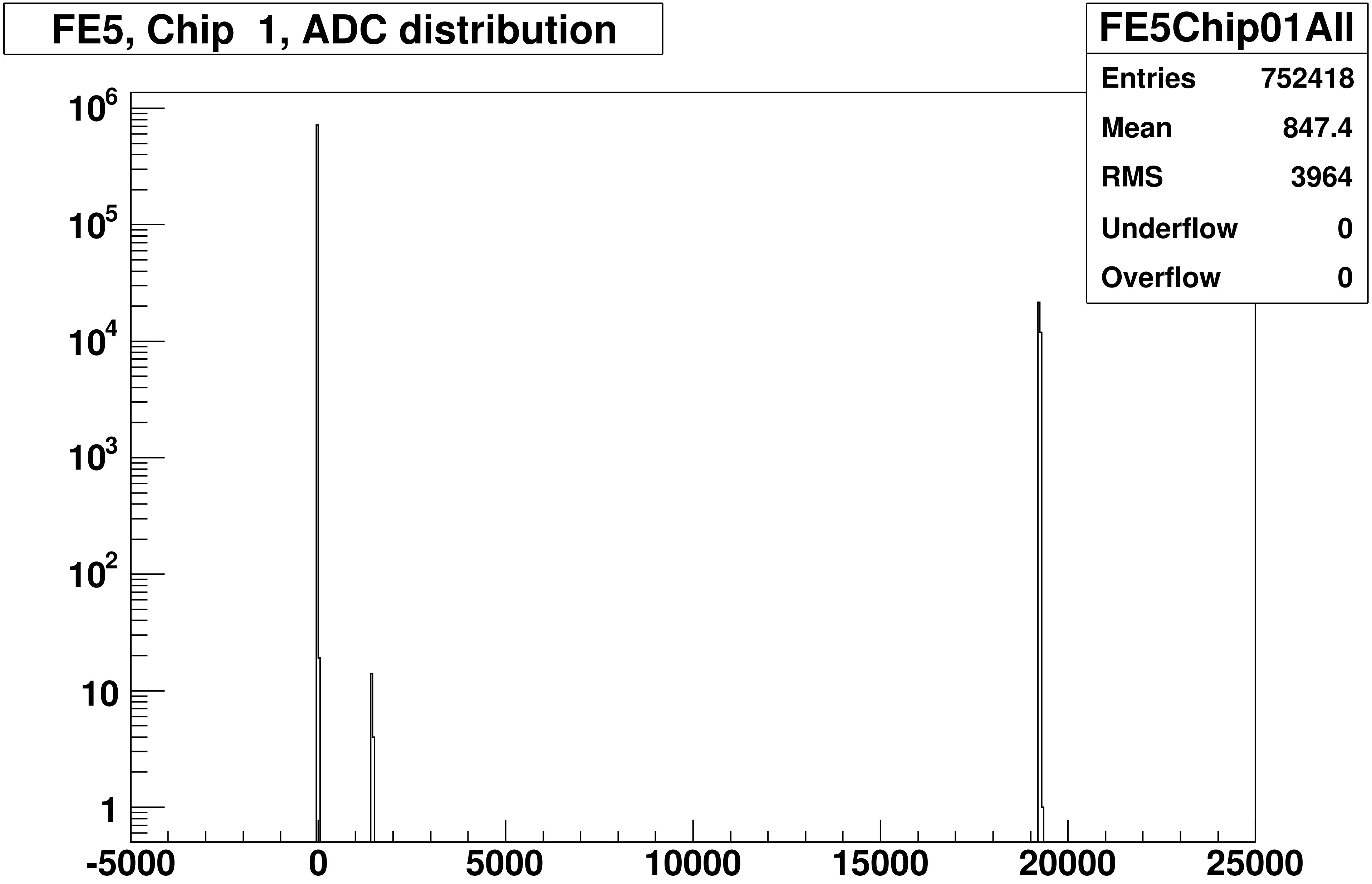

FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 752418
Mean 847.4
RMS 3964
Underflow 0
Overflow 0
$10^6$
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$10^4$
$10^3$
$10^2$
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0
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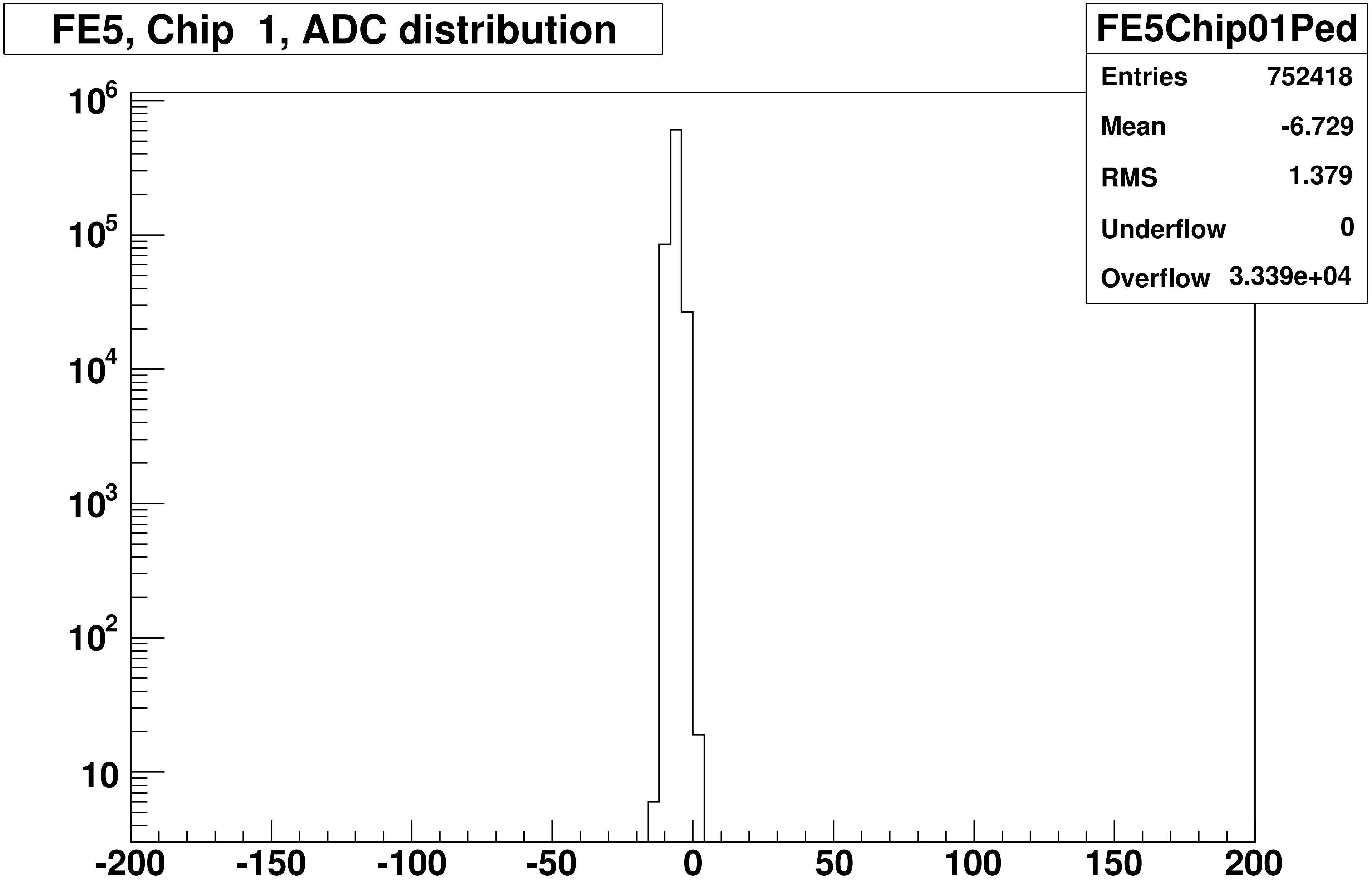
20000
25000


FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 752418
Mean -6.729
RMS 1.379
Underflow 0
Overflow 3.339e+04
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10^5
10^4
10^3
10^2
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-100
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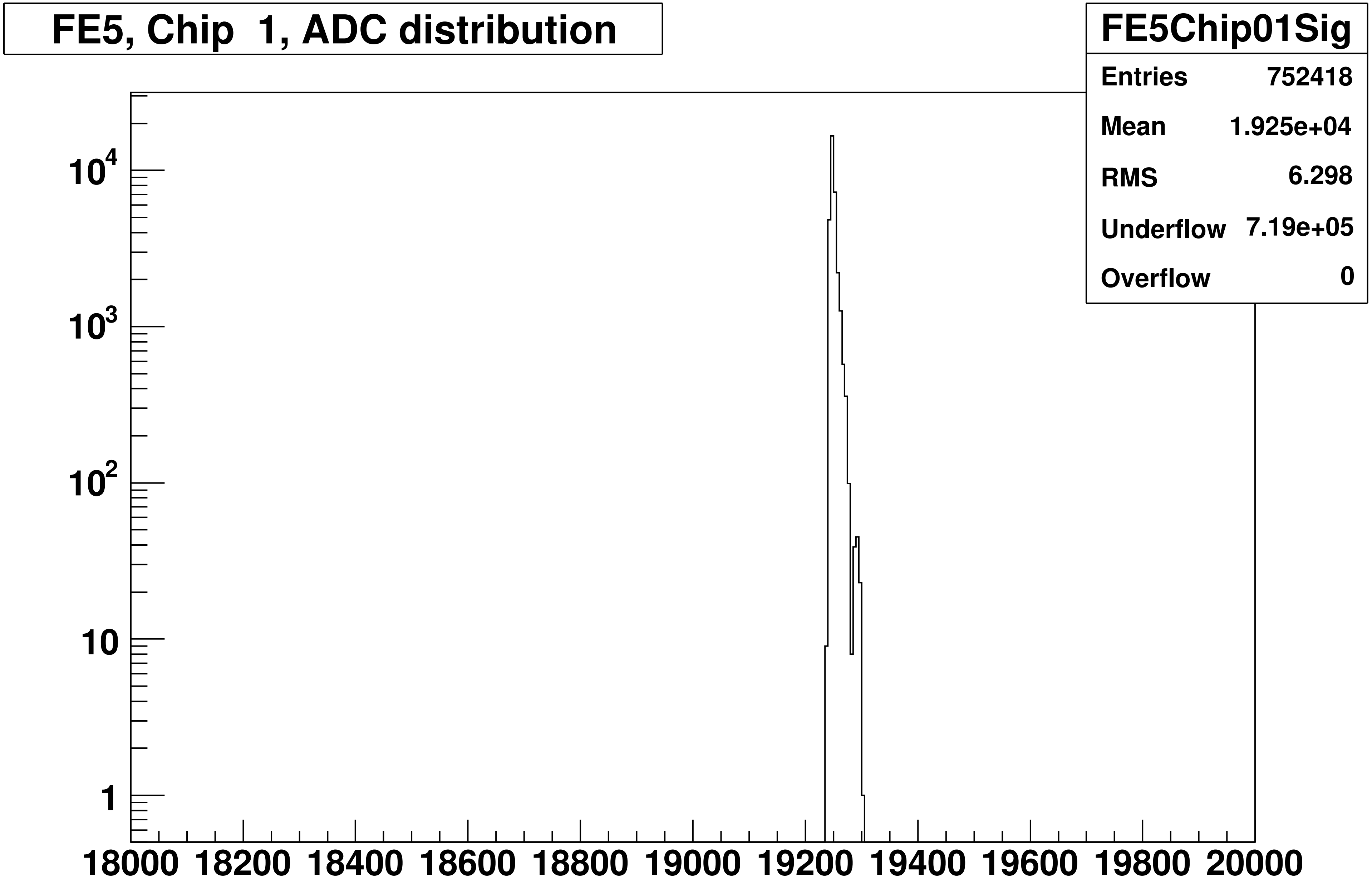
100
150
200



FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 752418
Mean 1.925e+04
RMS 6.298
Underflow 7.19e+05
Overflow 0
10^4
10^3
10^2
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18000
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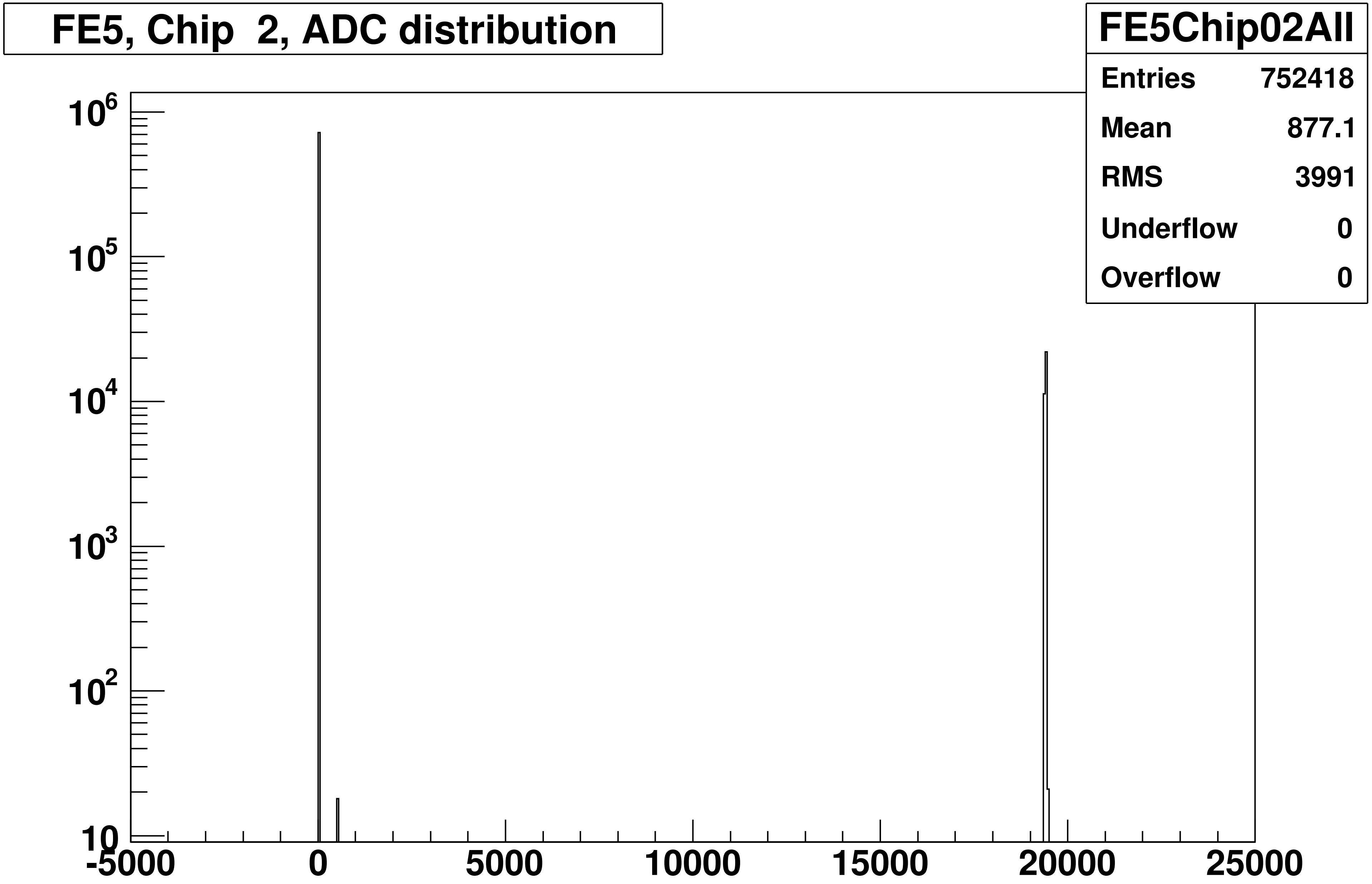
19600
19800
20000


FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 752418
Mean 877.1
RMS 3991
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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-5000
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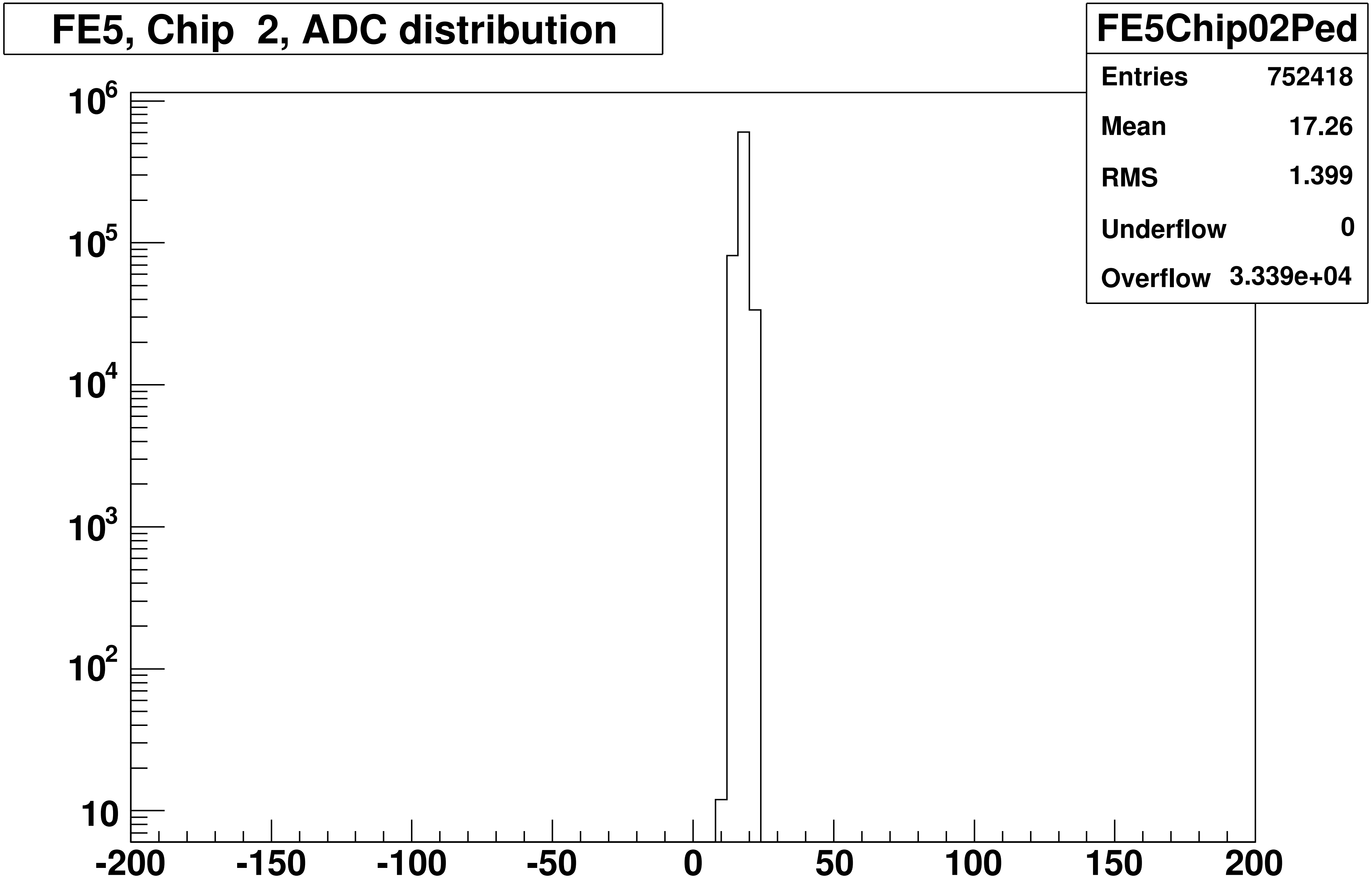
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 752418
Mean 17.26
RMS 1.399
Underflow 0
Overflow 3.339e+04
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10^5
10^4
10^3
10^2
10
-200
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-100
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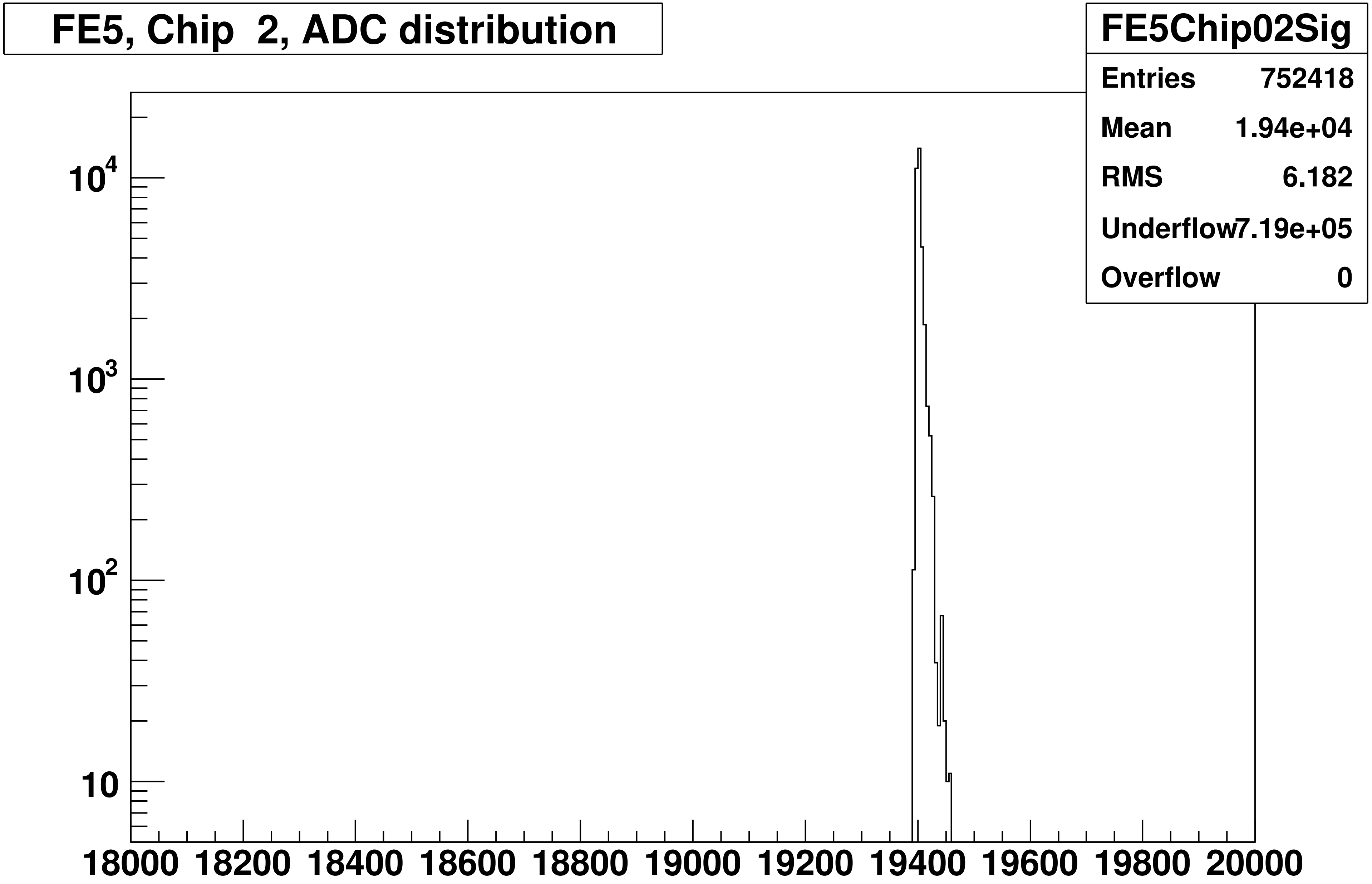
0
50
100
150
200


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 752418
Mean 1.94e+04
RMS 6.182
Underflow 7.19e+05
Overflow 0
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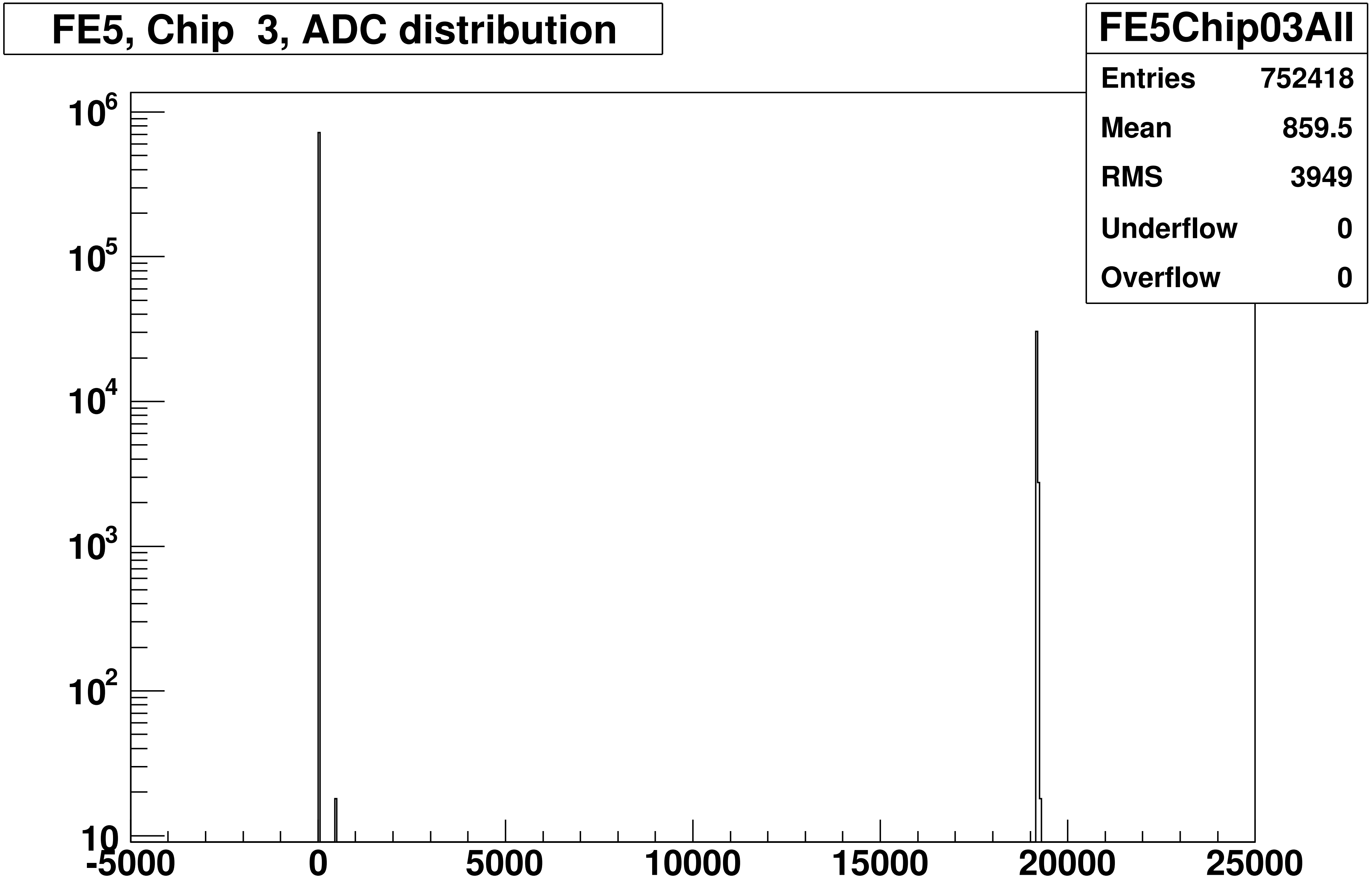
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 752418
Mean 859.5
RMS 3949
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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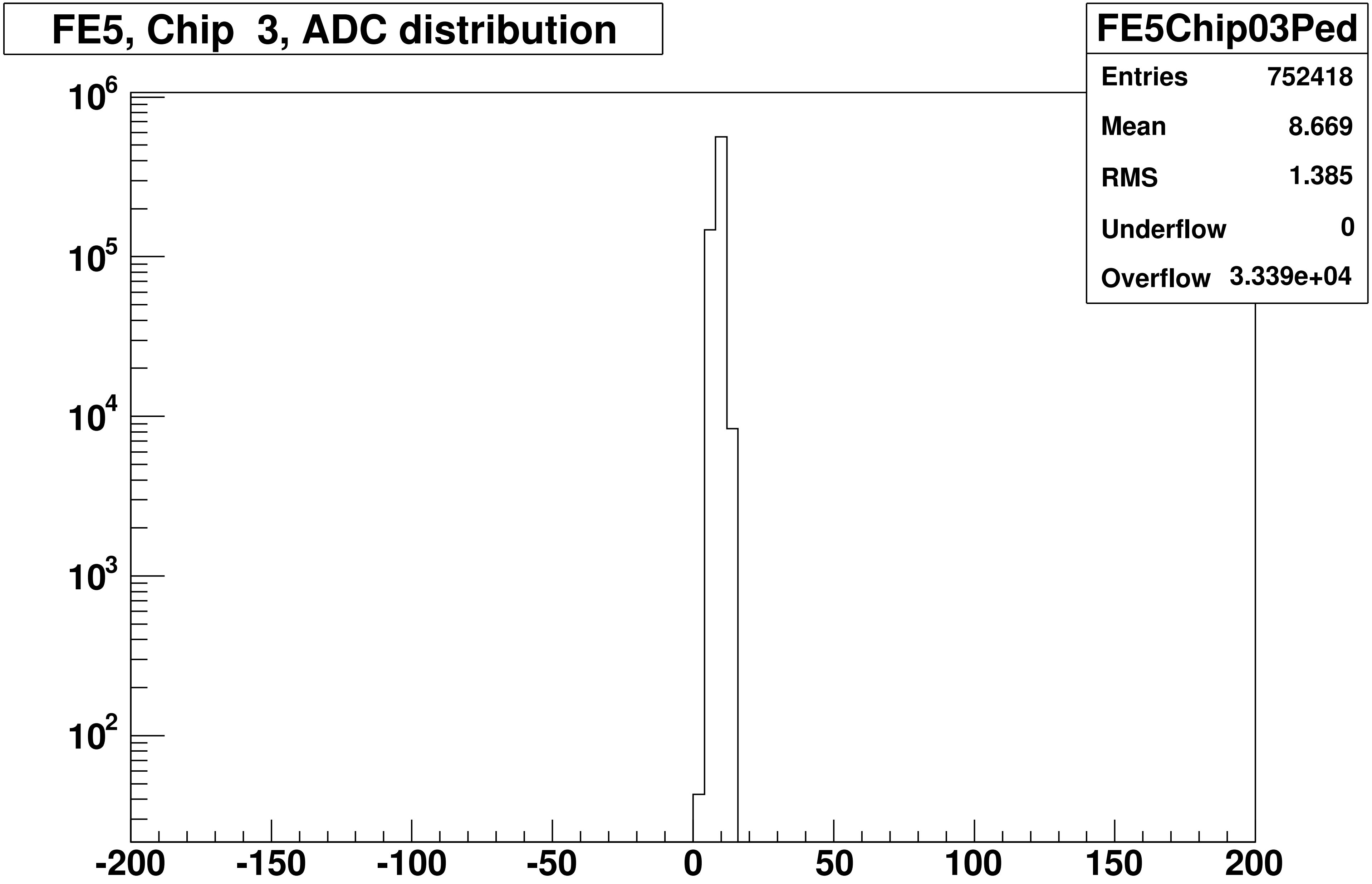
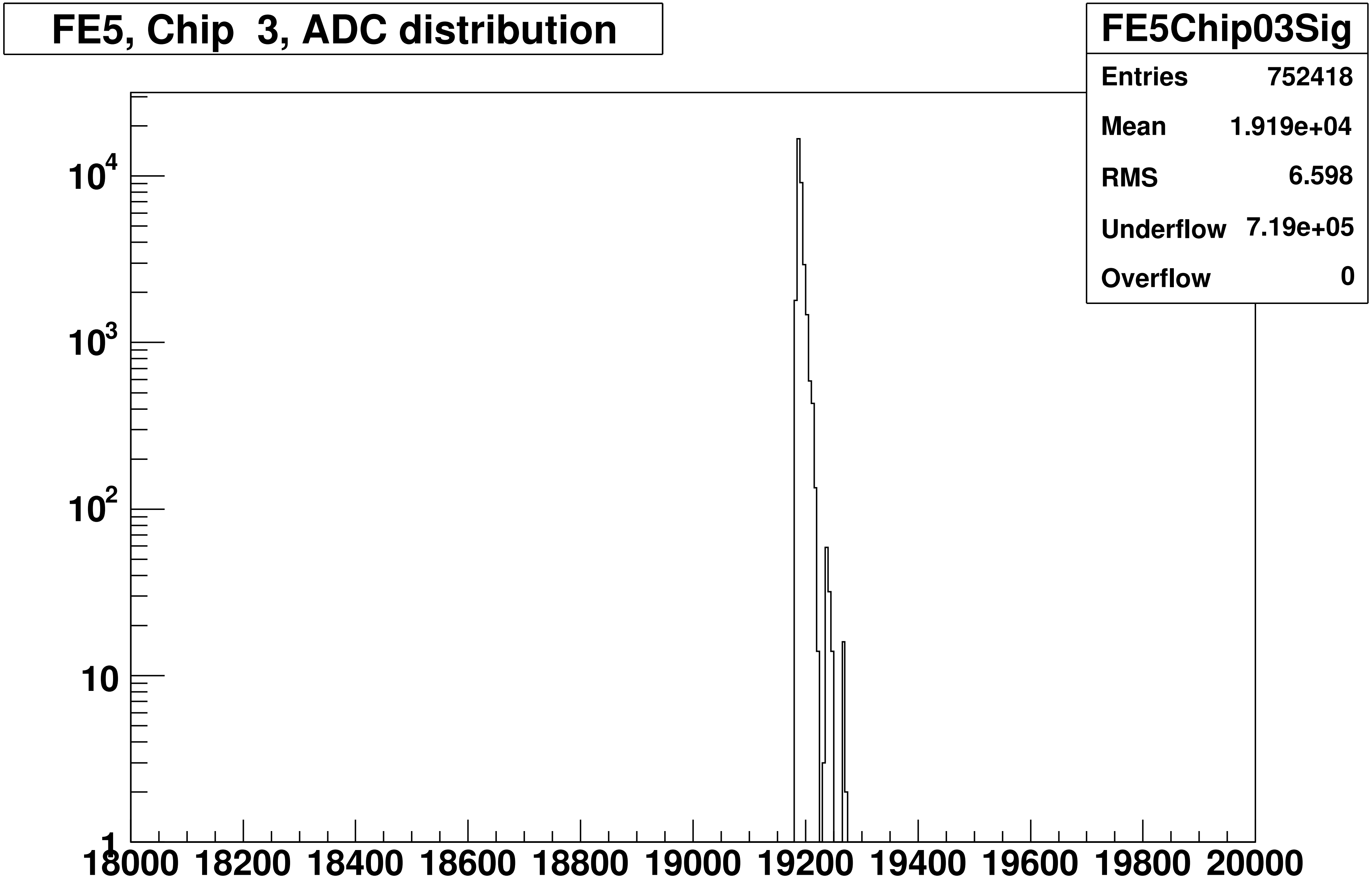
20000
25000


FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 752418
Mean 8.669
RMS 1.385
Underflow 0
Overflow 3.339e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
-200
-150
-100
-50
0
50
100
150
200



FE5, Chip  3, ADC distribution
FE5Chip03Sig
Entries 752418
Mean 1.919e+04
RMS 6.598
Underflow 7.19e+05
Overflow 0
10^4
10^3
10^2
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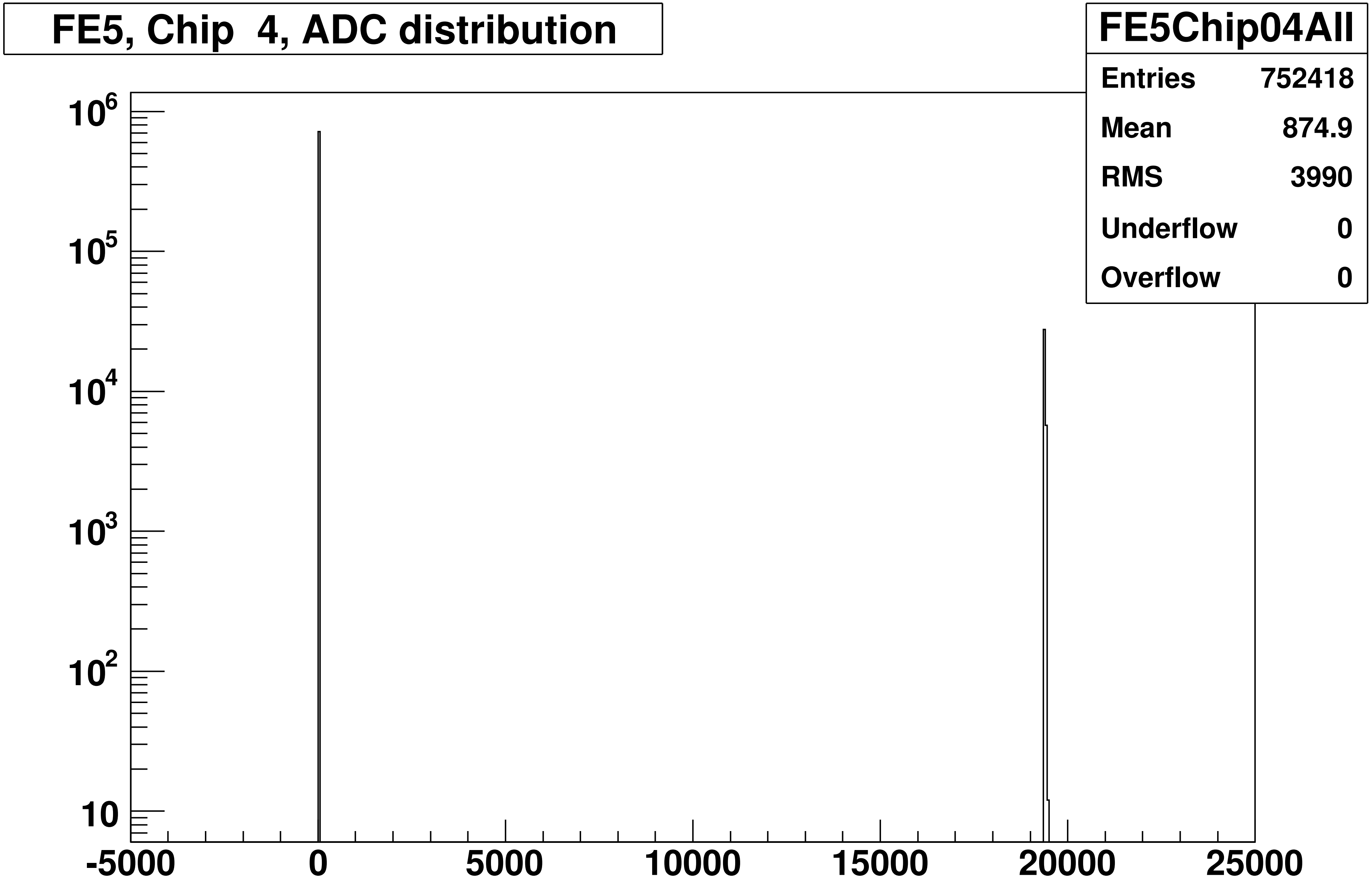
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 752418
Mean 874.9
RMS 3990
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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-5000
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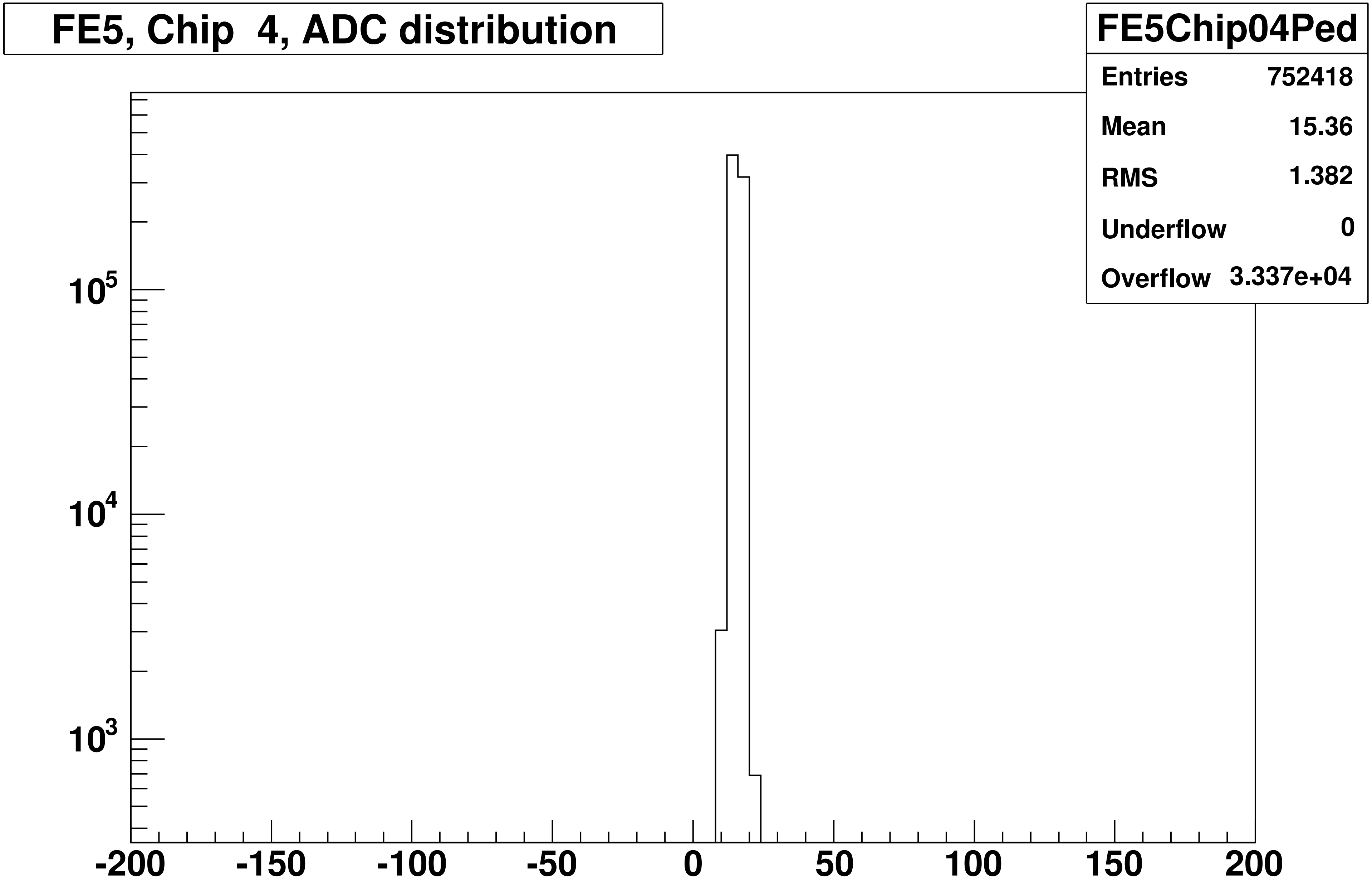
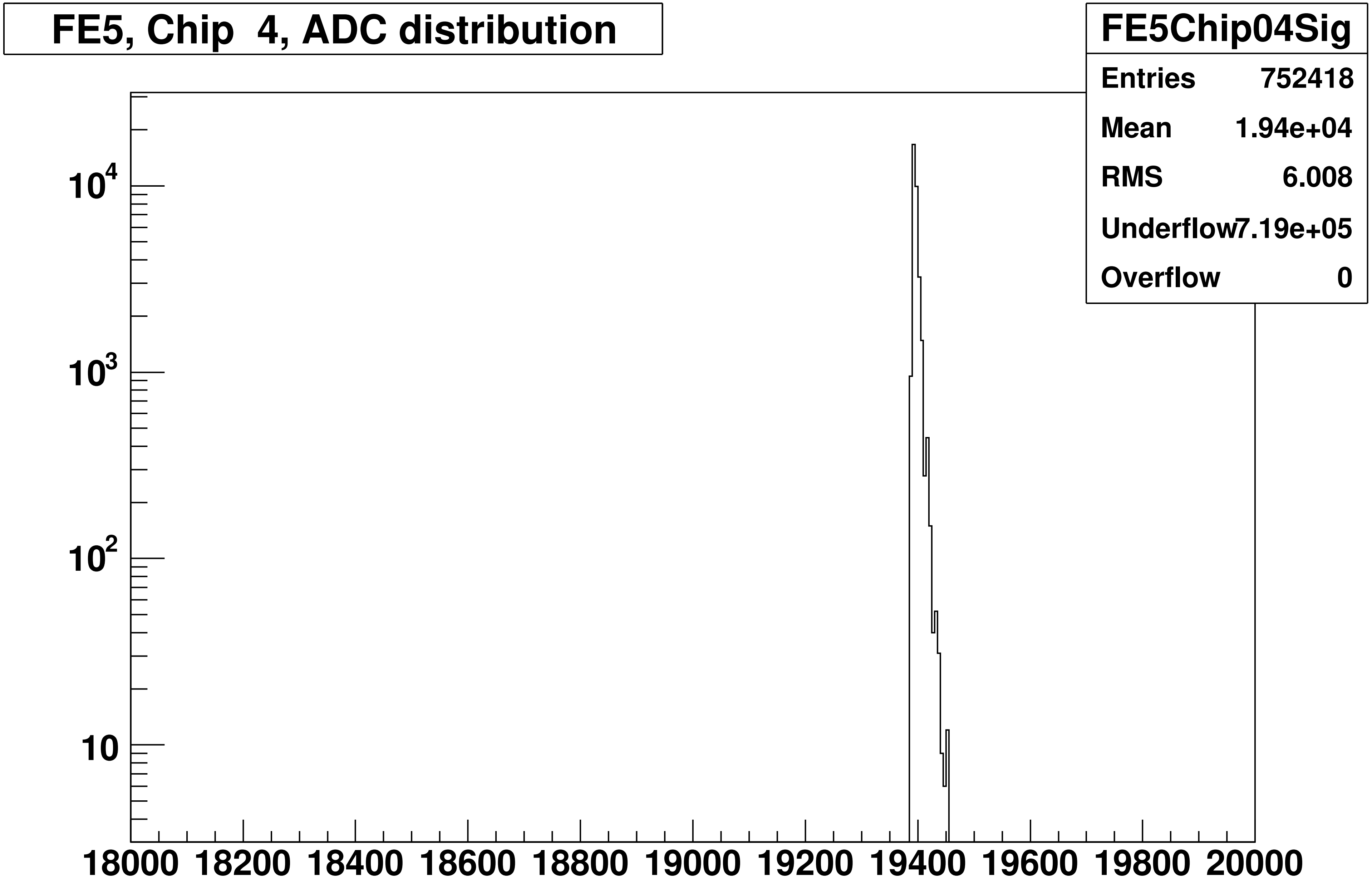
15000
20000
25000


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 752418
Mean 15.36
RMS 1.382
Underflow 0
Overflow 3.337e+04
10^5
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200



FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 752418
Mean 1.94e+04
RMS 6.008
Underflow 7.19e+05
Overflow 0
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$10^3$
$10^2$
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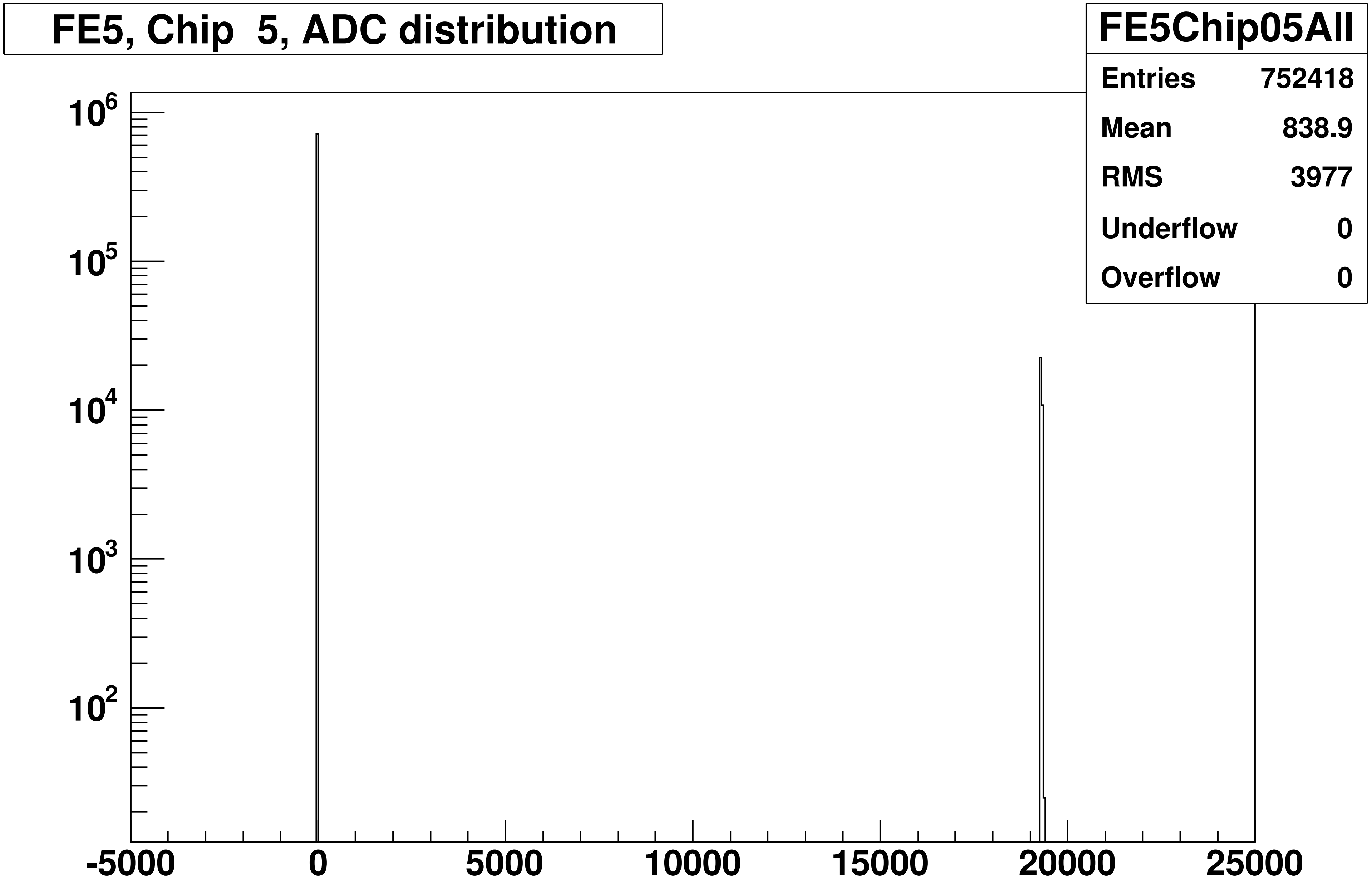
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 752418
Mean 838.9
RMS 3977
Underflow 0
Overflow 0
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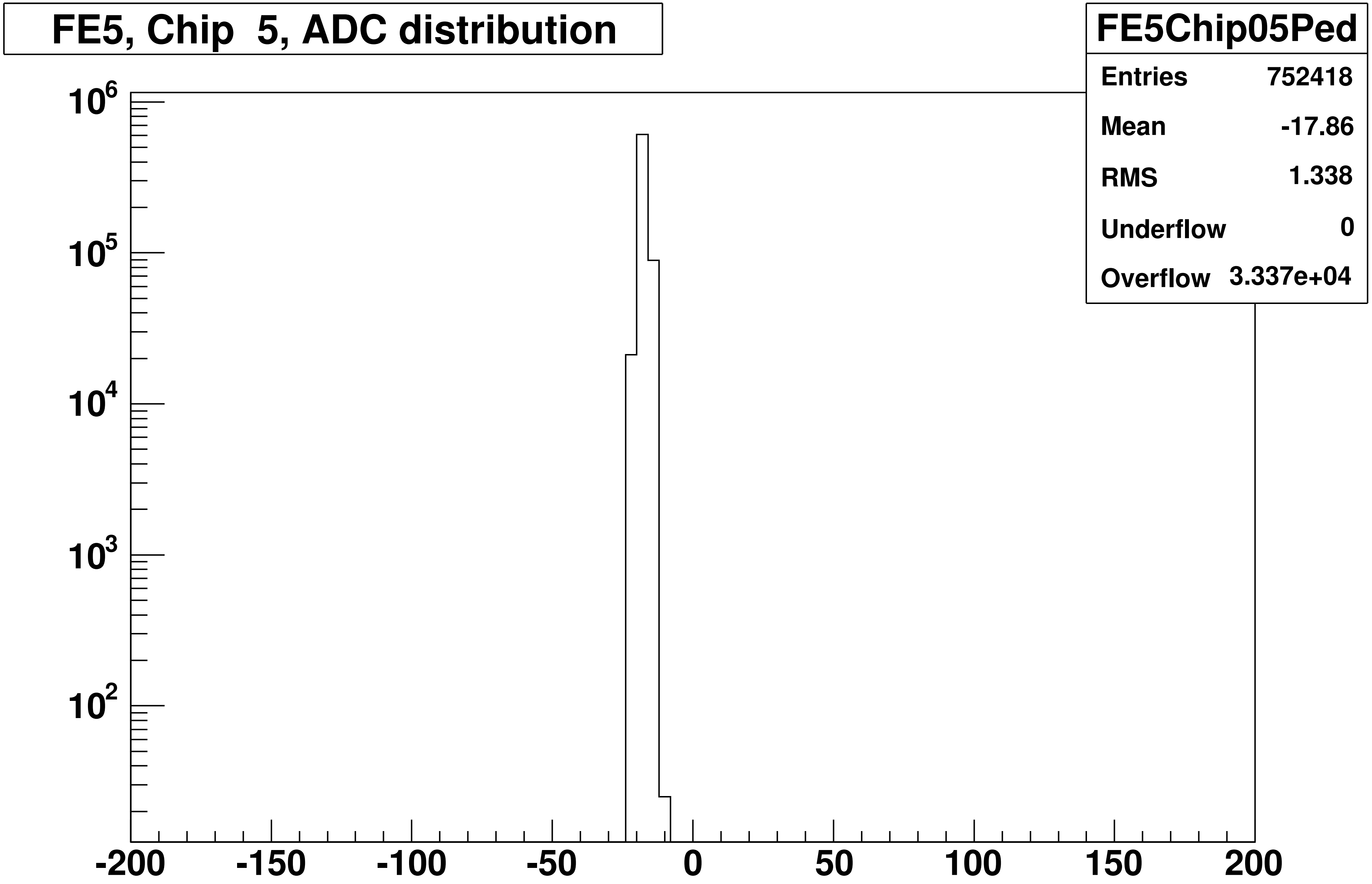
20000
25000



FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 752418
Mean -17.86
RMS 1.338
Underflow 0
Overflow 3.337e+04
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$10^4$
$10^3$
$10^2$
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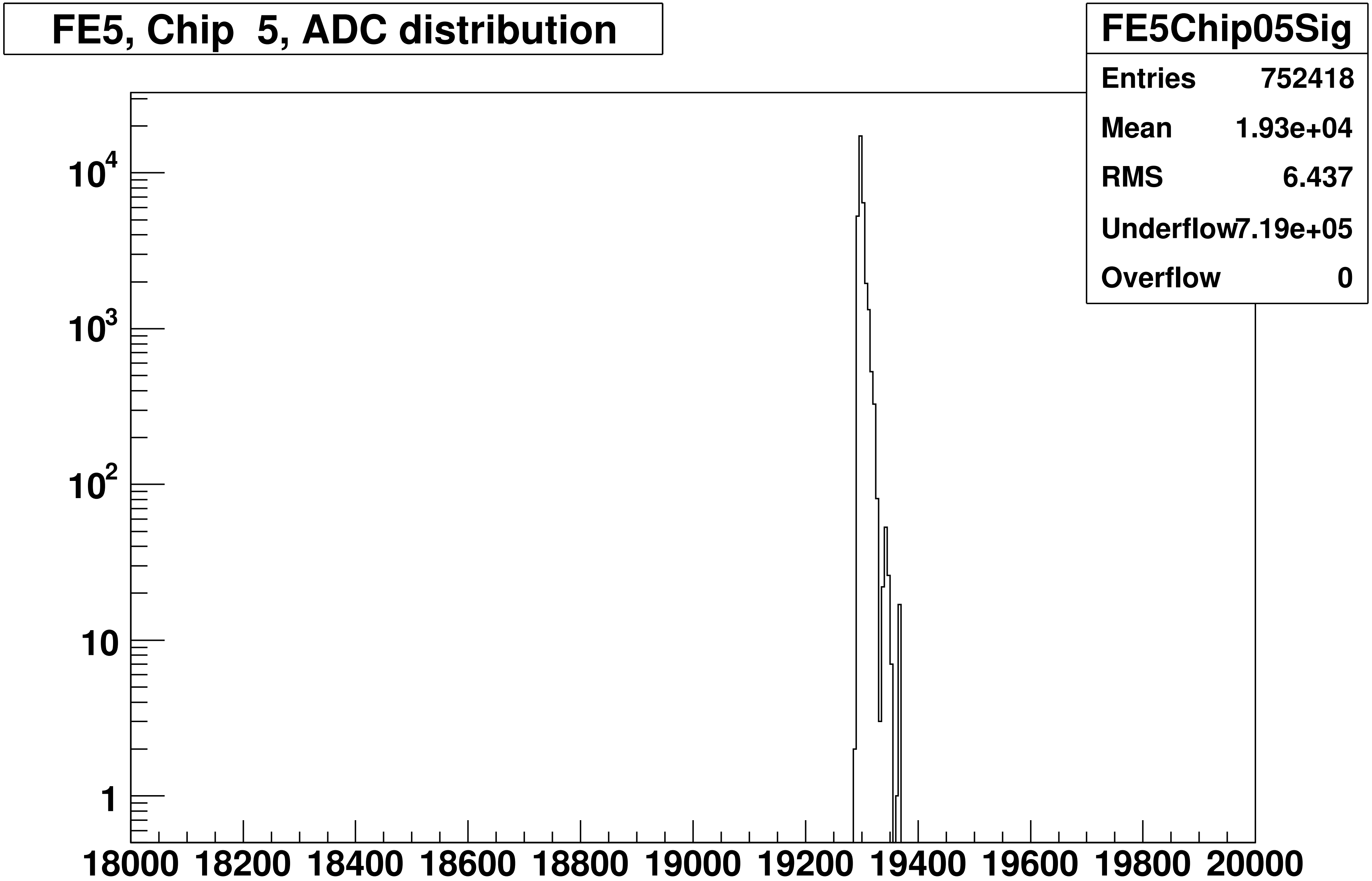
FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 752418
Mean 1.93e+04
RMS 6.437
Underflow 7.19e+05
Overflow 0
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$10^3$
$10^2$
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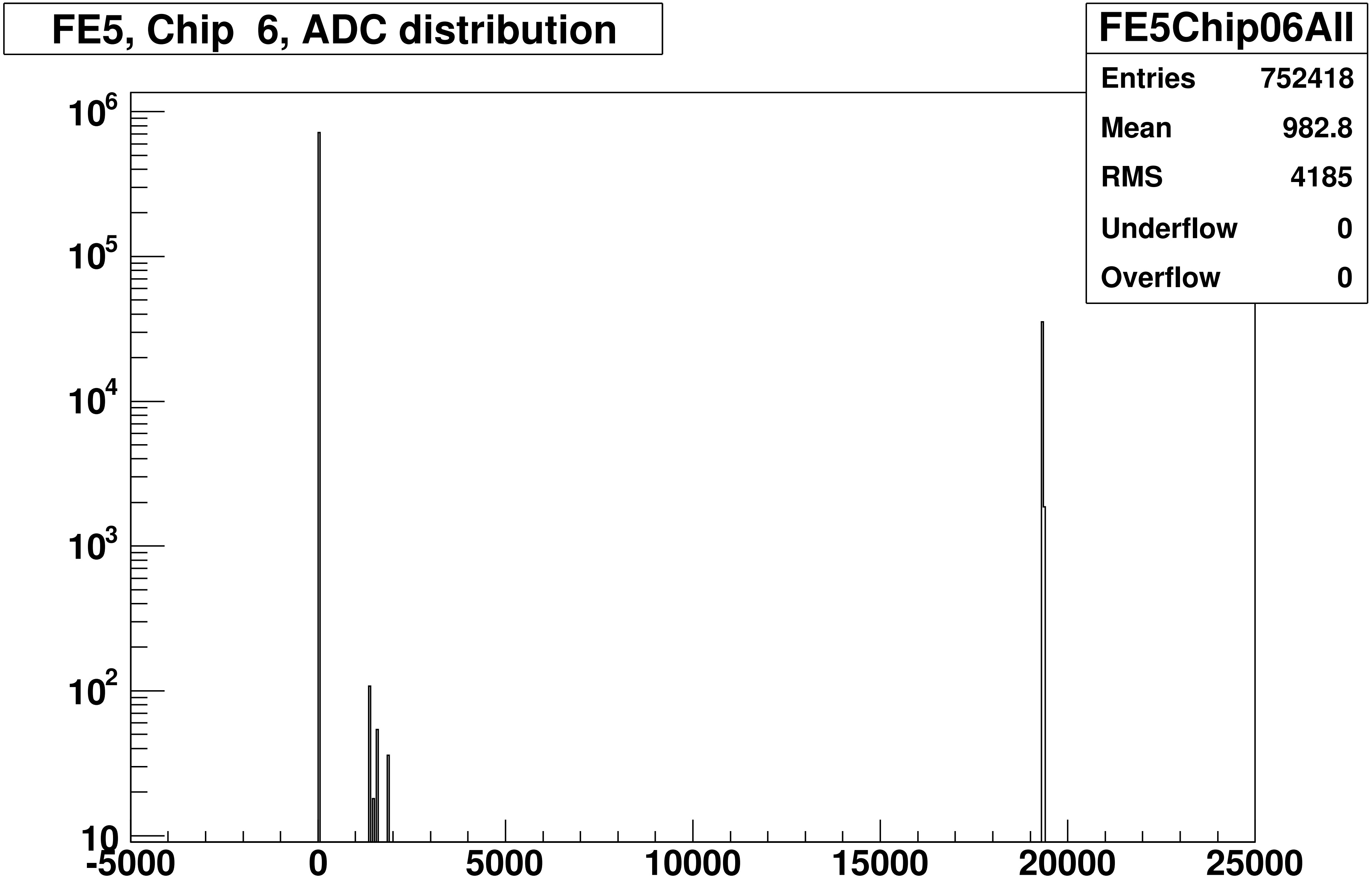
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip  6, ADC distribution
FE5Chip06All
Entries 752418
Mean 982.8
RMS 4185
Underflow 0
Overflow 0
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$10^2$
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$10^4$
$10^5$
$10^6$
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20000
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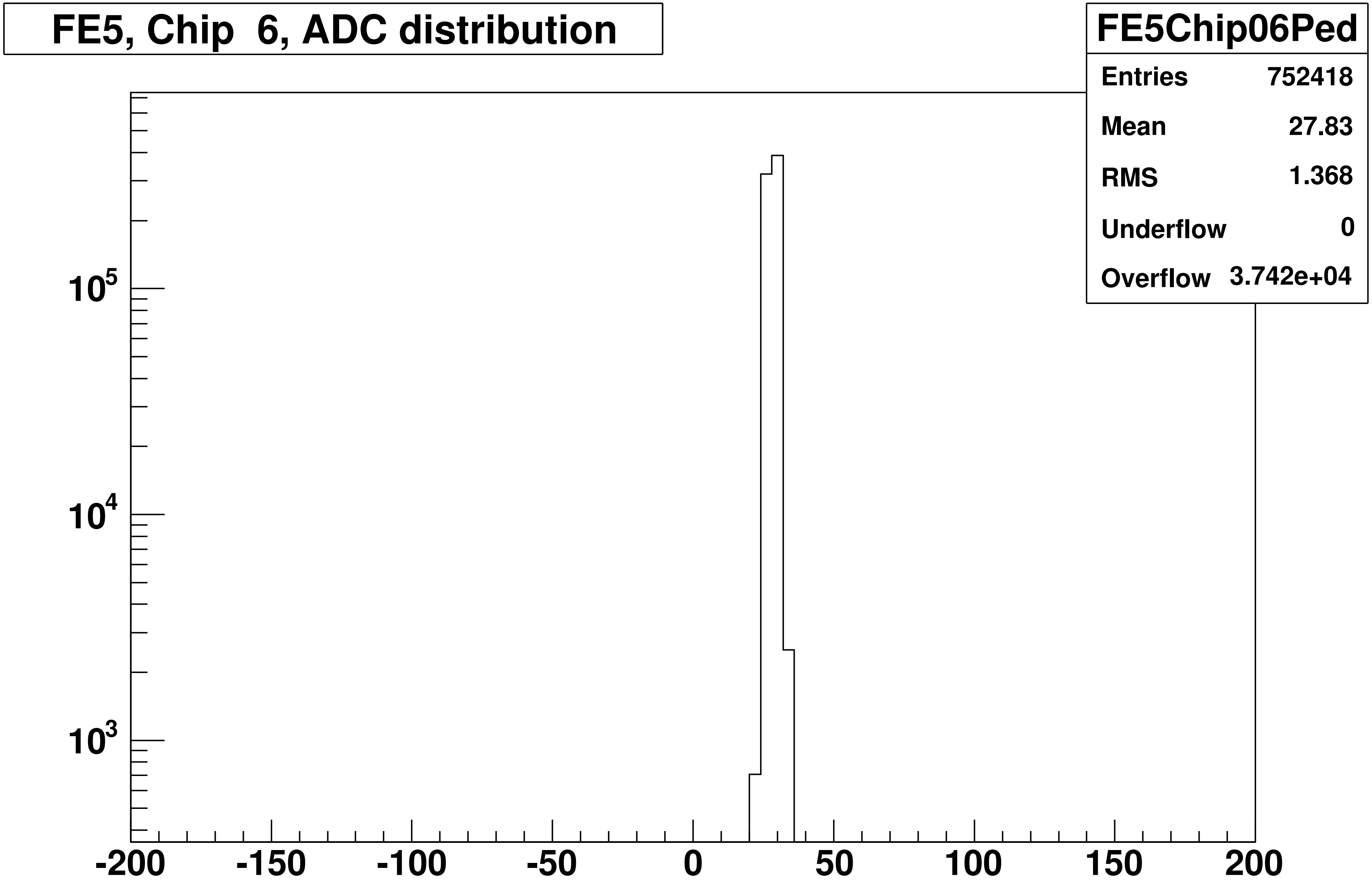
FE5, Chip  6, ADC distribution
FE5Chip06Ped
Entries 752418
Mean 27.83
RMS 1.368
Underflow 0
Overflow 3.742e+04
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$10^4$
$10^3$
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-100
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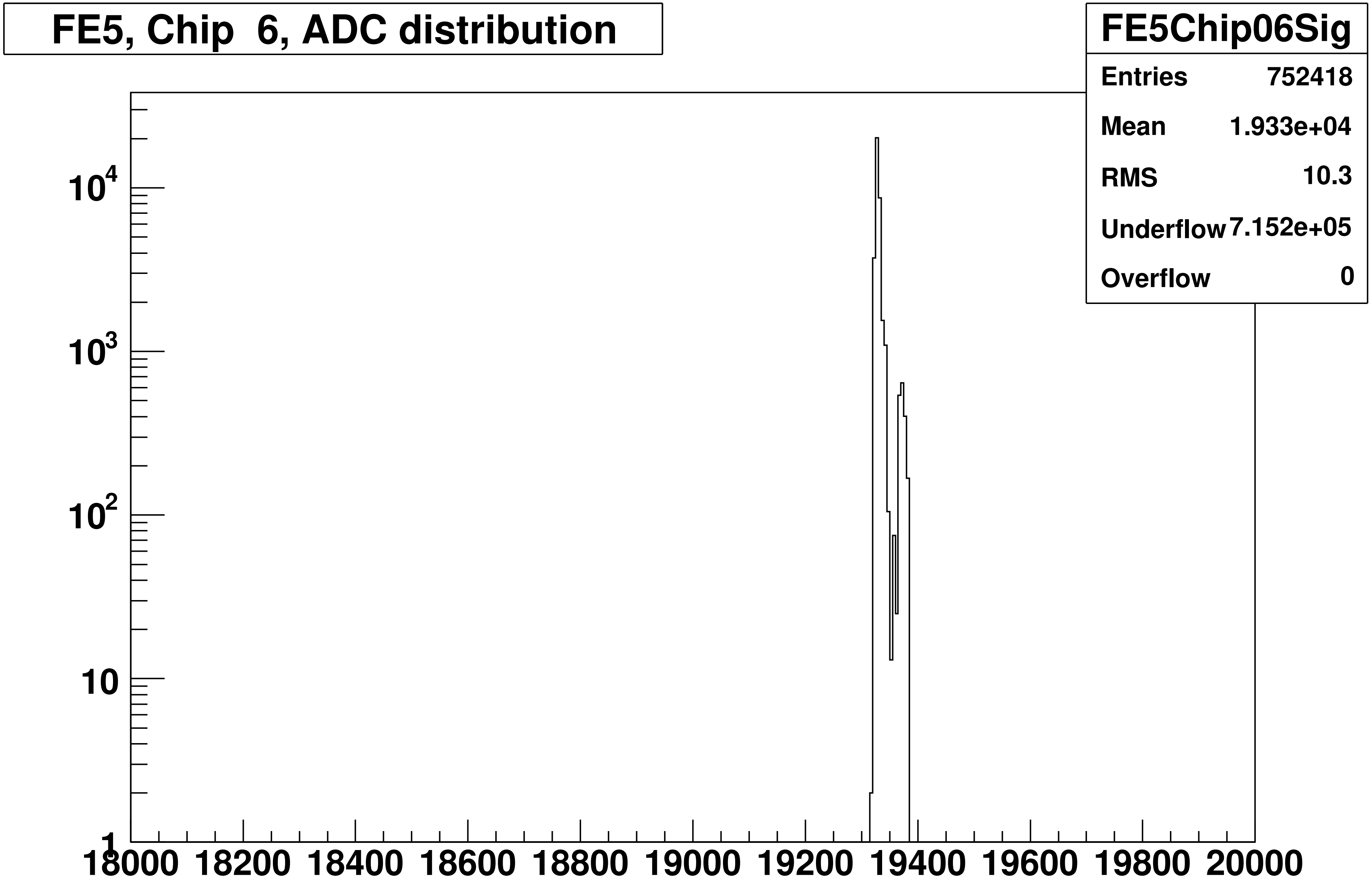
50
100
150
200



FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 752418
Mean 1.933e+04
RMS 10.3
Underflow 7.152e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
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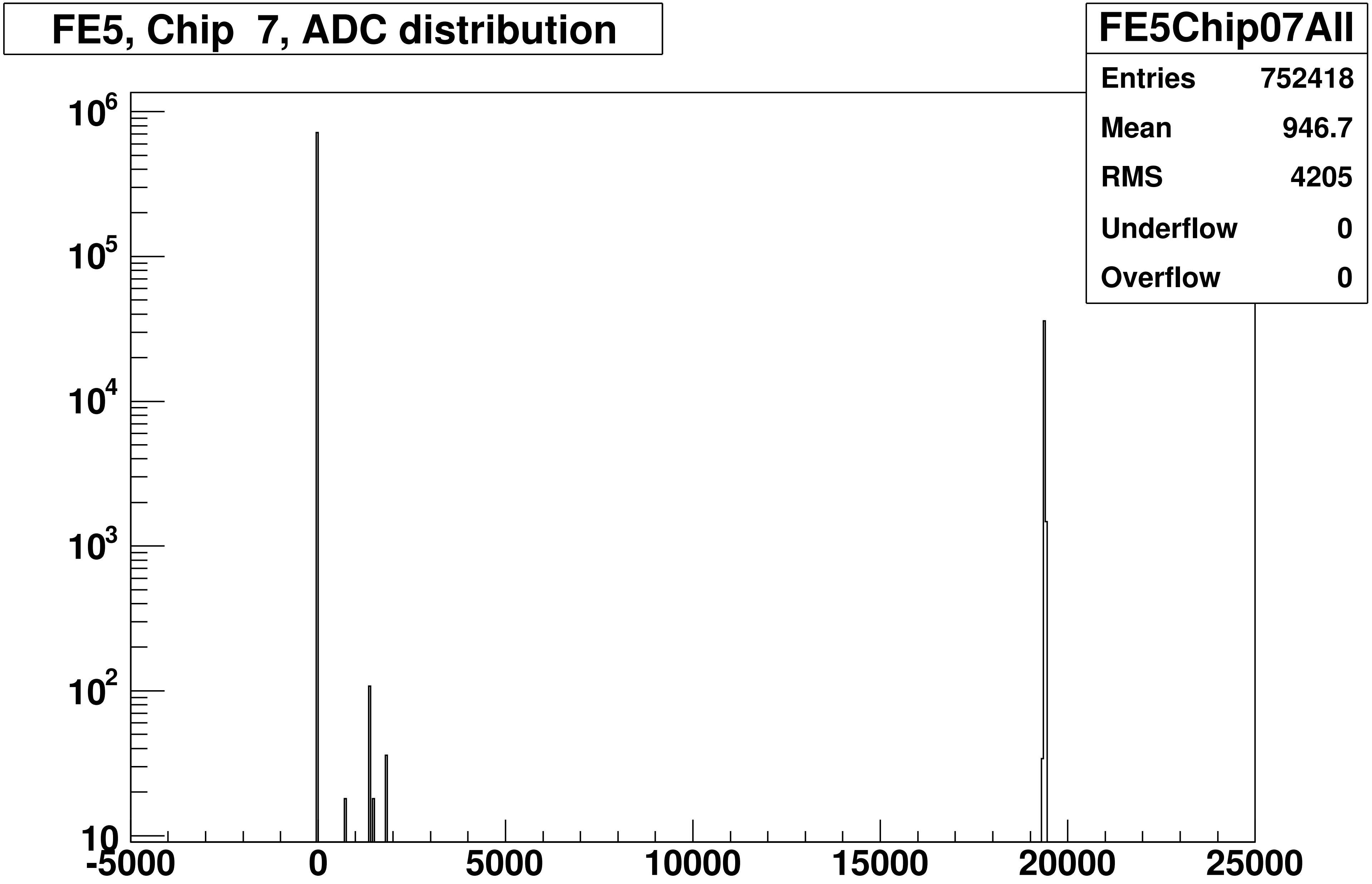
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 752418
Mean 946.7
RMS 4205
Underflow 0
Overflow 0
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$10^2$
$10^3$
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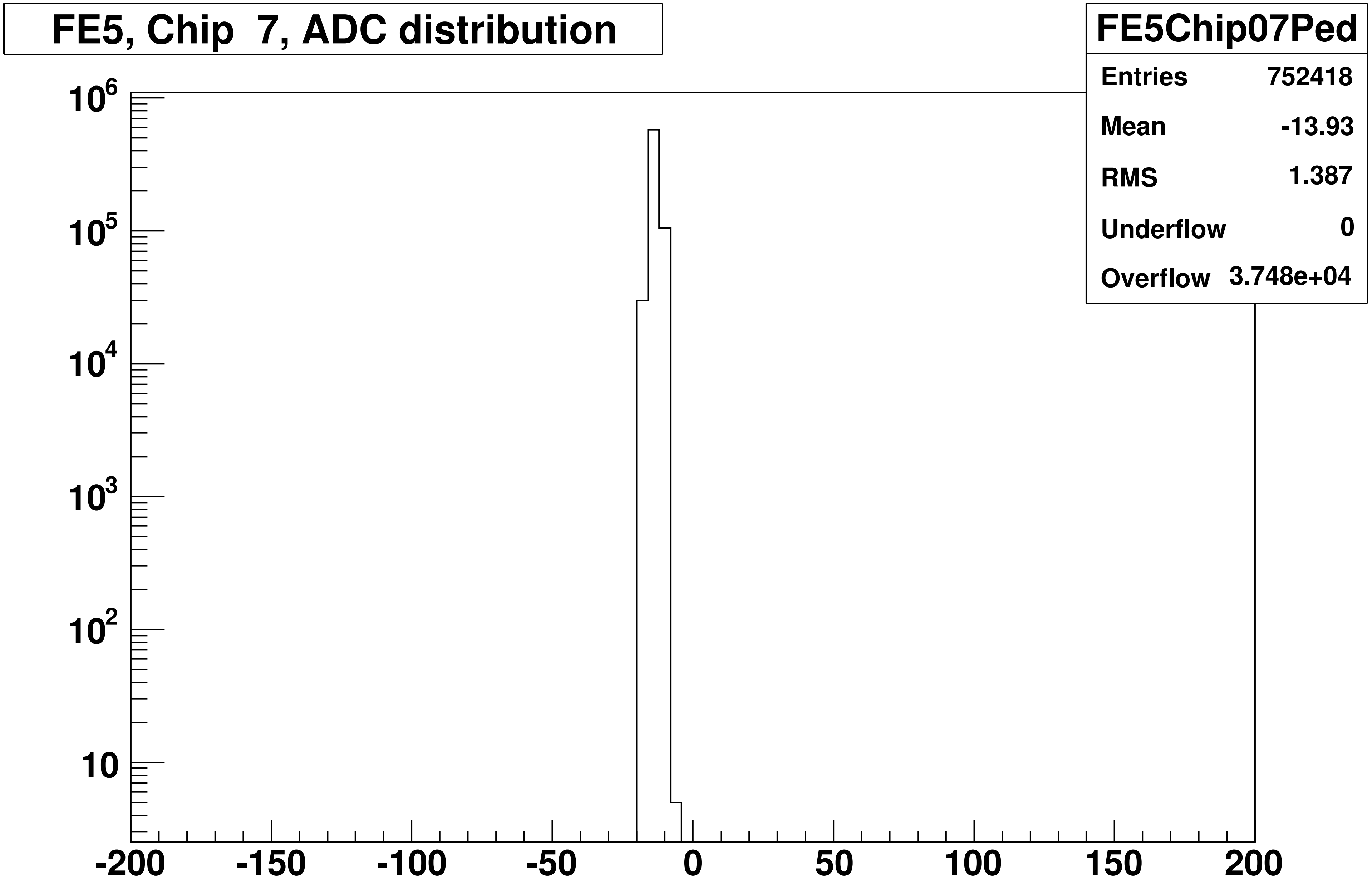
15000
20000
25000



FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 752418
Mean -13.93
RMS 1.387
Underflow 0
Overflow 3.748e+04
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10^4
10^3
10^2
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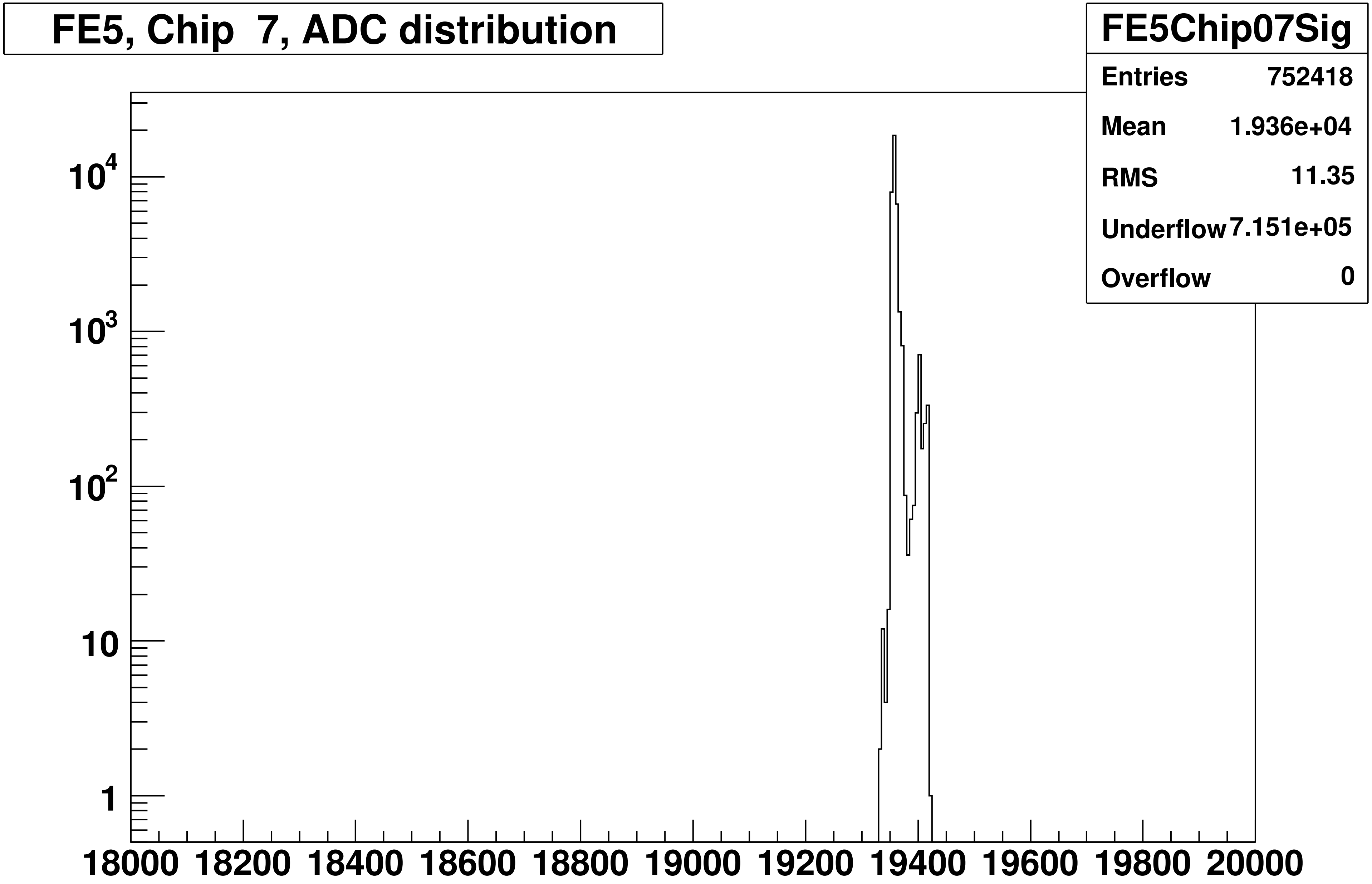
50
100
150
200


FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 752418
Mean 1.936e+04
RMS 11.35
Underflow 7.151e+05
Overflow 0
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$10^3$
$10^2$
10
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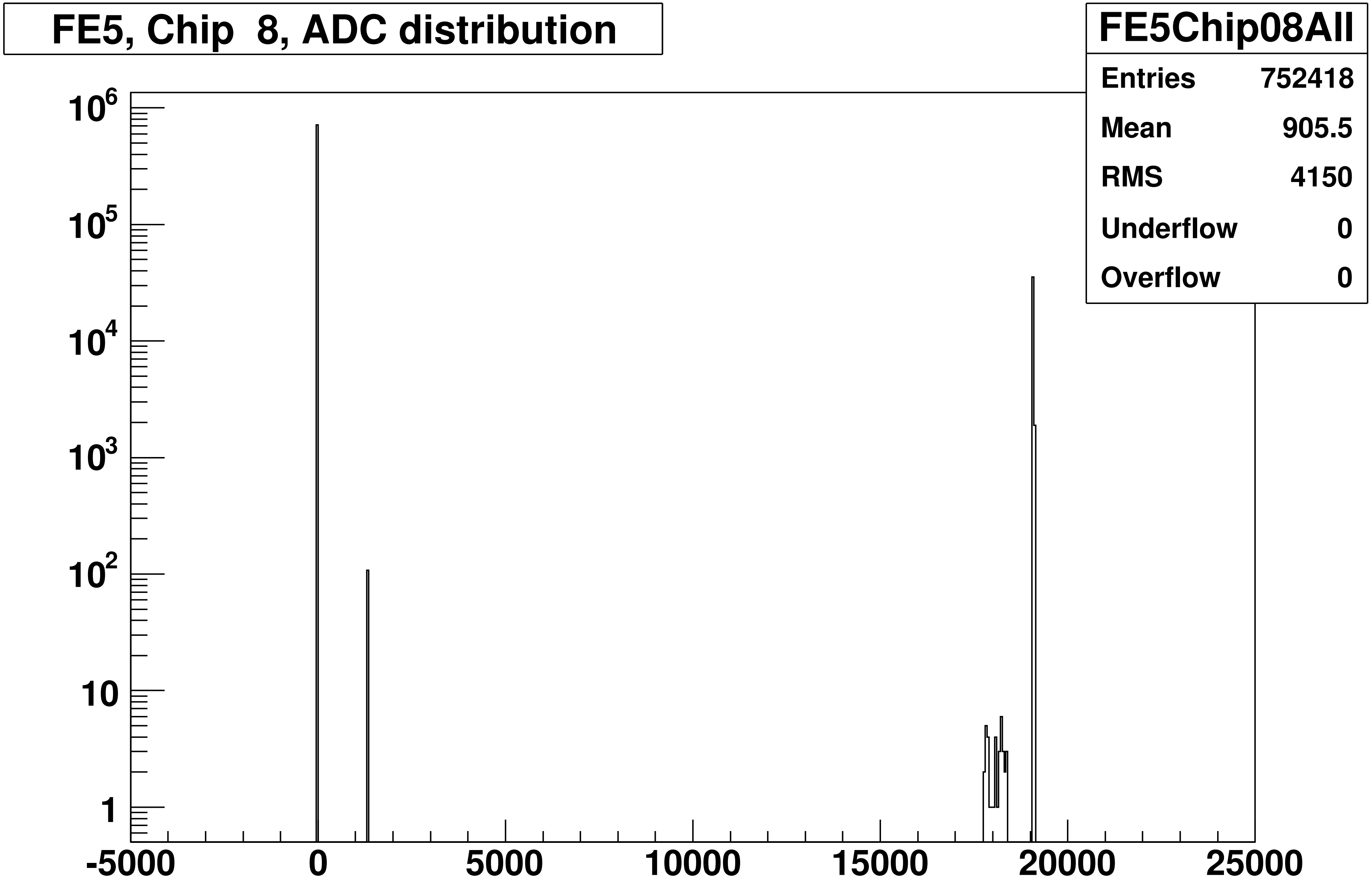
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 752418
Mean 905.5
RMS 4150
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
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0
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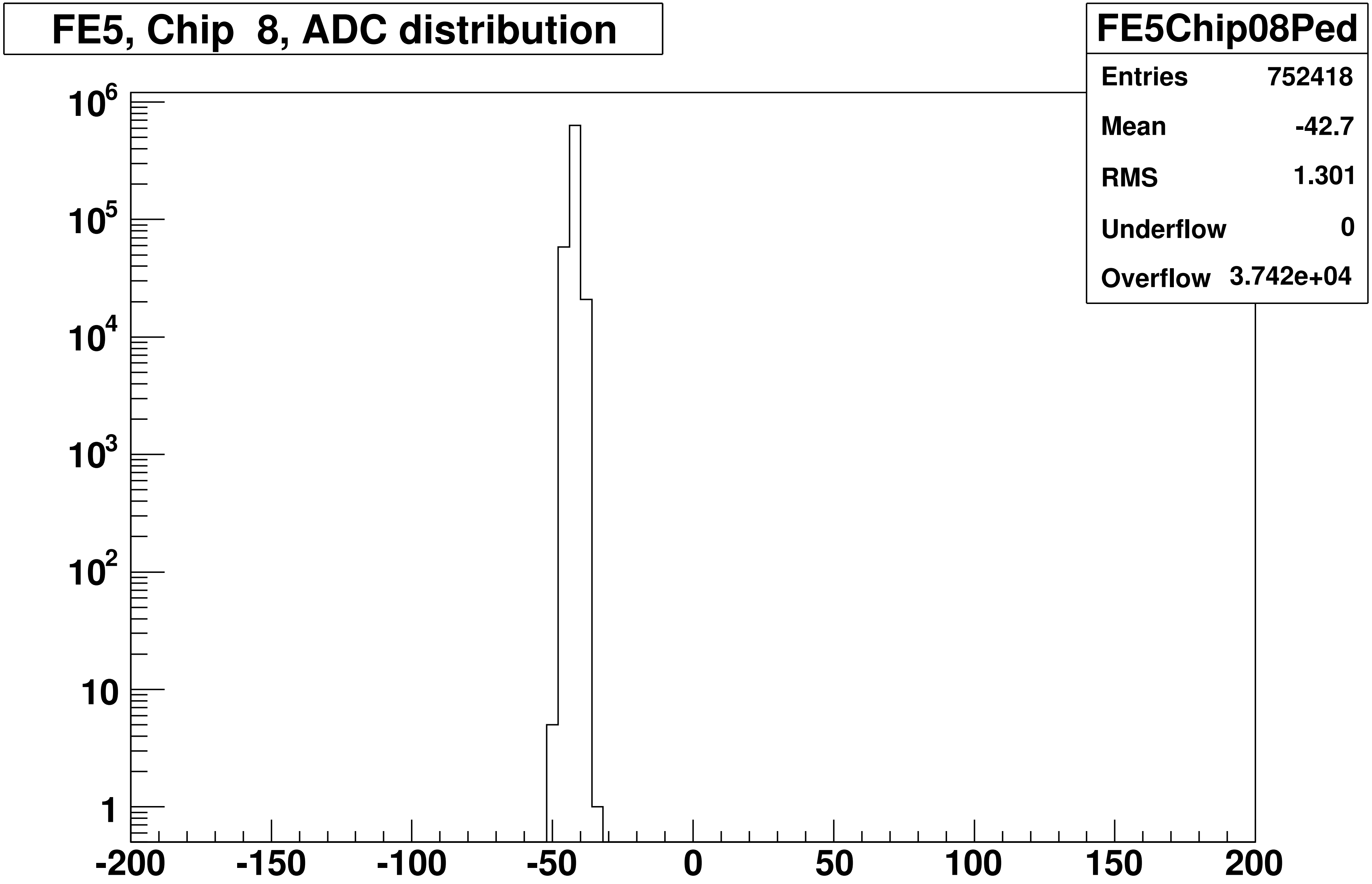
10000
15000
20000
25000


FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 752418
Mean -42.7
RMS 1.301
Underflow 0
Overflow 3.742e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
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1
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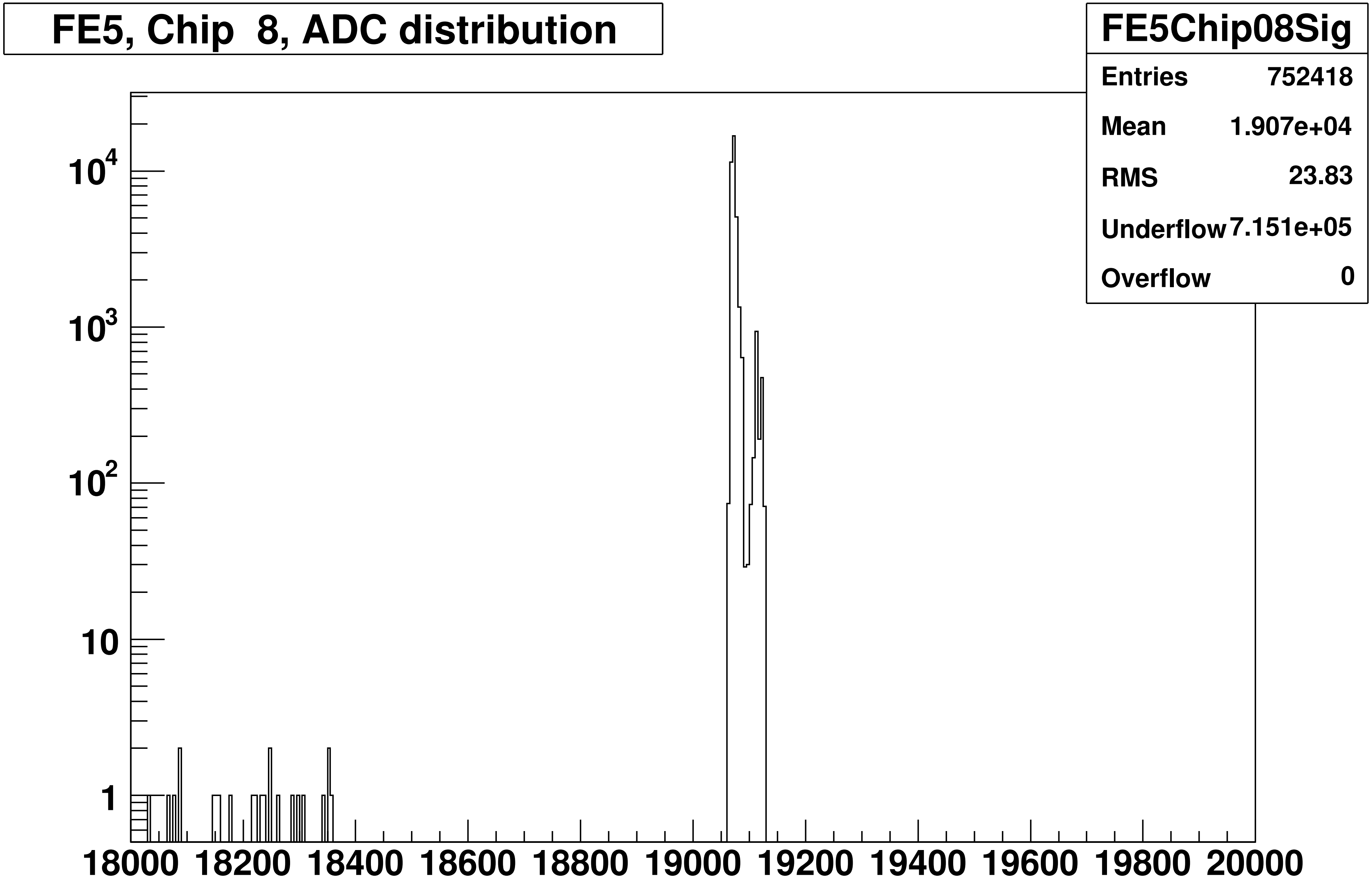
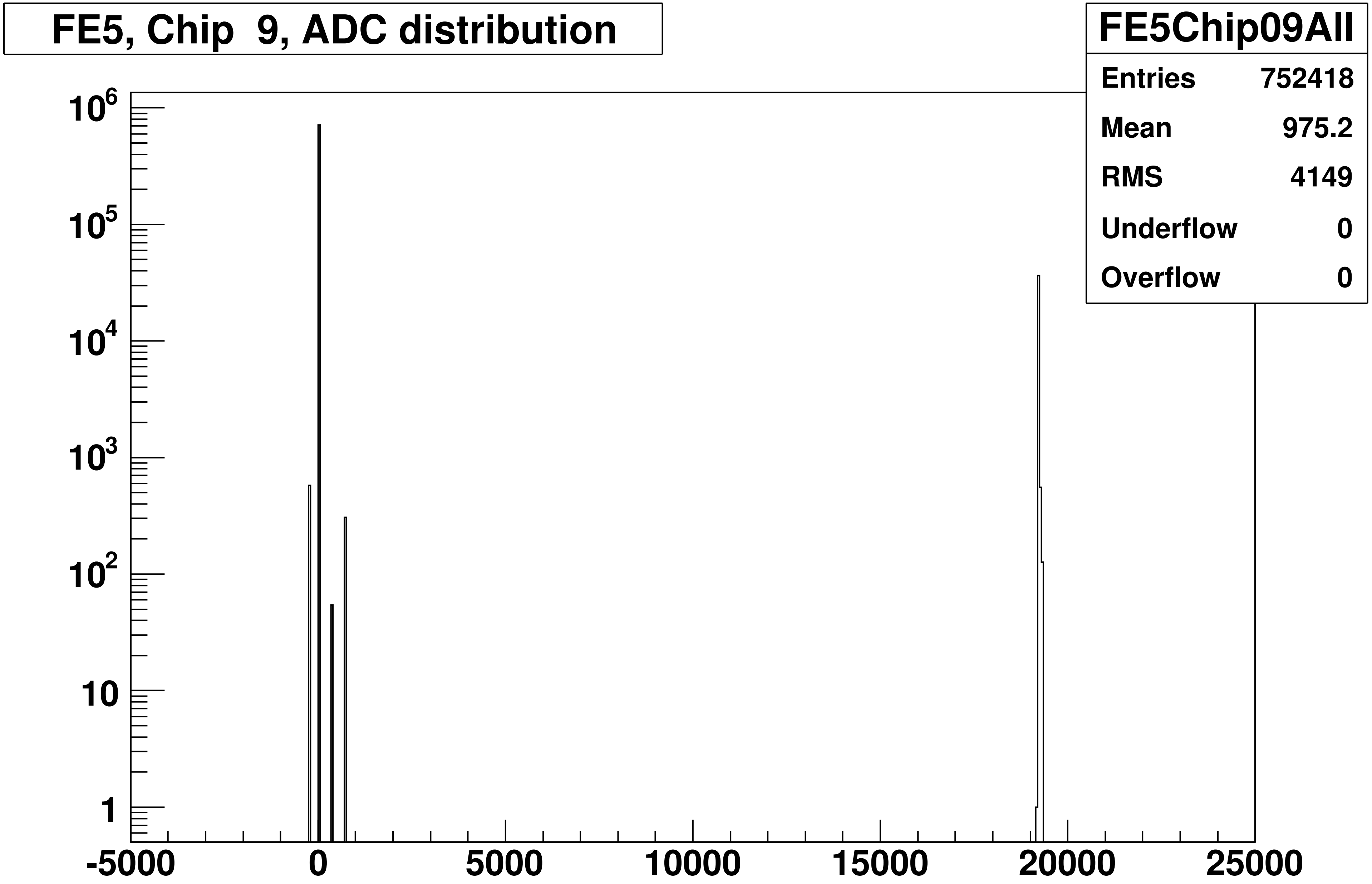
-100
-50
0
50
100
150
200



FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 752418
Mean 1.907e+04
RMS 23.83
Underflow 7.151e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 752418
Mean 975.2
RMS 4149
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
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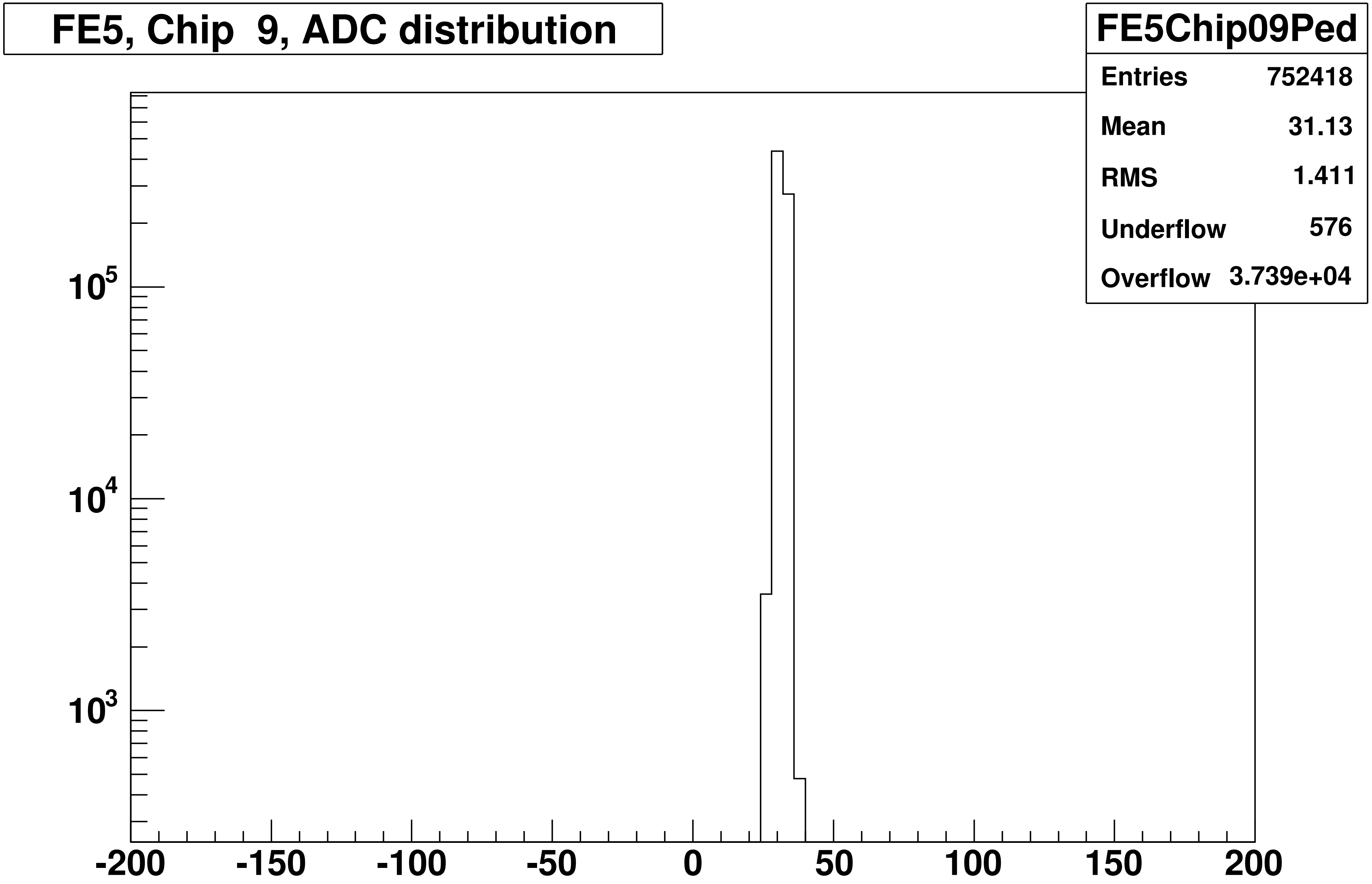
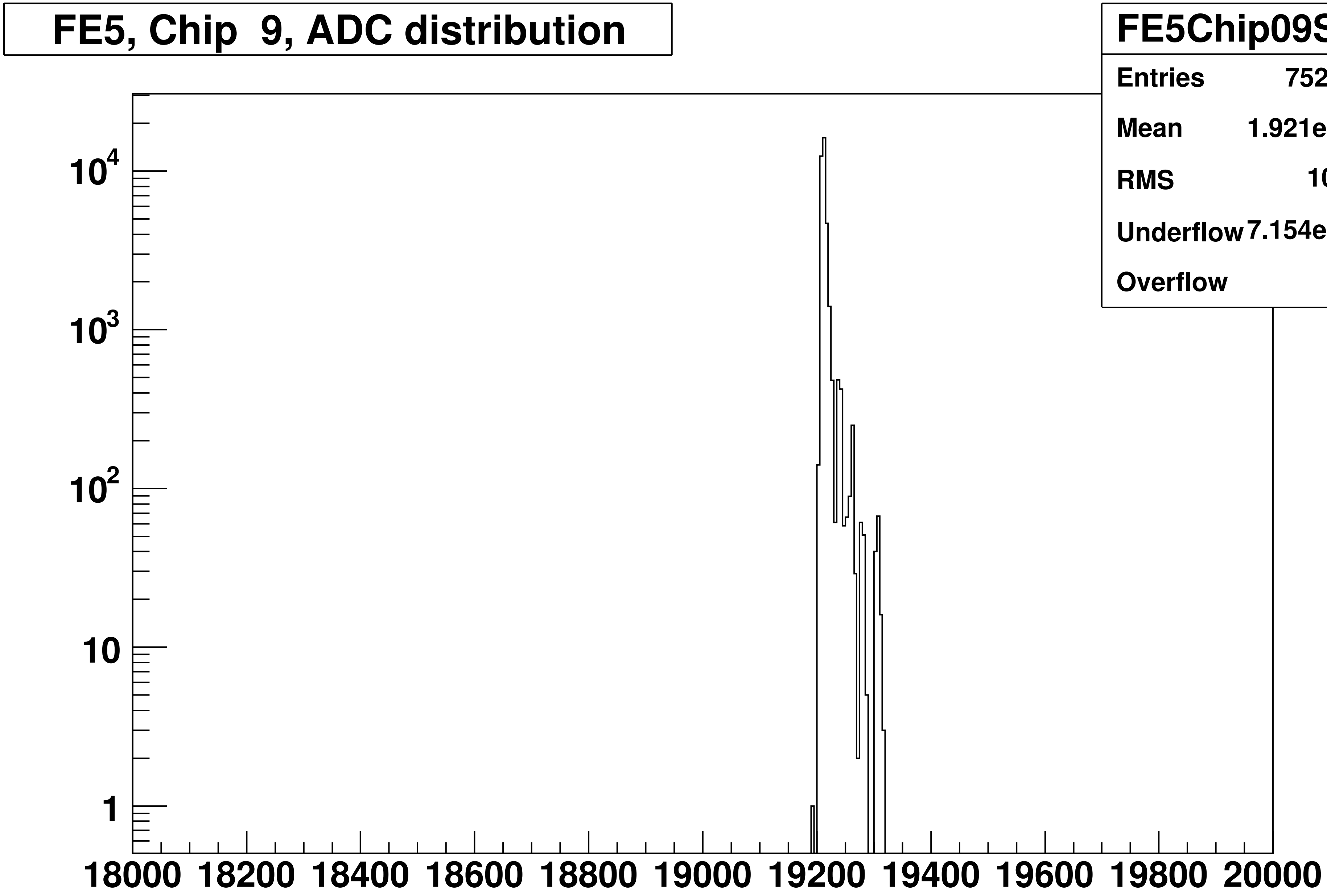
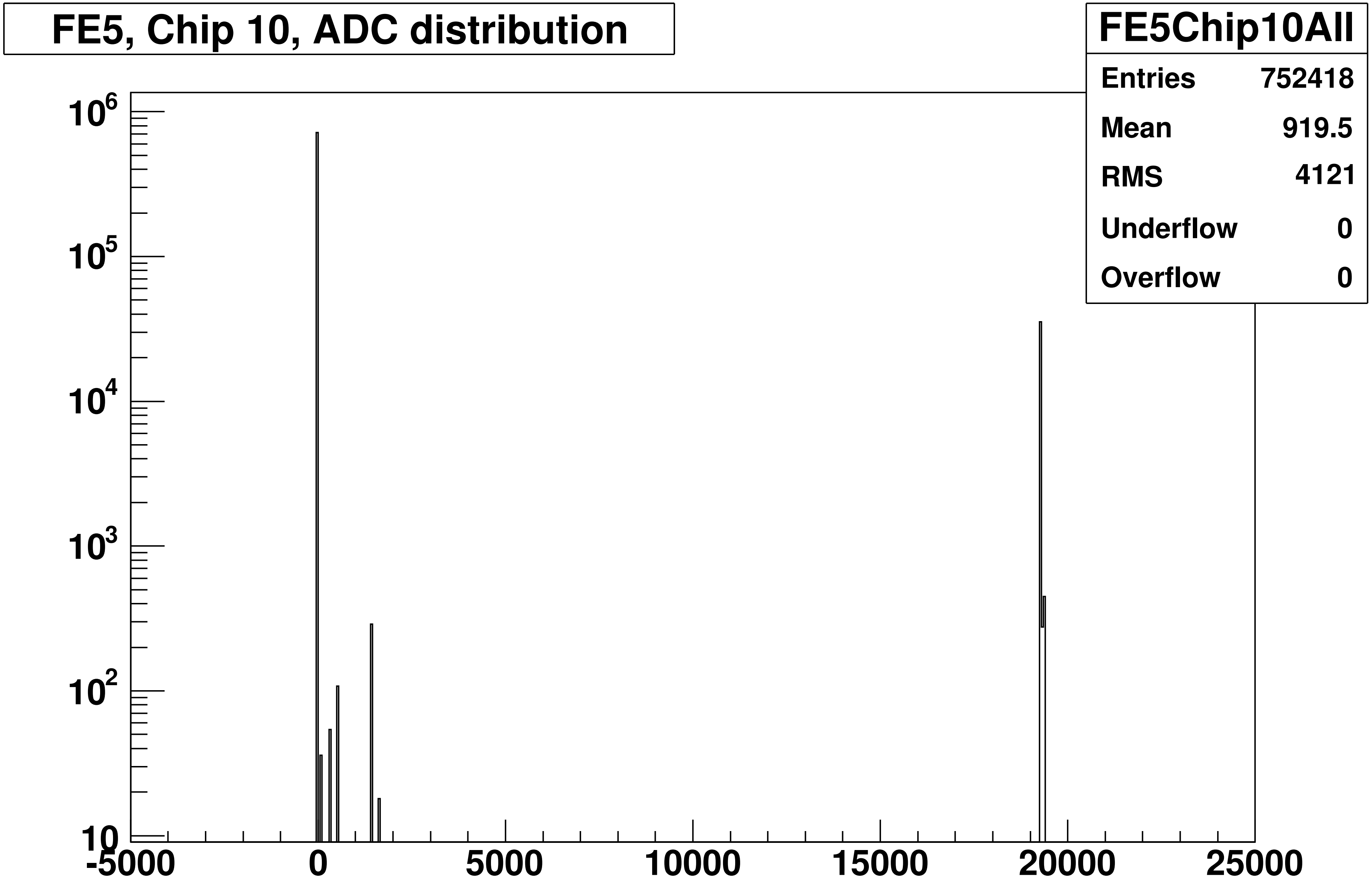
-5000
0
5000
10000
15000
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 752418
Mean 31.13
RMS 1.411
Underflow 576
Overflow 3.739e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200


FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 752418
Mean 1.921e+04
RMS 10.37
Underflow 7.154e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 752418
Mean 919.5
RMS 4121
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
10
-5000
0
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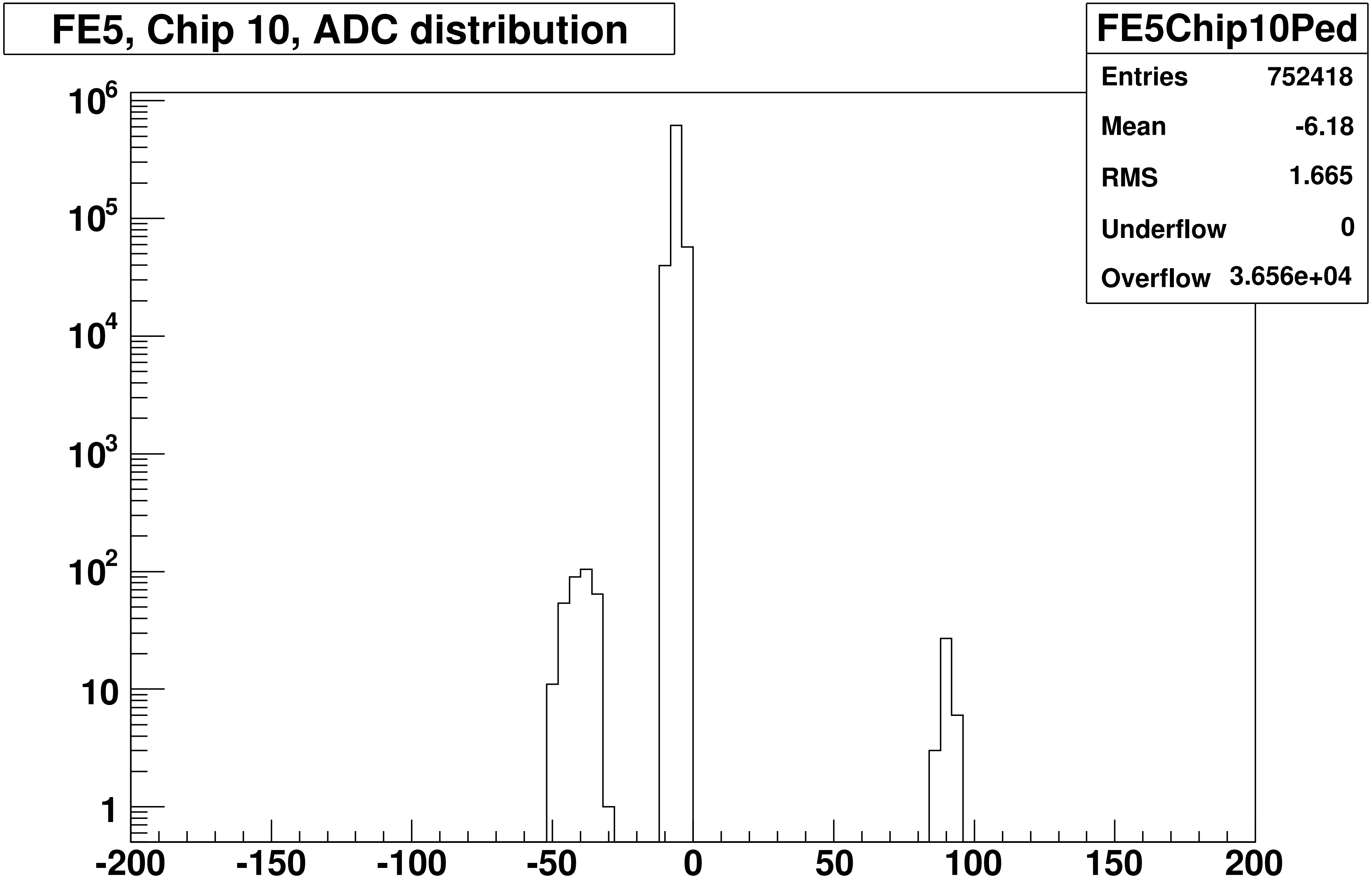
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 752418
Mean -6.18
RMS 1.665
Underflow 0
Overflow 3.656e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
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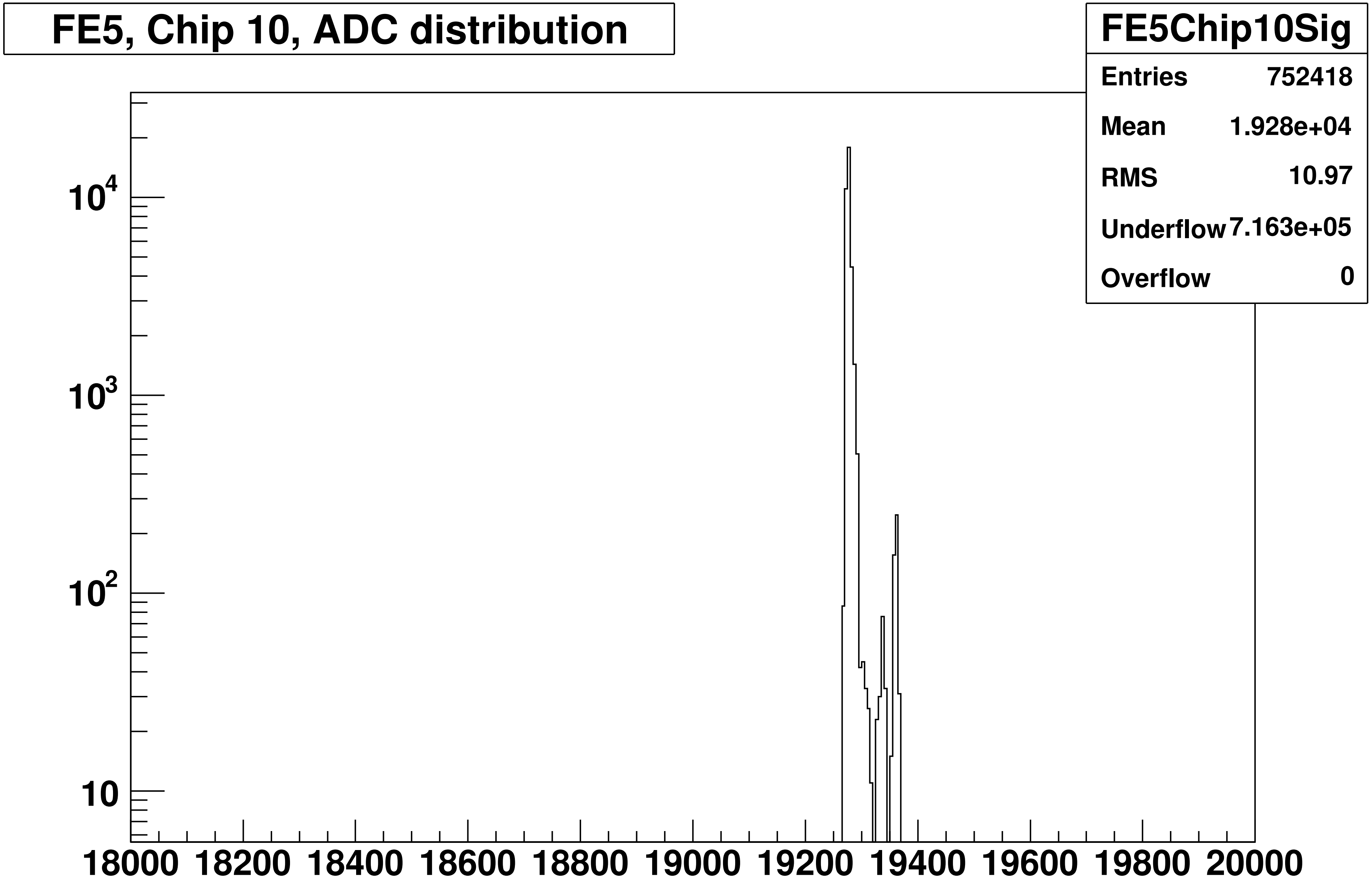
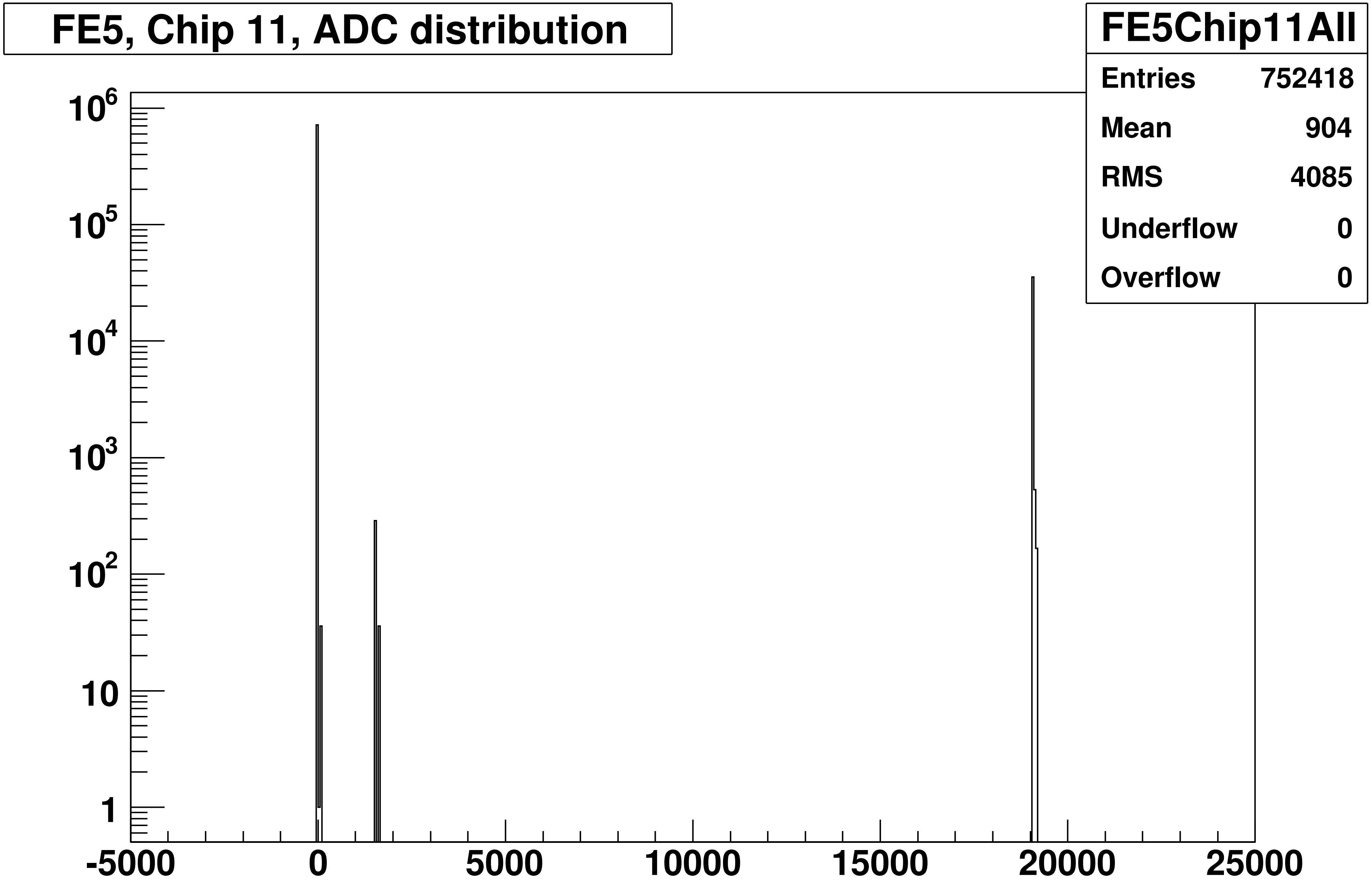
-100
-50
0
50
100
150
200



FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 752418
Mean 1.928e+04
RMS 10.97
Underflow 7.163e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 752418
Mean 904
RMS 4085
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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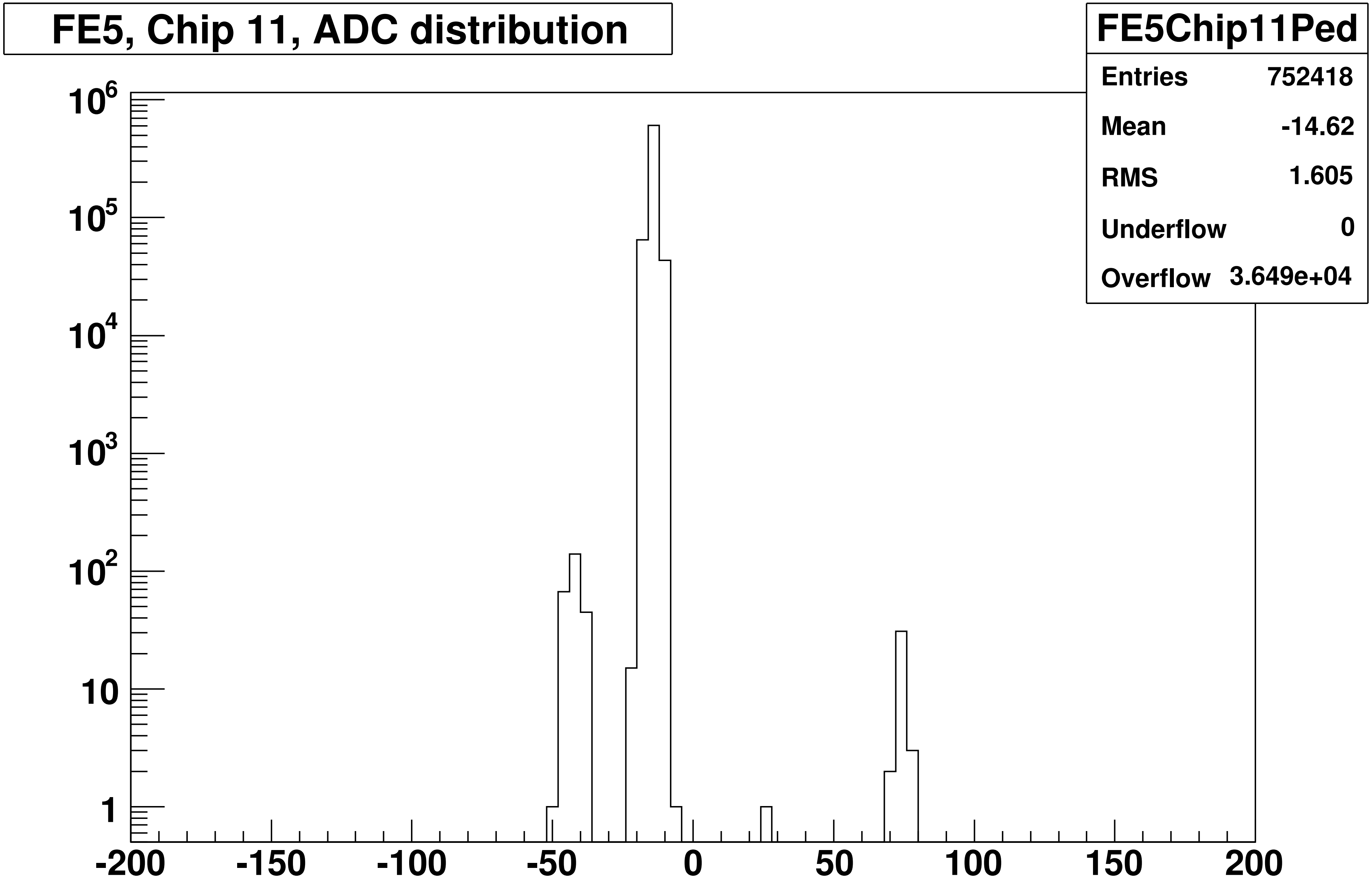
15000
20000
25000



FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 752418
Mean -14.62
RMS 1.605
Underflow 0
Overflow 3.649e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
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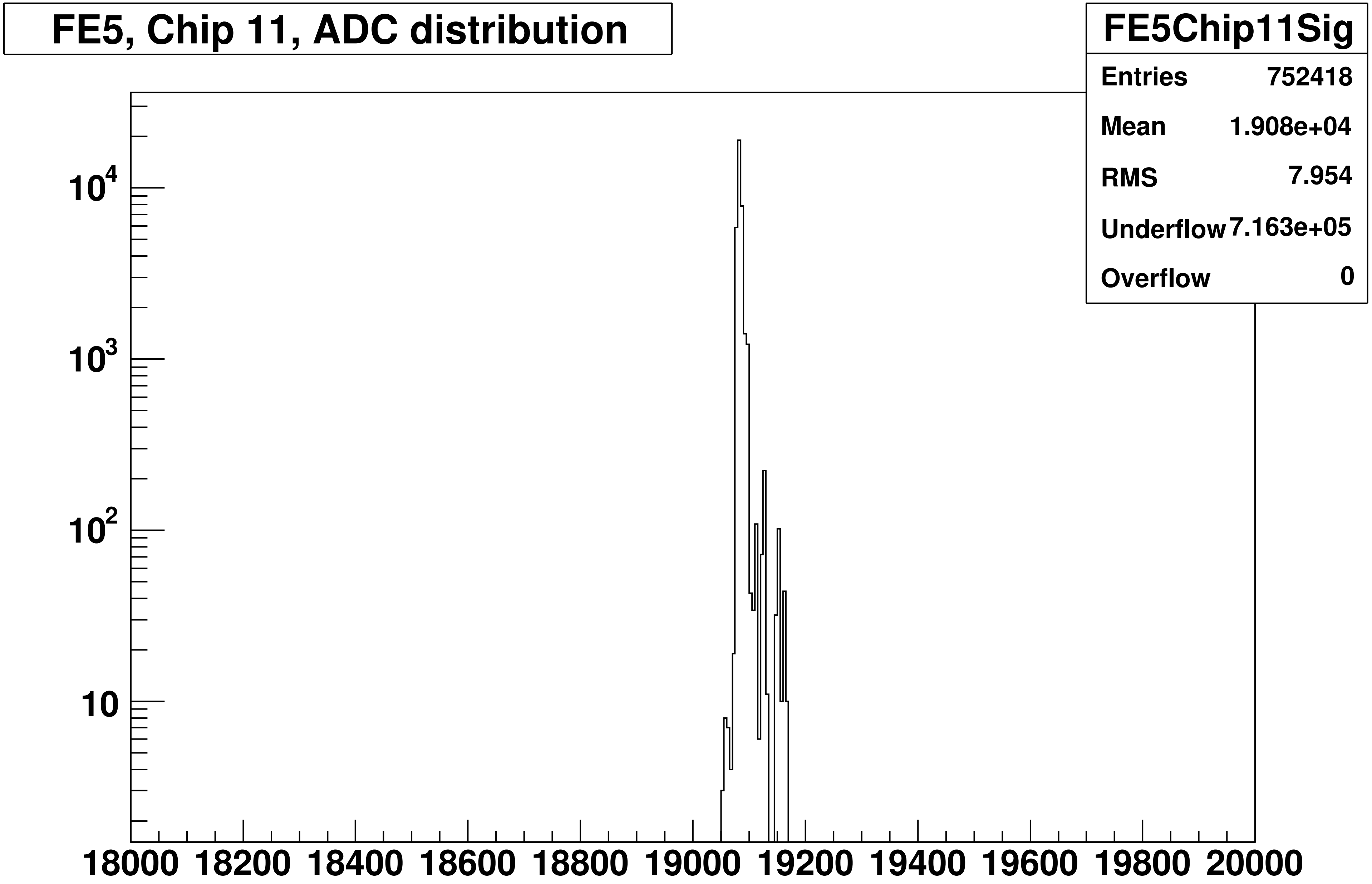
-100
-50
0
50
100
150
200


FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 752418
Mean 1.908e+04
RMS 7.954
Underflow 7.163e+05
Overflow 0
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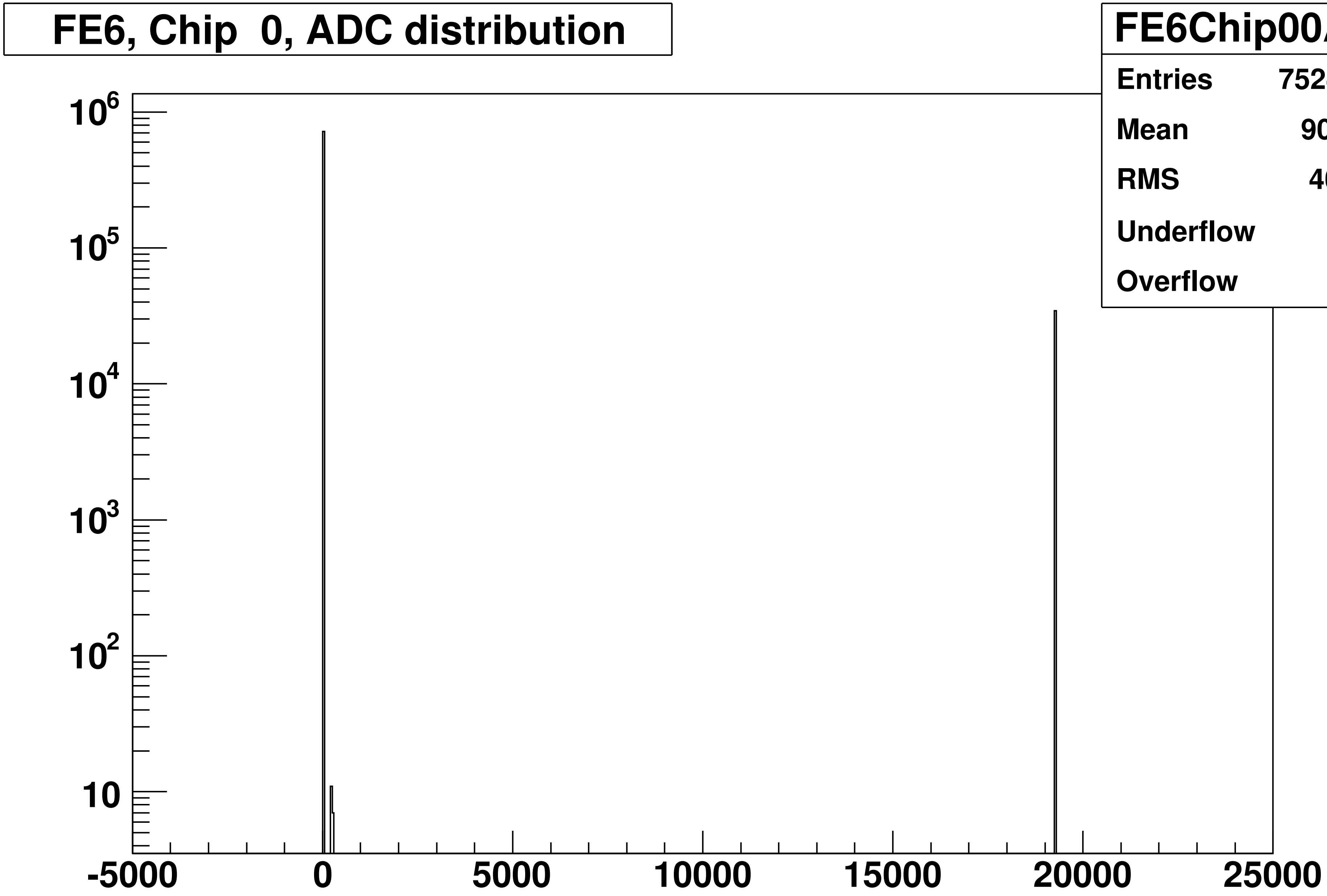
$10^2$
$10^3$
$10^4$
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 752418
Mean 902.2
RMS 4032
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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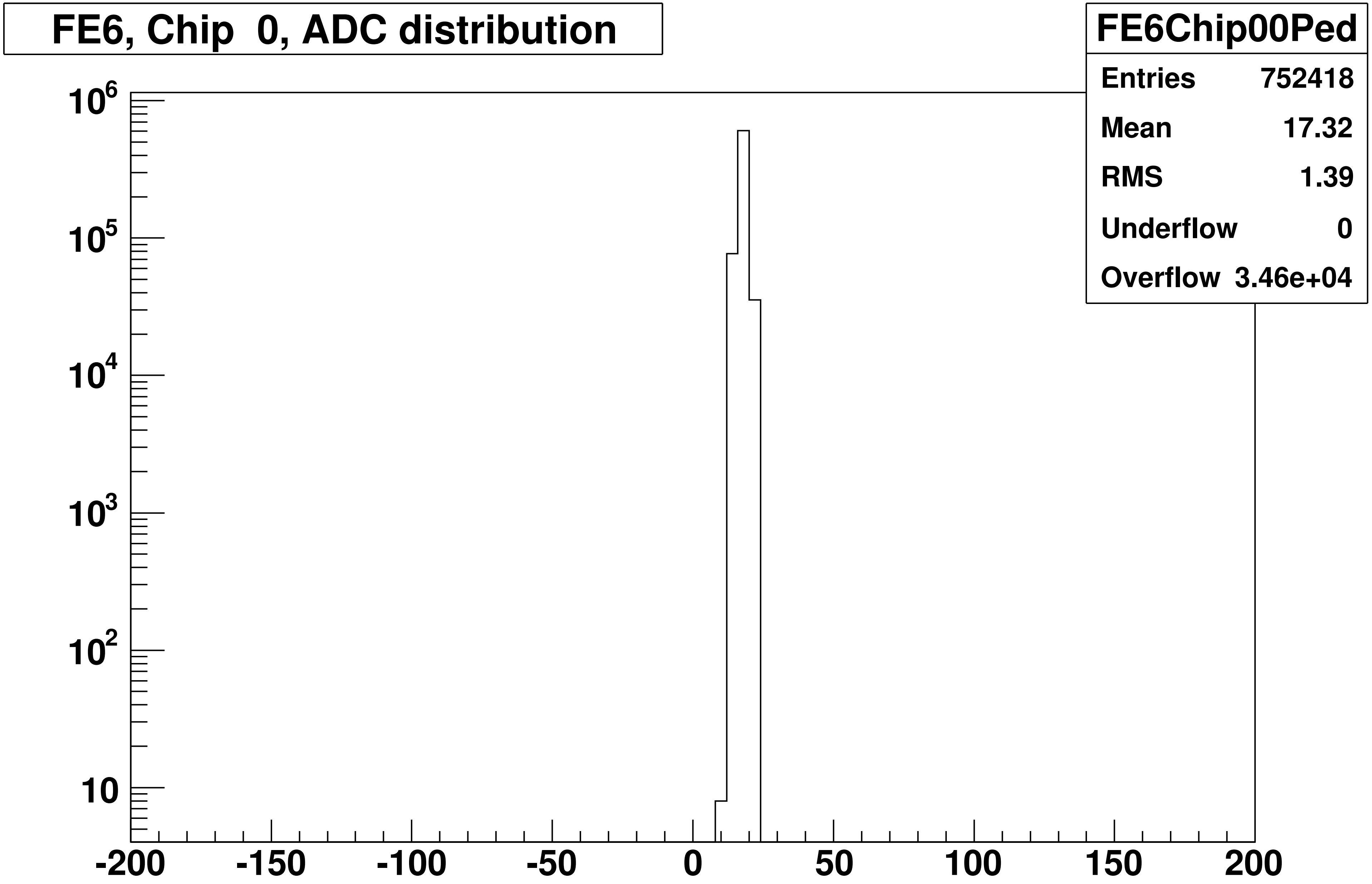
20000
25000


FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 752418
Mean 17.32
RMS 1.39
Underflow 0
Overflow 3.46e+04
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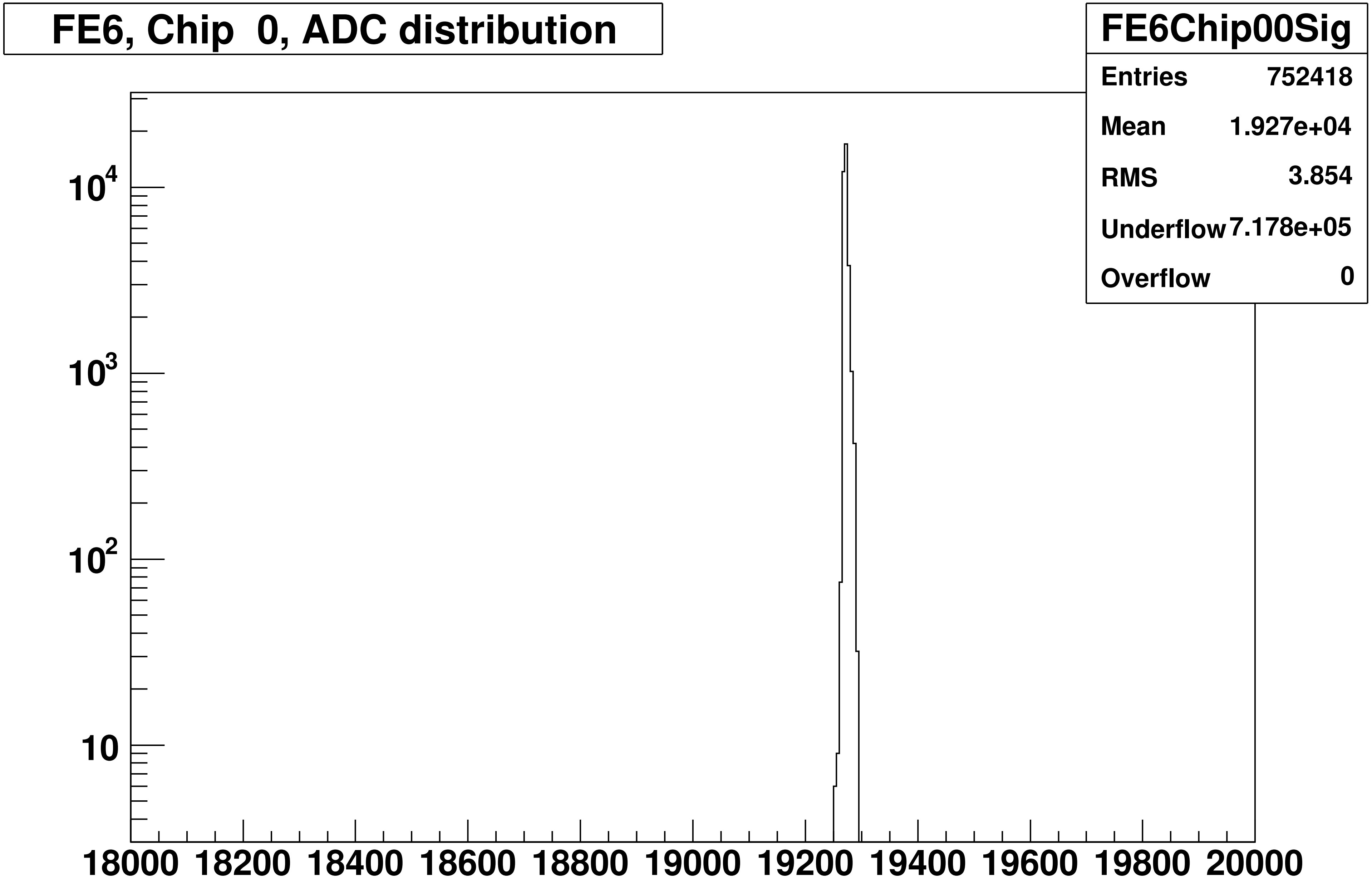
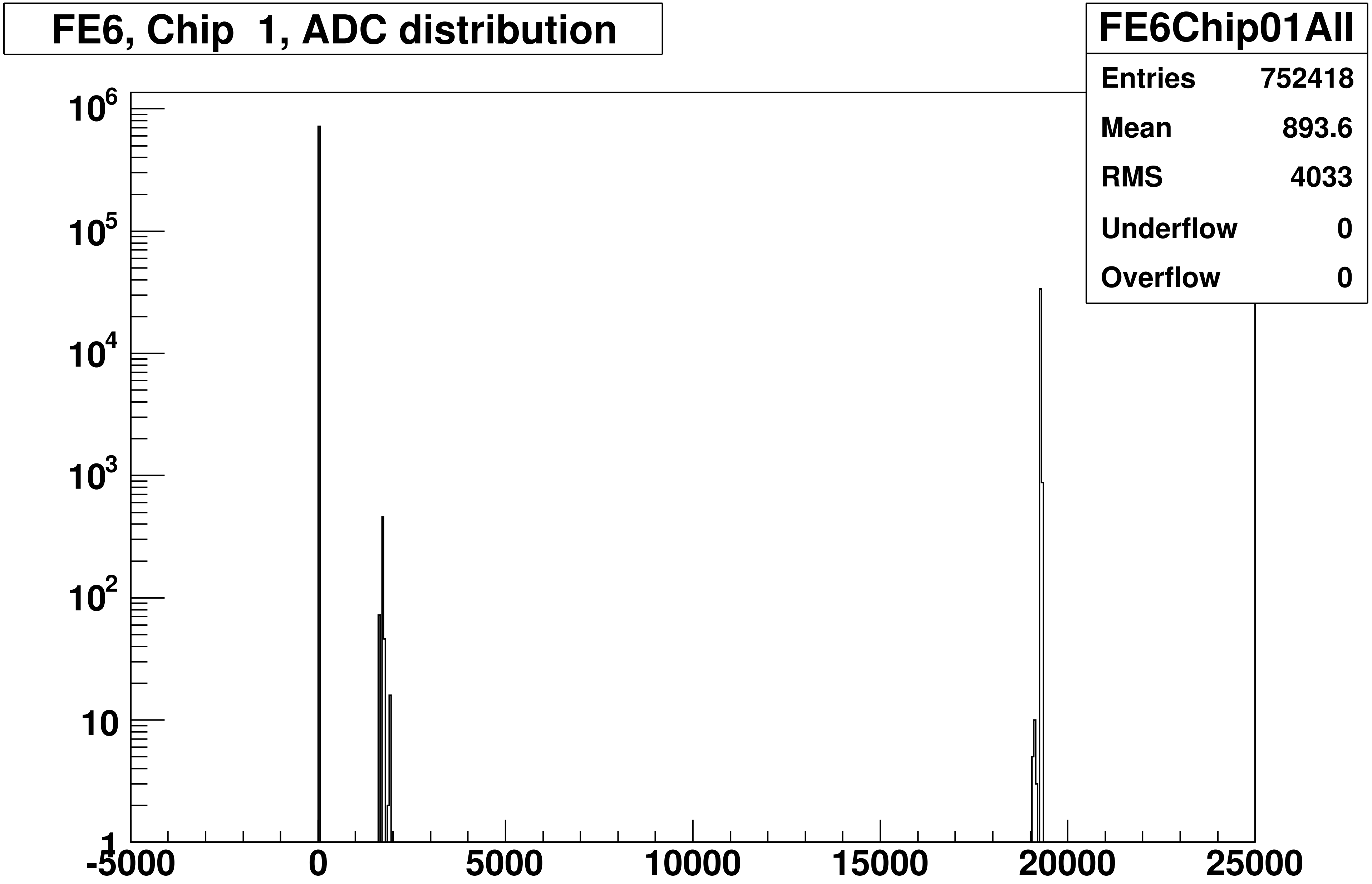
10^5
10^4
10^3
10^2
10
-200
-150
-100
-50
0
50
100
150
200


FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 752418
Mean 1.927e+04
RMS 3.854
Underflow 7.178e+05
Overflow 0
10
$10^2$
$10^3$
$10^4$
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 752418
Mean 893.6
RMS 4033
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
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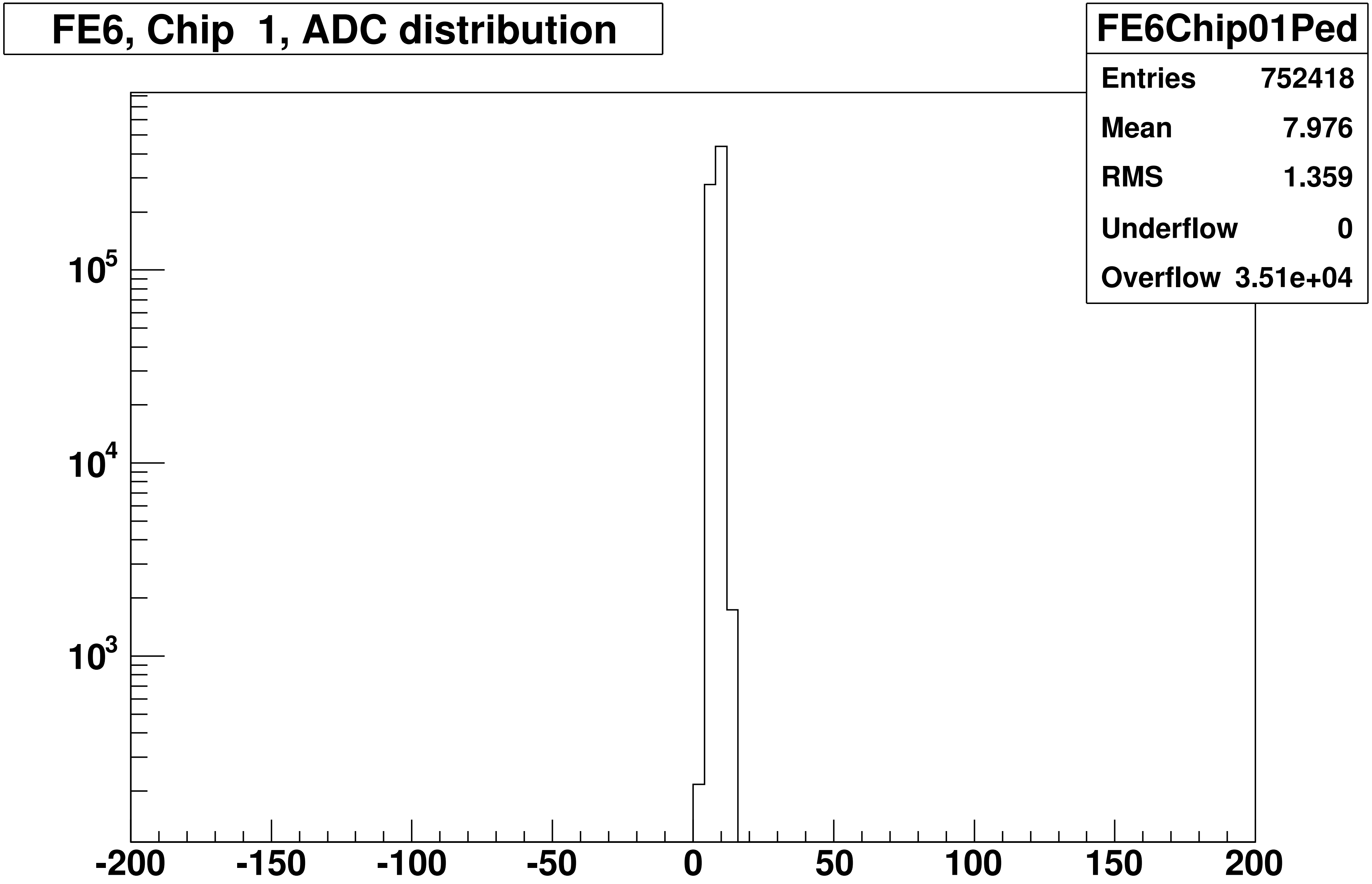
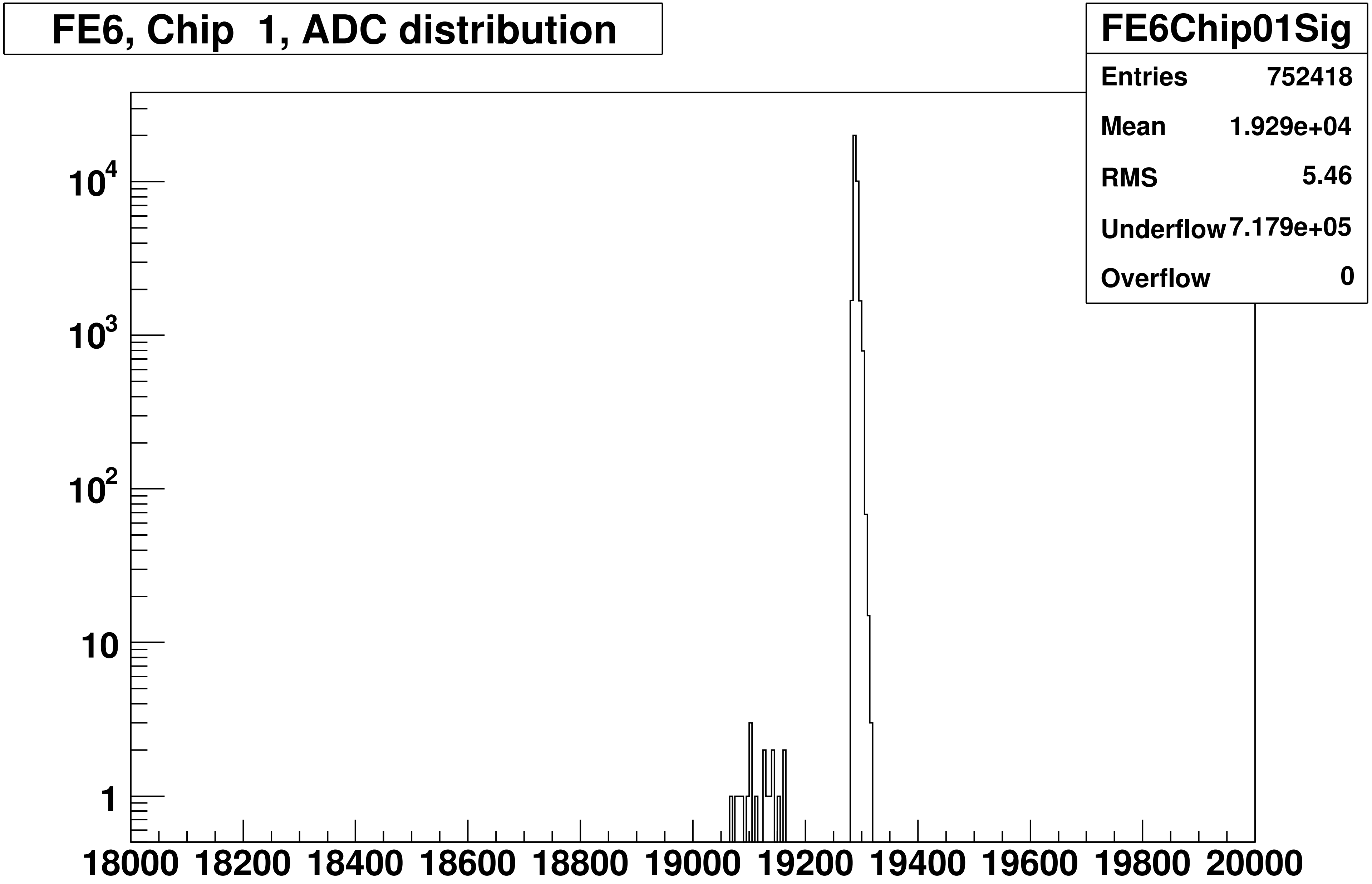
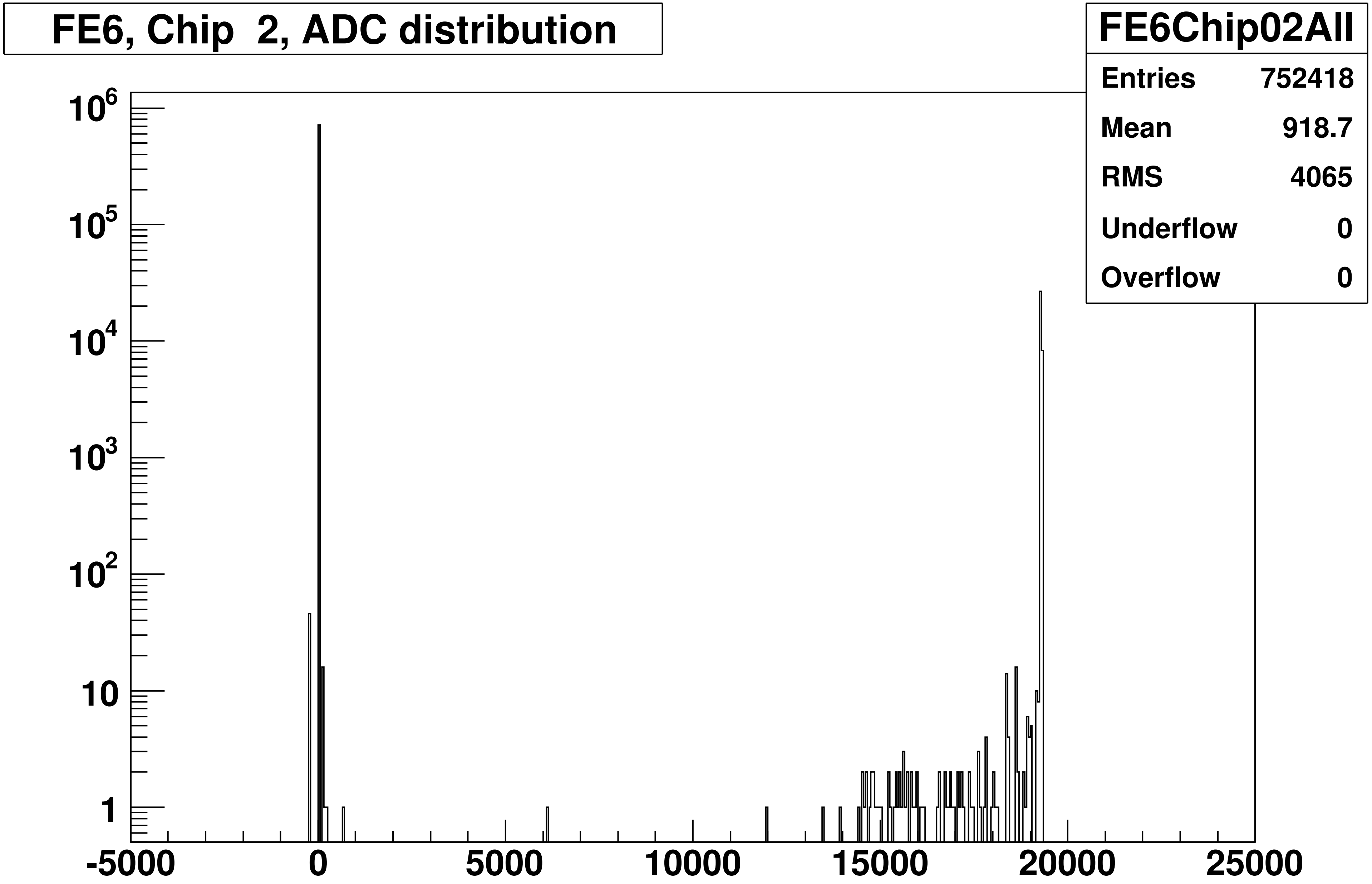
20000
25000


FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 752418
Mean 7.976
RMS 1.359
Underflow 0
Overflow 3.51e+04
10^5
10^4
10^3
-200
-150
-100
-50
0
50
100
150
200



FE6, Chip  1, ADC distribution
FE6Chip01Sig
Entries 752418
Mean 1.929e+04
RMS 5.46
Underflow 7.179e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 752418
Mean 918.7
RMS 4065
Underflow 0
Overflow 0
$10^6$
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$10^3$
$10^2$
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0
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10000
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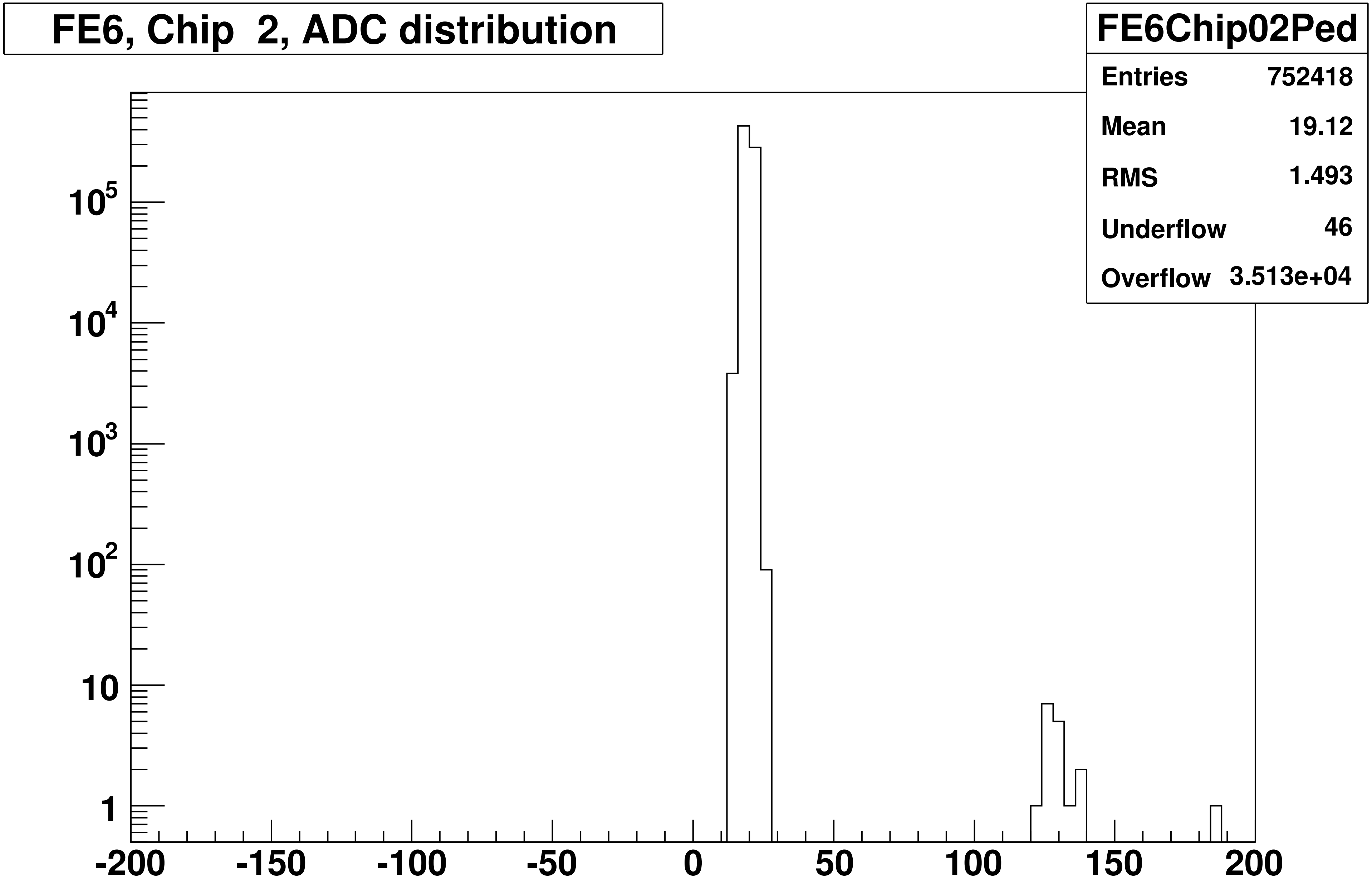
20000
25000


FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 752418
Mean 19.12
RMS 1.493
Underflow 46
Overflow 3.513e+04
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10^3
10^2
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-100
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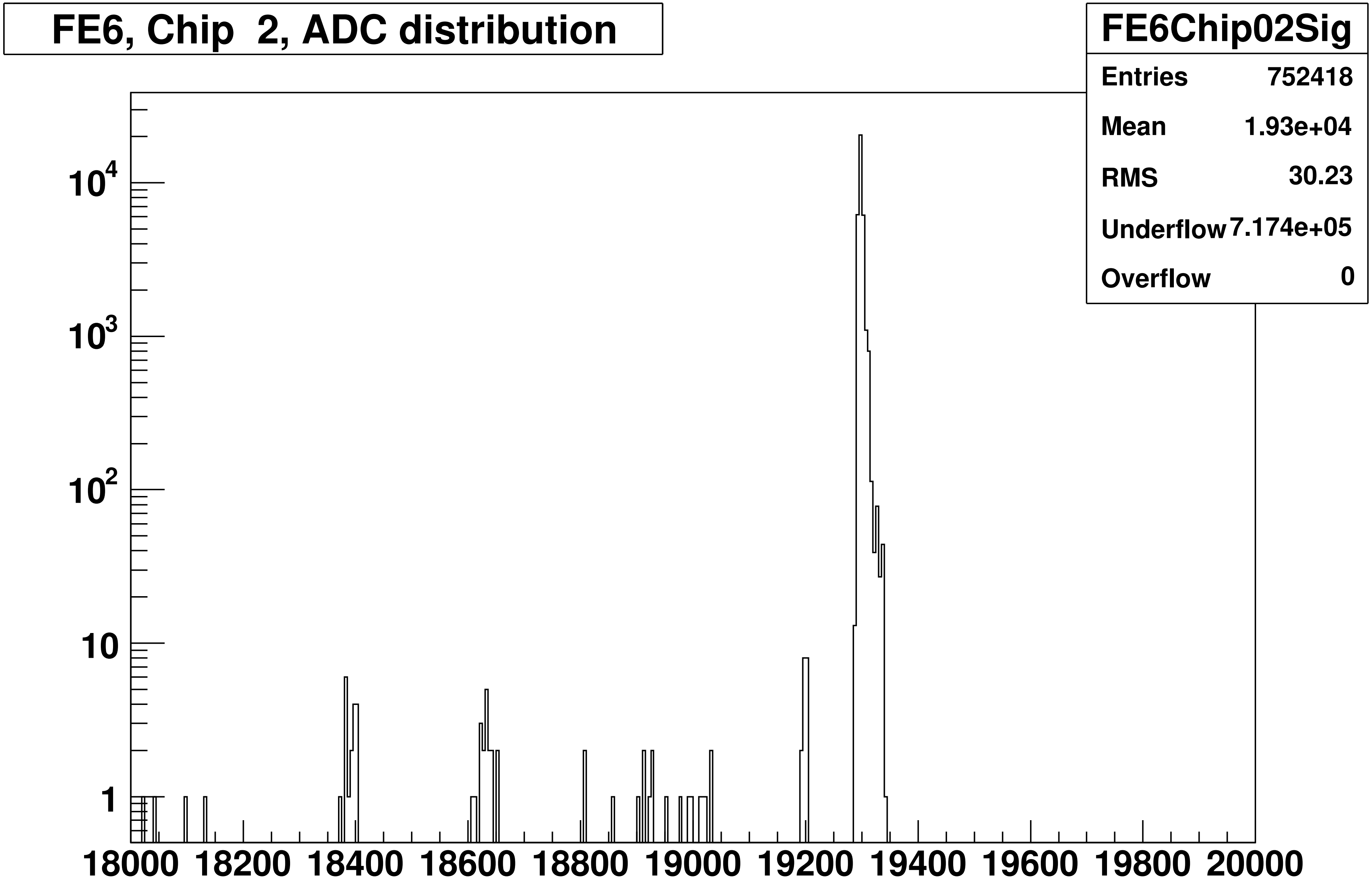
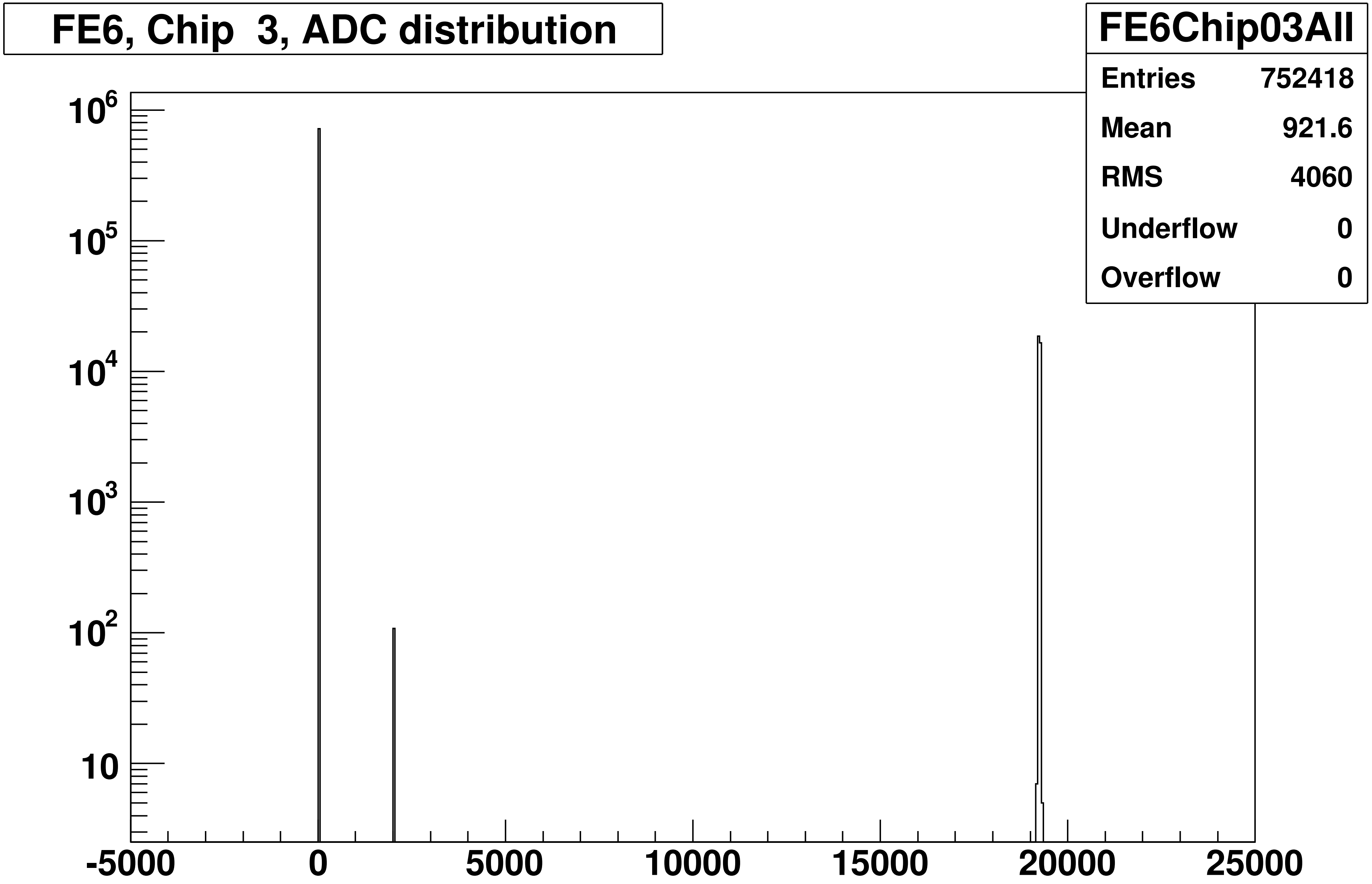
50
100
150
200



FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 752418
Mean 1.93e+04
RMS 30.23
Underflow 7.174e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 752418
Mean 921.6
RMS 4060
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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-5000
0
5000
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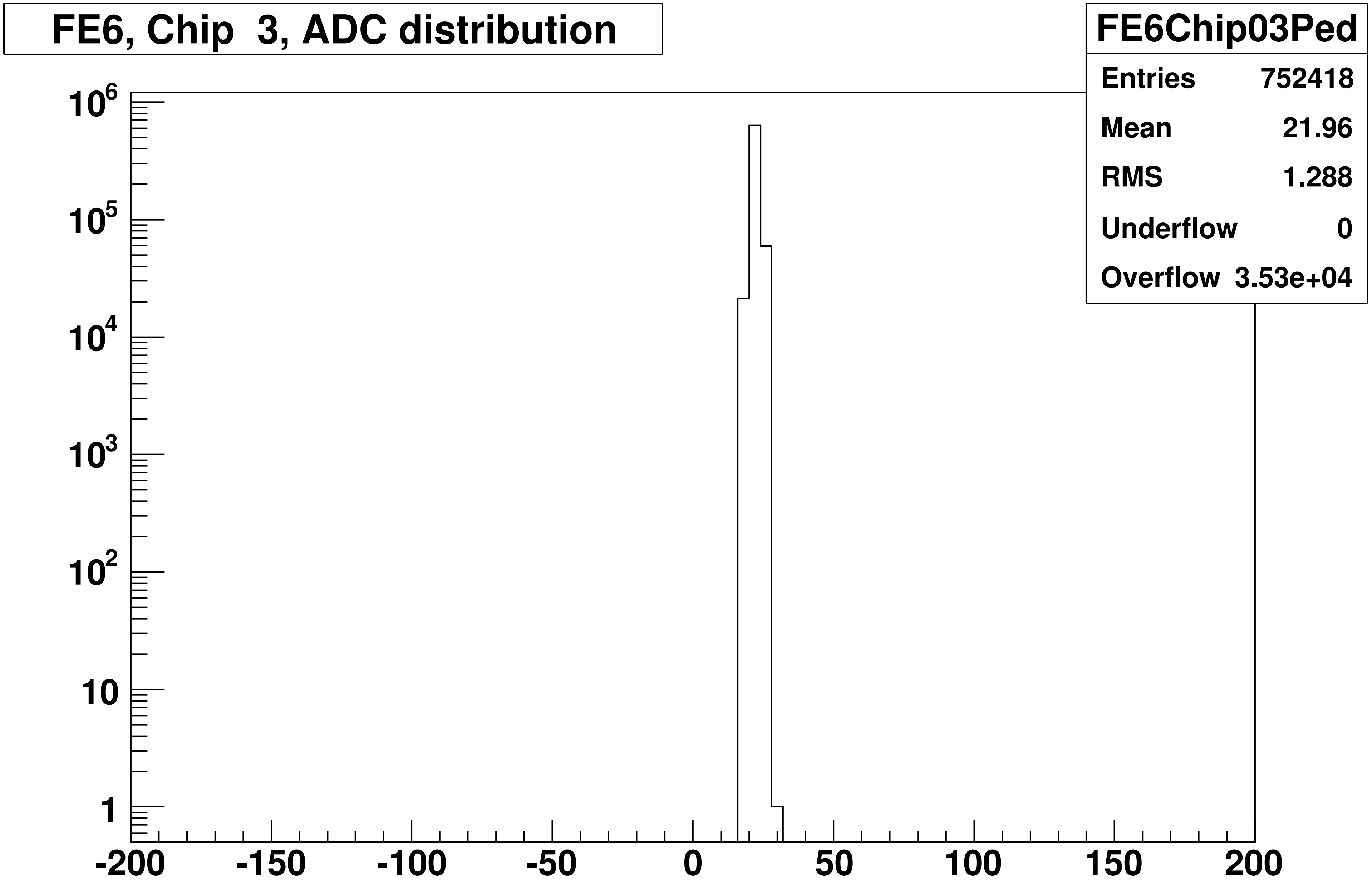
20000
25000



FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 752418
Mean 21.96
RMS 1.288
Underflow 0
Overflow 3.53e+04
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$10^5$
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$10^2$
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-100
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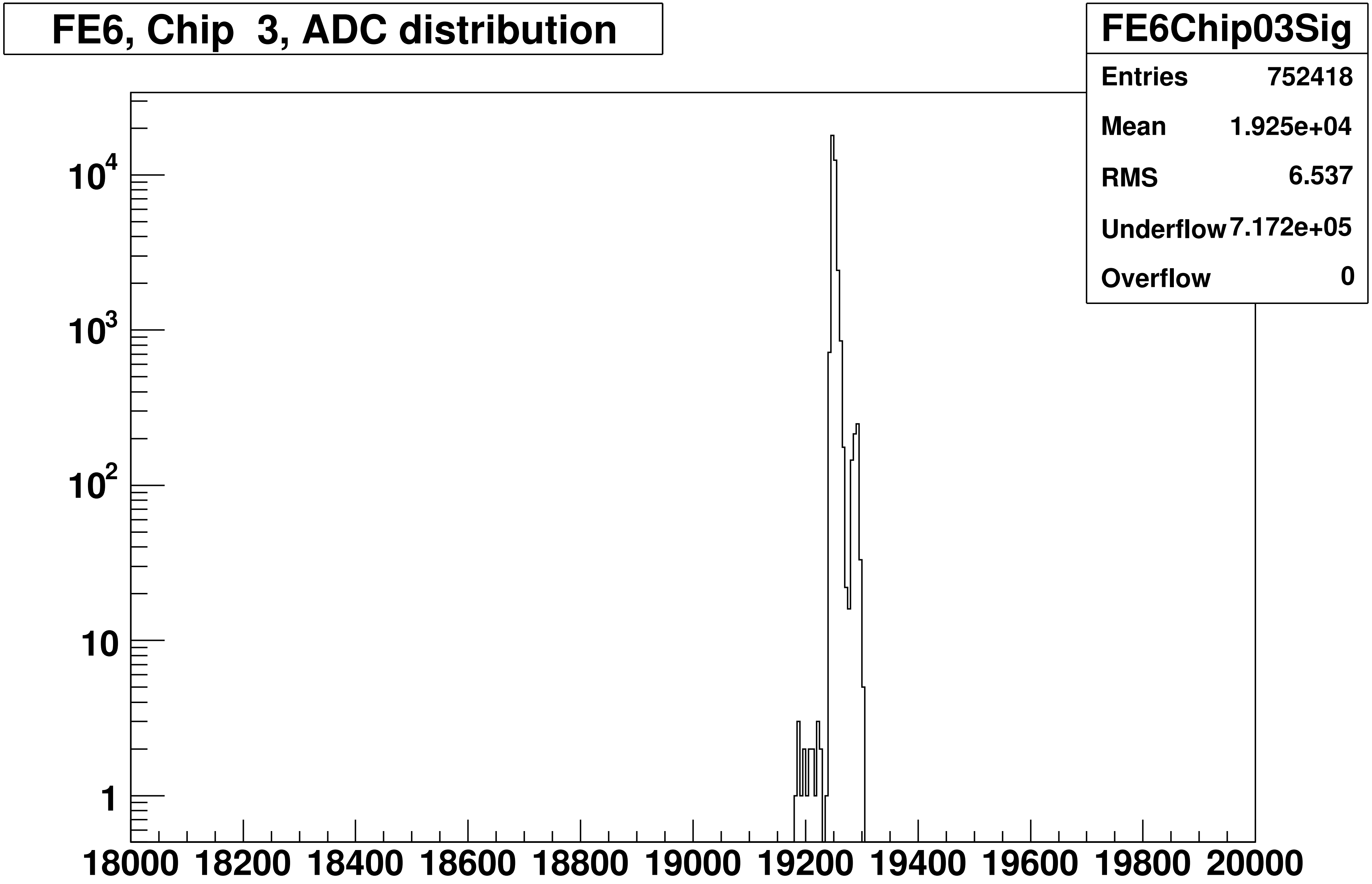
150
200



FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 752418
Mean 1.925e+04
RMS 6.537
Underflow 7.172e+05
Overflow 0
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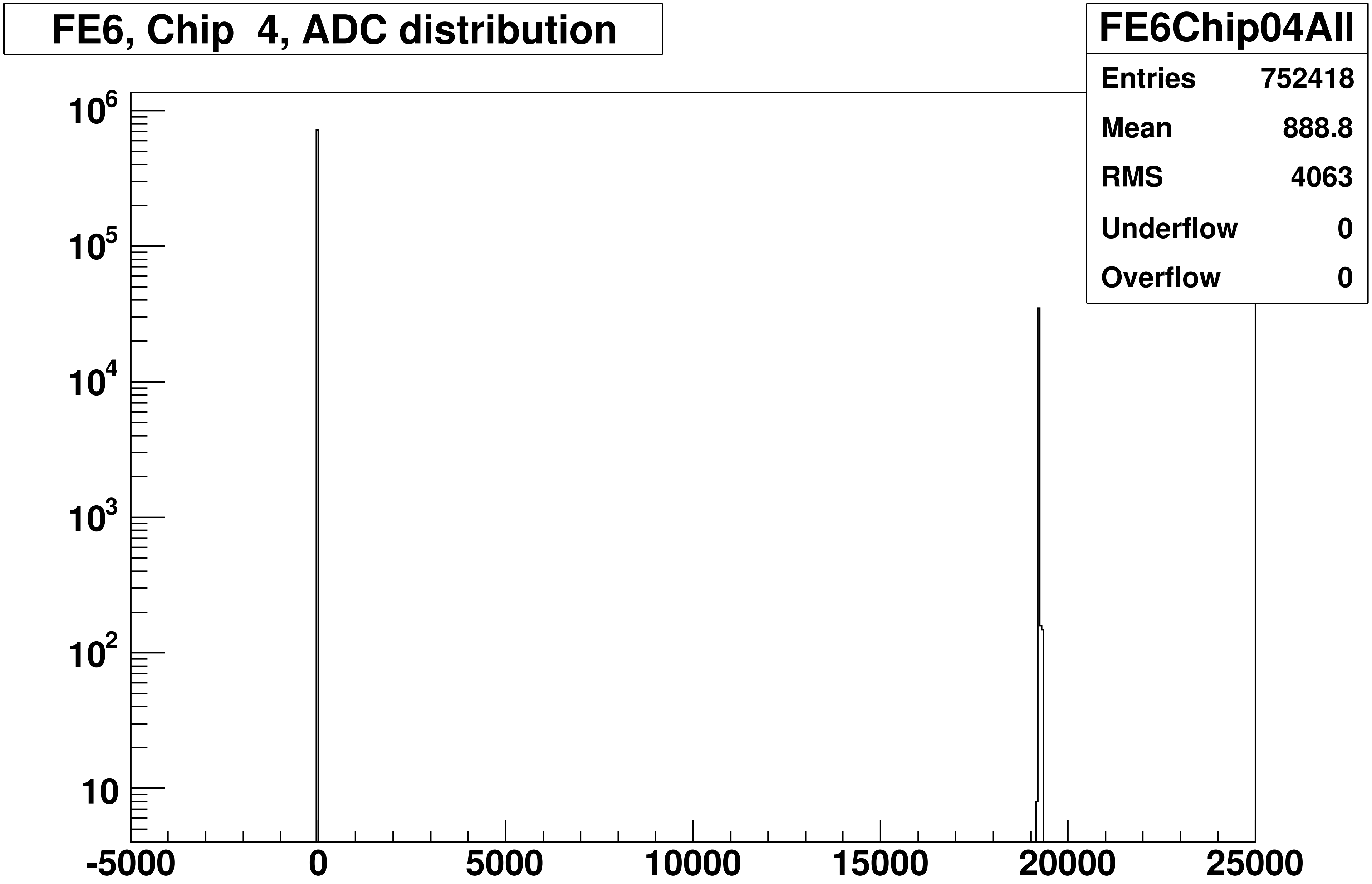
$10^3$
$10^2$
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 752418
Mean 888.8
RMS 4063
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
10
-5000
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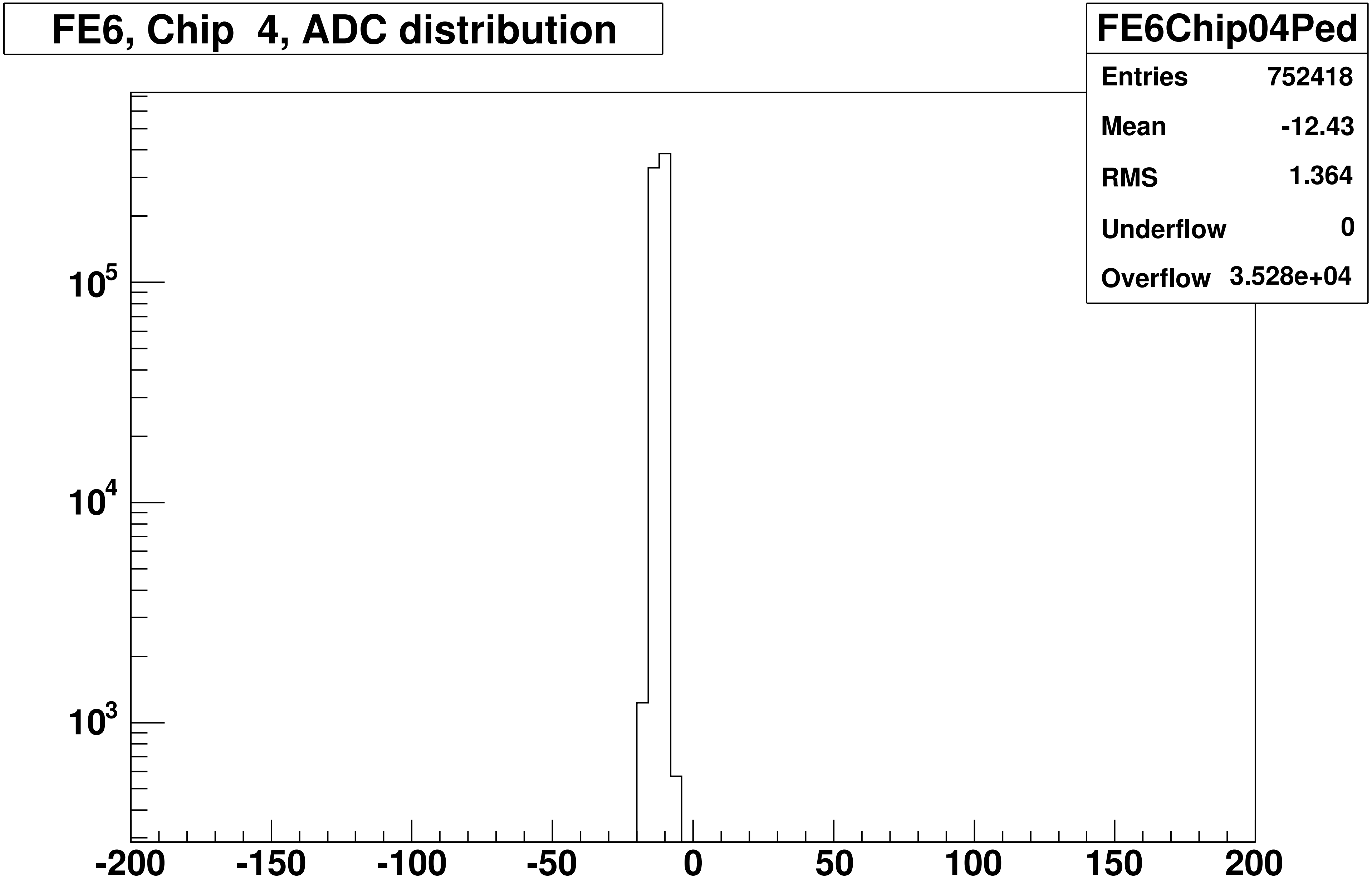
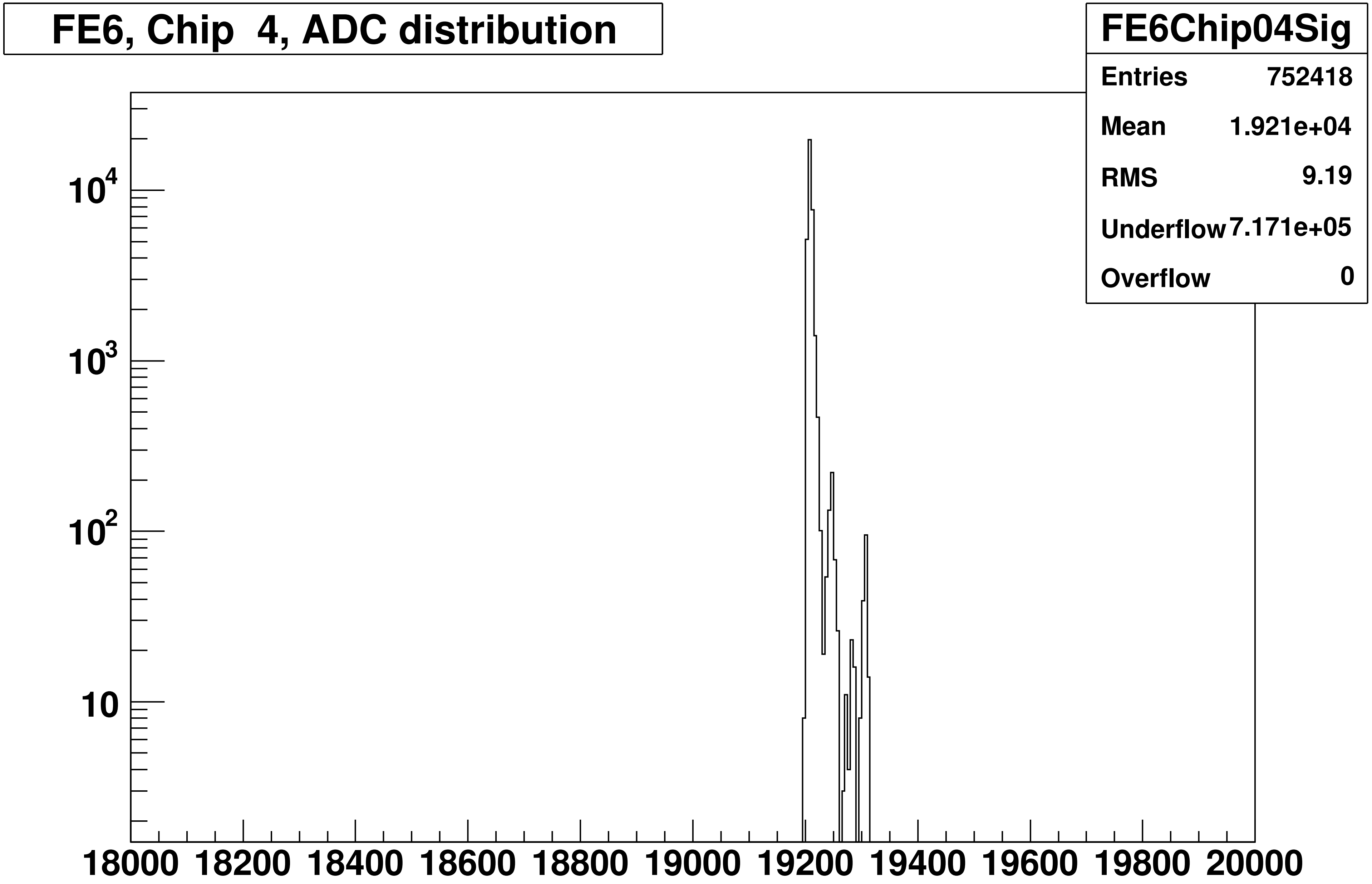
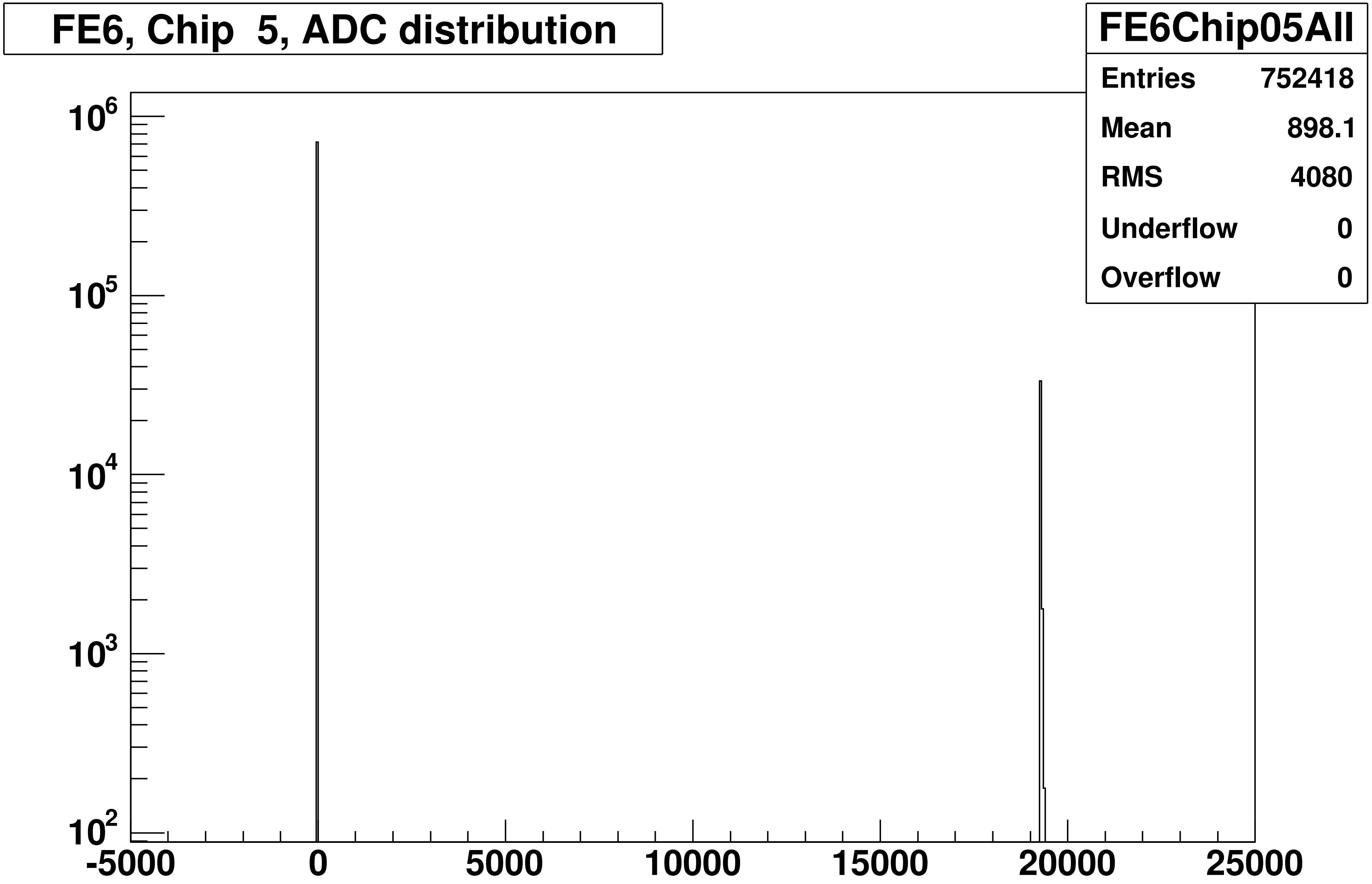
15000
20000
25000


FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 752418
Mean -12.43
RMS 1.364
Underflow 0
Overflow 3.528e+04
10^5
10^4
10^3
-200 -150 -100 -50 0 50 100 150 200


FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 752418
Mean 1.921e+04
RMS 9.19
Underflow 7.171e+05
Overflow 0
10^4
10^3
10^2
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 752418
Mean 898.1
RMS 4080
Underflow 0
Overflow 0
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$10^4$
$10^3$
$10^2$
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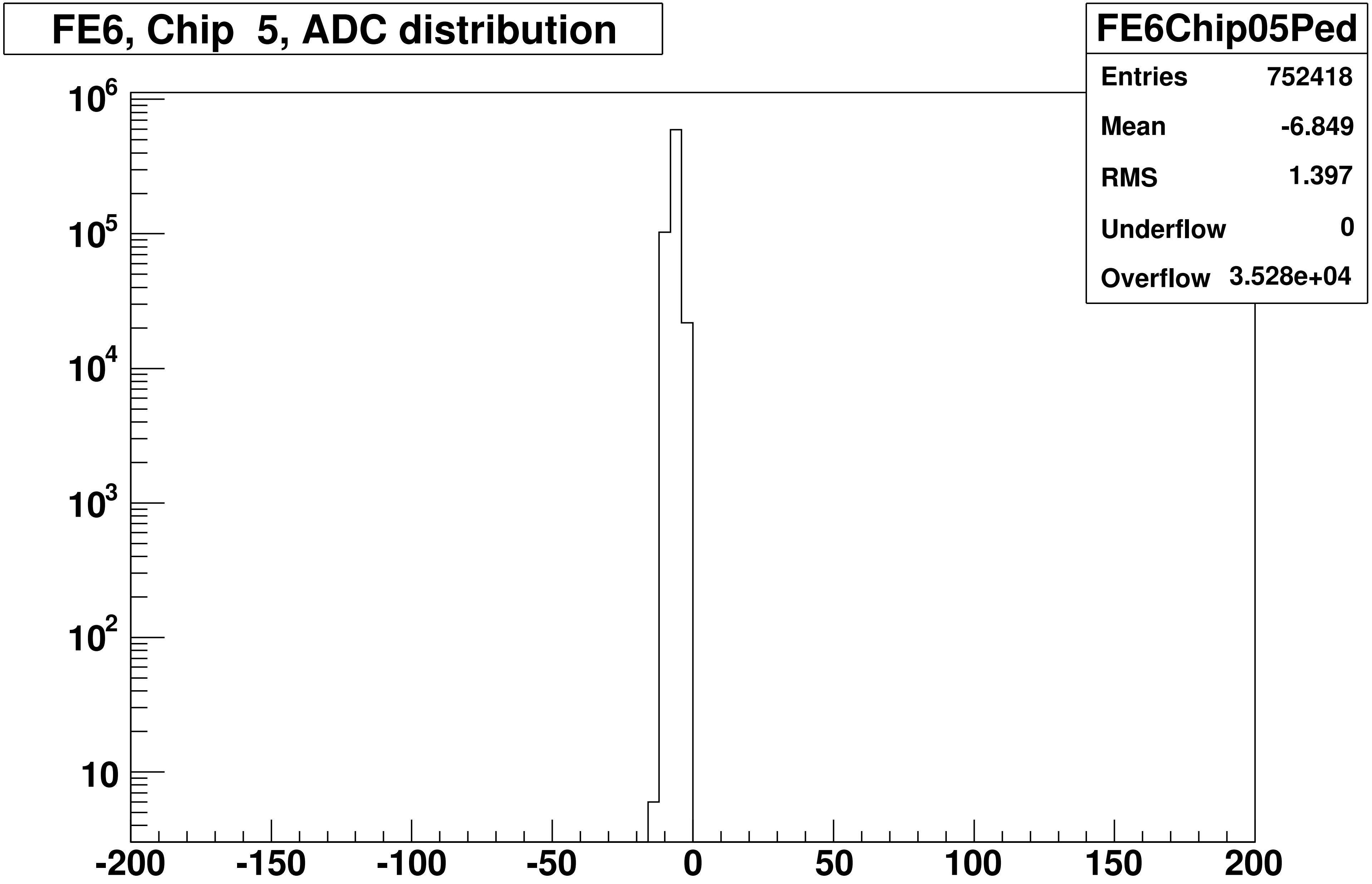
25000


FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 752418
Mean -6.849
RMS 1.397
Underflow 0
Overflow 3.528e+04
$10^6$
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$10^2$
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50
100
150
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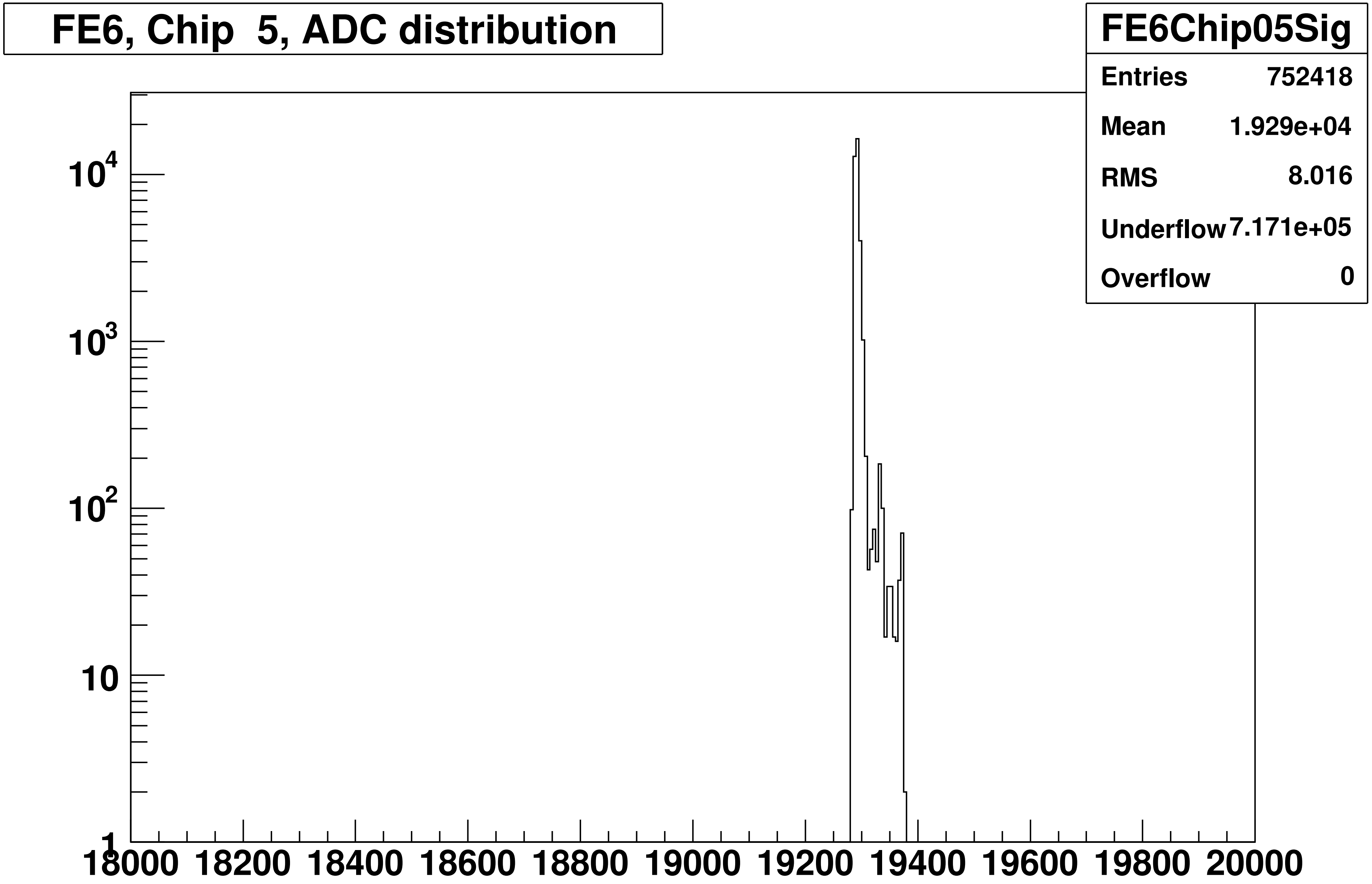

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 752418
Mean 1.929e+04
RMS 8.016
Underflow 7.171e+05
Overflow 0
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$10^2$
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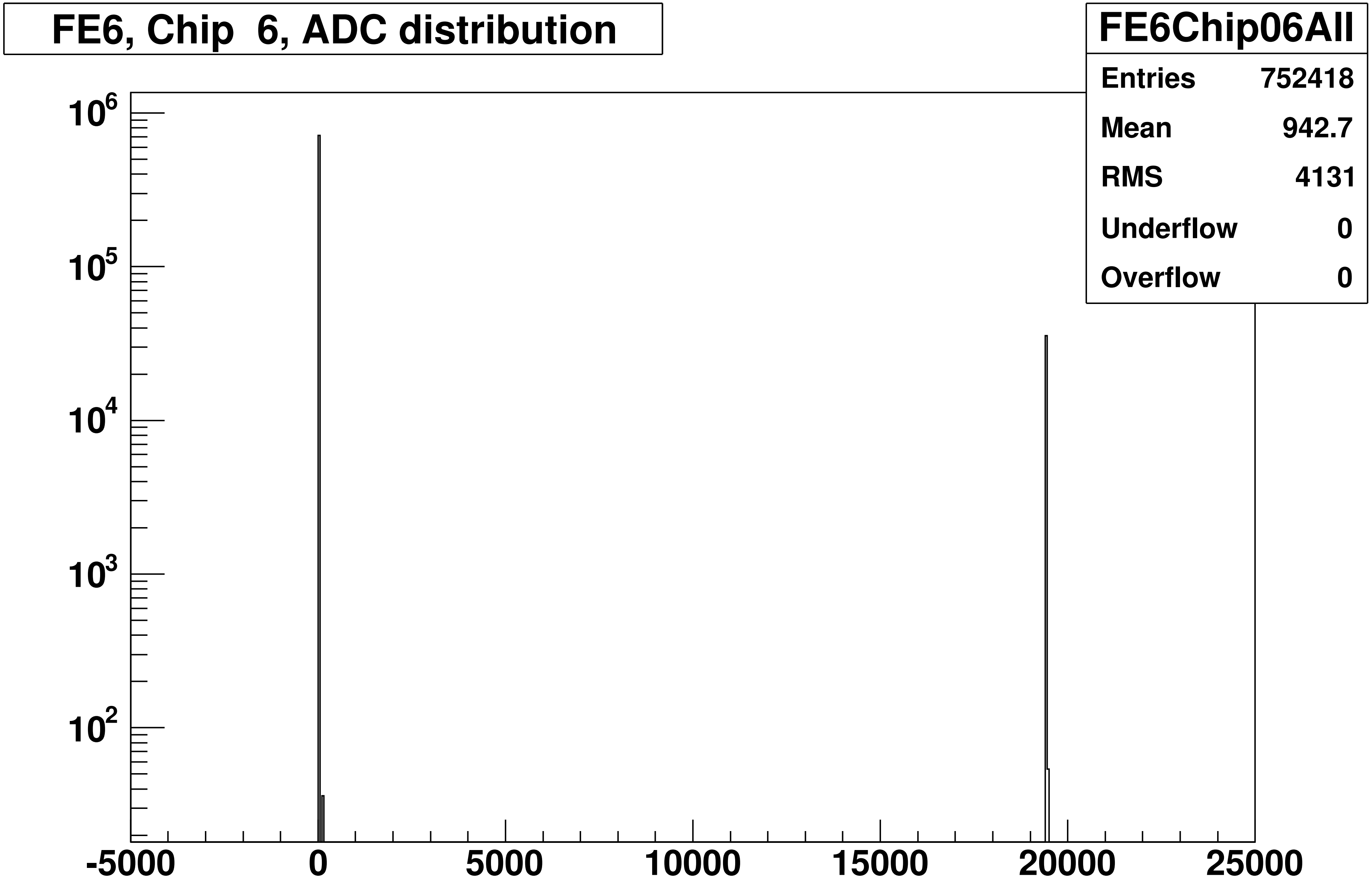
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 752418
Mean 942.7
RMS 4131
Underflow 0
Overflow 0
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$10^4$
$10^3$
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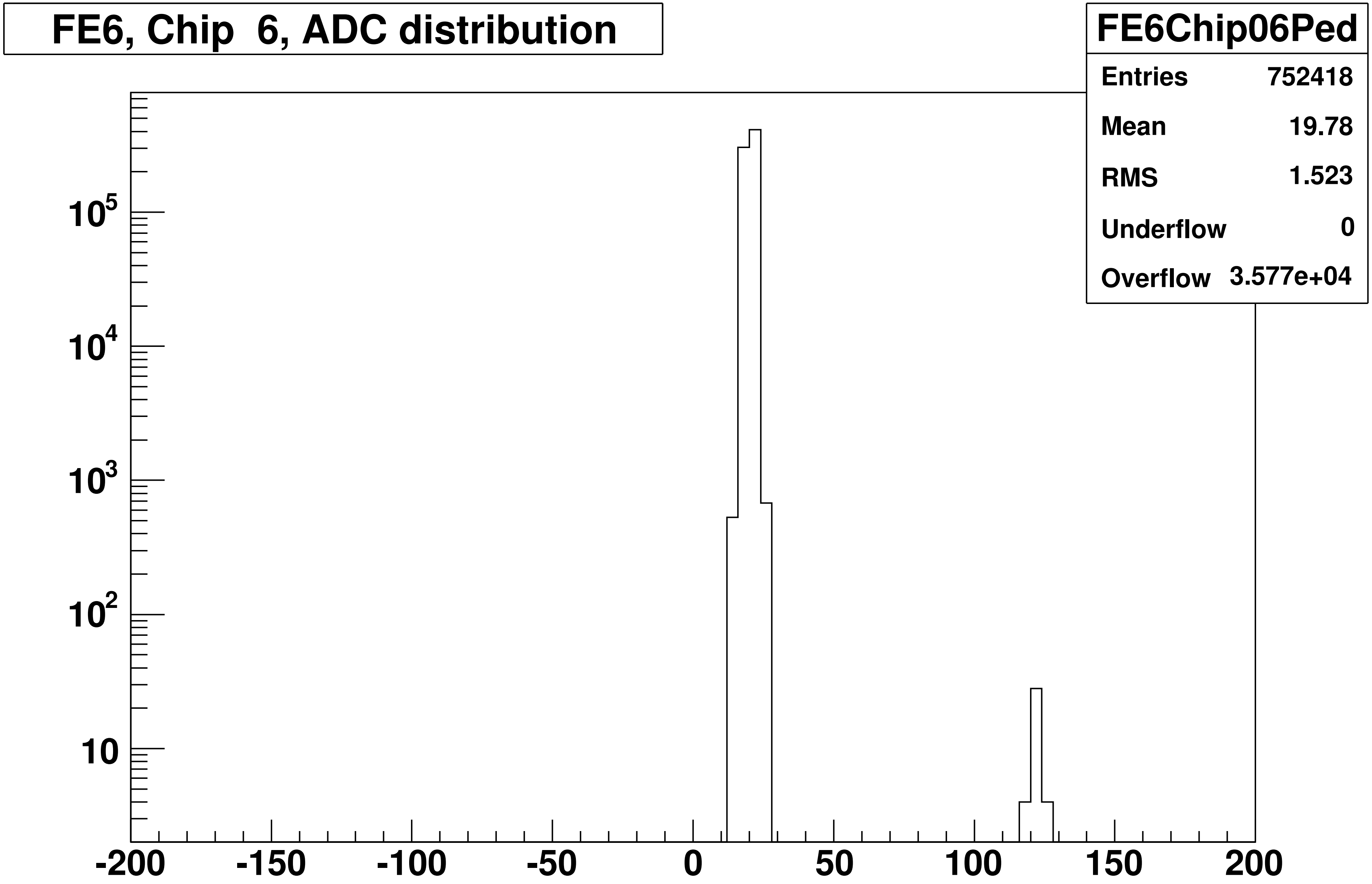
20000
25000


FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 752418
Mean 19.78
RMS 1.523
Underflow 0
Overflow 3.577e+04
10
$10^2$
$10^3$
$10^4$
$10^5$
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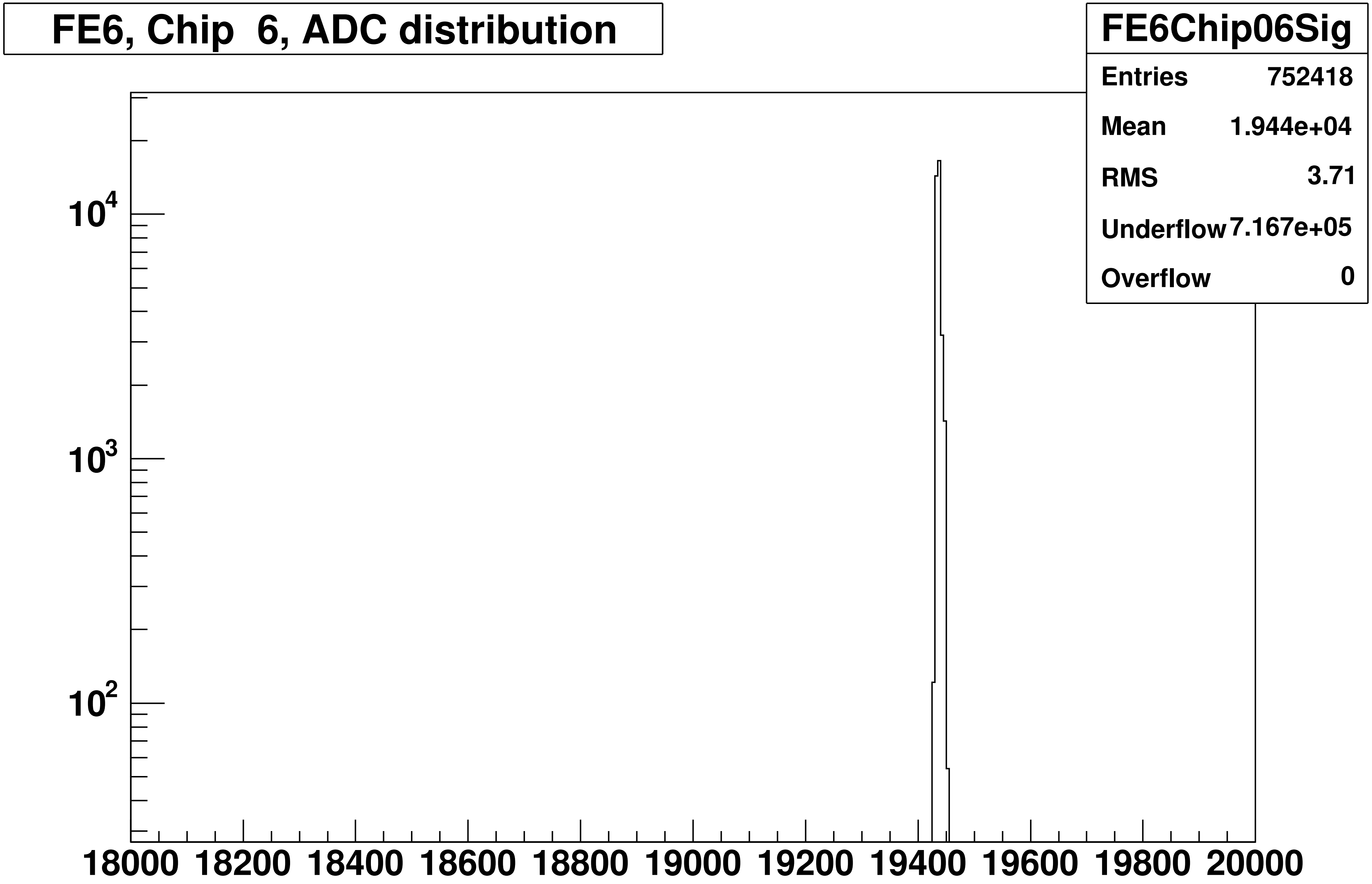
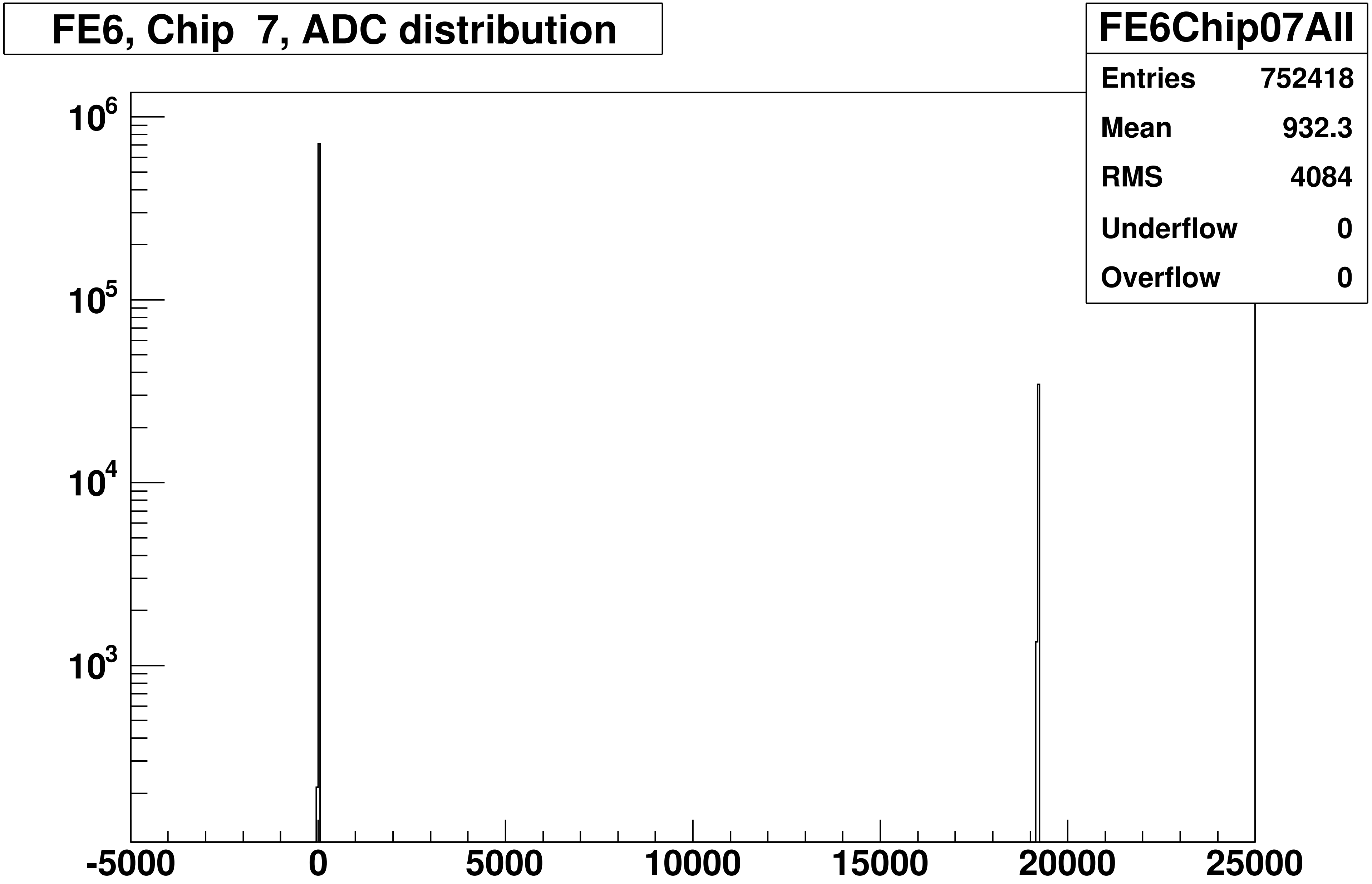
50
100
150
200



FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 752418
Mean 1.944e+04
RMS 3.71
Underflow 7.167e+05
Overflow 0
$10^4$
$10^3$
$10^2$
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000



FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 752418
Mean 932.3
RMS 4084
Underflow 0
Overflow 0
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$10^4$
$10^3$
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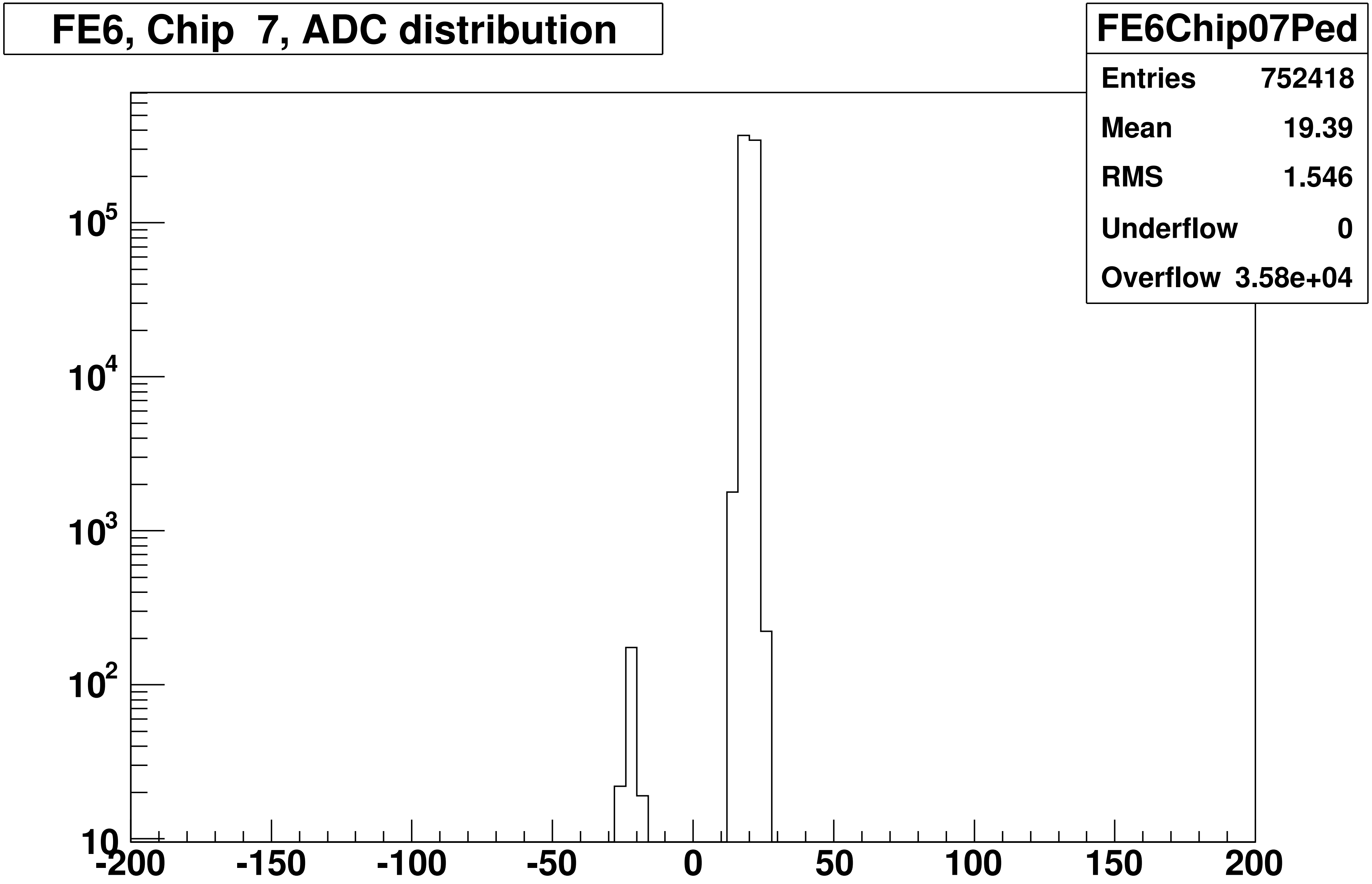

FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 752418
Mean 19.39
RMS 1.546
Underflow 0
Overflow 3.58e+04
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$10^2$
$10^3$
$10^4$
$10^5$
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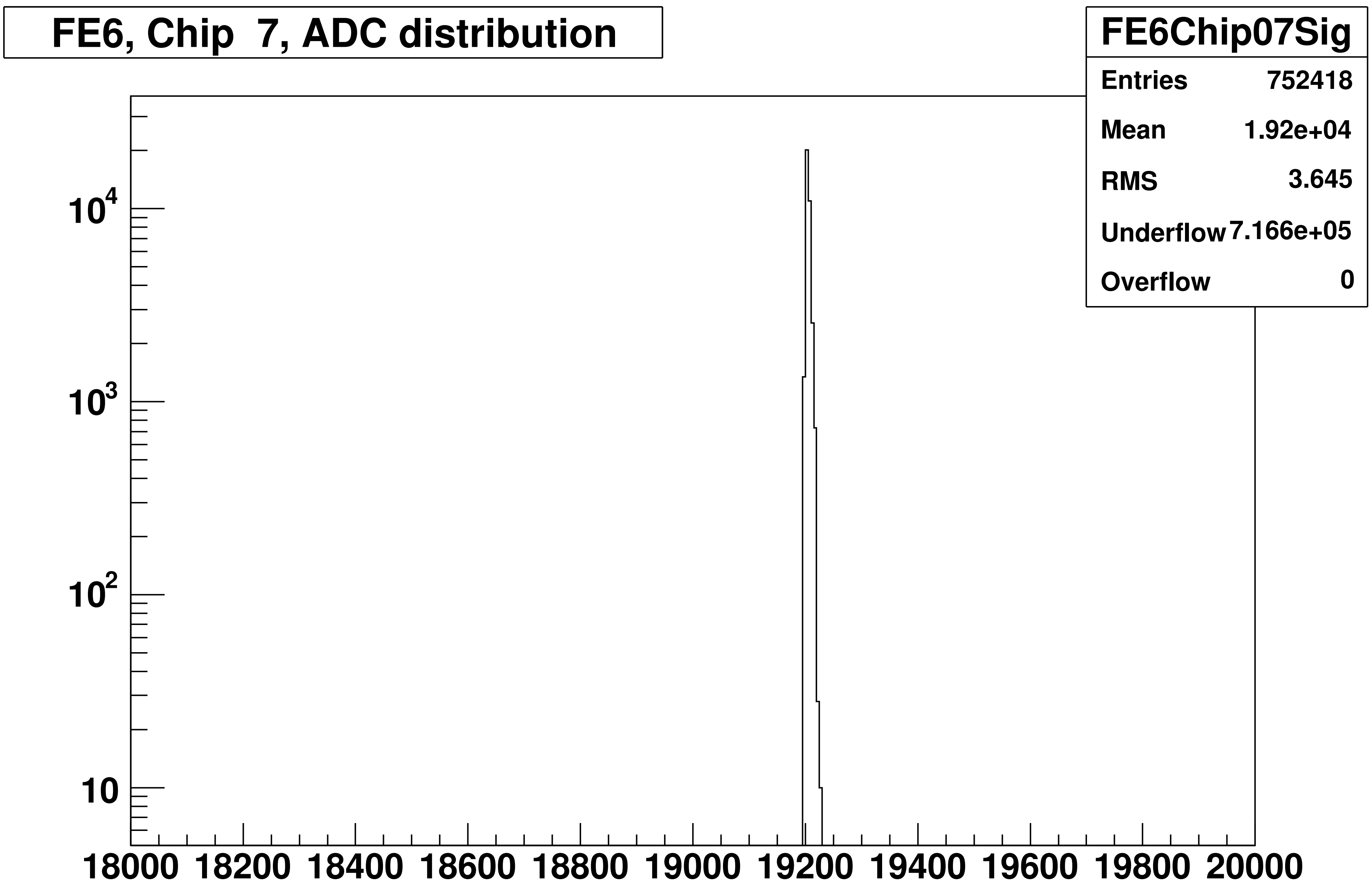
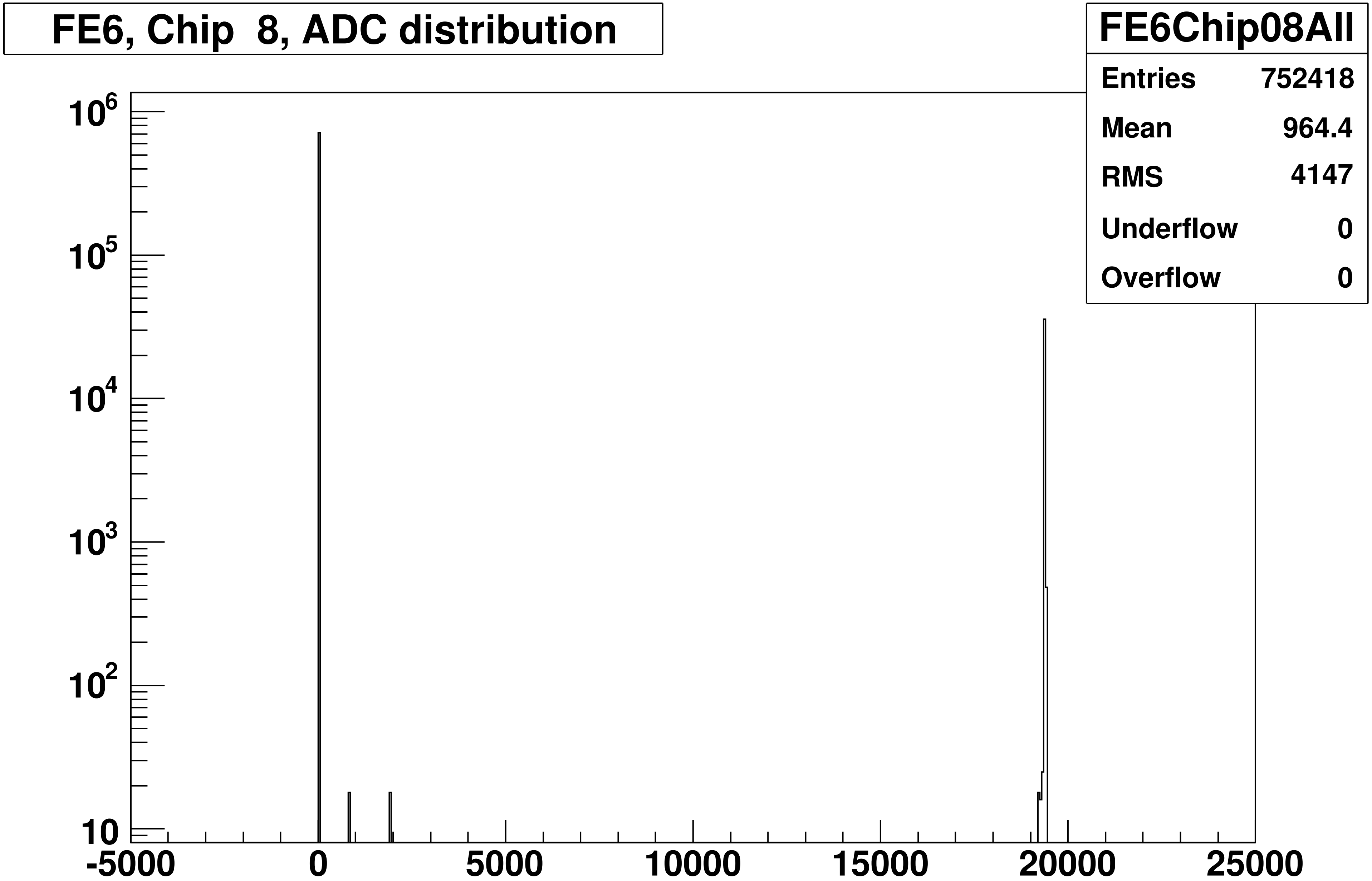
50
100
150
200


FE6, Chip  7, ADC distribution
FE6Chip07Sig
Entries 752418
Mean 1.92e+04
RMS 3.645
Underflow 7.166e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 752418
Mean 964.4
RMS 4147
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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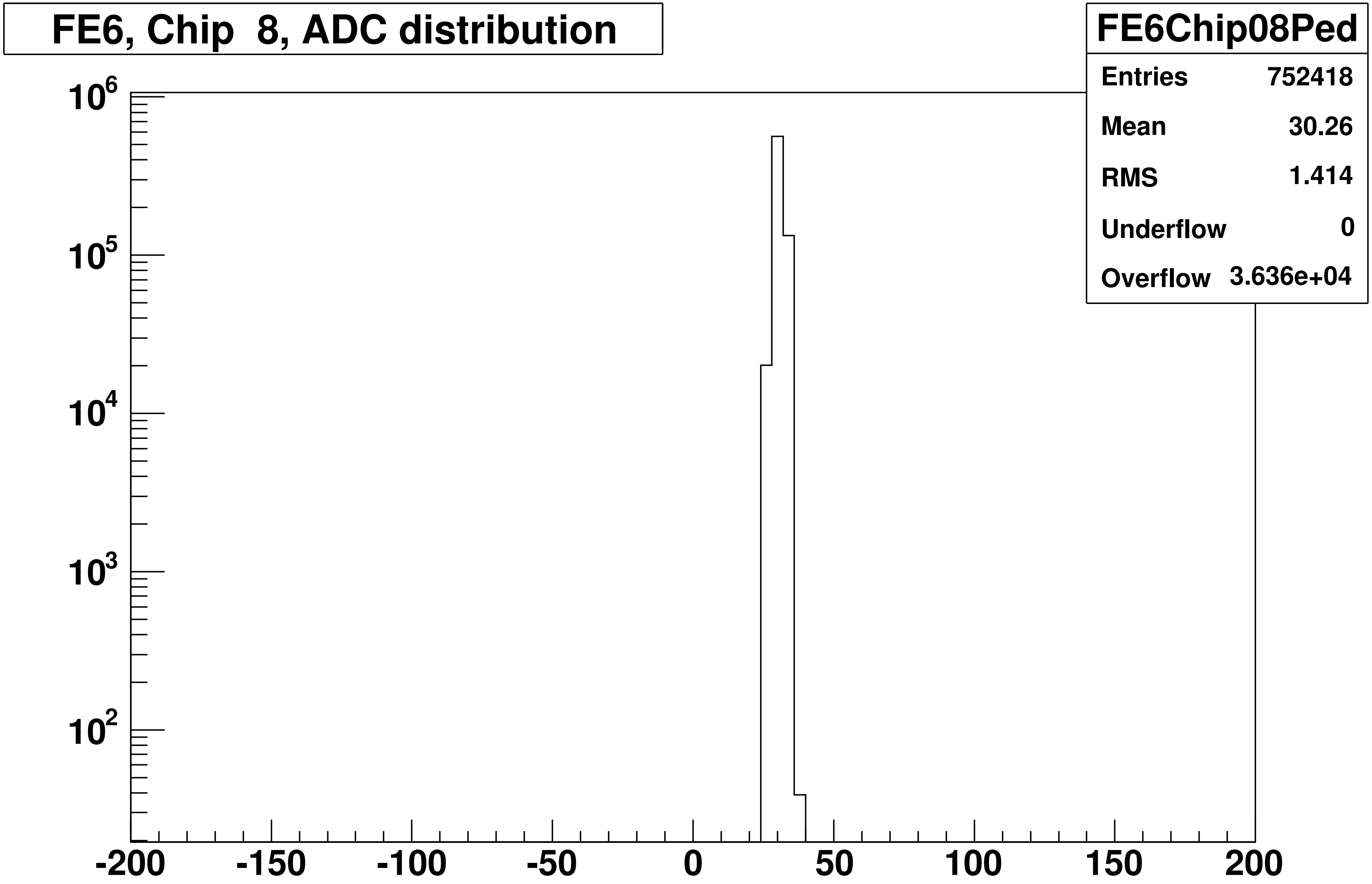
20000
25000



FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 752418
Mean 30.26
RMS 1.414
Underflow 0
Overflow 3.636e+04
$10^6$
$10^5$
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$10^3$
$10^2$
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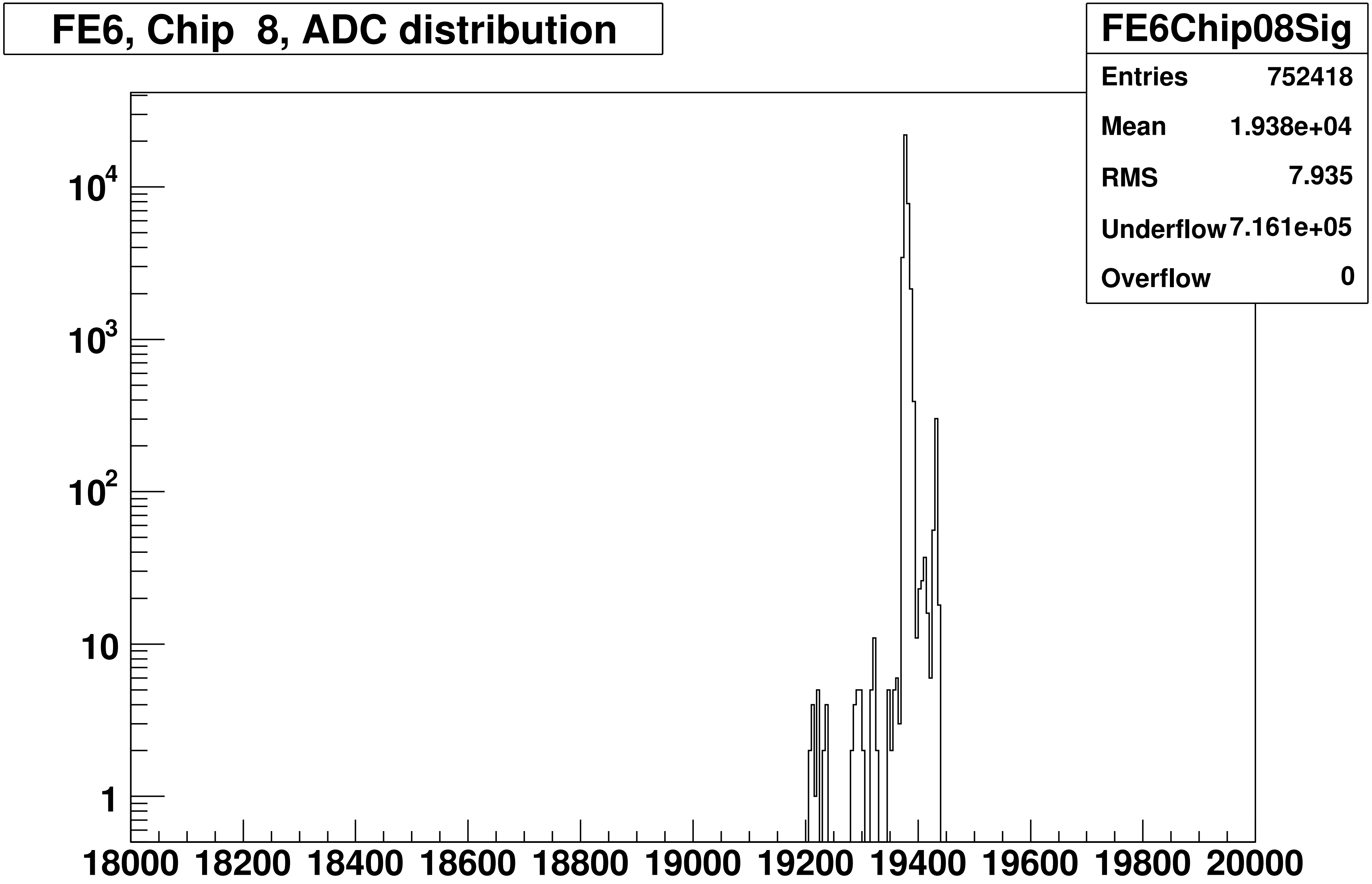
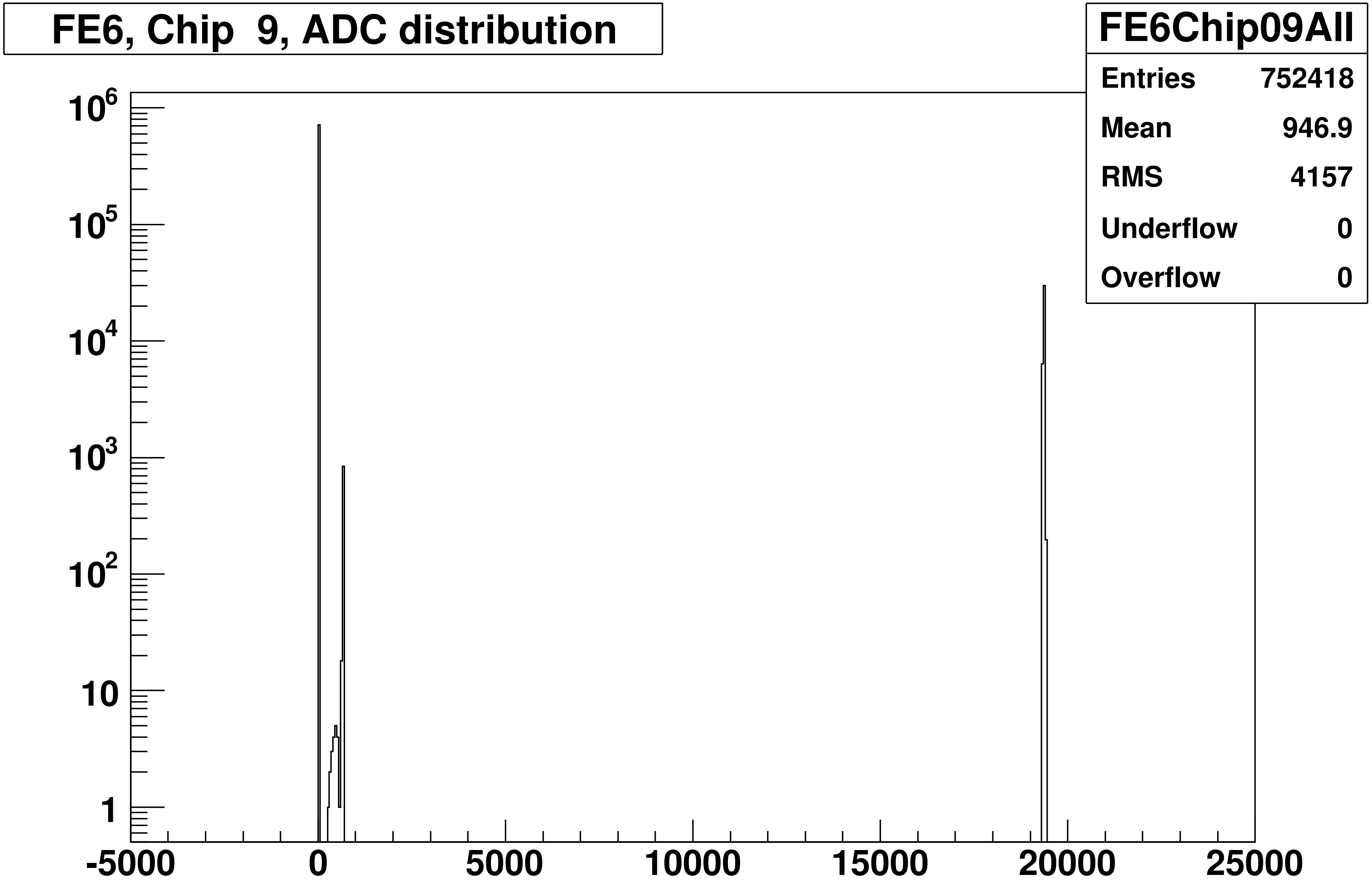
150
200



FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 752418
Mean 1.938e+04
RMS 7.935
Underflow 7.161e+05
Overflow 0
1
10
10^2
10^3
10^4
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 752418
Mean 946.9
RMS 4157
Underflow 0
Overflow 0
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$10^3$
$10^2$
10
1
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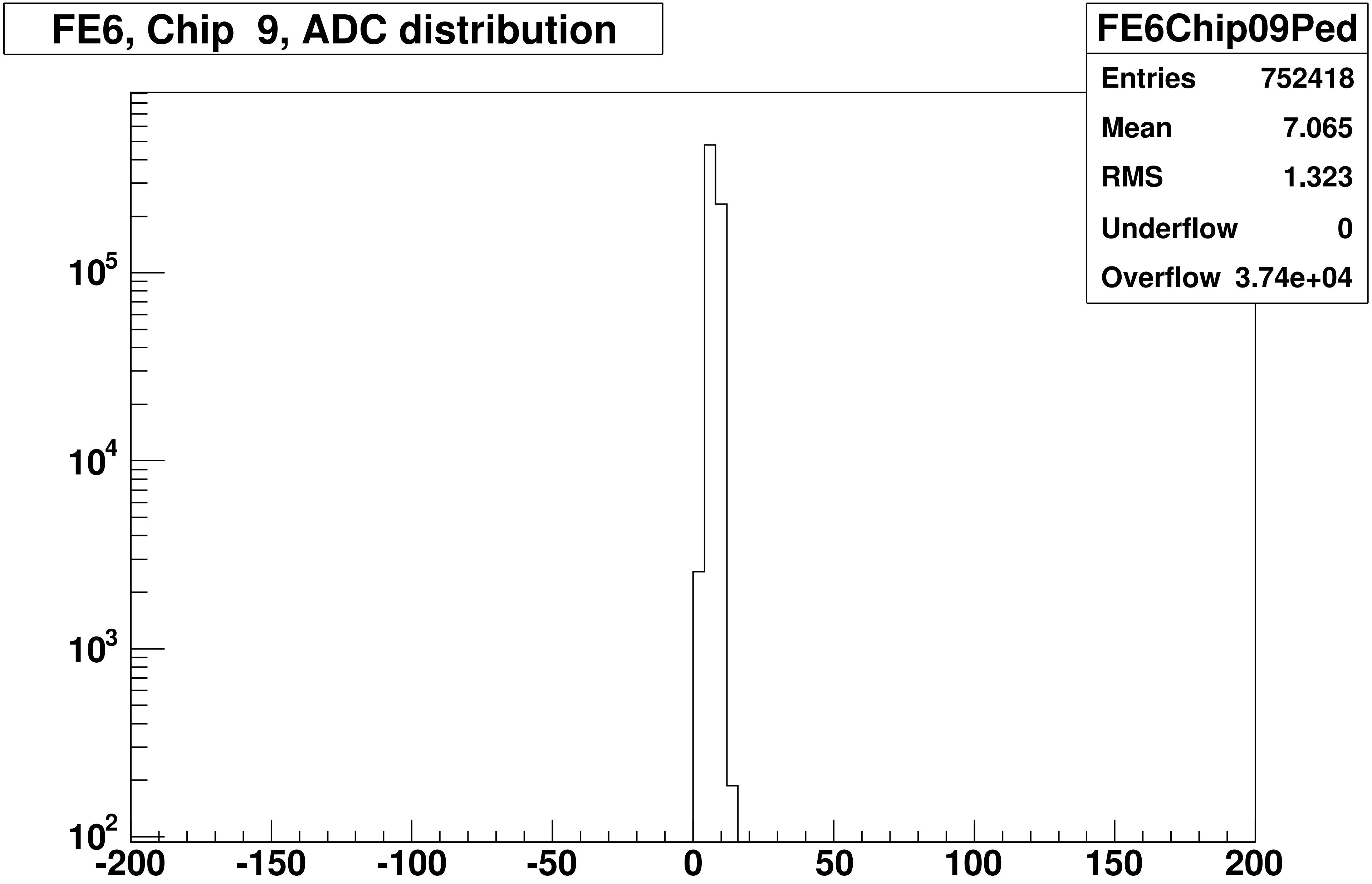
0
5000
10000
15000
20000
25000



FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 752418
Mean 7.065
RMS 1.323
Underflow 0
Overflow 3.74e+04
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$10^3$
$10^2$
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-150
-100
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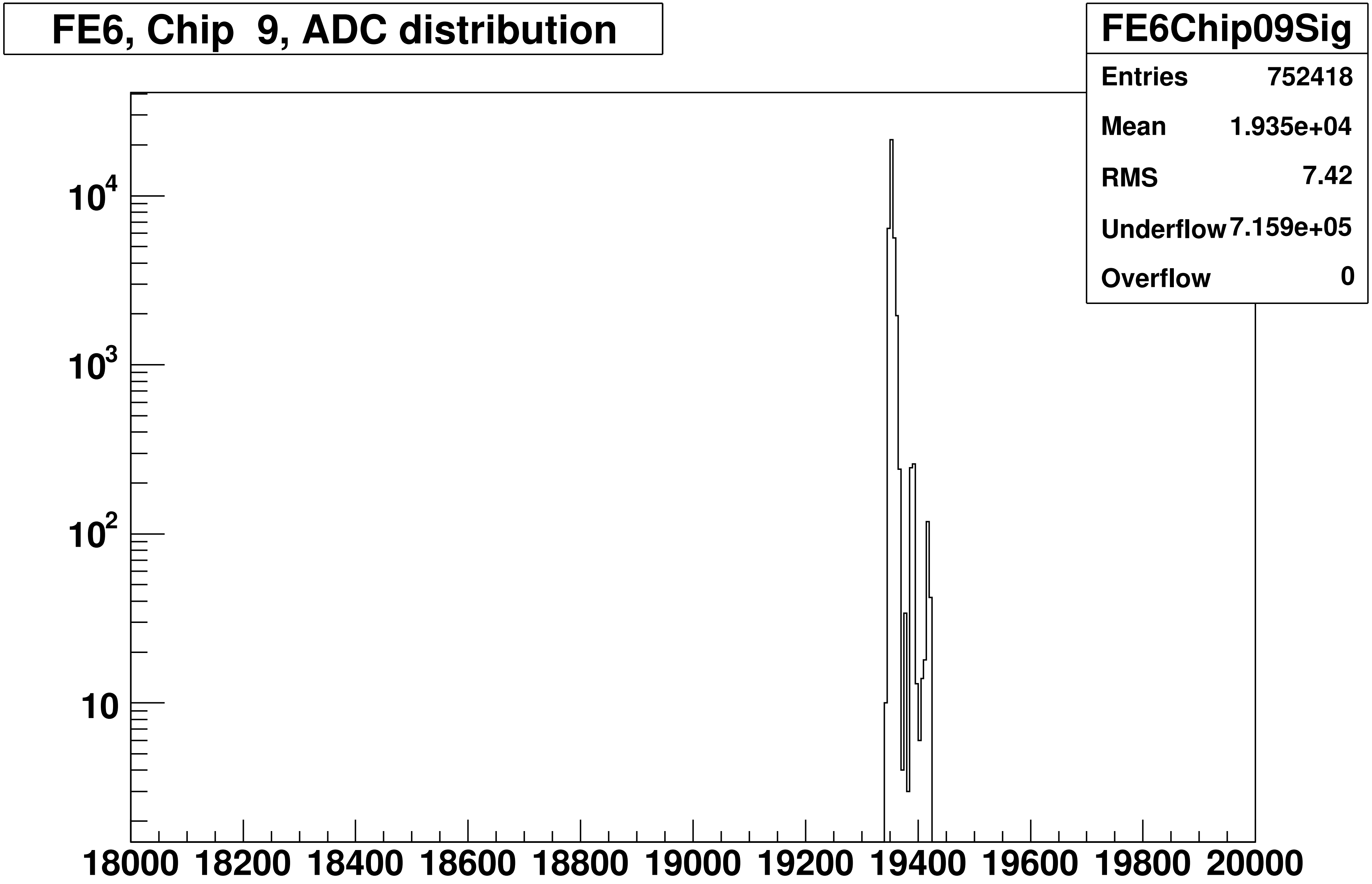
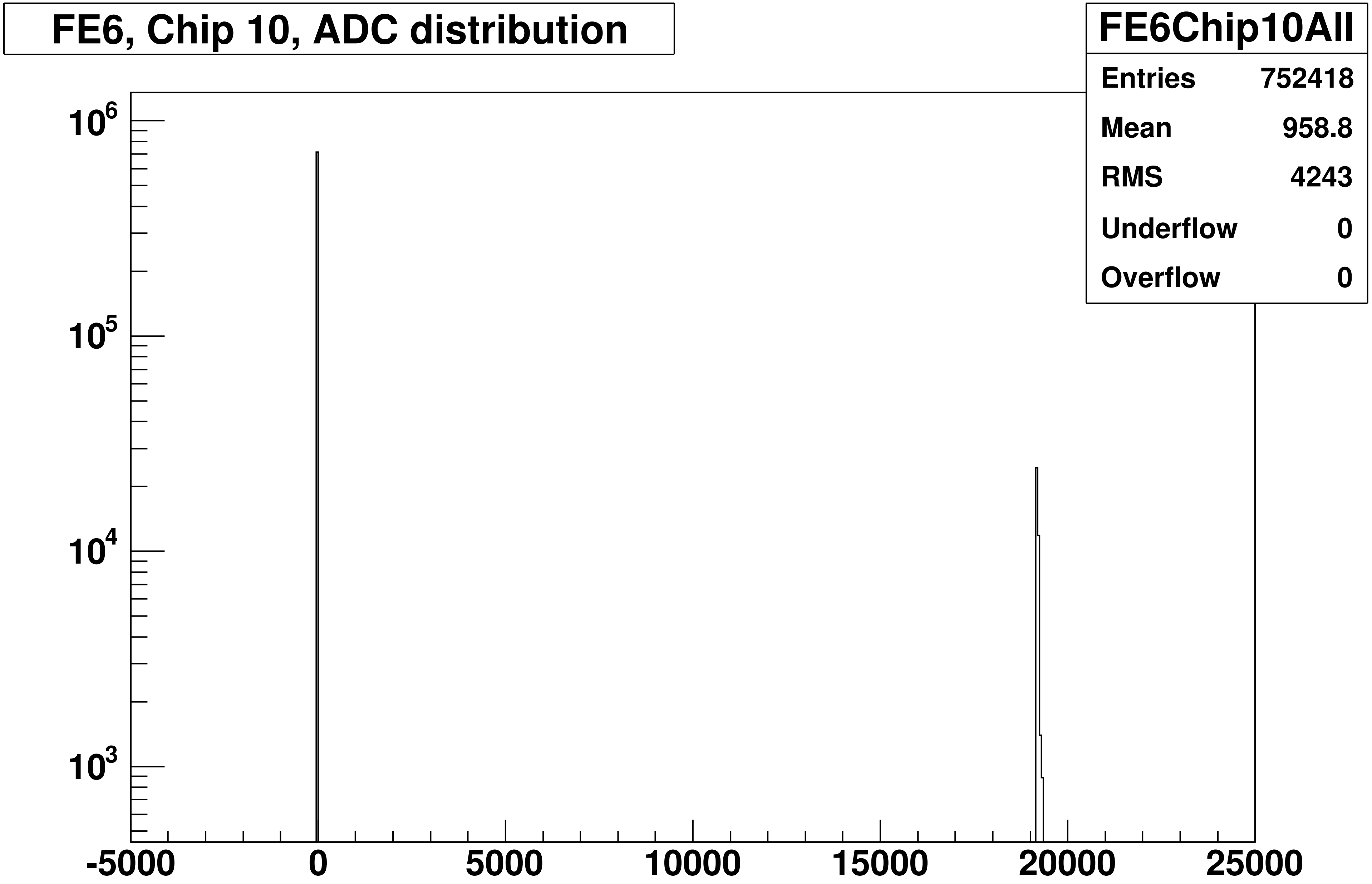
0
50
100
150
200


FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 752418
Mean 1.935e+04
RMS 7.42
Underflow 7.159e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 752418
Mean 958.8
RMS 4243
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
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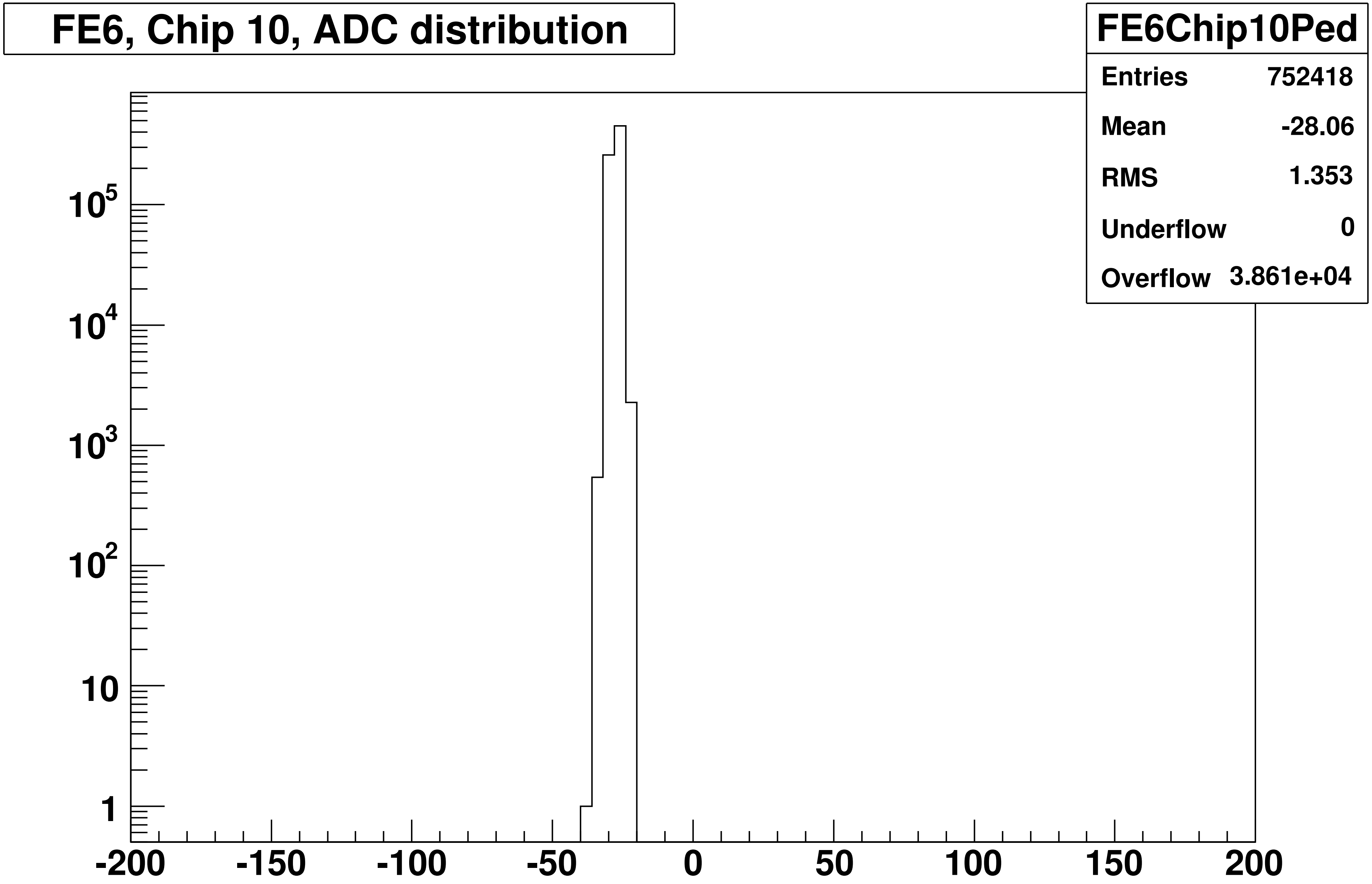
0
5000
10000
15000
20000
25000



FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 752418
Mean -28.06
RMS 1.353
Underflow 0
Overflow 3.861e+04
$10^5$
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$10^3$
$10^2$
10
1
-200
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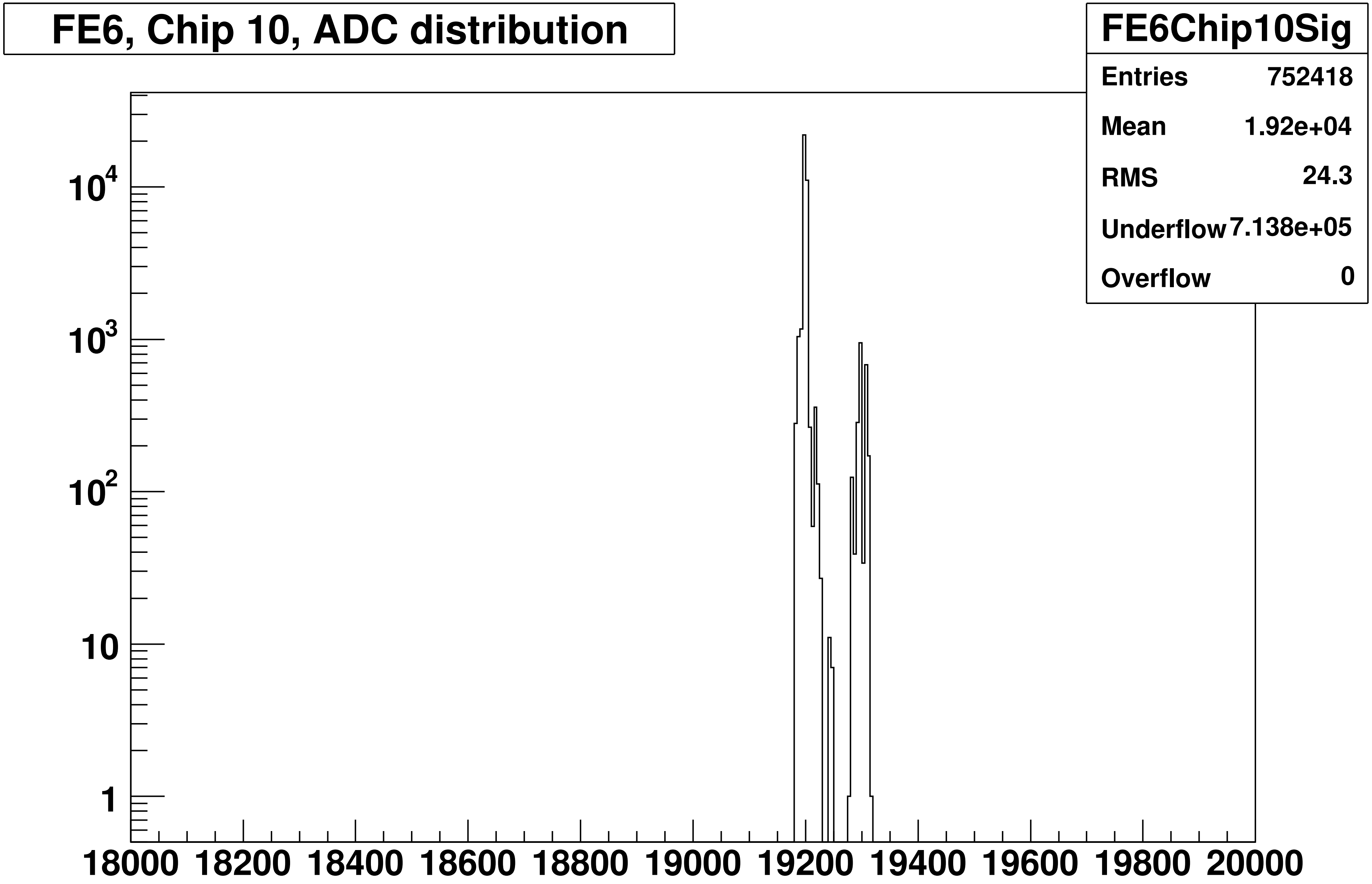
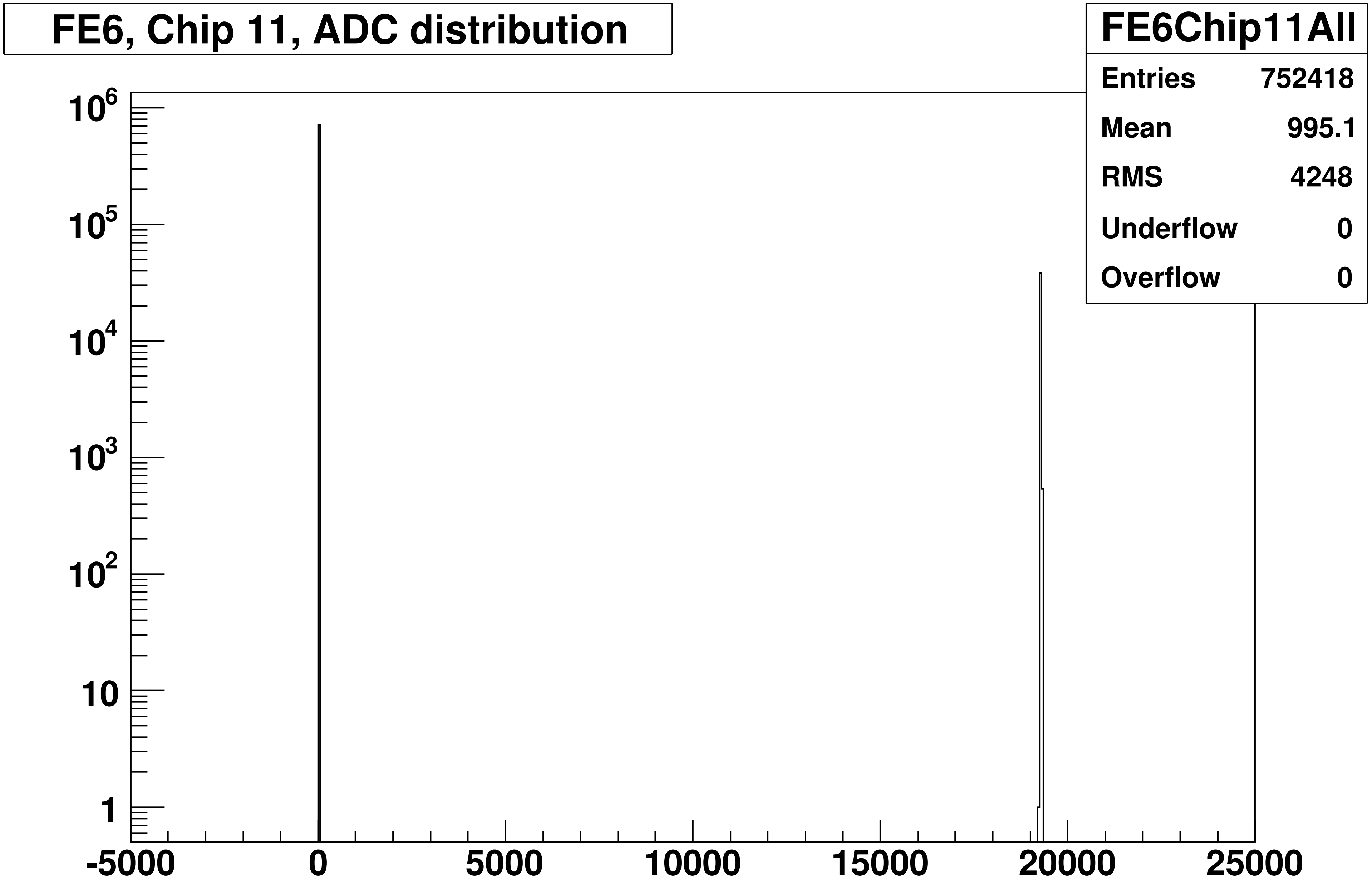
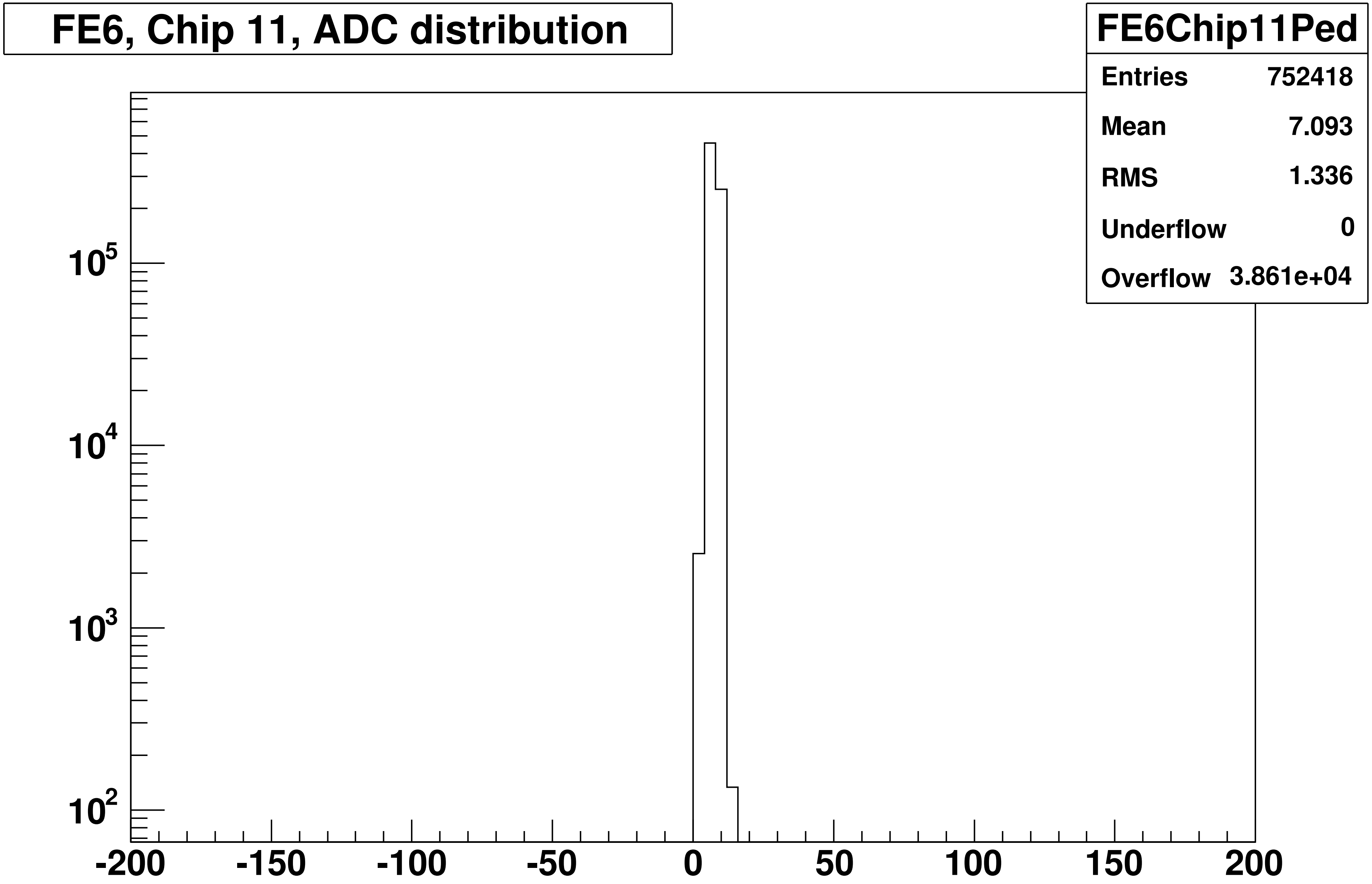
-100
-50
0
50
100
150
200


FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 752418
Mean 1.92e+04
RMS 24.3
Underflow 7.138e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 752418
Mean 995.1
RMS 4248
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-5000
0
5000
10000
15000
20000
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 752418
Mean 7.093
RMS 1.336
Underflow 0
Overflow 3.861e+04
$10^5$
$10^4$
$10^3$
$10^2$
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-150
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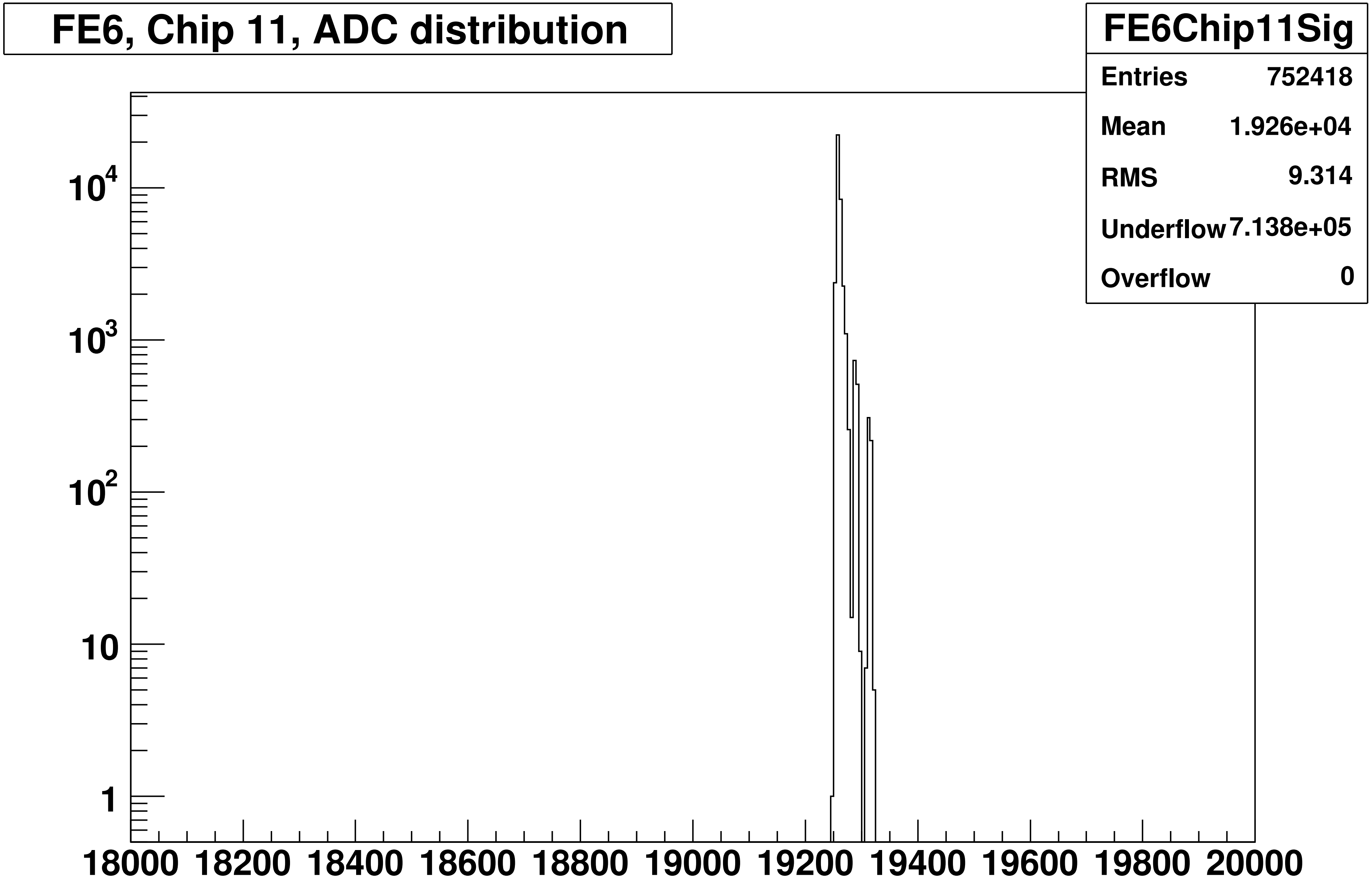
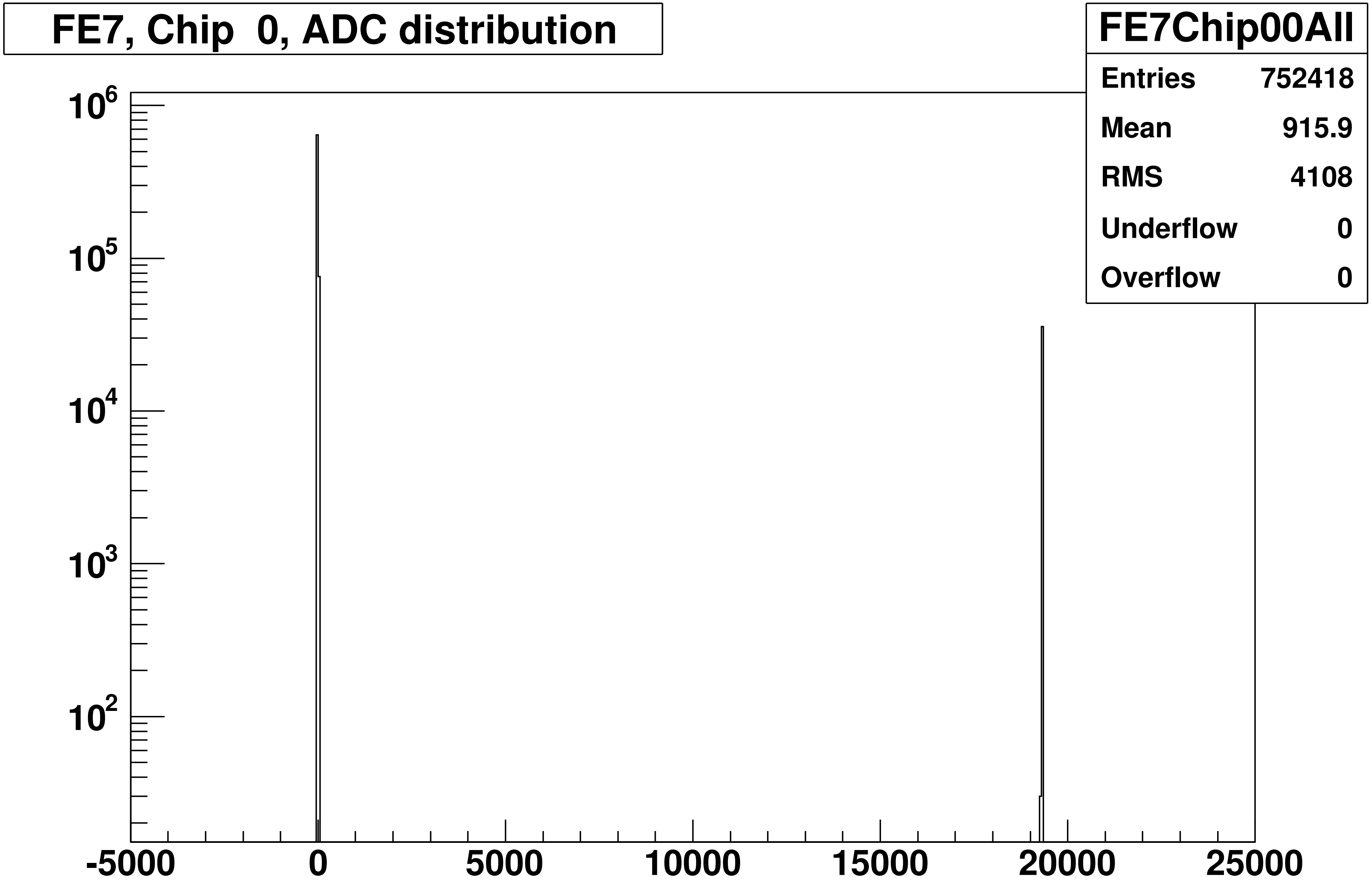
-50
0
50
100
150
200


FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 752418
Mean 1.926e+04
RMS 9.314
Underflow 7.138e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 752418
Mean 915.9
RMS 4108
Underflow 0
Overflow 0
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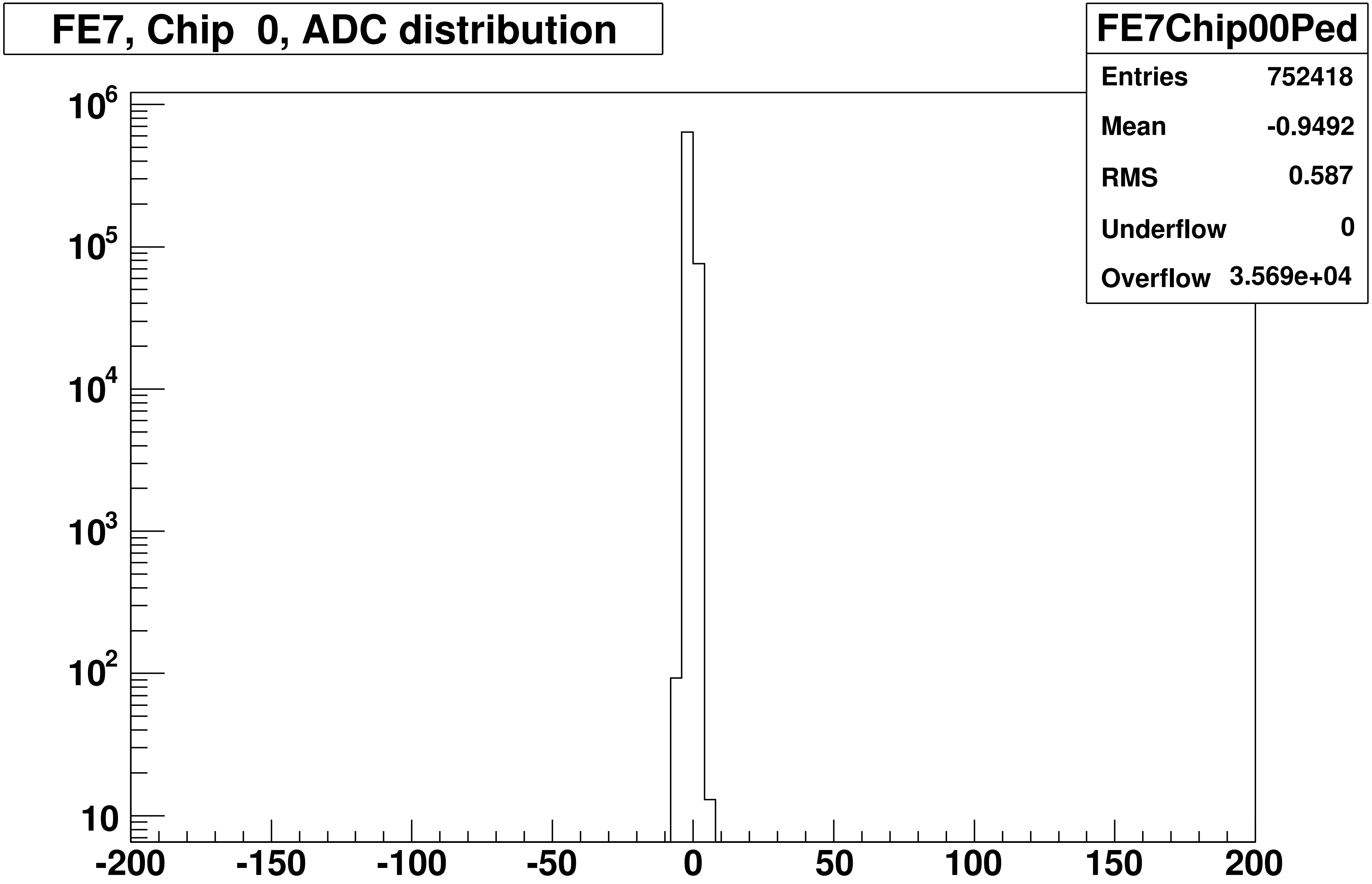
$10^4$
$10^3$
$10^2$
-5000
0
5000
10000
15000
20000
25000


FE7, Chip  0, ADC distribution
FE7Chip00Ped
Entries 752418
Mean -0.9492
RMS 0.587
Underflow 0
Overflow 3.569e+04
$10^6$
$10^5$
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$10^3$
$10^2$
10
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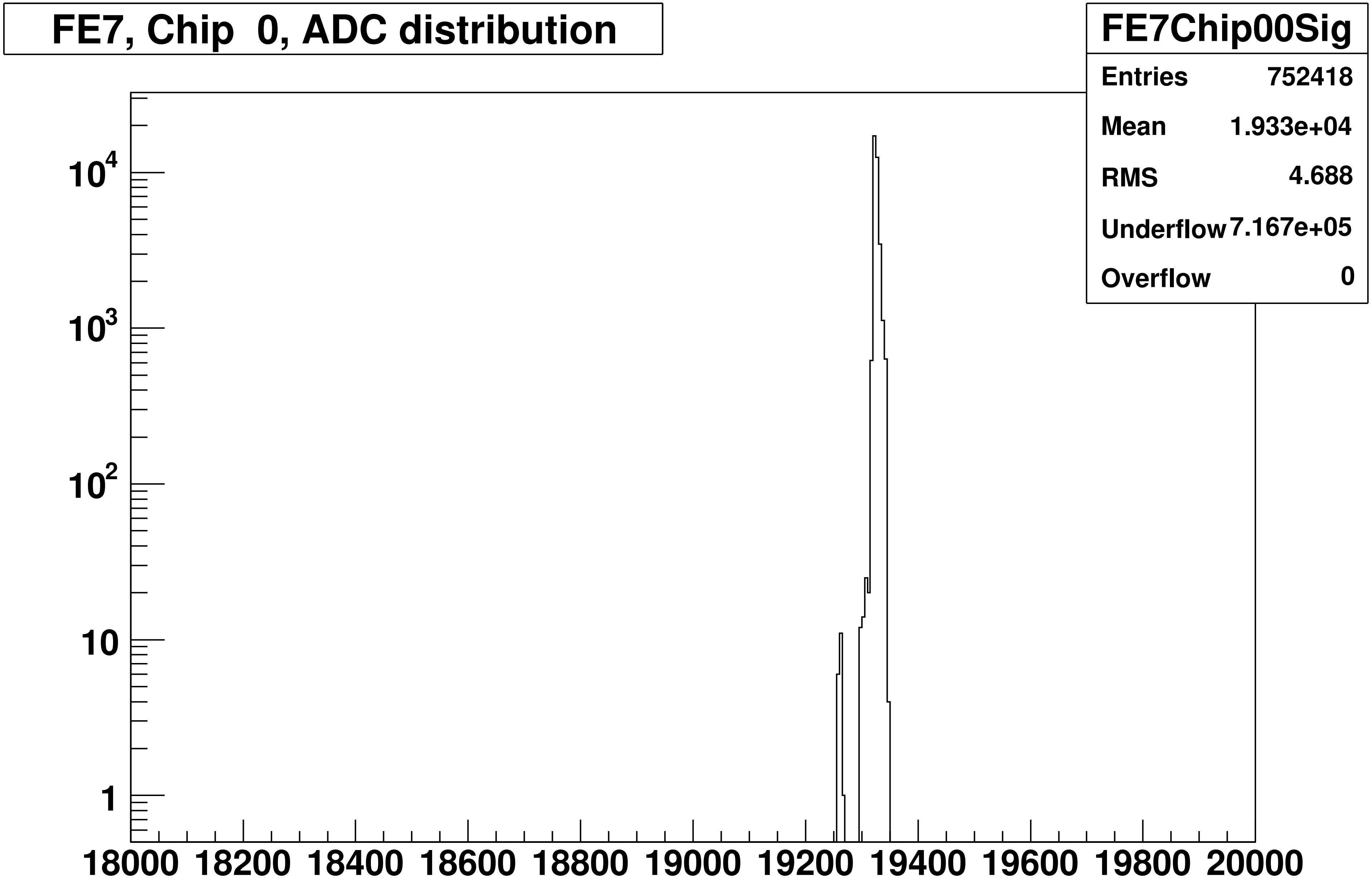
100
150
200


FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 752418
Mean 1.933e+04
RMS 4.688
Underflow 7.167e+05
Overflow 0
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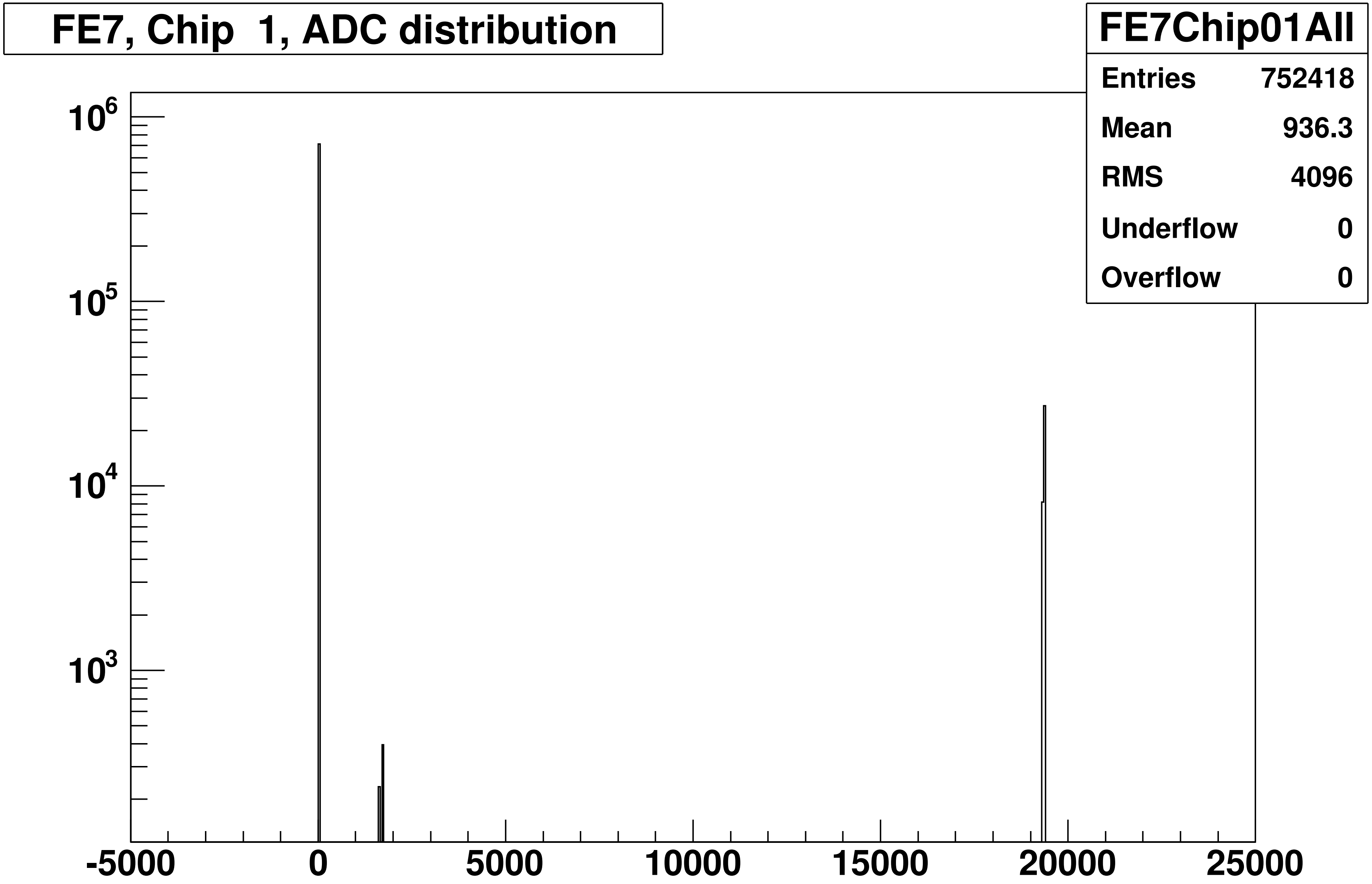
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 752418
Mean 936.3
RMS 4096
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
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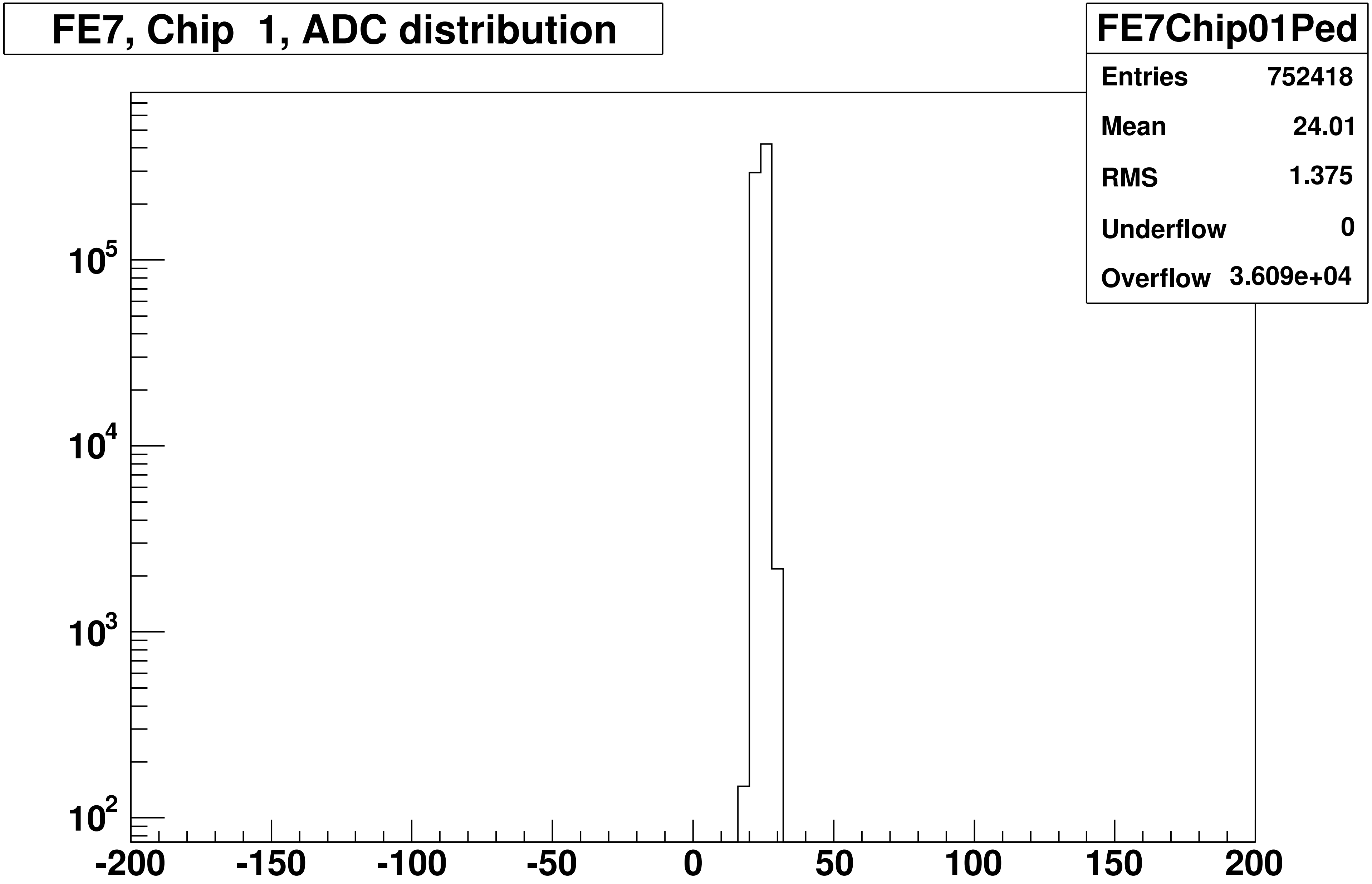
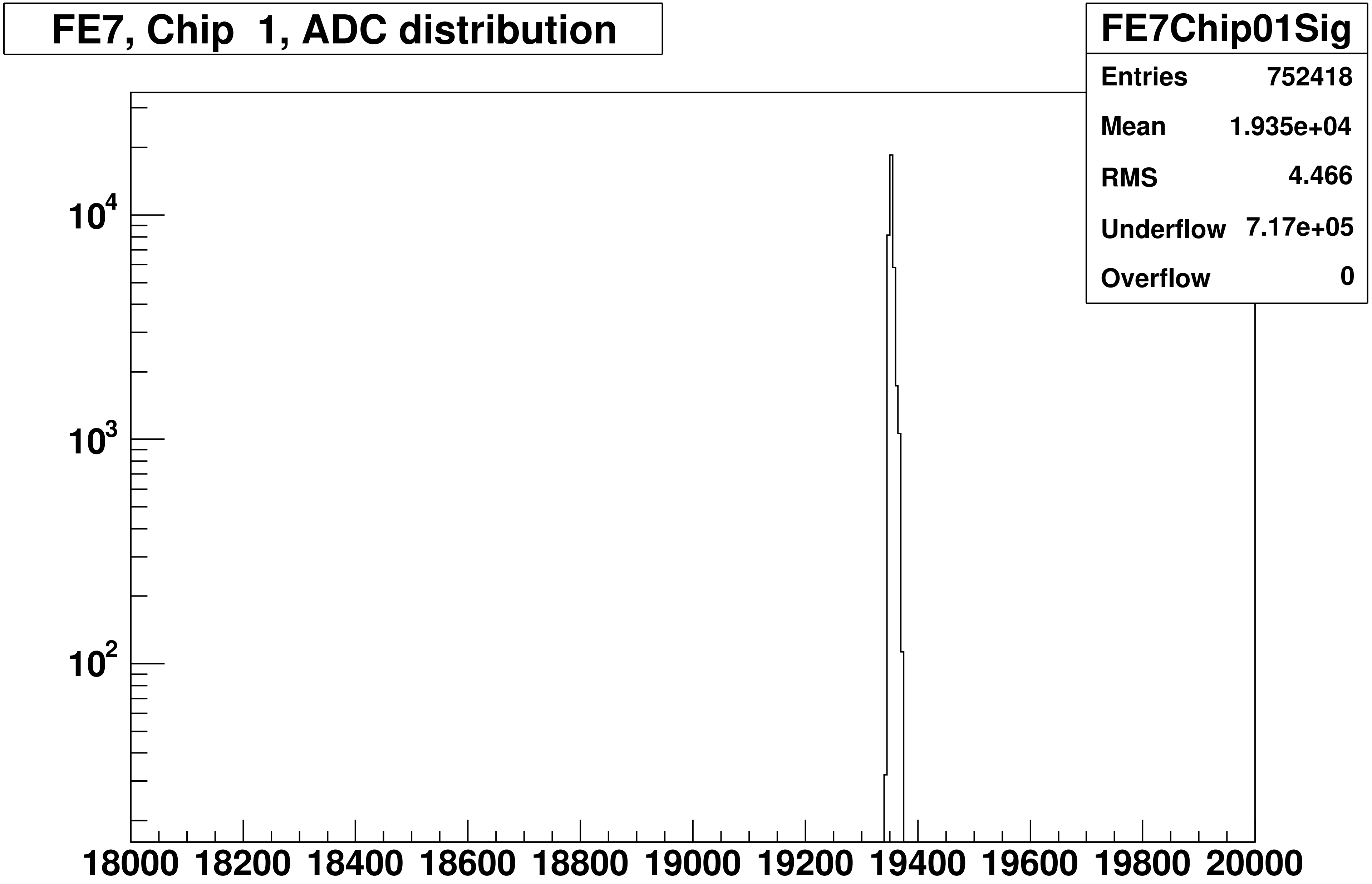
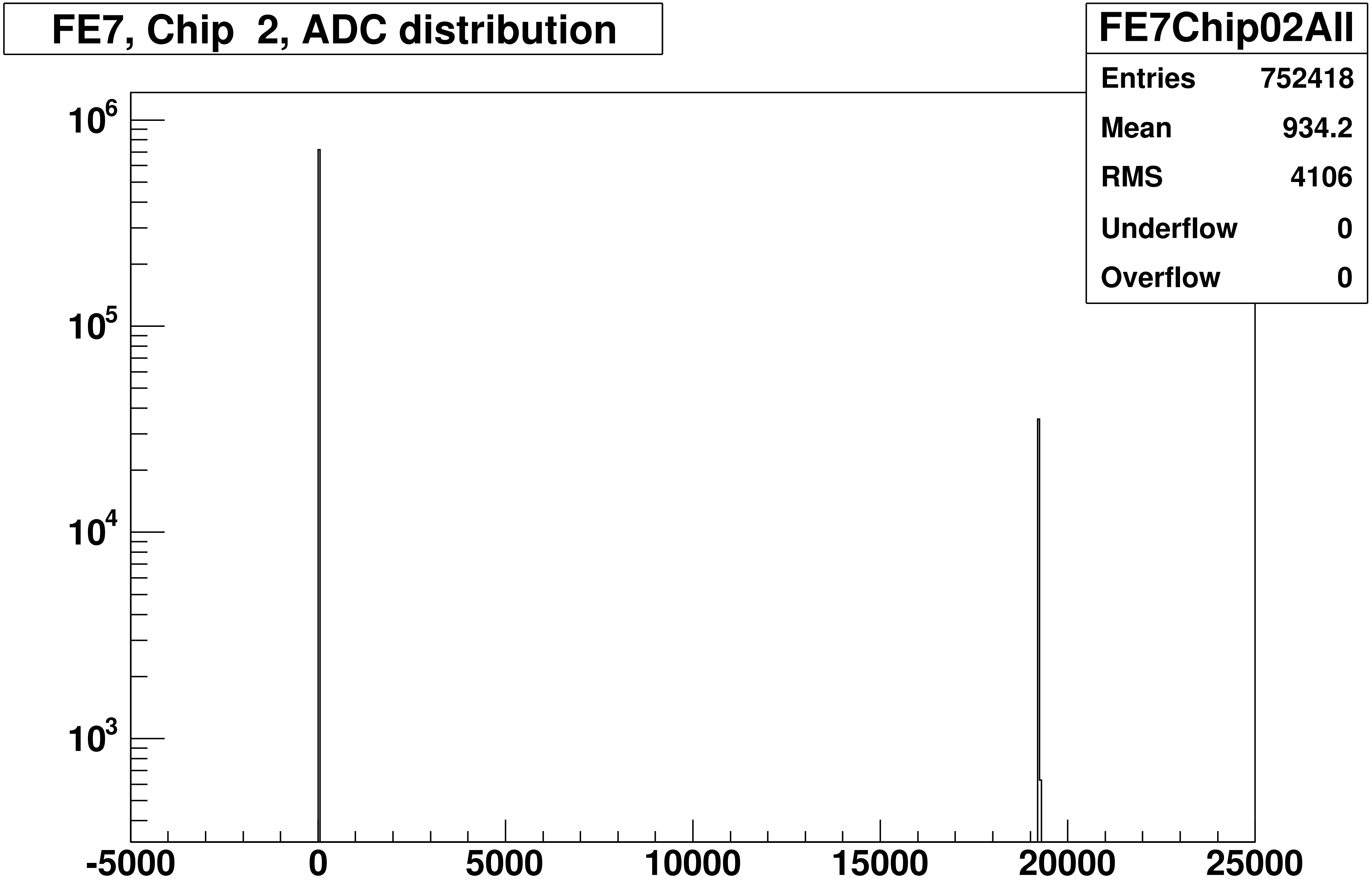
10000
15000
20000
25000


FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 752418
Mean 24.01
RMS 1.375
Underflow 0
Overflow 3.609e+04
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 752418
Mean 1.935e+04
RMS 4.466
Underflow 7.17e+05
Overflow 0
10^4
10^3
10^2
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000



FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 752418
Mean 934.2
RMS 4106
Underflow 0
Overflow 0
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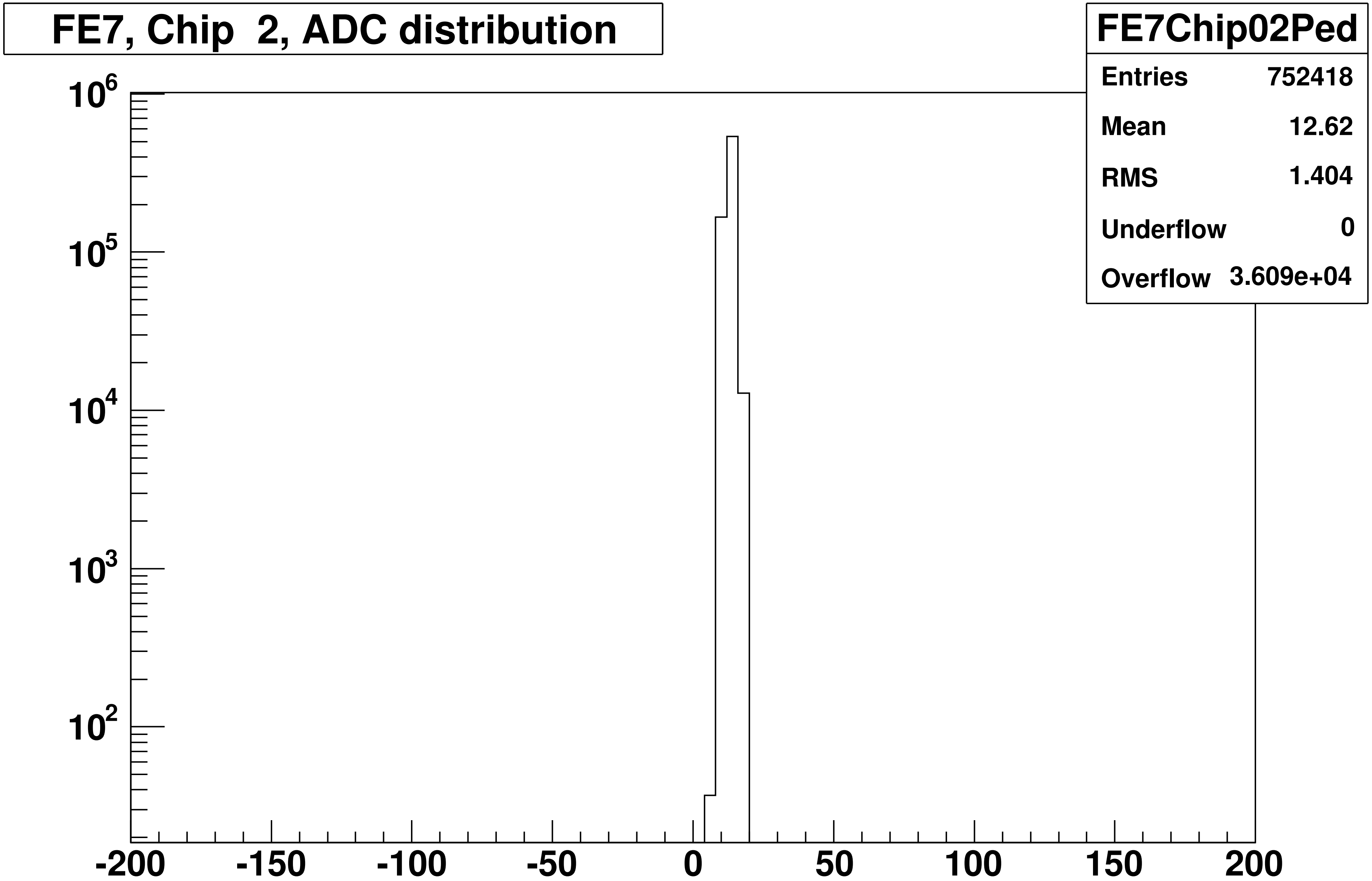
$10^5$
$10^4$
$10^3$
-5000
0
5000
10000
15000
20000
25000



FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 752418
Mean 12.62
RMS 1.404
Underflow 0
Overflow 3.609e+04
$10^6$
$10^5$
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$10^3$
$10^2$
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-100
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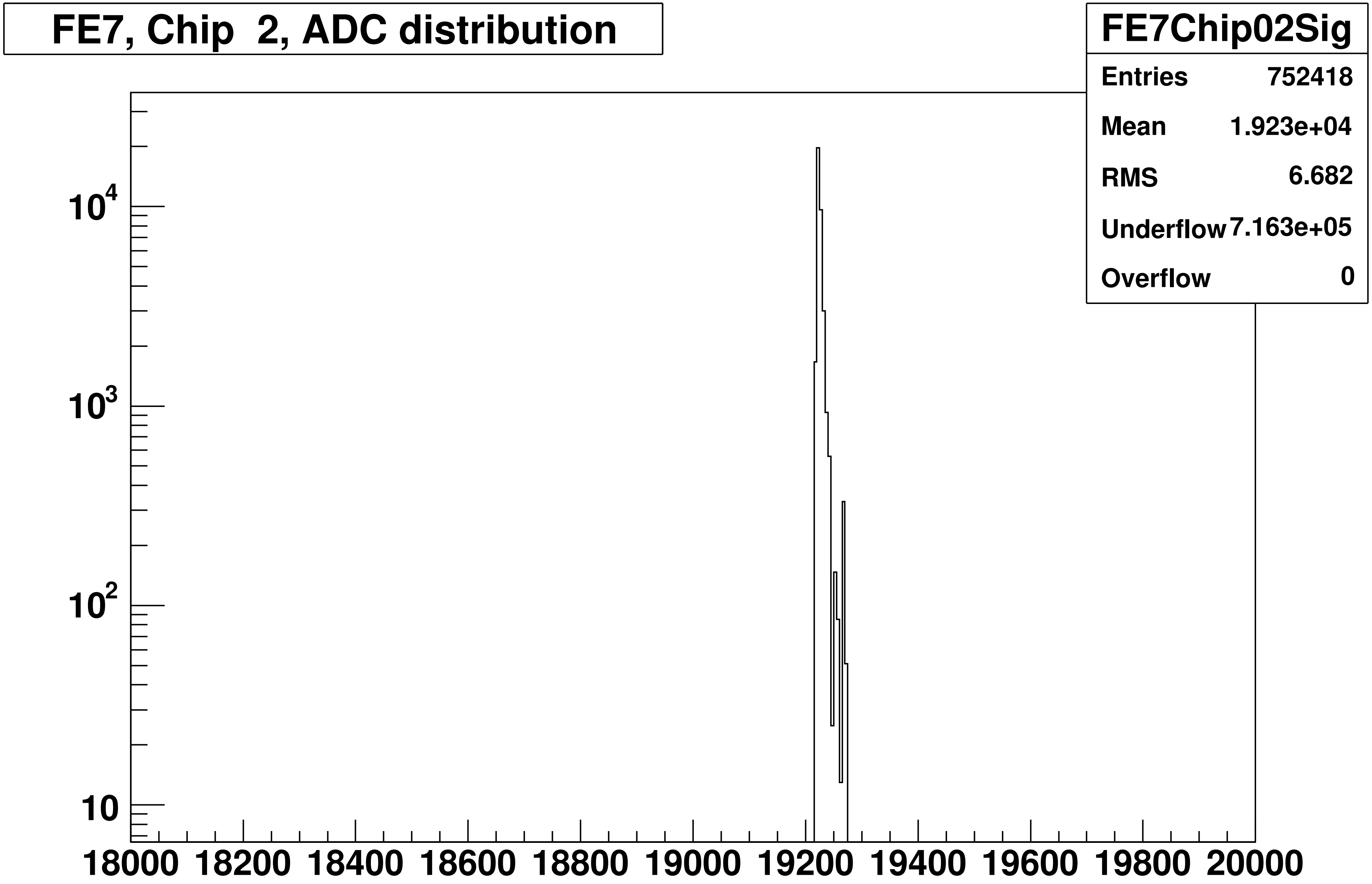
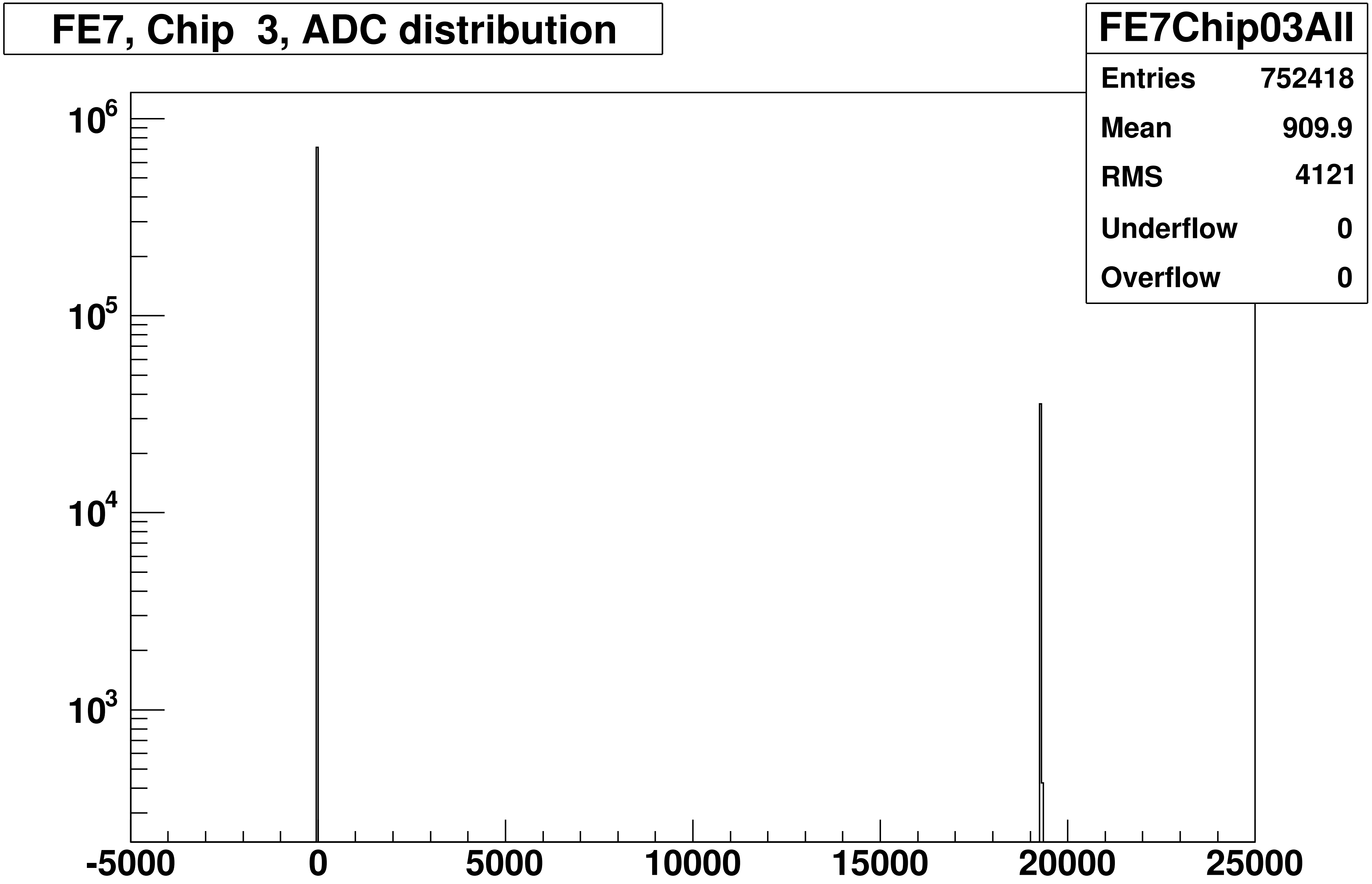
0
50
100
150
200



FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 752418
Mean 1.923e+04
RMS 6.682
Underflow 7.163e+05
Overflow 0
10
10^2
10^3
10^4
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 752418
Mean 909.9
RMS 4121
Underflow 0
Overflow 0
10^6
10^5
10^4
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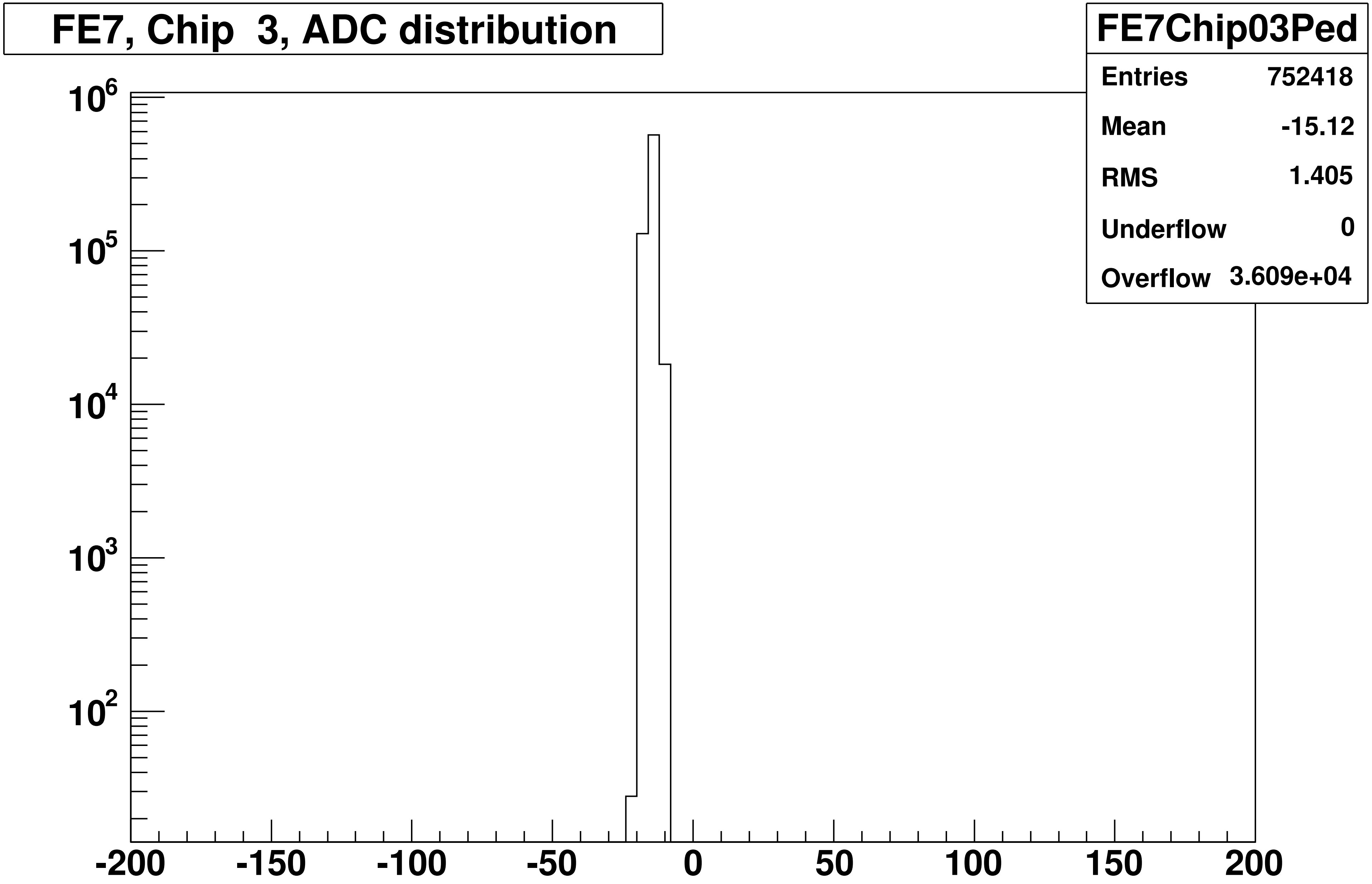
10000
15000
20000
25000



FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 752418
Mean -15.12
RMS 1.405
Underflow 0
Overflow 3.609e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
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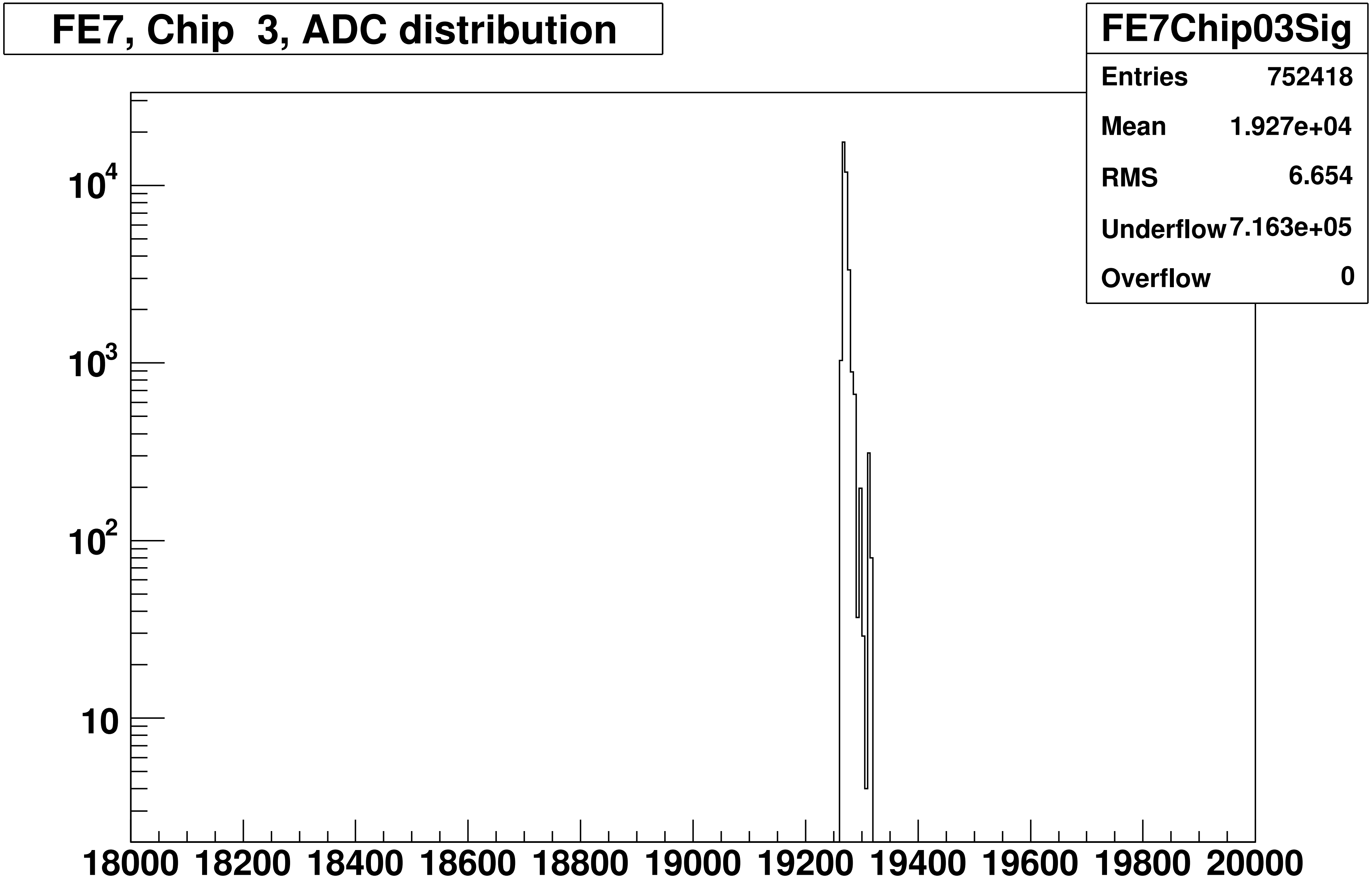
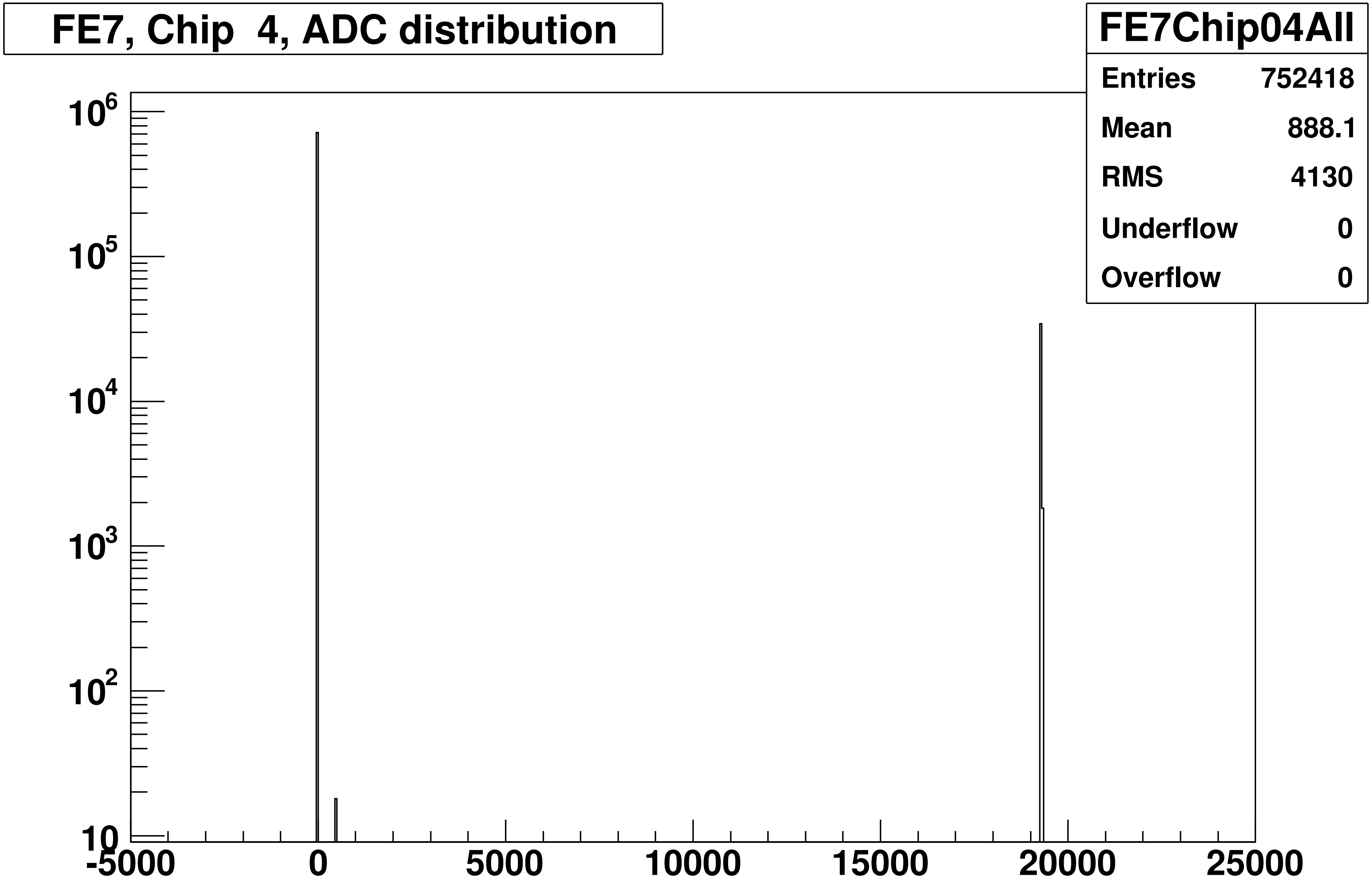
-100
-50
0
50
100
150
200



FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 752418
Mean 1.927e+04
RMS 6.654
Underflow 7.163e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000


FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 752418
Mean 888.1
RMS 4130
Underflow 0
Overflow 0
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$10^5$
$10^4$
$10^3$
$10^2$
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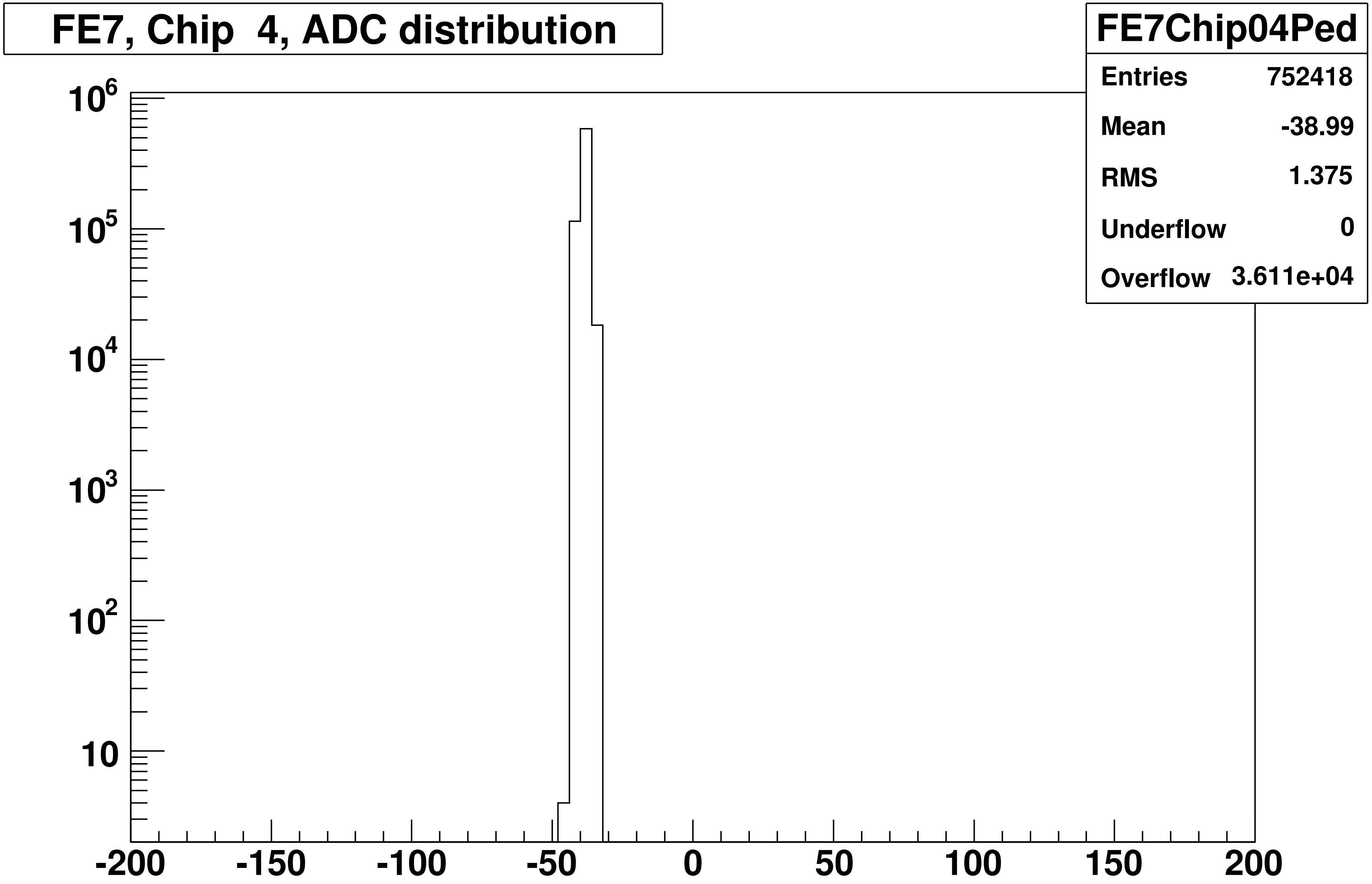
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 752418
Mean -38.99
RMS 1.375
Underflow 0
Overflow 3.611e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
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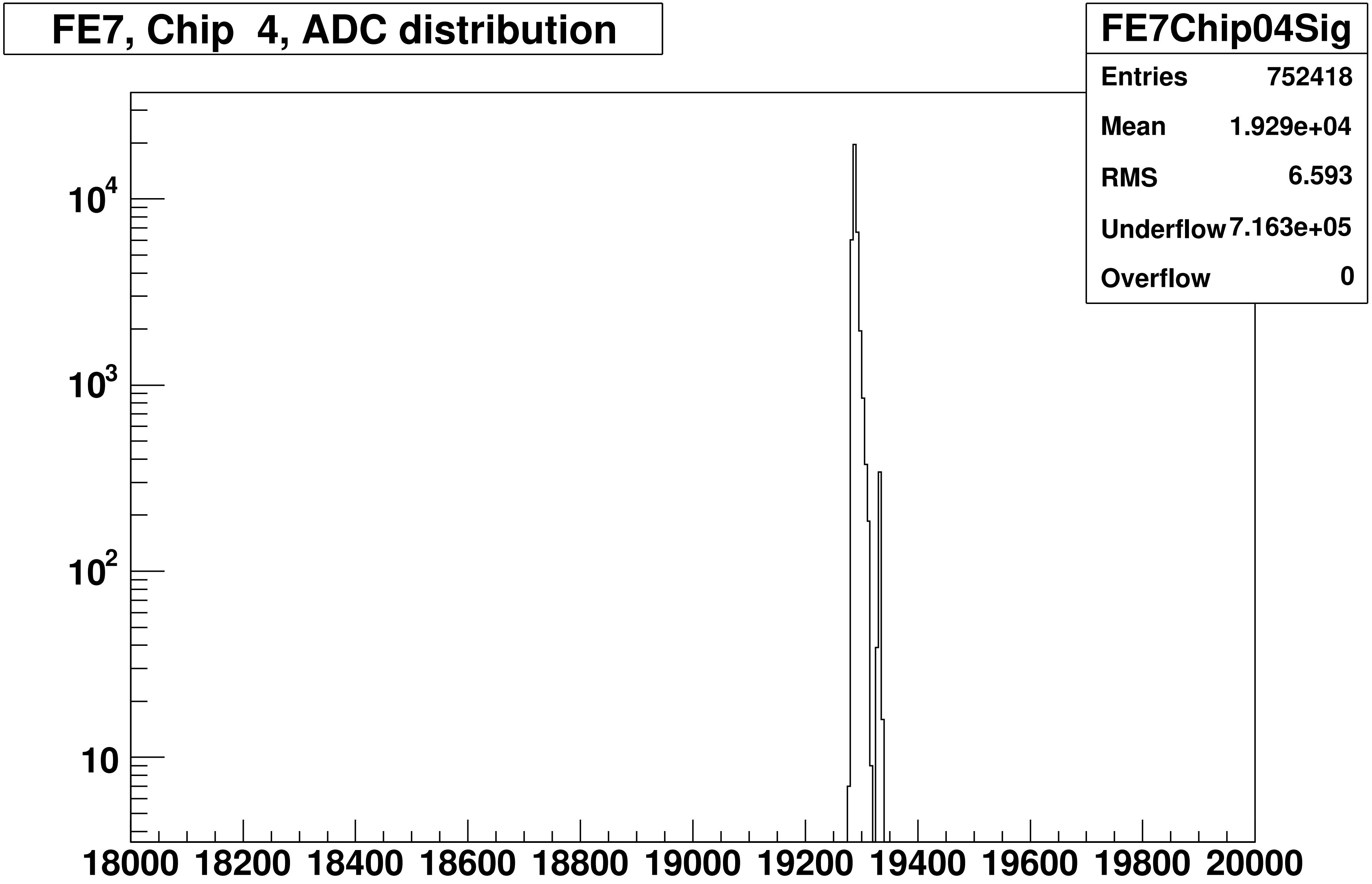
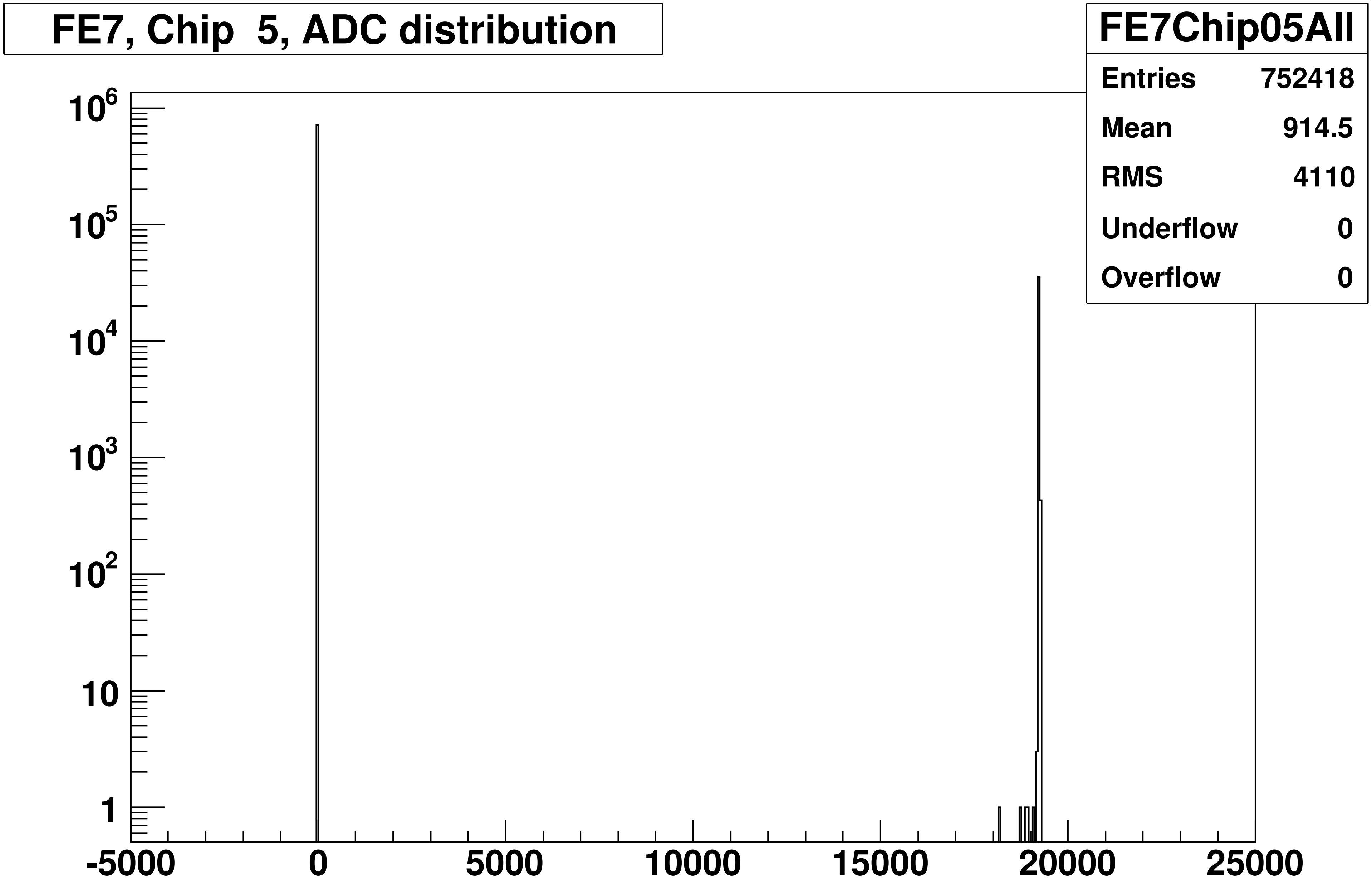
-100
-50
0
50
100
150
200



FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 752418
Mean 1.929e+04
RMS 6.593
Underflow 7.163e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000


FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 752418
Mean 914.5
RMS 4110
Underflow 0
Overflow 0
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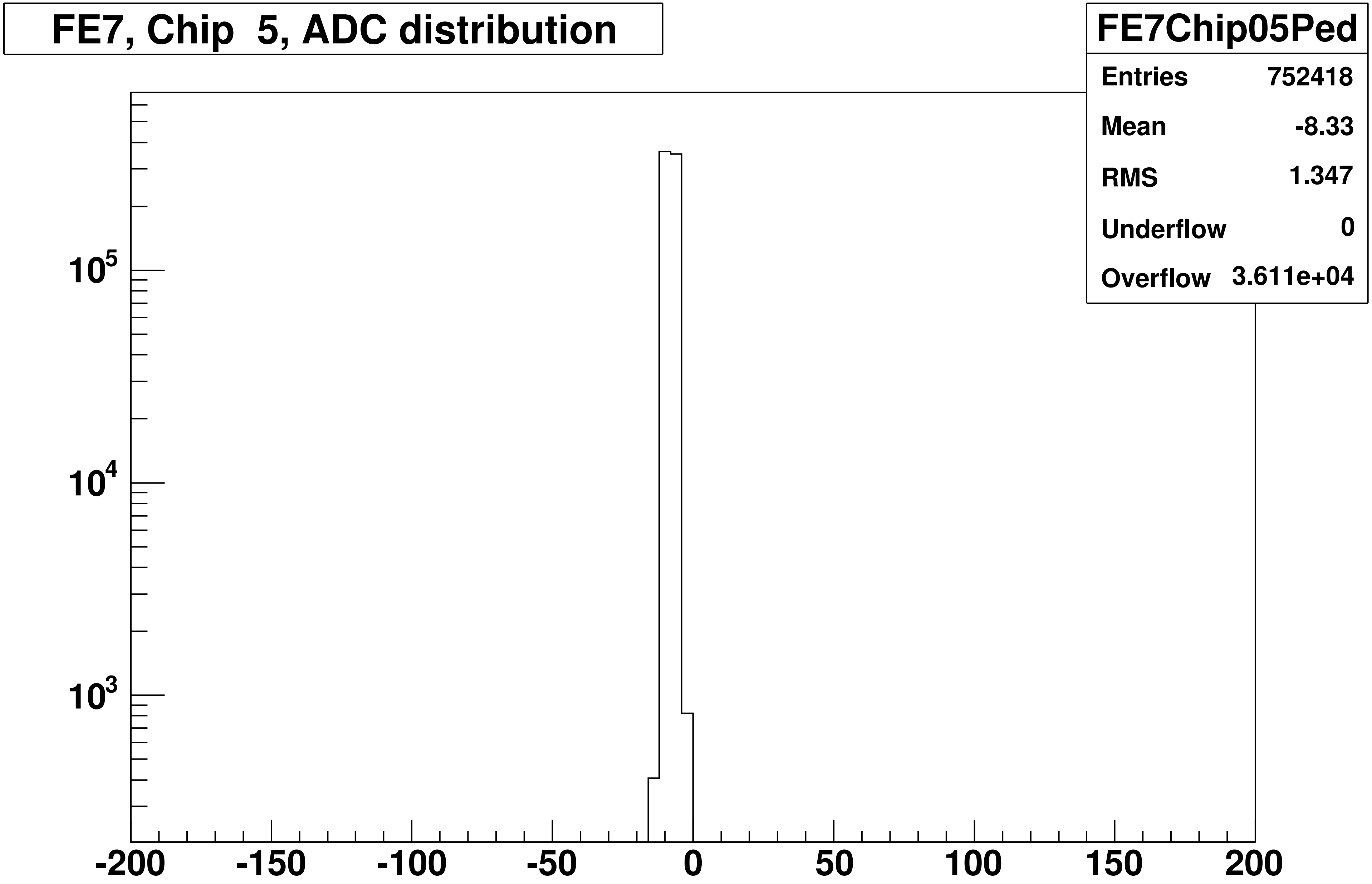
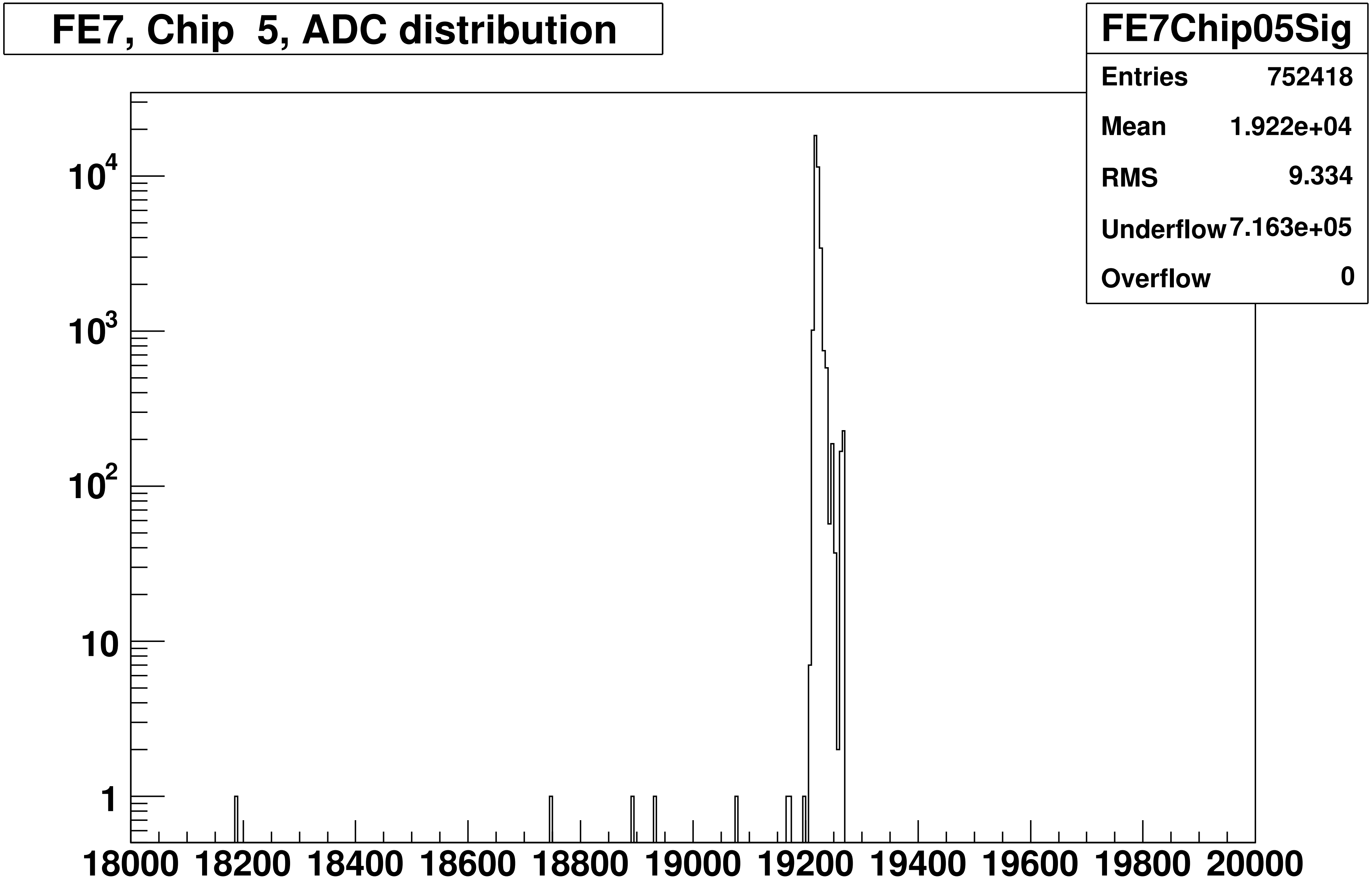
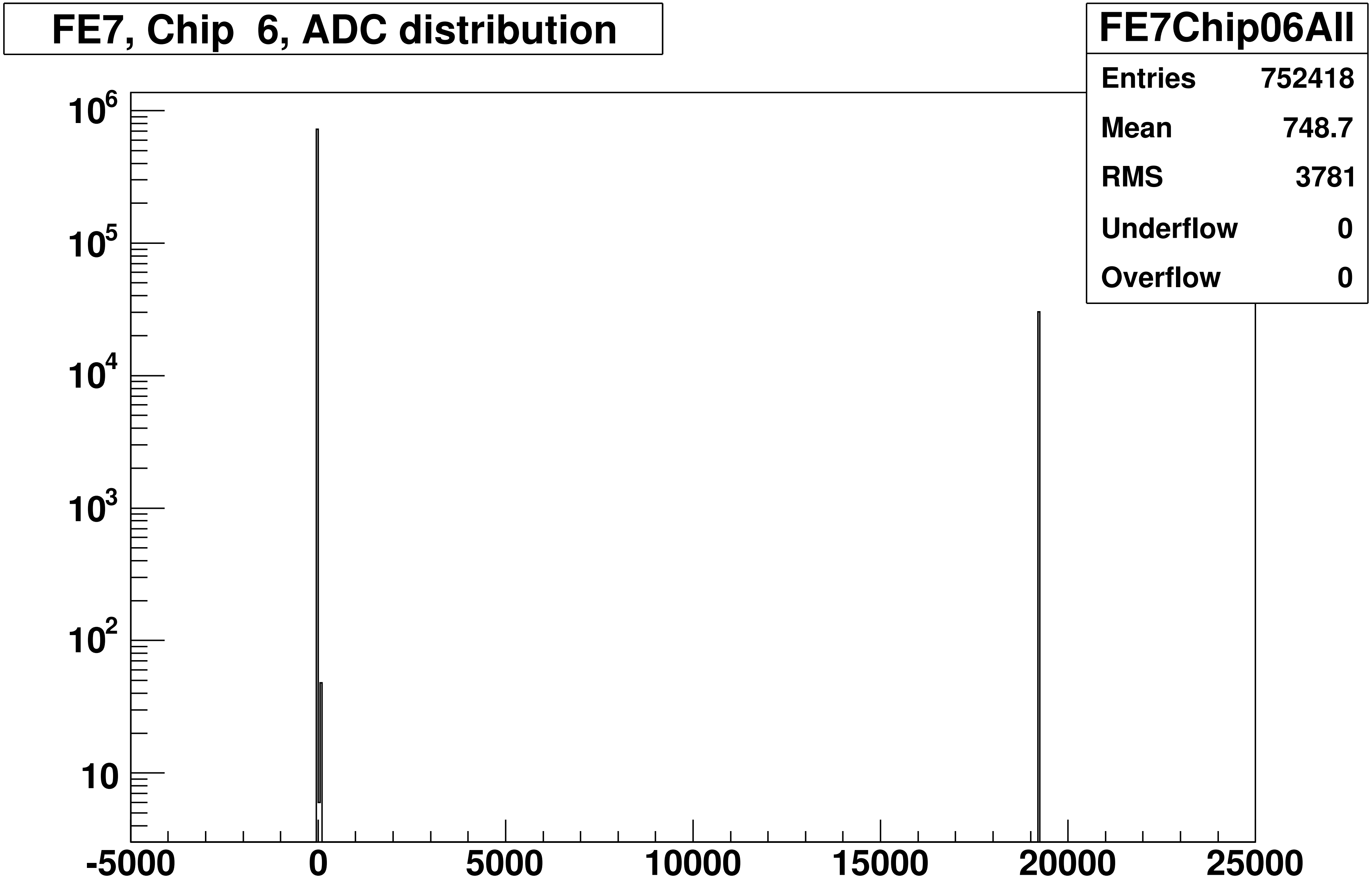
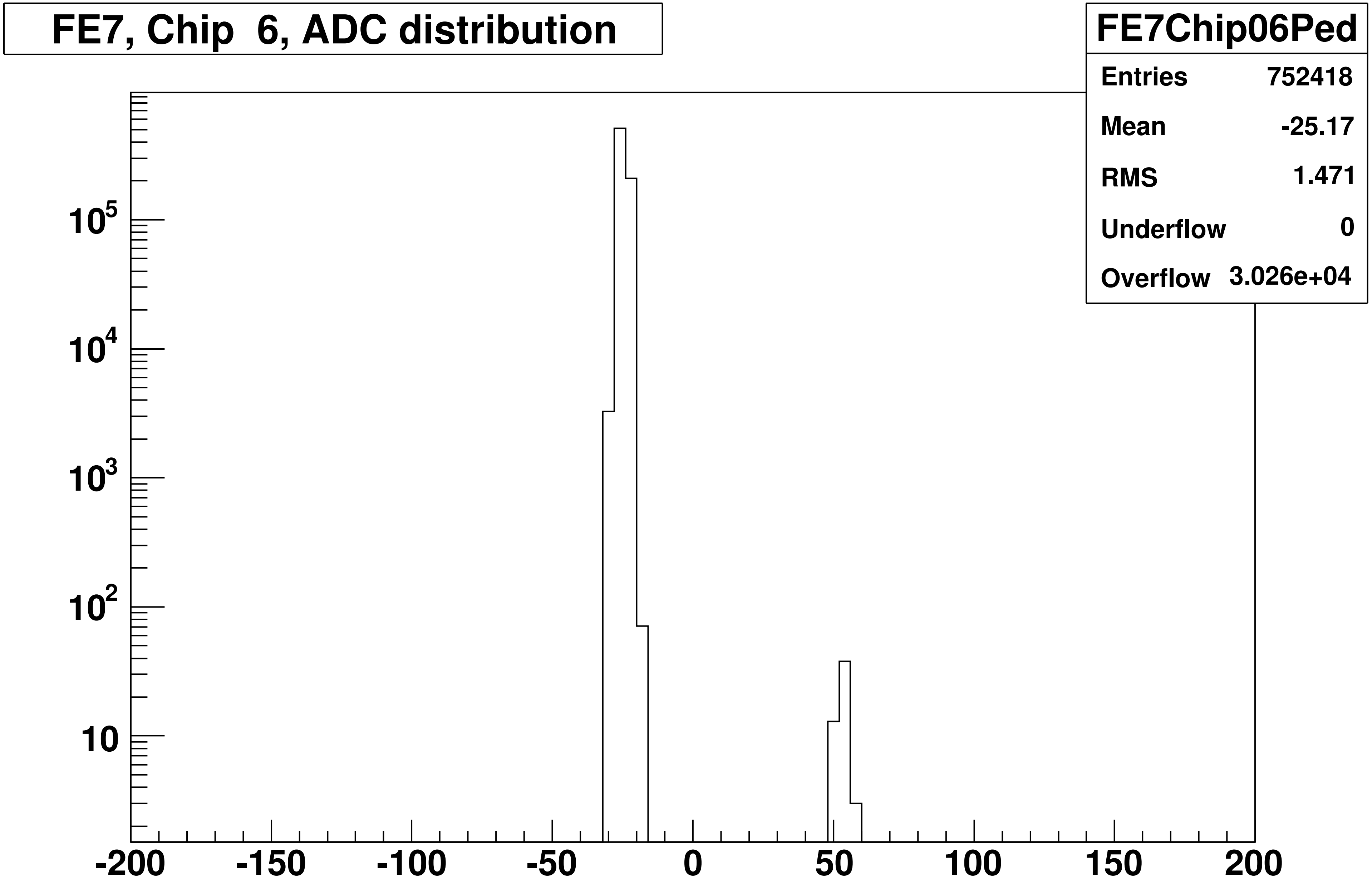
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-5000
0
5000
10000
15000
20000
25000

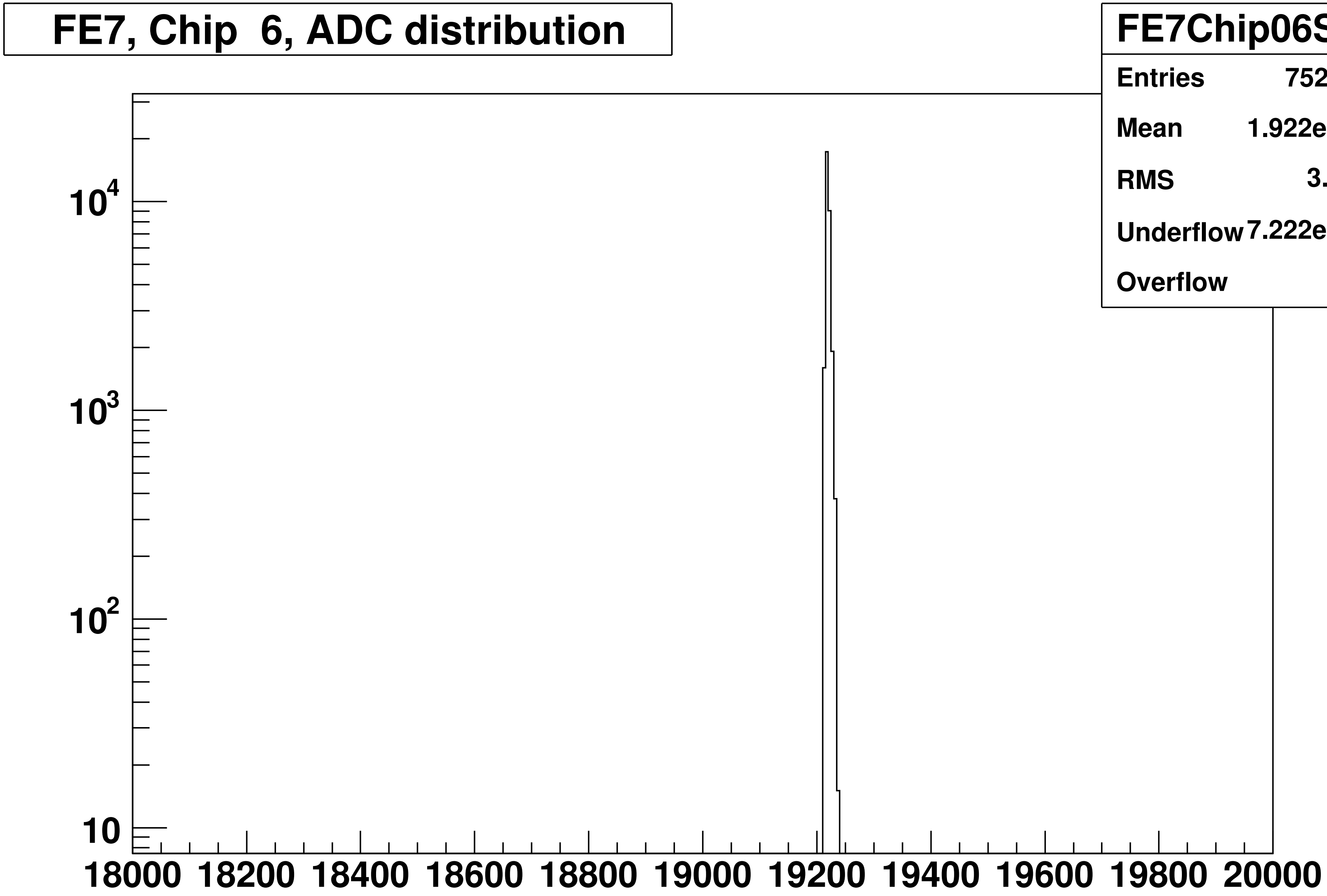

FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 752418
Mean -8.33
RMS 1.347
Underflow 0
Overflow 3.611e+04
$10^5$
$10^4$
$10^3$
-200
-150
-100
-50
0
50
100
150
200

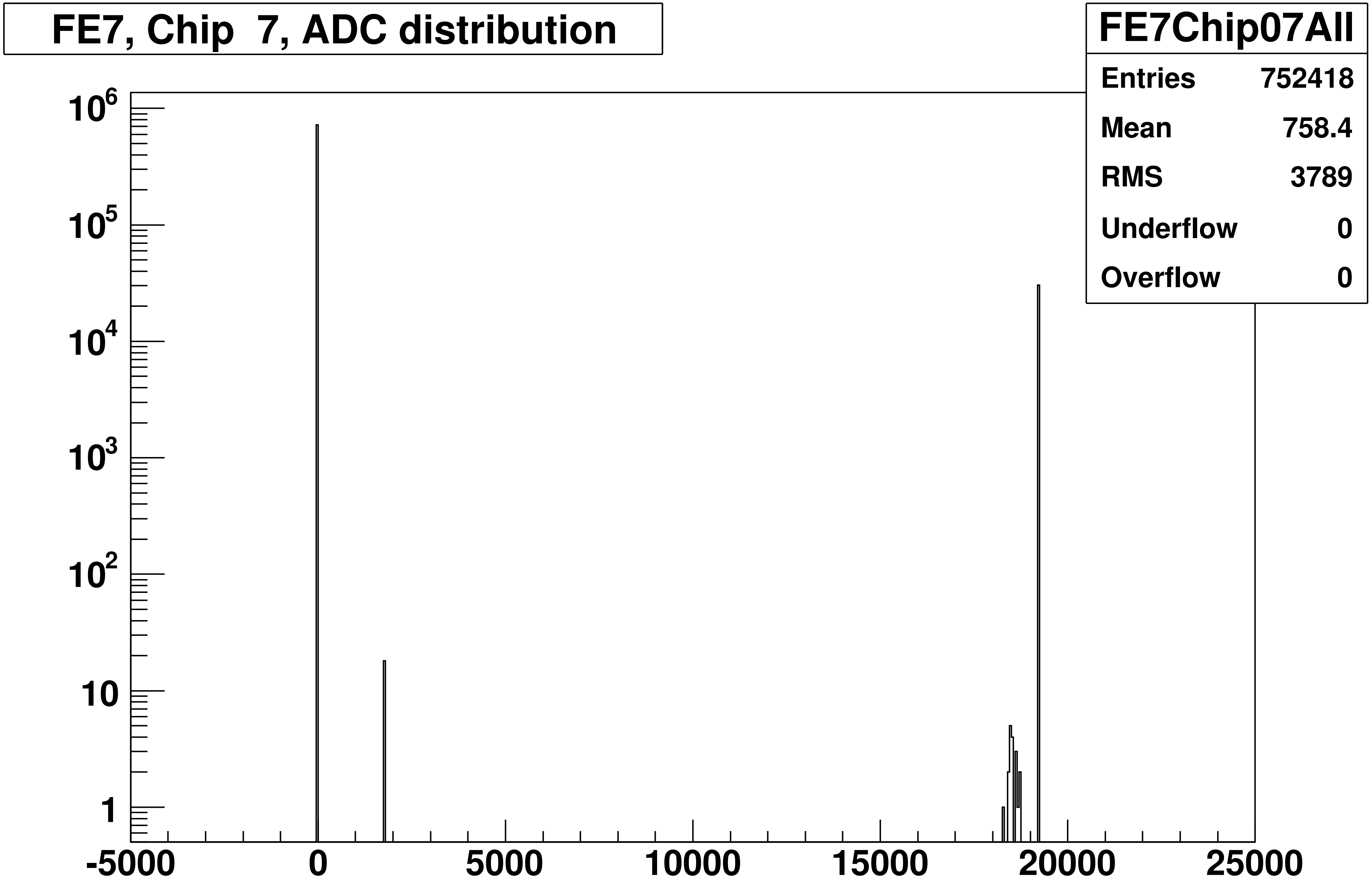

FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 752418
Mean 1.922e+04
RMS 9.334
Underflow 7.163e+05
Overflow 0
10^4
10^3
10^2
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

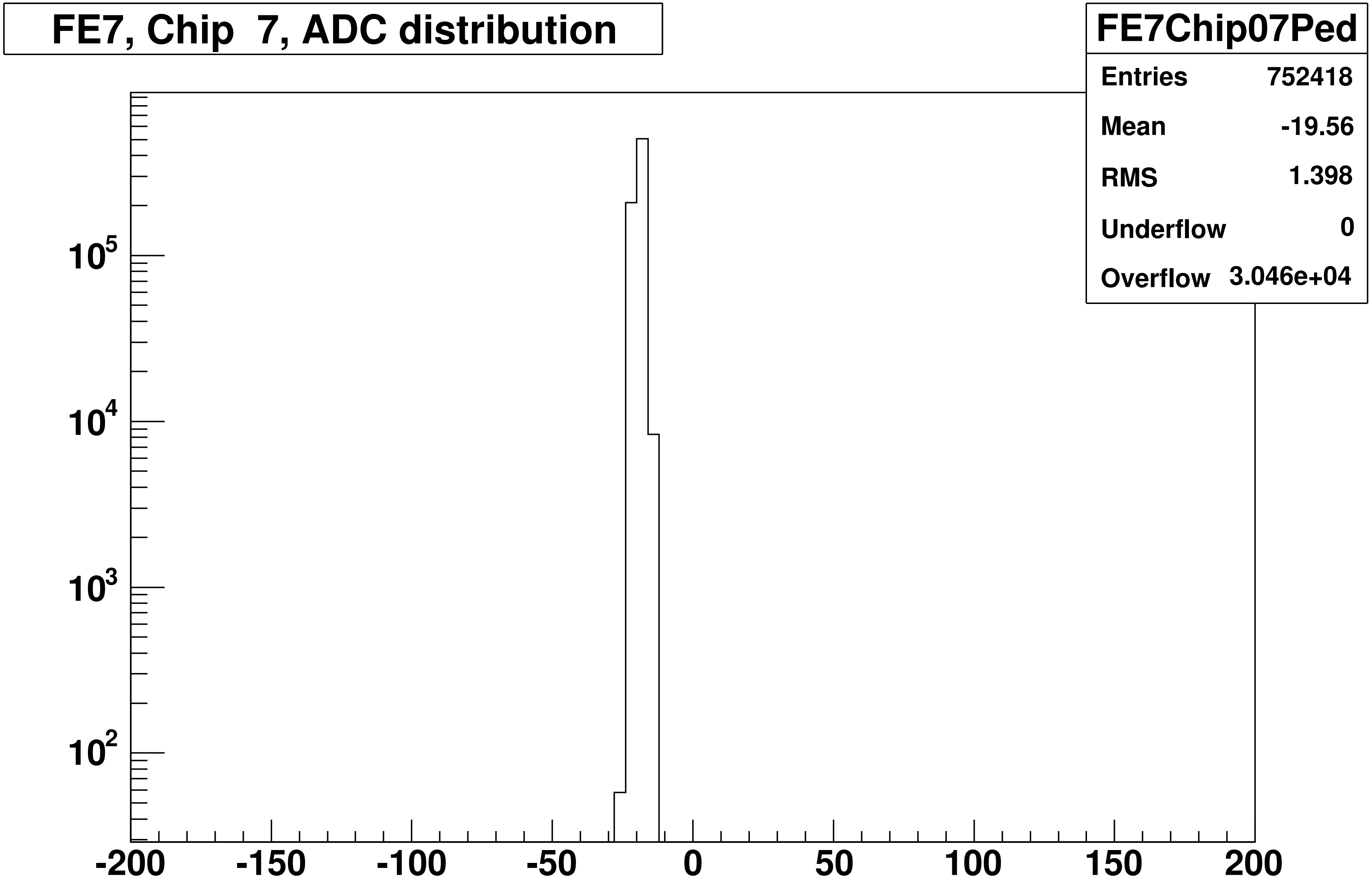


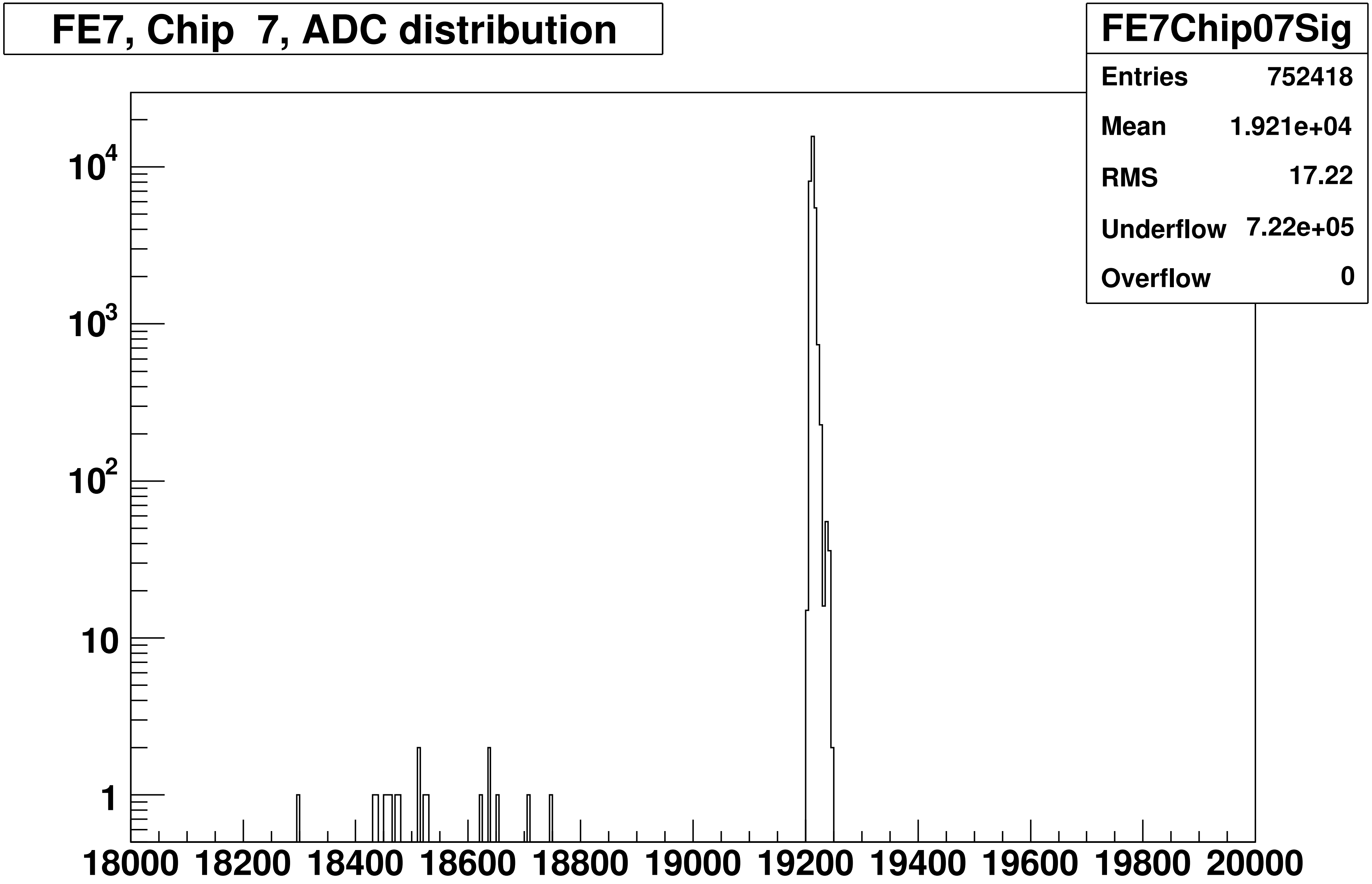
FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 752418
Mean 748.7
RMS 3781
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 752418
Mean -25.17
RMS 1.471
Underflow 0
Overflow 3.026e+04
$10^5$
$10^4$
$10^3$
$10^2$
10
-200
-150
-100
-50
0
50
100
150
200

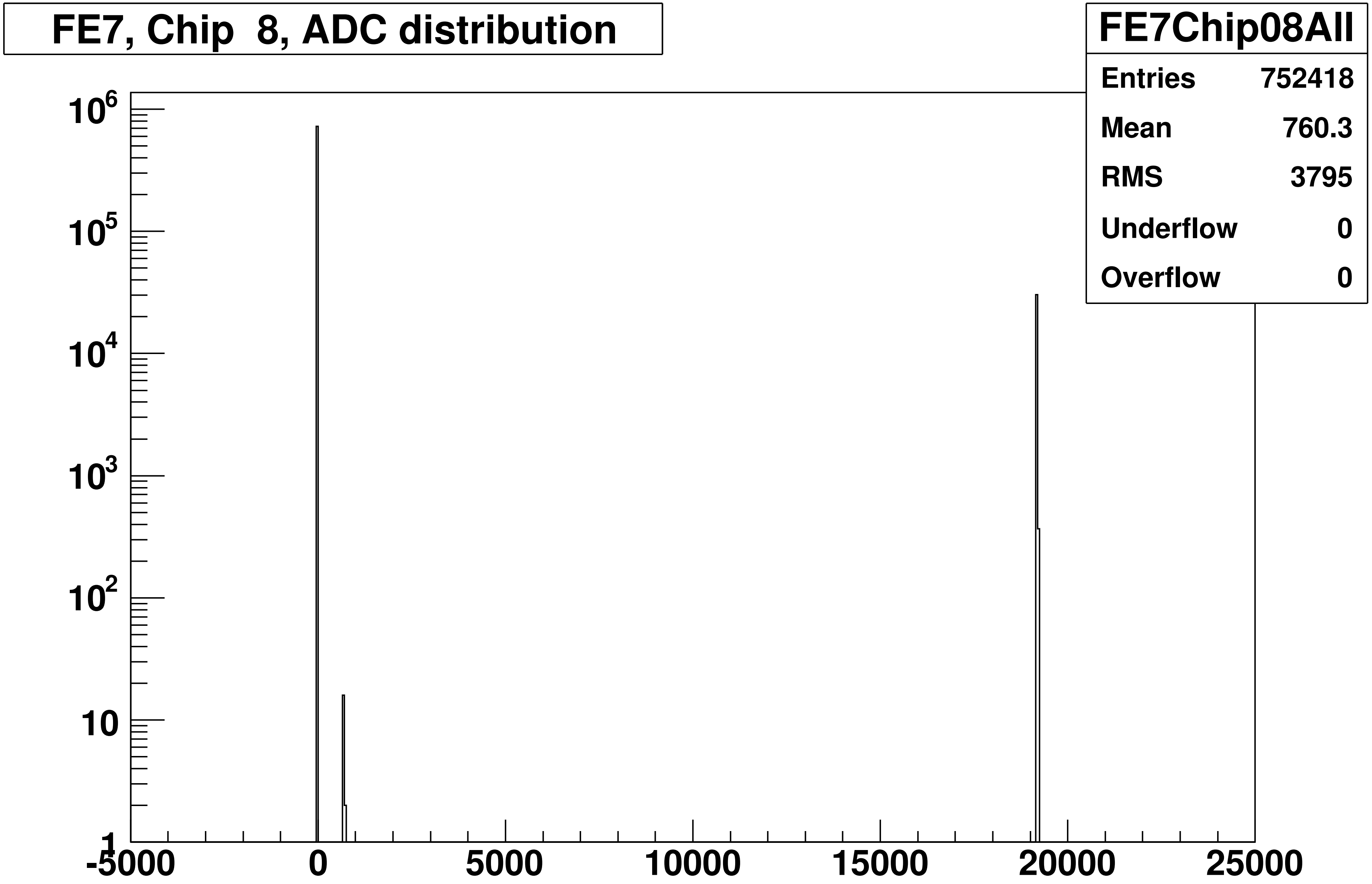

FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 752418
Mean 1.922e+04
RMS 3.566
Underflow 7.222e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

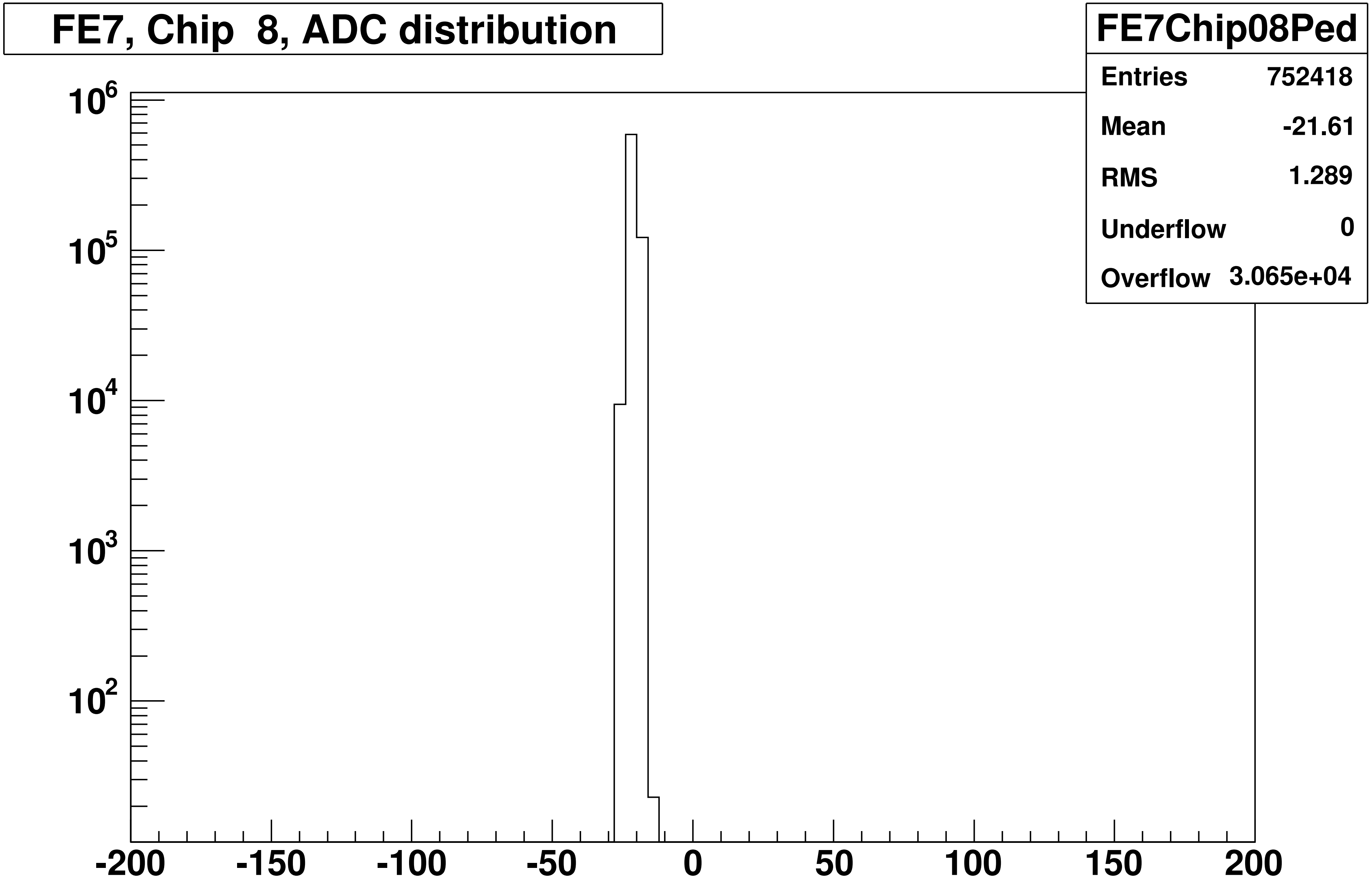

FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 752418
Mean 758.4
RMS 3789
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-5000
0
5000
10000
15000
20000
25000

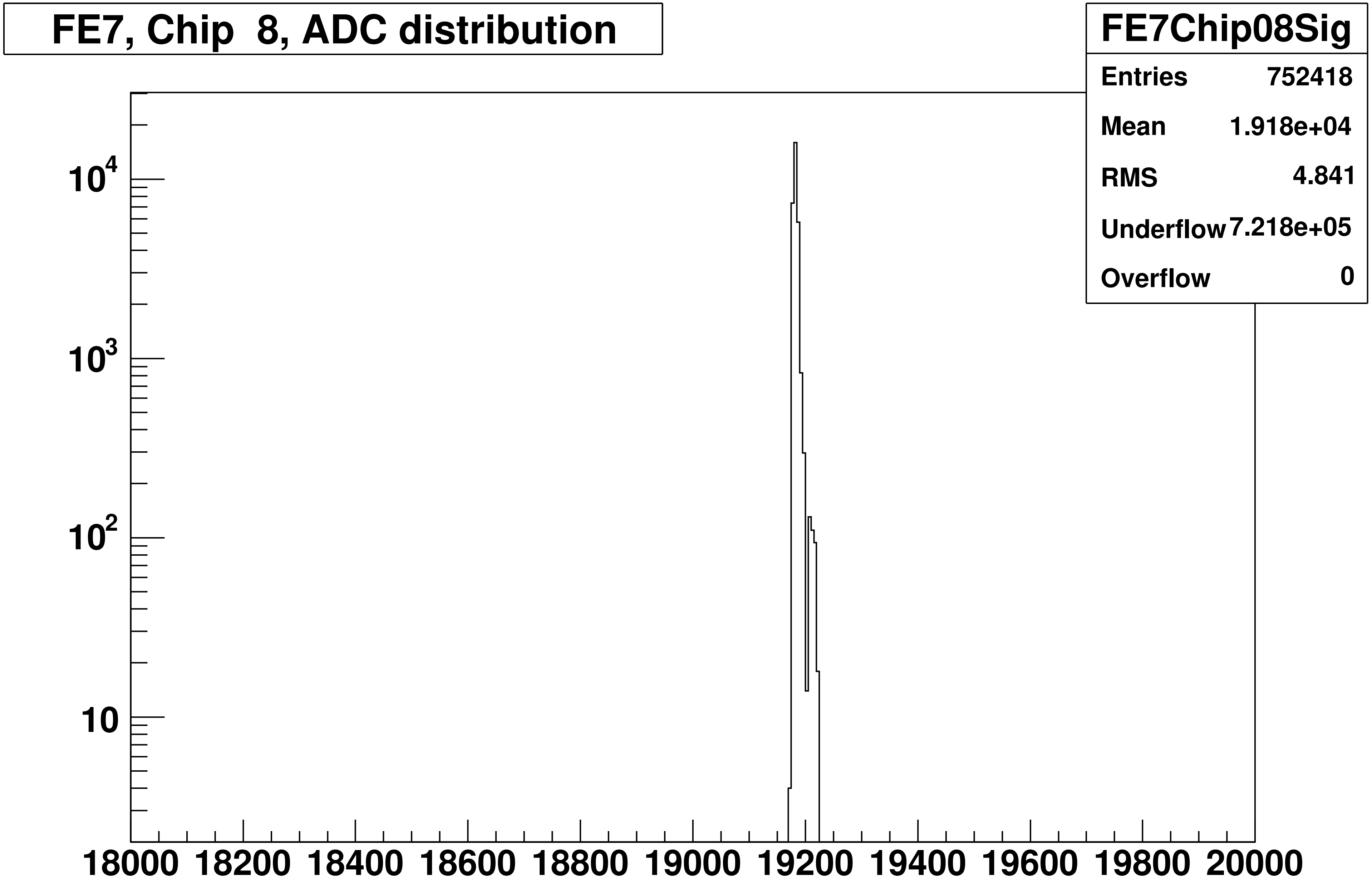

FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 752418
Mean -19.56
RMS 1.398
Underflow 0
Overflow 3.046e+04
$10^5$
$10^4$
$10^3$
$10^2$
-200
-150
-100
-50
0
50
100
150
200

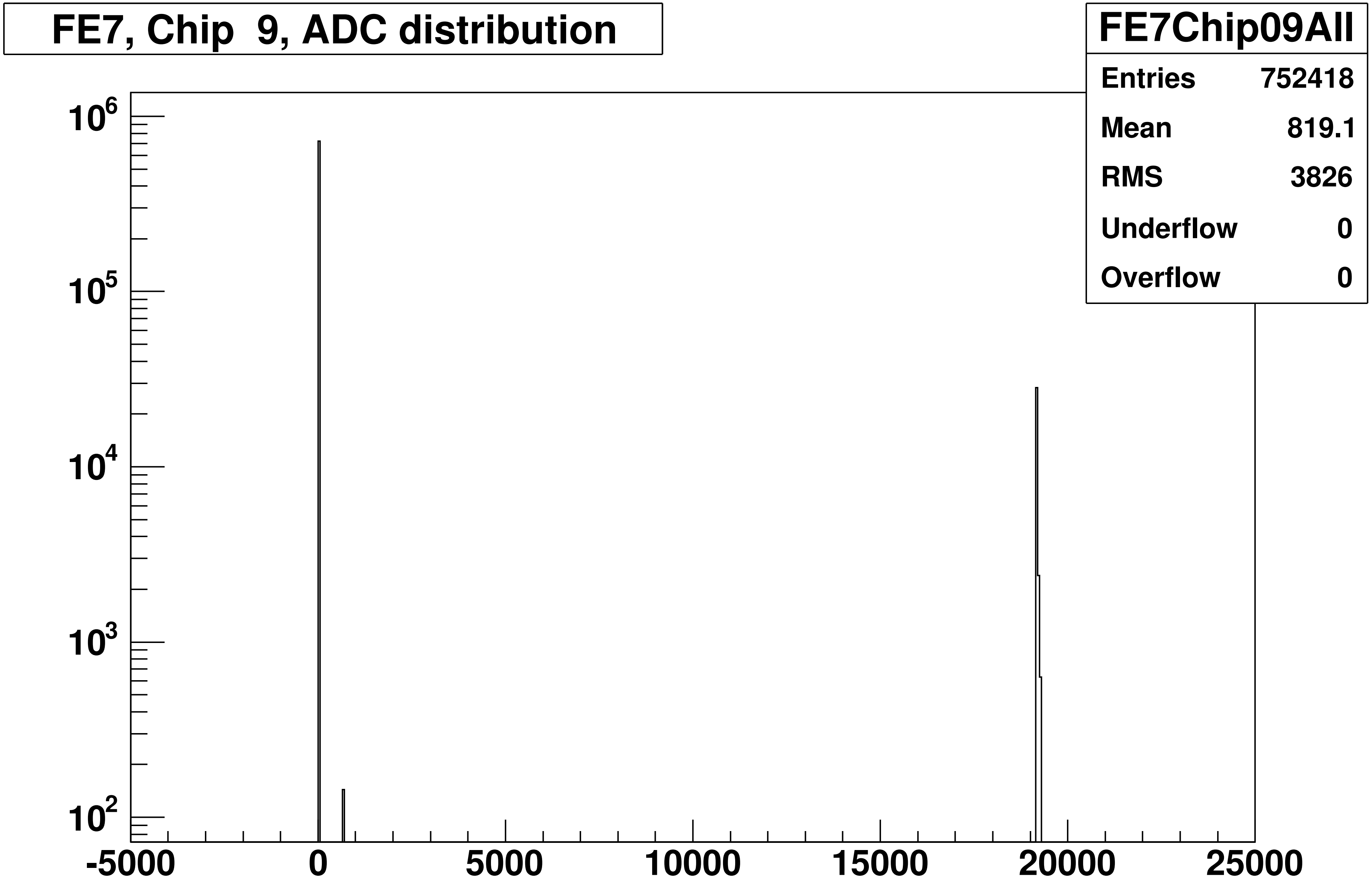


FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 752418
Mean 1.921e+04
RMS 17.22
Underflow 7.22e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

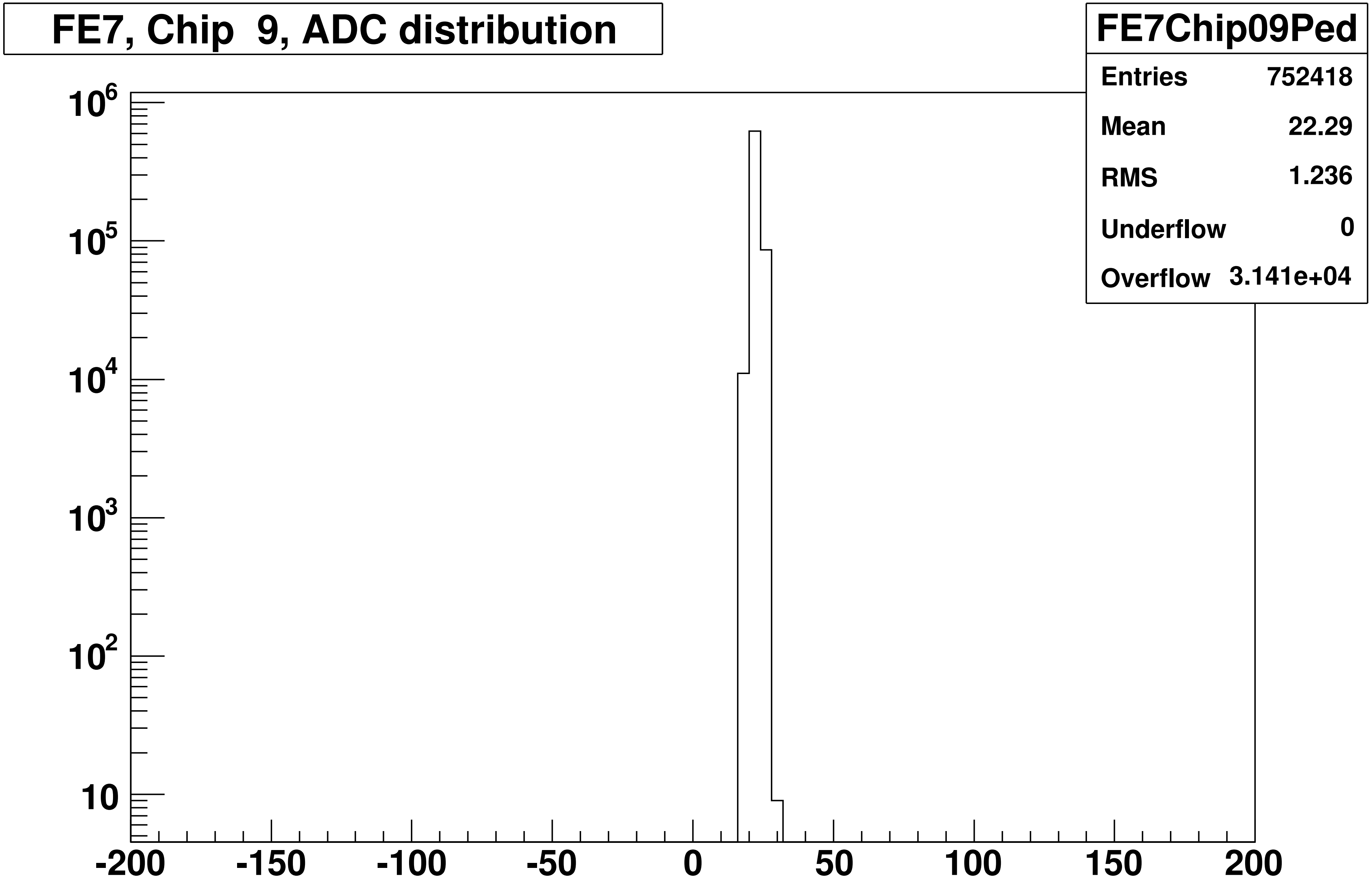

FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 752418
Mean 760.3
RMS 3795
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-5000
0
5000
10000
15000
20000
25000

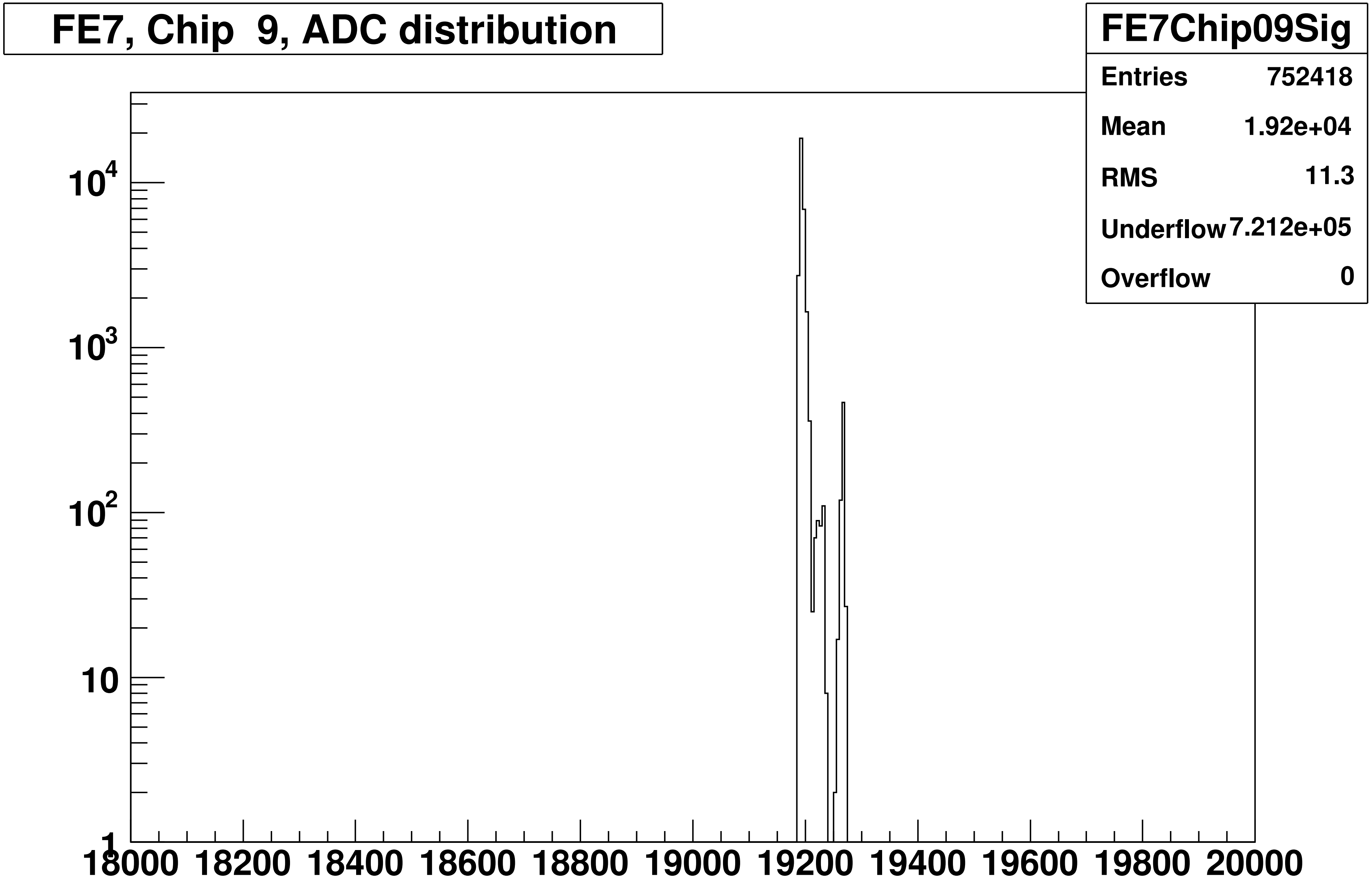

FE7, Chip  8, ADC distribution
FE7Chip08Ped
Entries 752418
Mean -21.61
RMS 1.289
Underflow 0
Overflow 3.065e+04
10^6
10^5
10^4
10^3
10^2
-200
-150
-100
-50
0
50
100
150
200


FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 752418
Mean 1.918e+04
RMS 4.841
Underflow 7.218e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

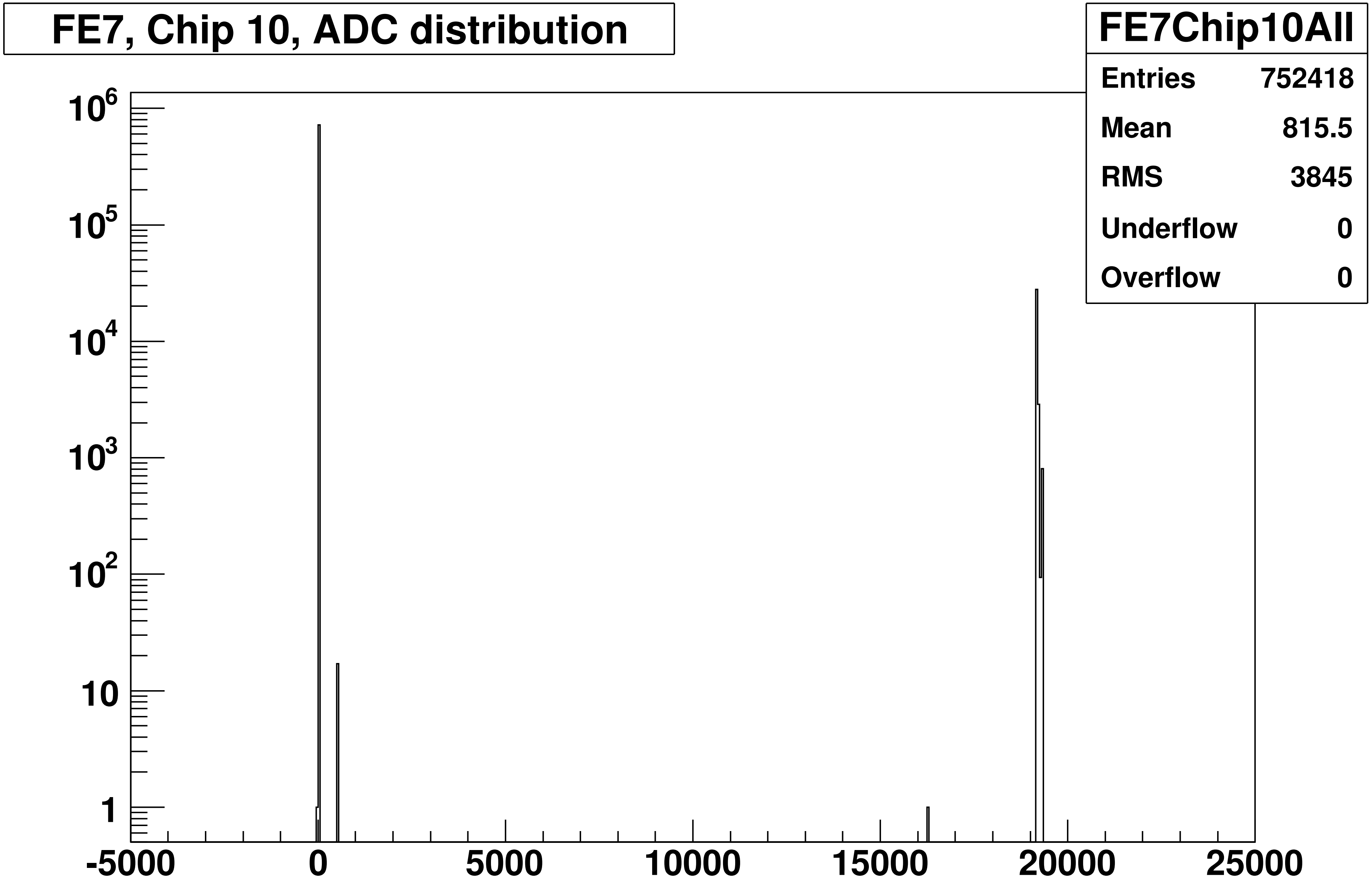


FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 752418
Mean 819.1
RMS 3826
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
-5000
0
5000
10000
15000
20000
25000

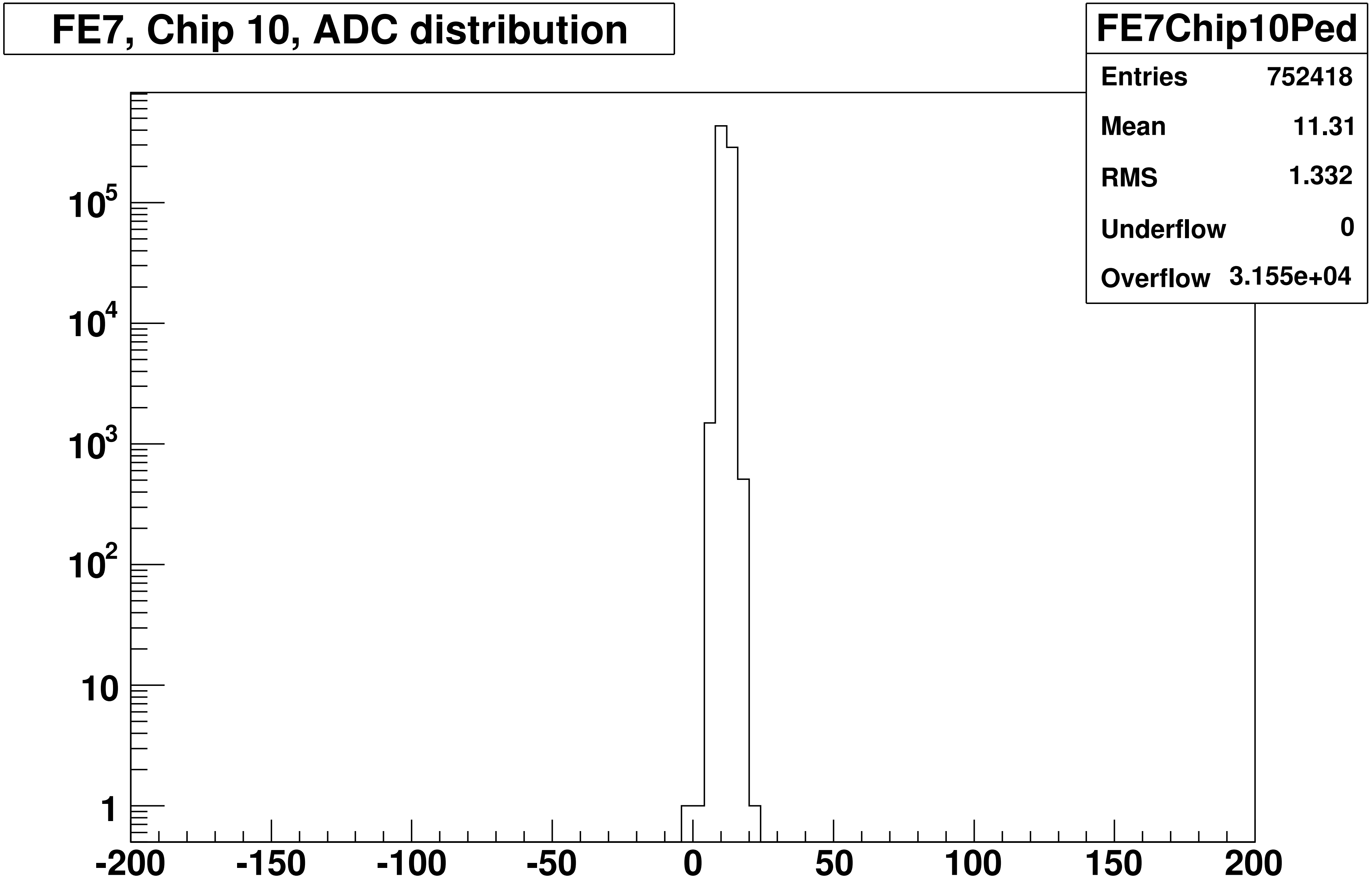

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 752418
Mean 22.29
RMS 1.236
Underflow 0
Overflow 3.141e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
-200
-150
-100
-50
0
50
100
150
200

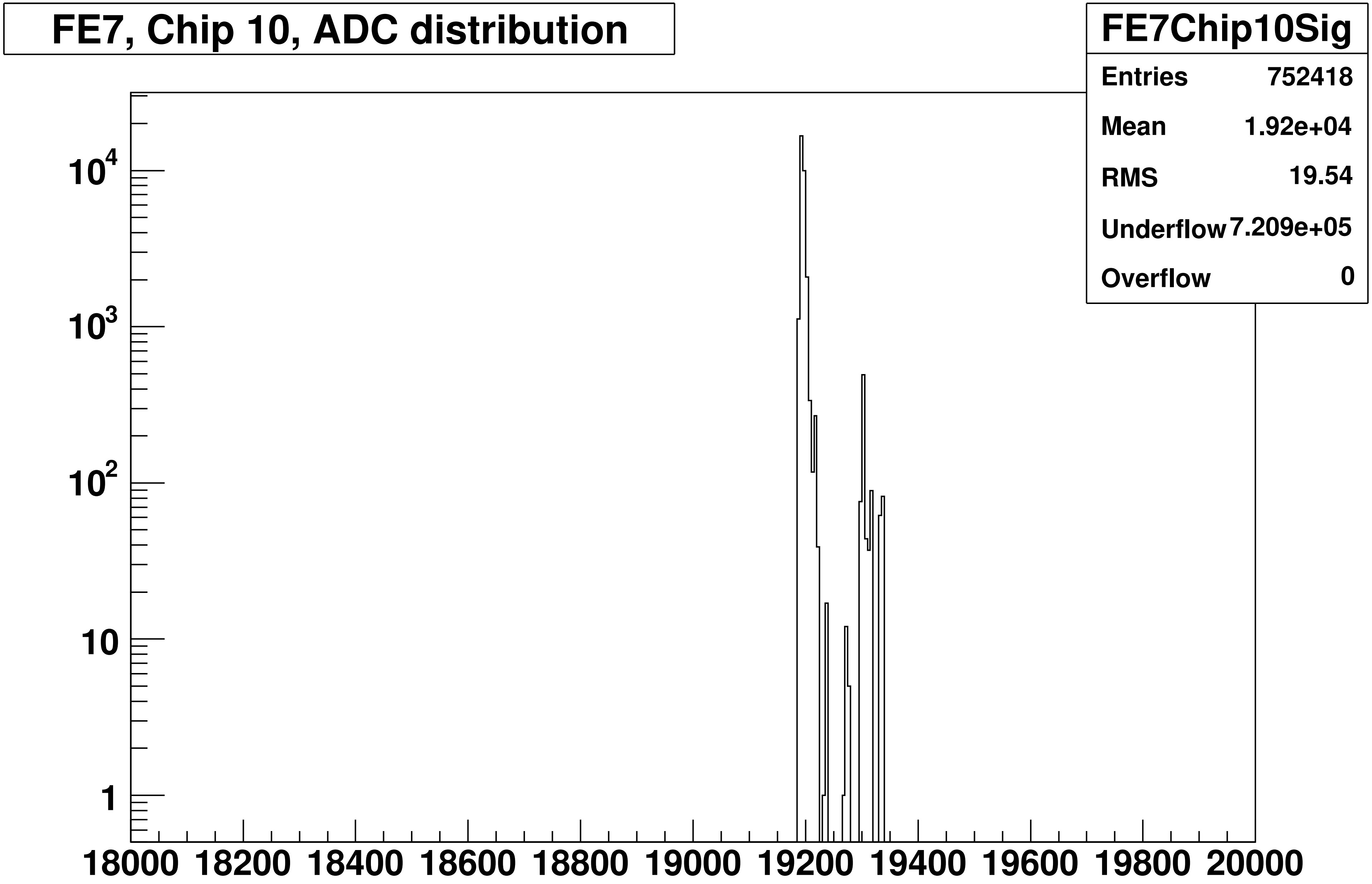


FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 752418
Mean 1.92e+04
RMS 11.3
Underflow 7.212e+05
Overflow 0
$10^4$
$10^3$
$10^2$
10
1
18000
18200
18400
18600
18800
19000
19200
19400
19600
19800
20000

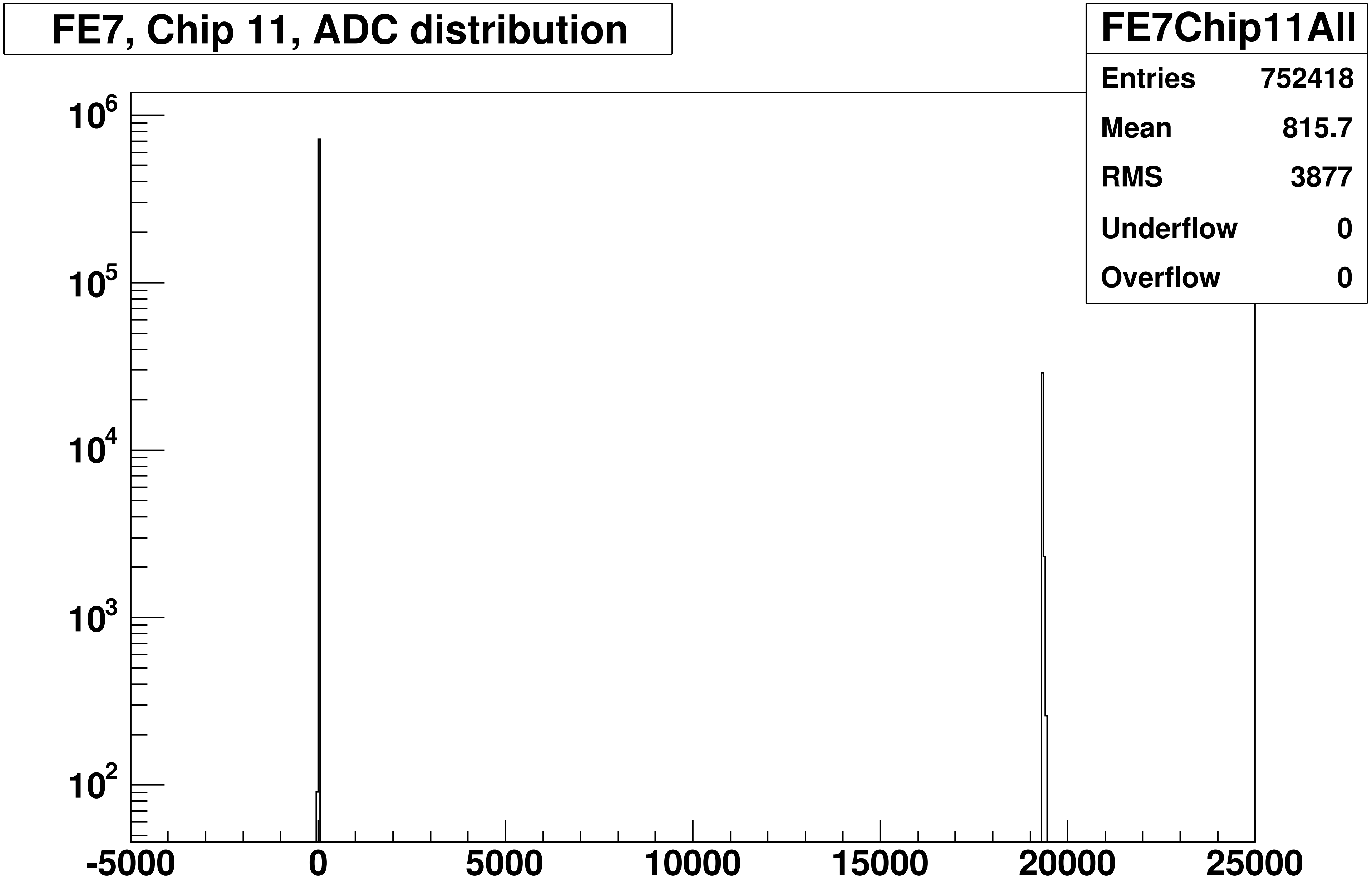


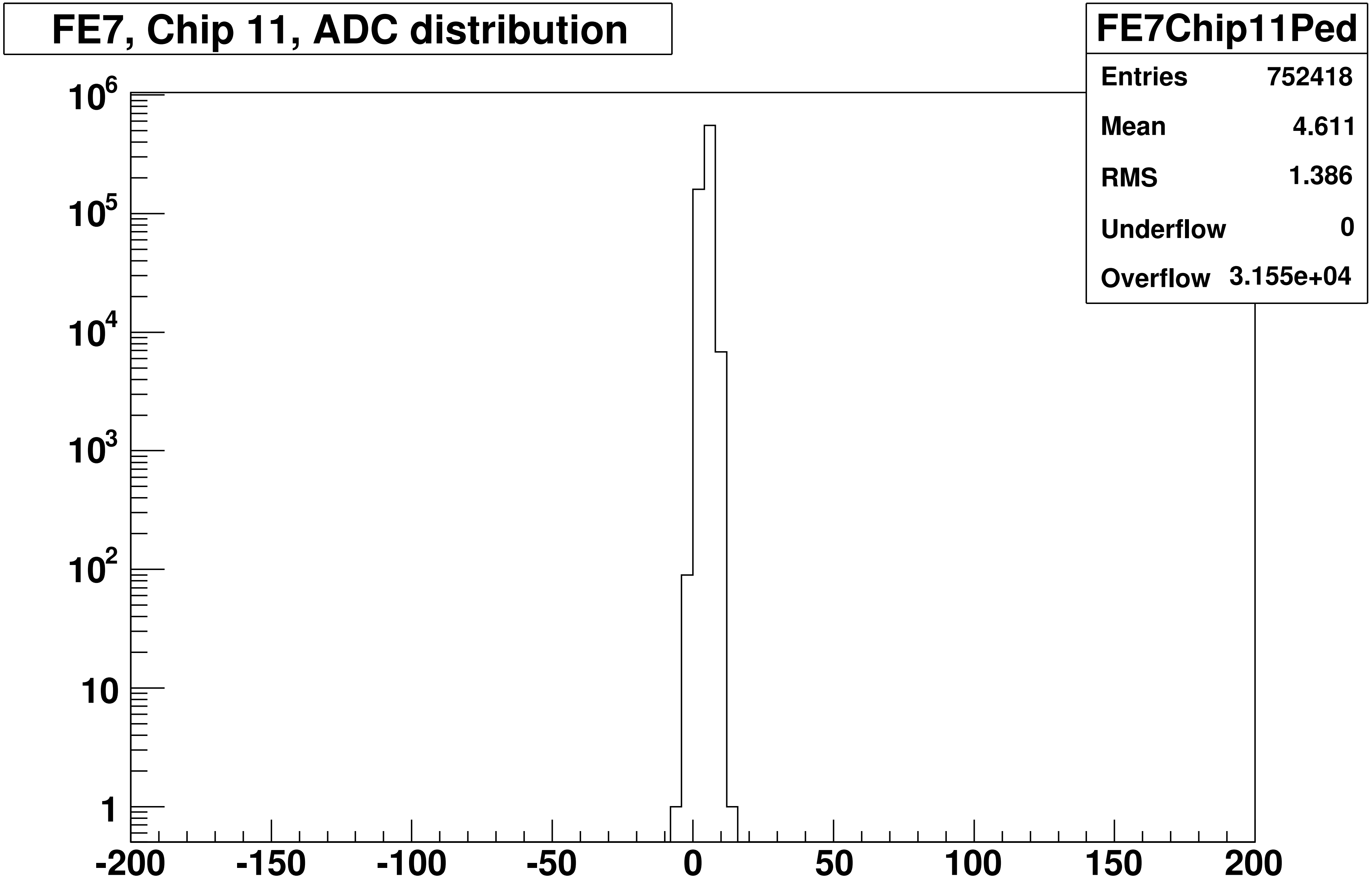
FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 752418
Mean 815.5
RMS 3845
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-5000
0
5000
10000
15000
20000
25000


FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 752418
Mean 11.31
RMS 1.332
Underflow 0
Overflow 3.155e+04
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-200
-150
-100
-50
0
50
100
150
200

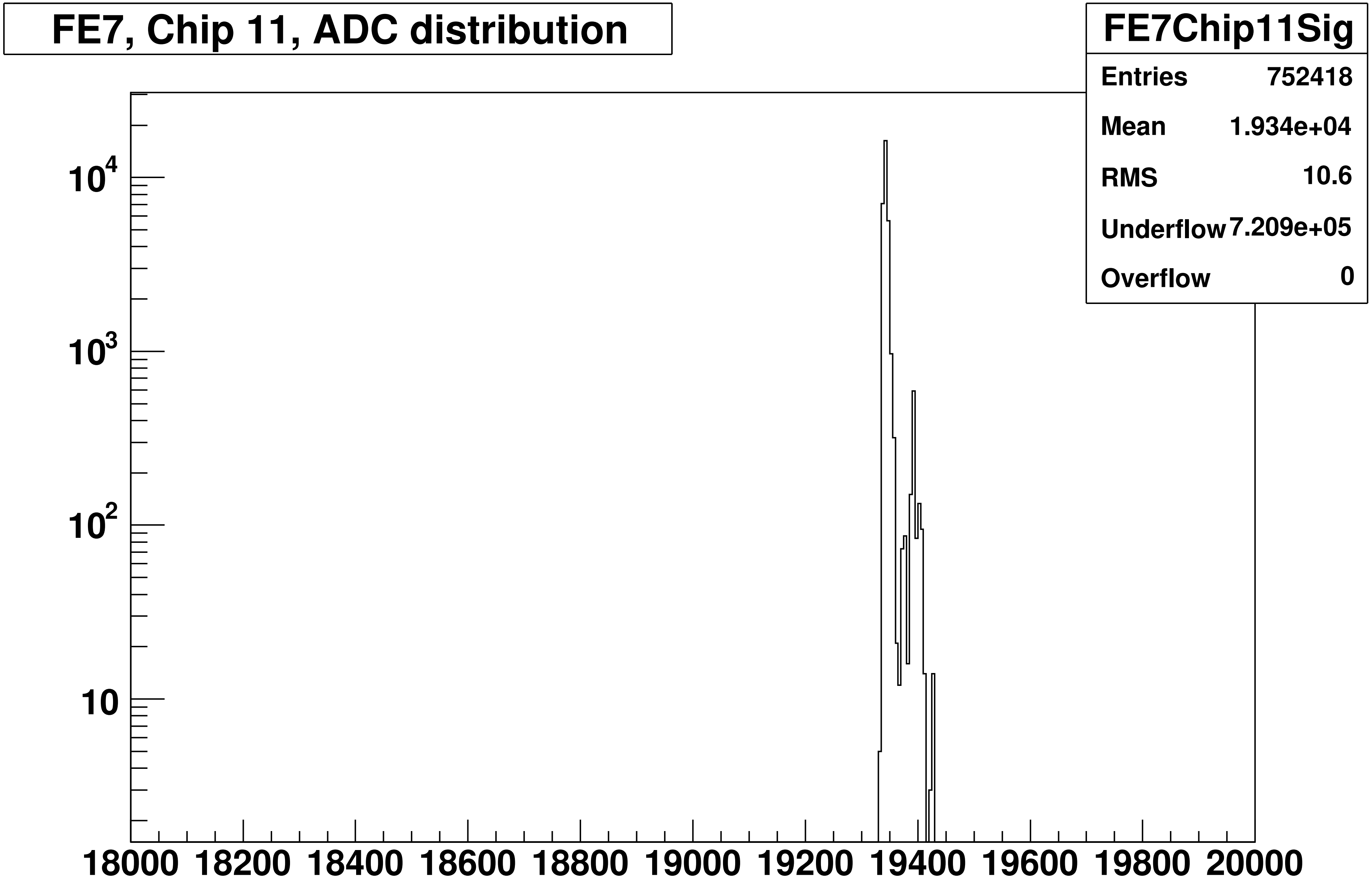


FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 752418
Mean 1.92e+04
RMS 19.54
Underflow 7.209e+05
Overflow 0
10^4
10^3
10^2
10
1
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

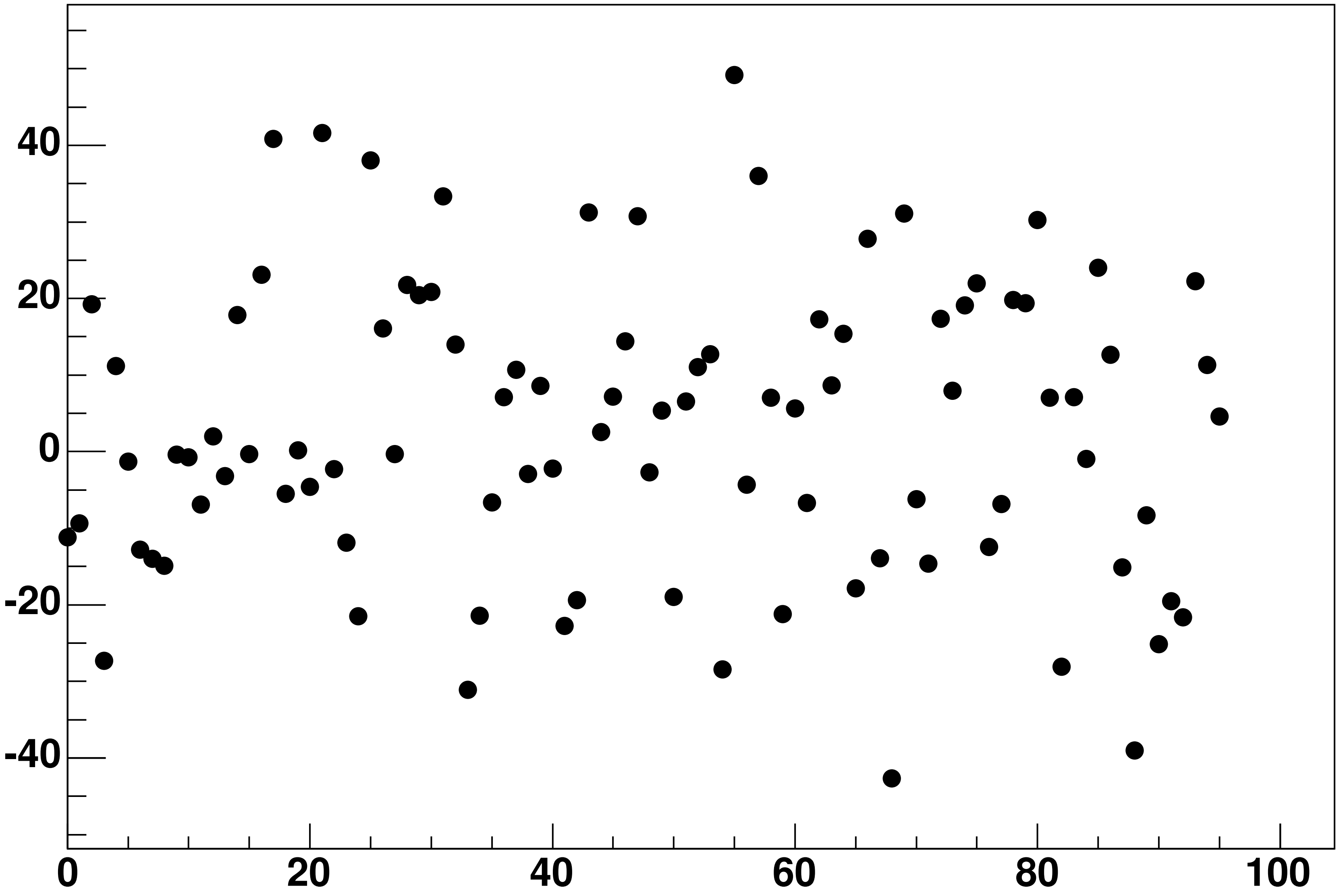

FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 752418
Mean 815.7
RMS 3877
Underflow 0
Overflow 0
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
-5000
0
5000
10000
15000
20000
25000

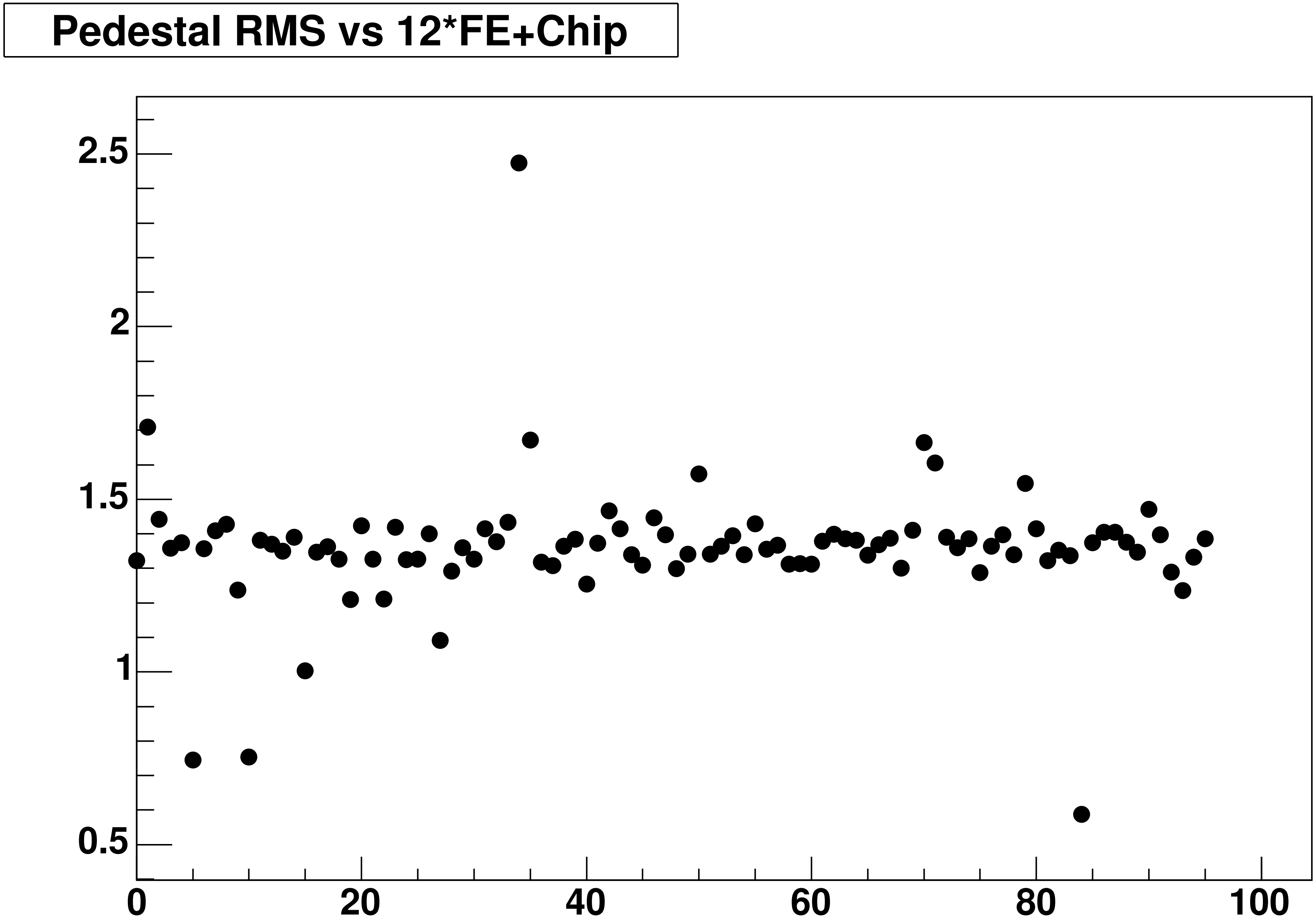


FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 752418
Mean 4.611
RMS 1.386
Underflow 0
Overflow 3.155e+04
$10^6$
$10^5$
$10^4$
$10^3$
$10^2$
10
1
-200
-150
-100
-50
0
50
100
150
200

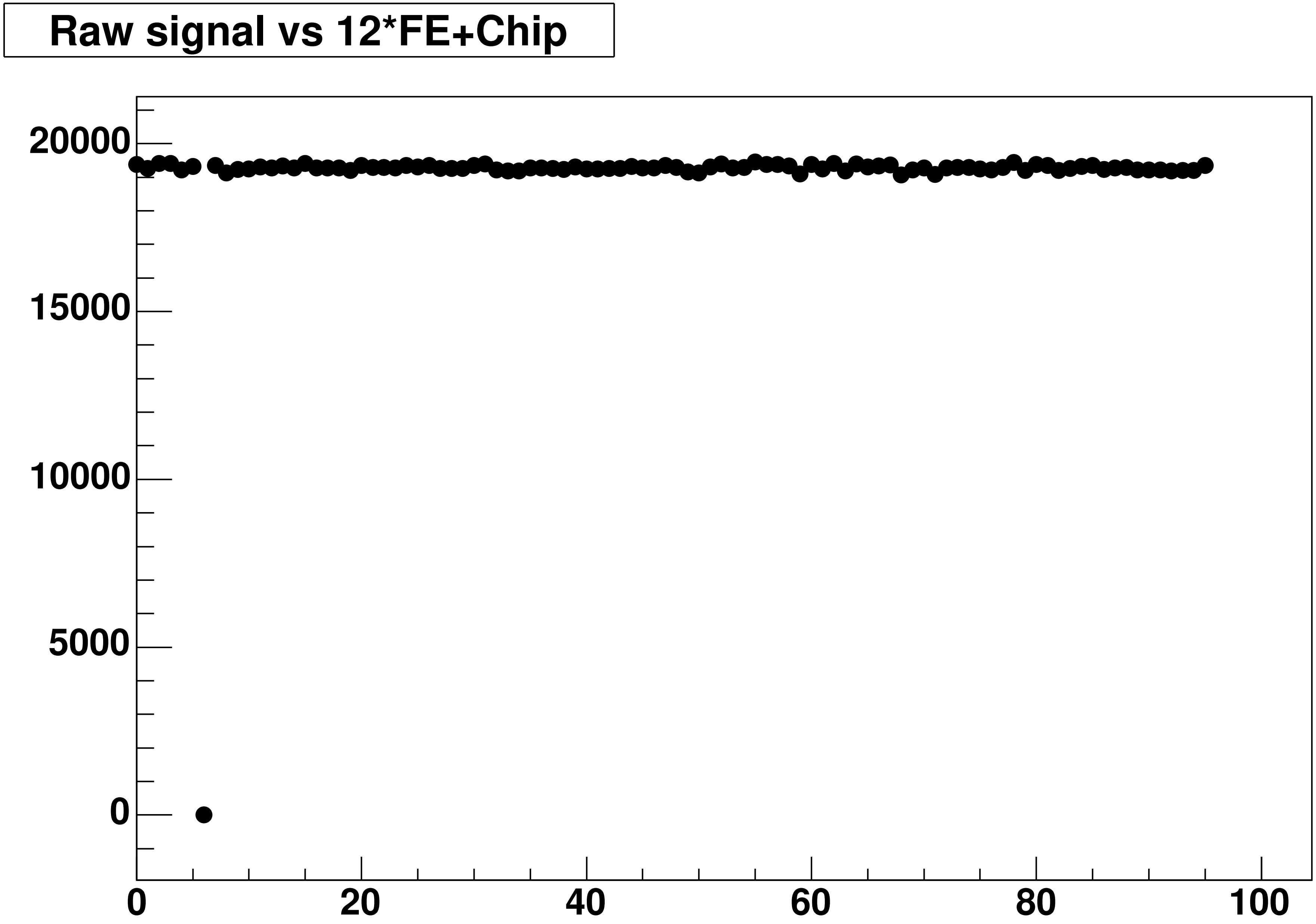

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 752418
Mean 1.934e+04
RMS 10.6
Underflow 7.209e+05
Overflow 0
10^4
10^3
10^2
10
18000 18200 18400 18600 18800 19000 19200 19400 19600 19800 20000

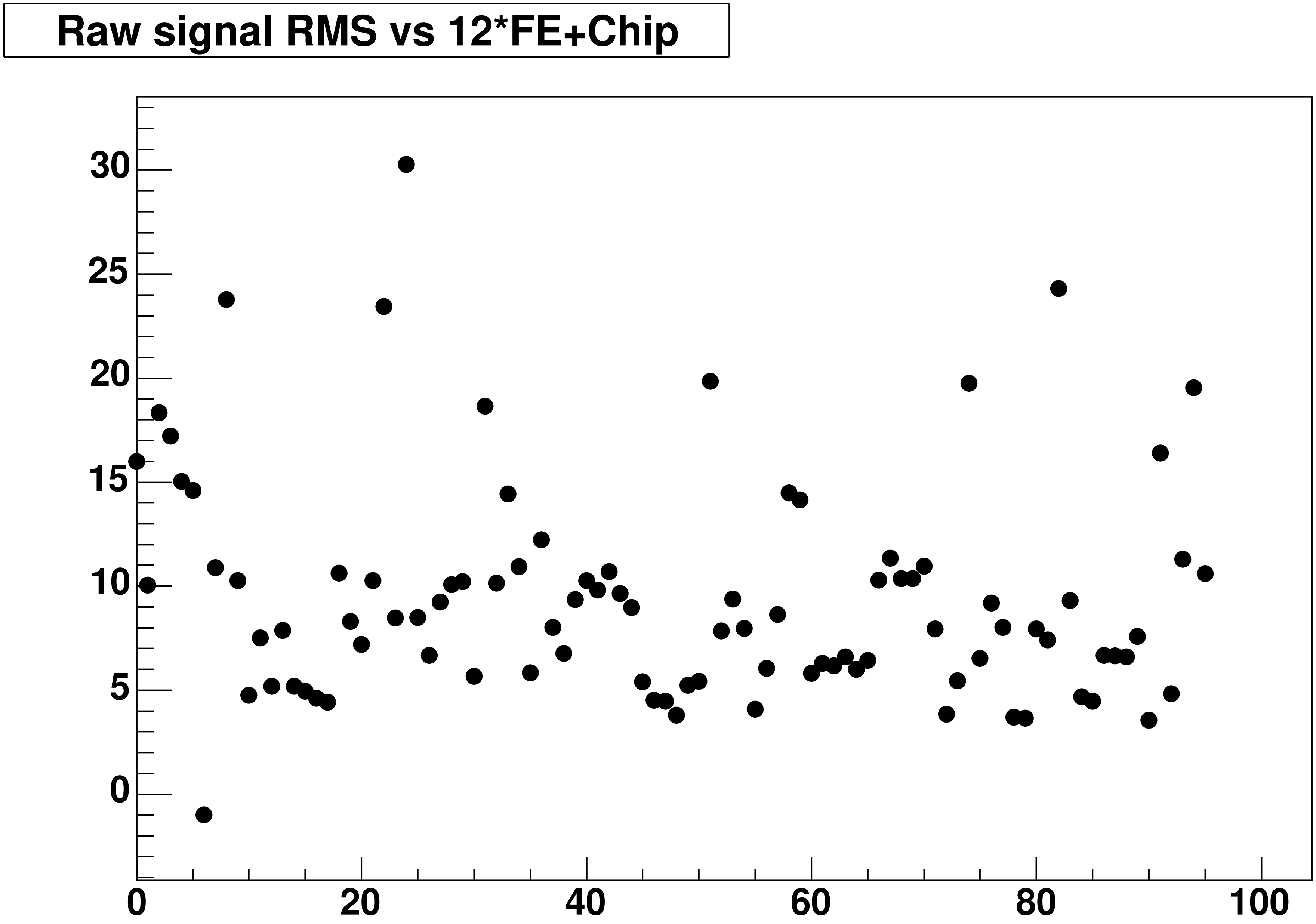
Pedestal vs 12*FE+Chip



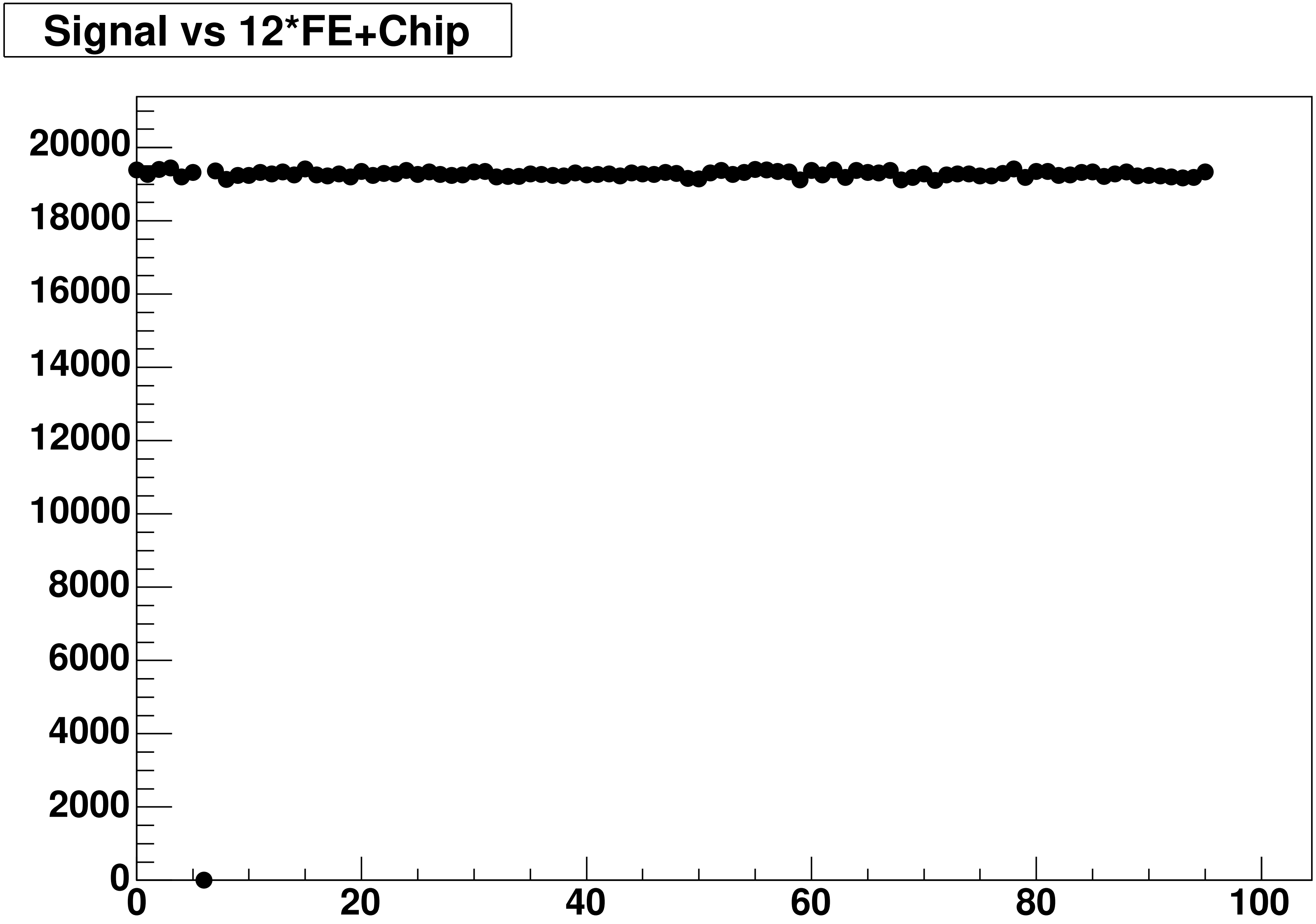


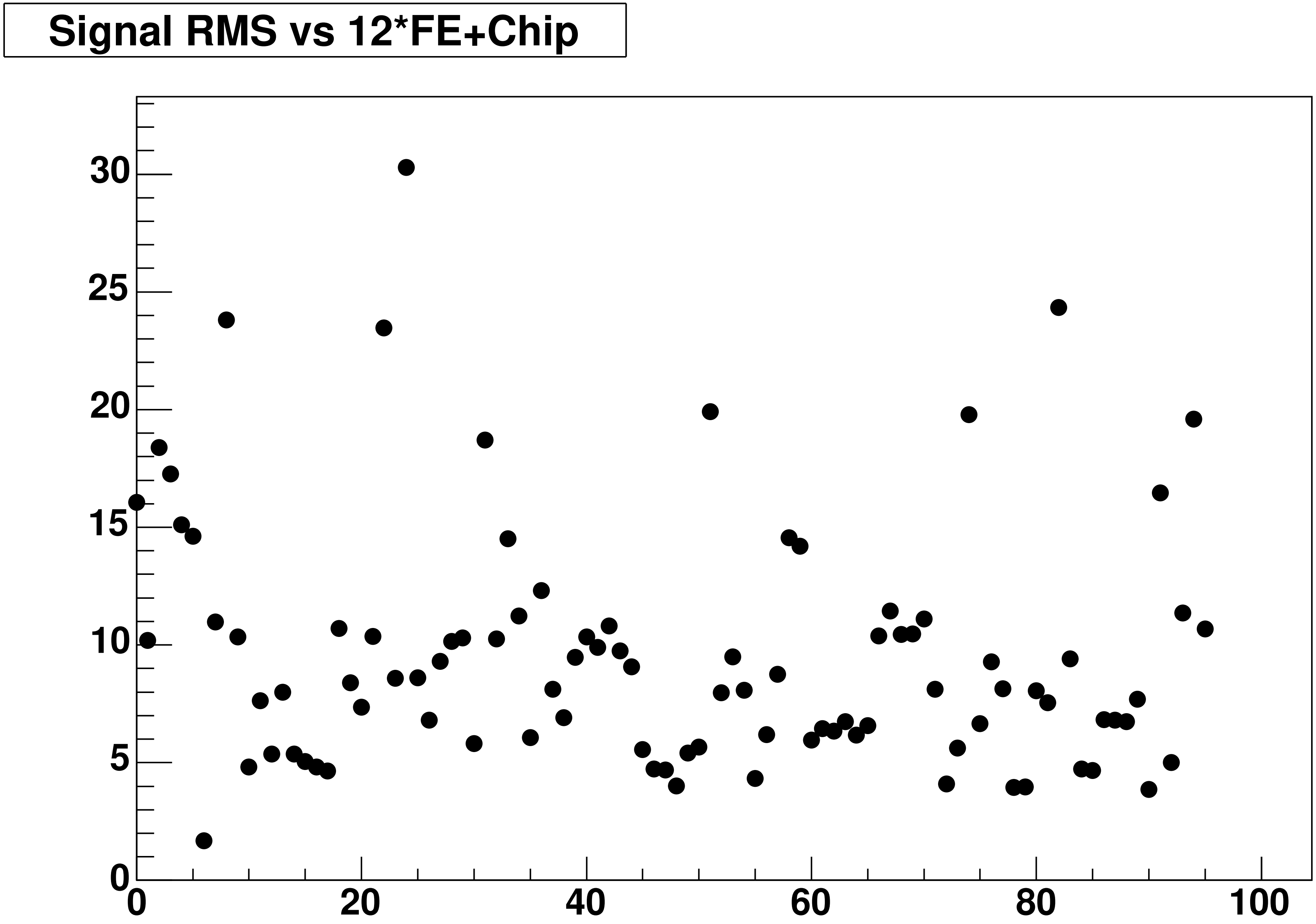
Pedestal RMS vs 12*FE+Chip
2.5
2
1.5
1
0.5
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
20000
15000
10000
5000
0
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
0
20
40
60
80
100


Signal vs 12*FE+Chip
20000
18000
16000
14000
12000
10000
8000
6000
4000
2000
0
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
0
5
10
15
20
25
30
0
20
40
60
80
100