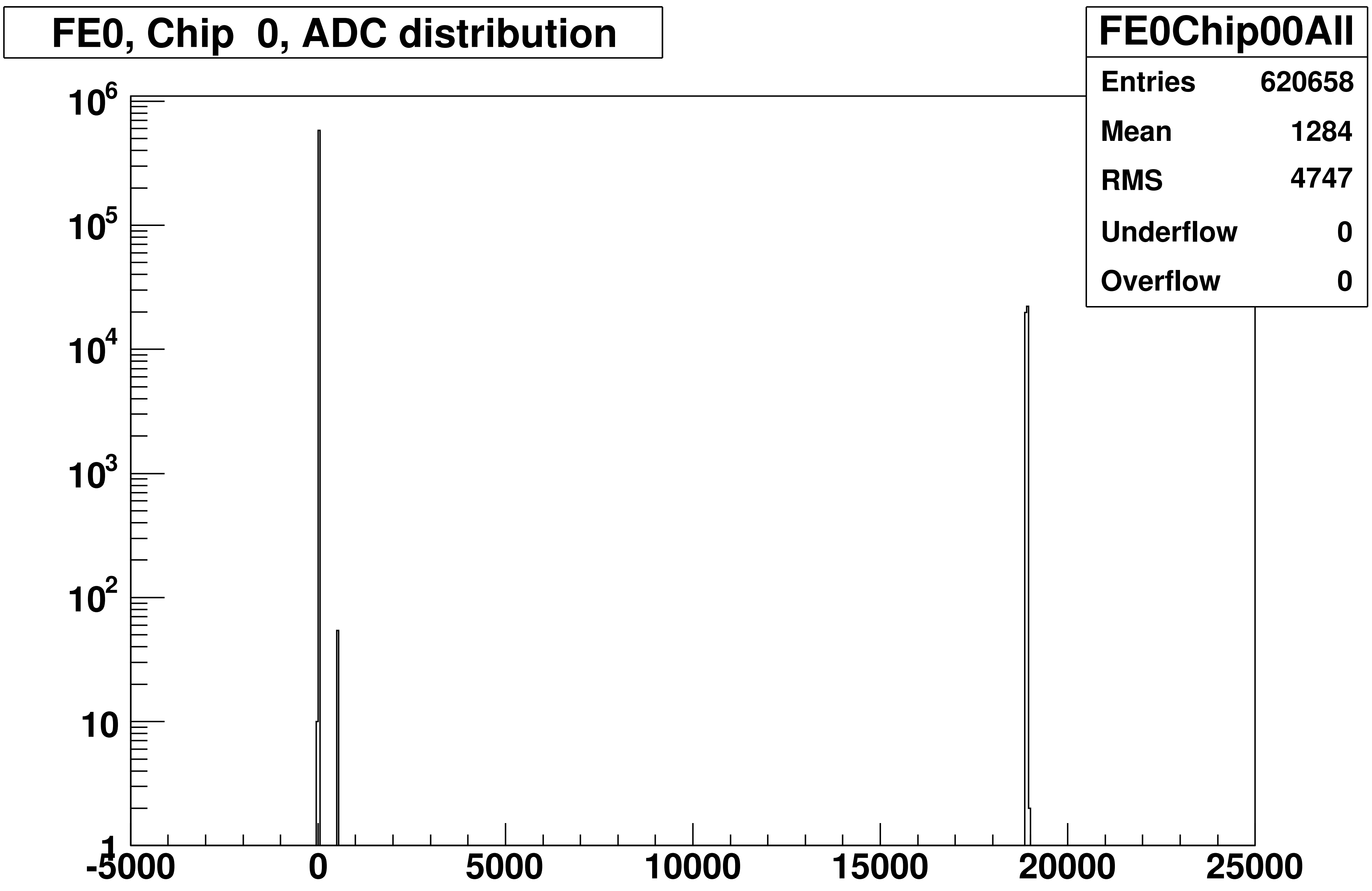
FE0, Chip 0, ADC distribution
FE0Chip00All
Entries 620658
Mean 1284
RMS 4747
Underflow 0
Overflow 0
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10^2
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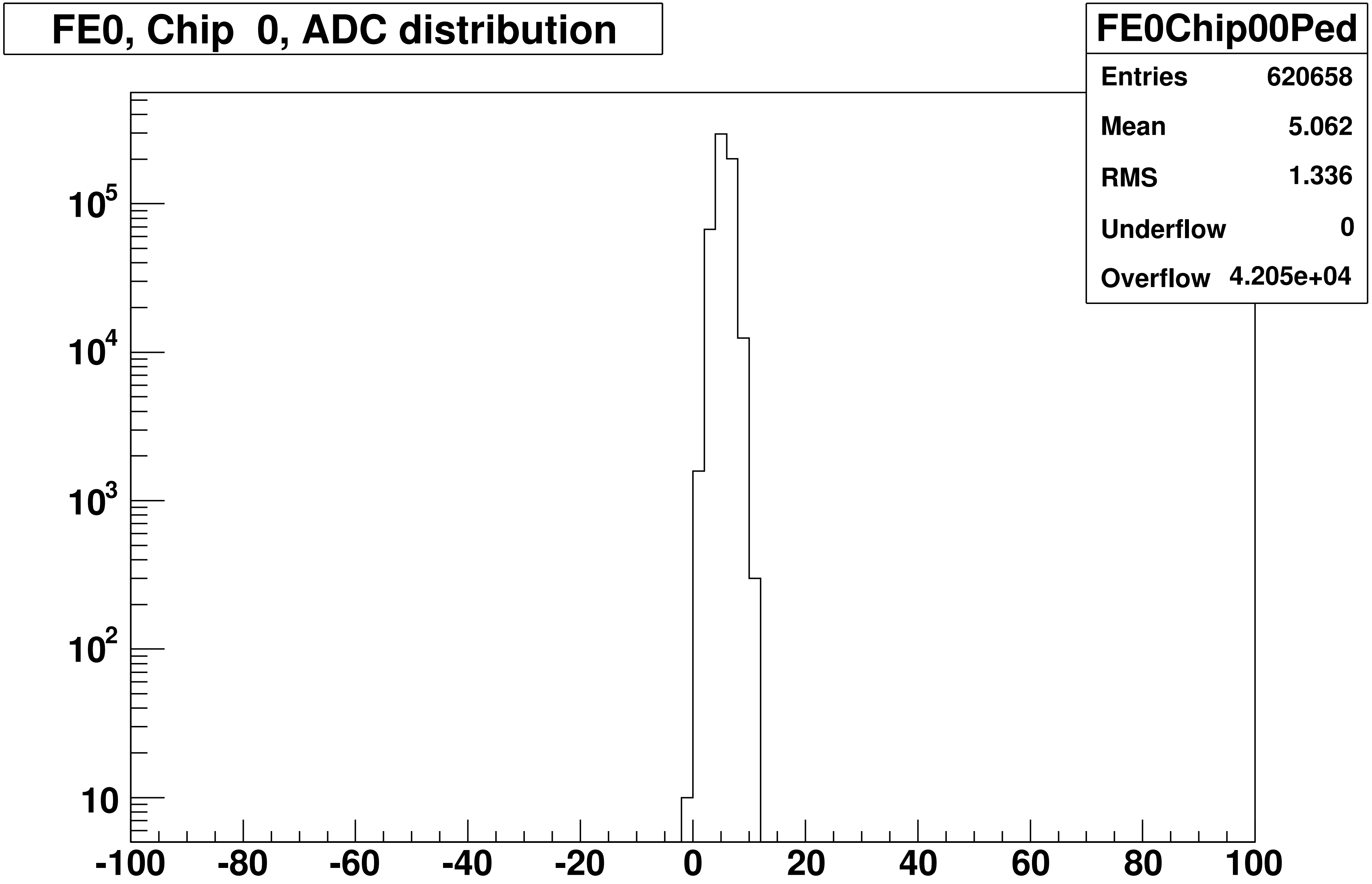
FE0, Chip 0, ADC distribution
FE0Chip00Ped
Entries 620658
Mean 5.062
RMS 1.336
Underflow 0
Overflow 4.205e+04
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10^2
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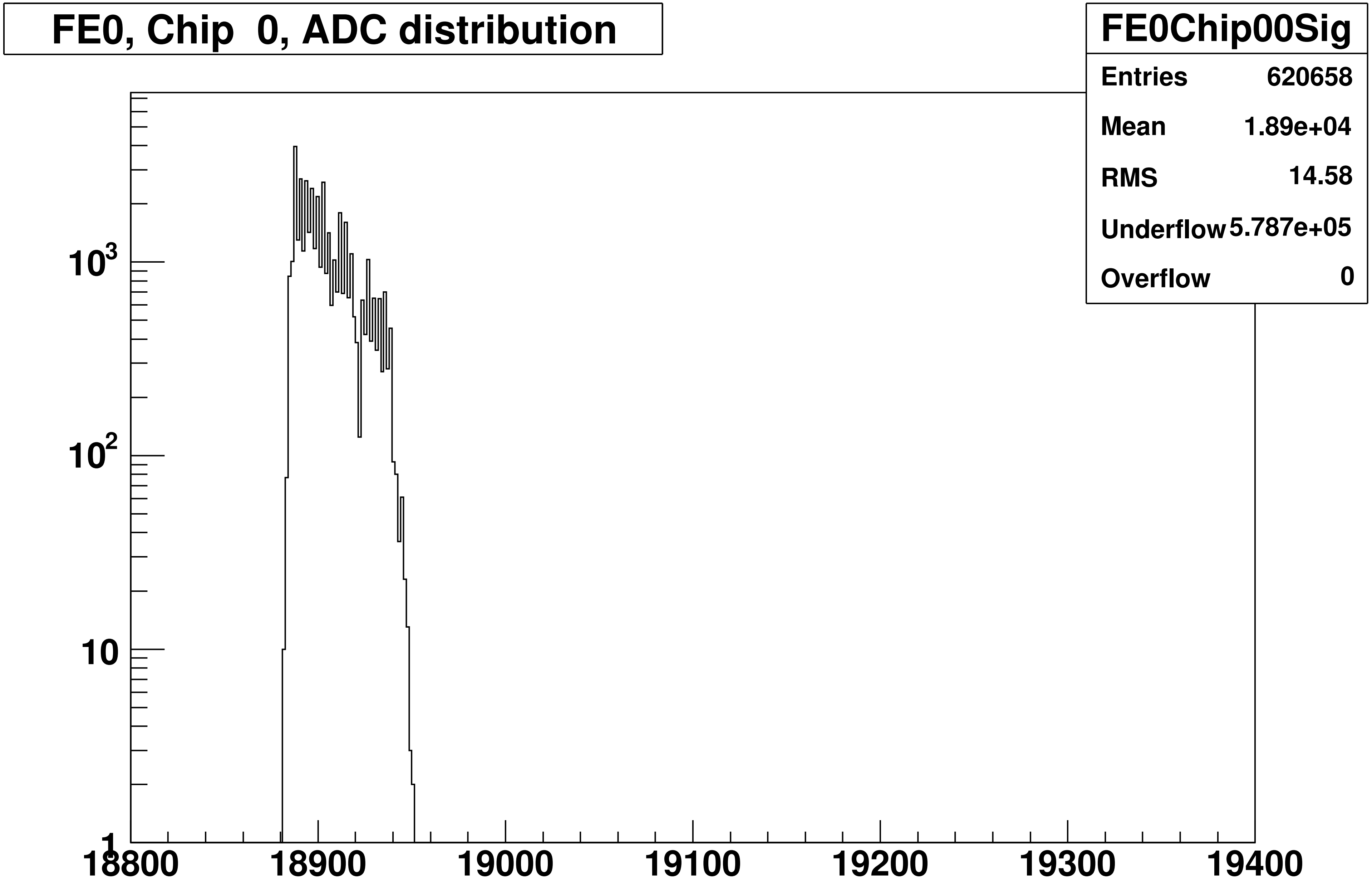

FE0, Chip 0, ADC distribution
FE0Chip00Sig
Entries 620658
Mean 1.89e+04
RMS 14.58
Underflow 5.787e+05
Overflow 0
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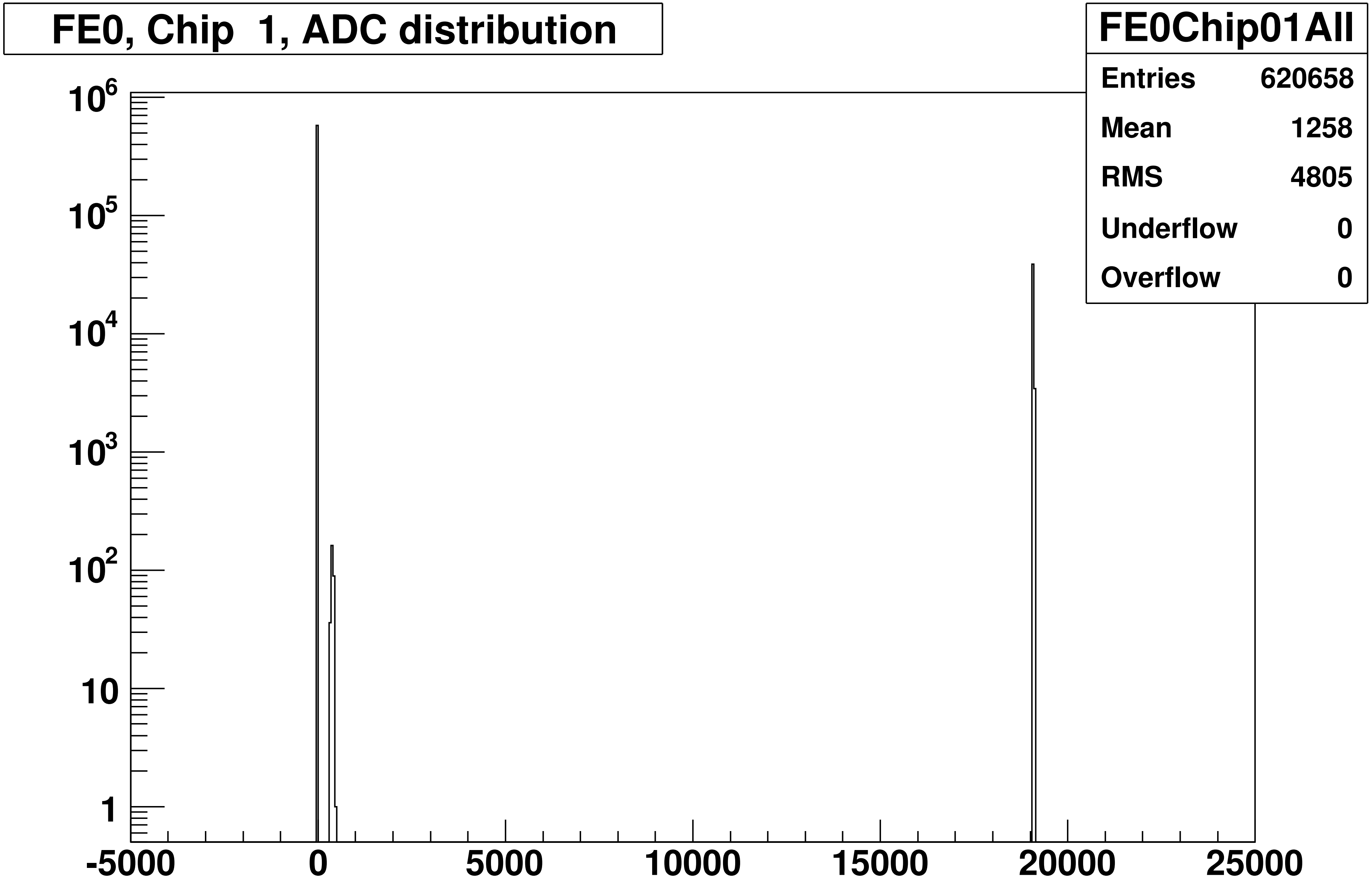
FE0, Chip 1, ADC distribution
FE0Chip01All
Entries 620658
Mean 1258
RMS 4805
Underflow 0
Overflow 0
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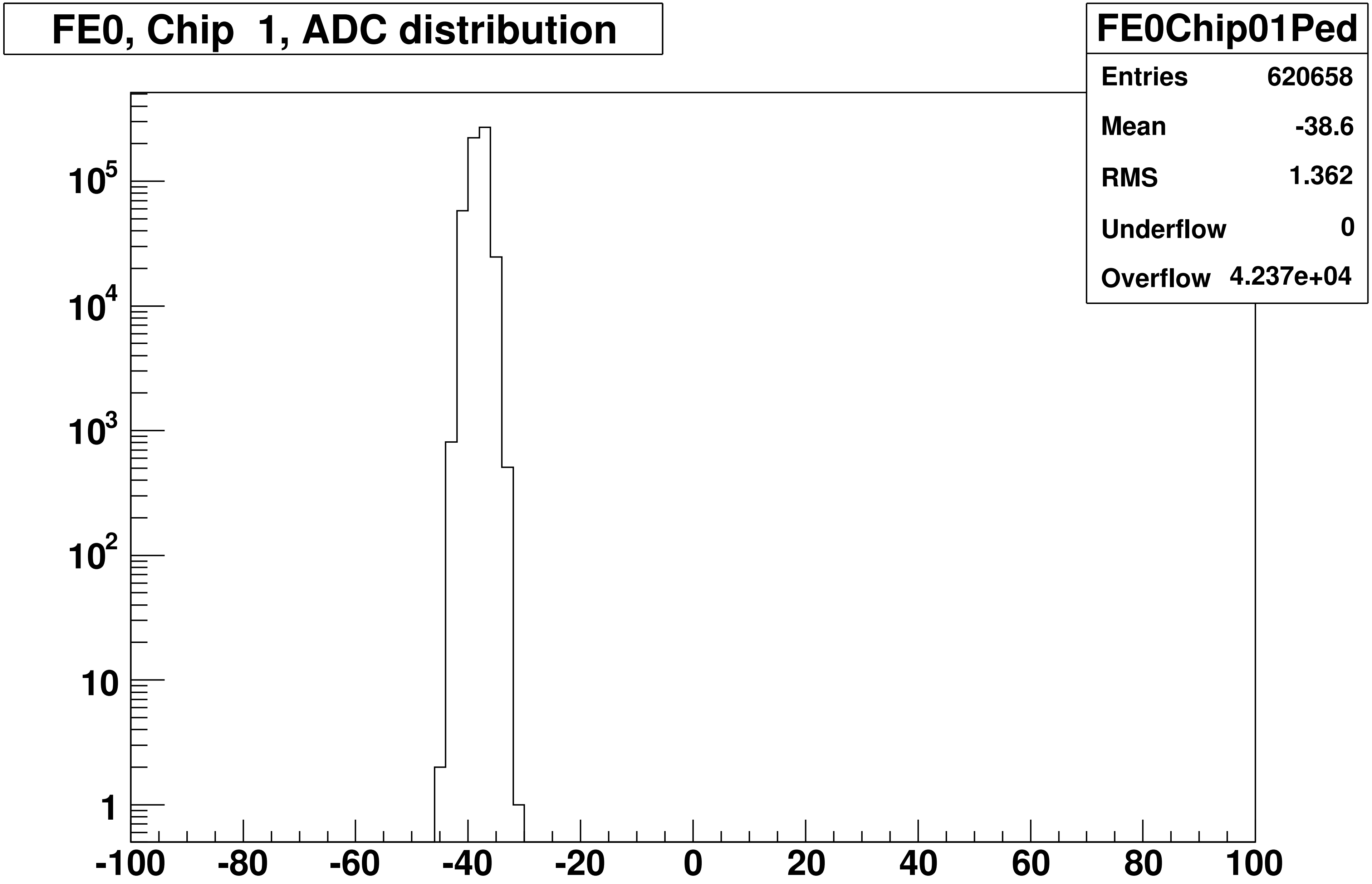
FE0, Chip 1, ADC distribution
FE0Chip01Ped
Entries 620658
Mean -38.6
RMS 1.362
Underflow 0
Overflow 4.237e+04
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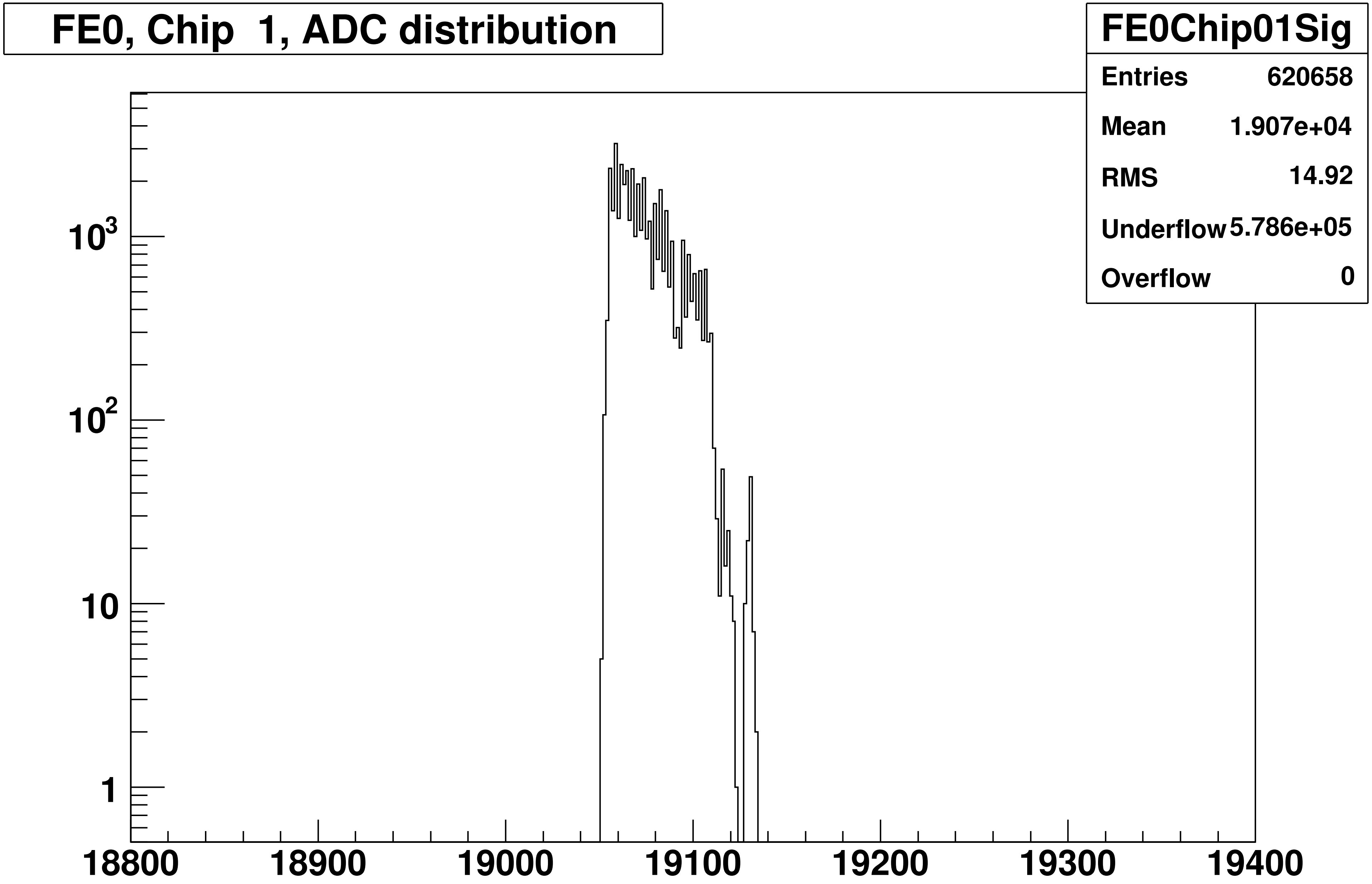

FE0, Chip 1, ADC distribution
FE0Chip01Sig
Entries 620658
Mean 1.907e+04
RMS 14.92
Underflow 5.786e+05
Overflow 0
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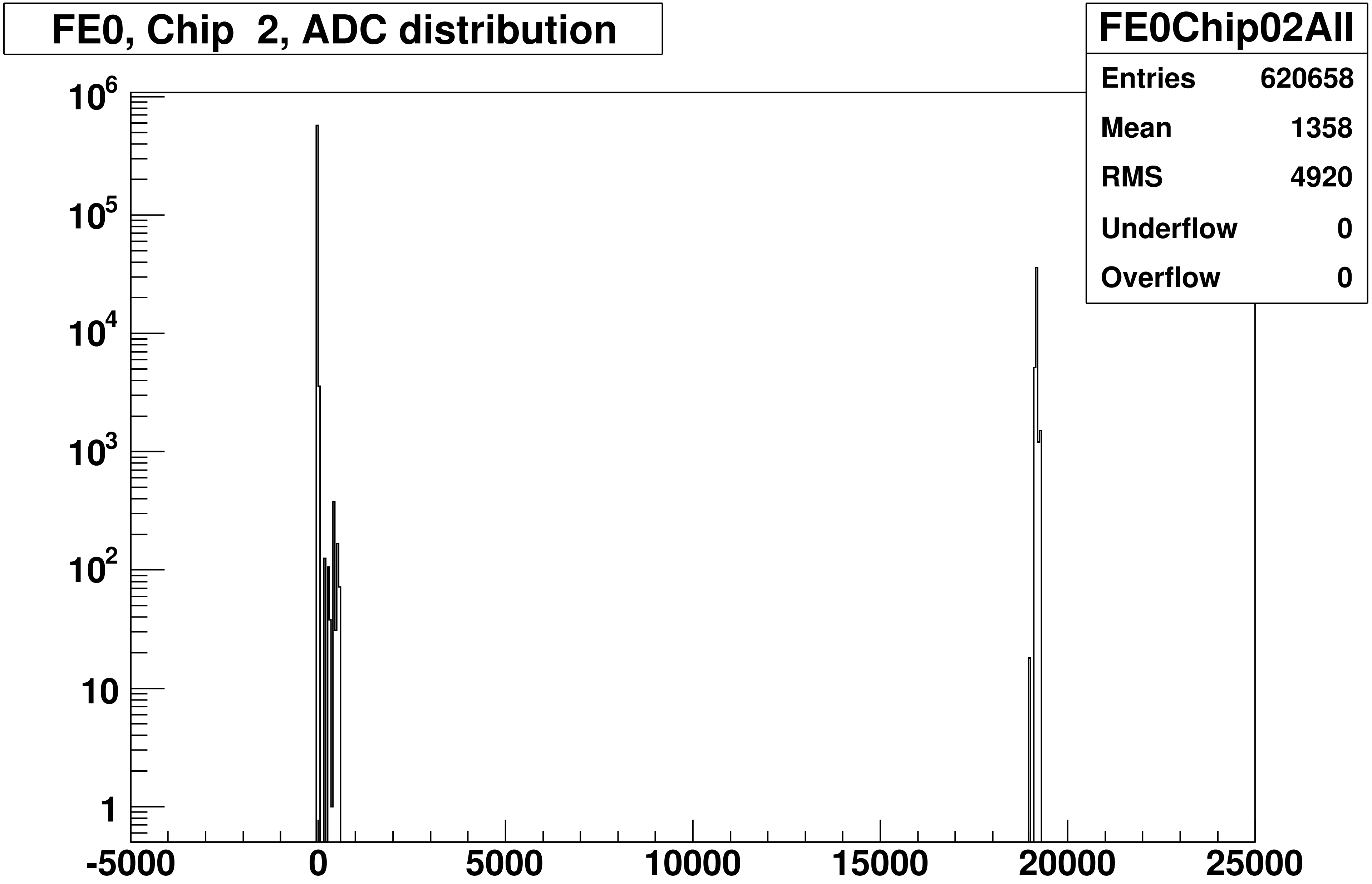
FE0, Chip 2, ADC distribution
FE0Chip02All
Entries 620658
Mean 1358
RMS 4920
Underflow 0
Overflow 0
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10^3
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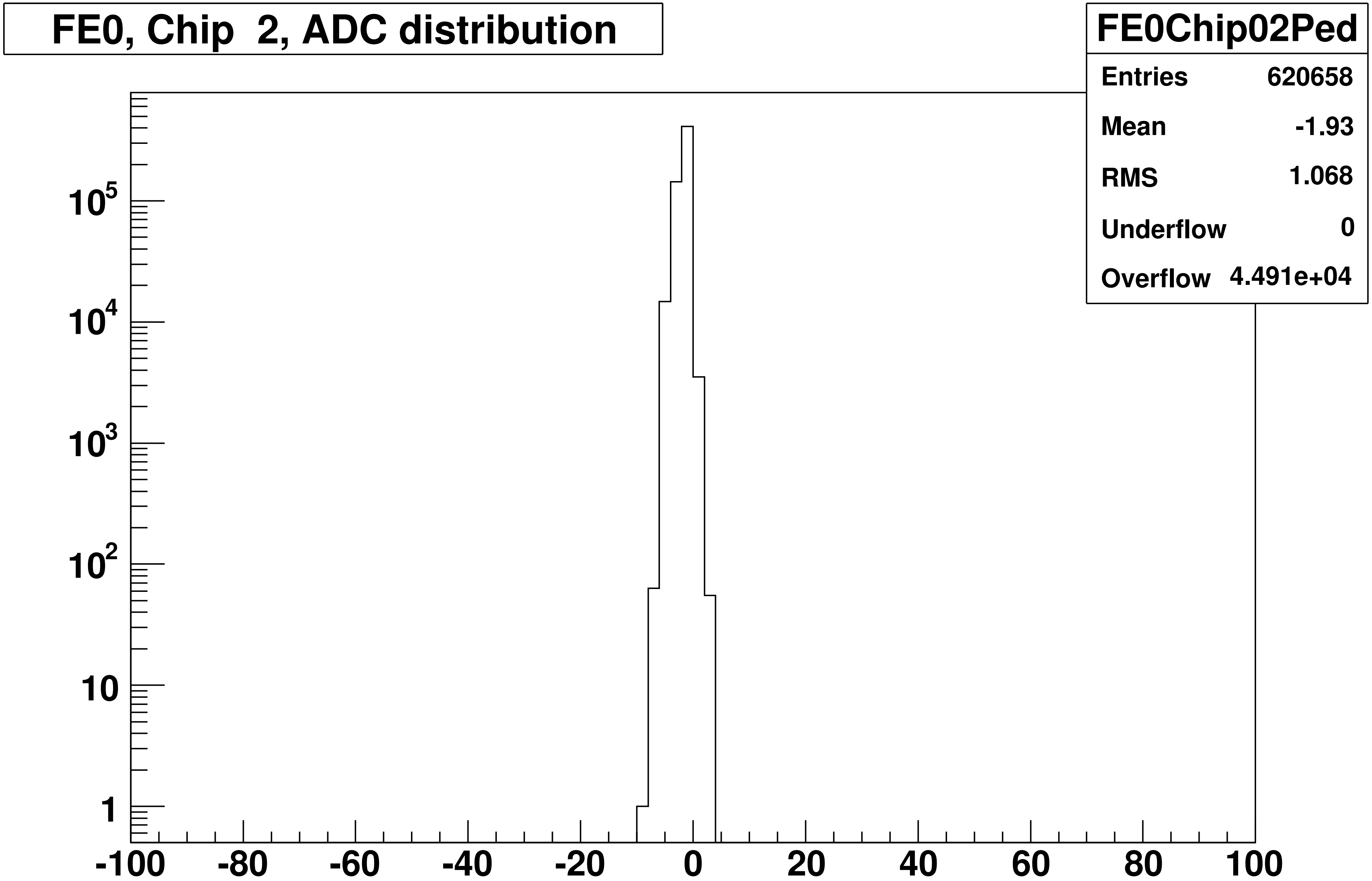
FE0, Chip 2, ADC distribution
FE0Chip02Ped
Entries 620658
Mean -1.93
RMS 1.068
Underflow 0
Overflow 4.491e+04
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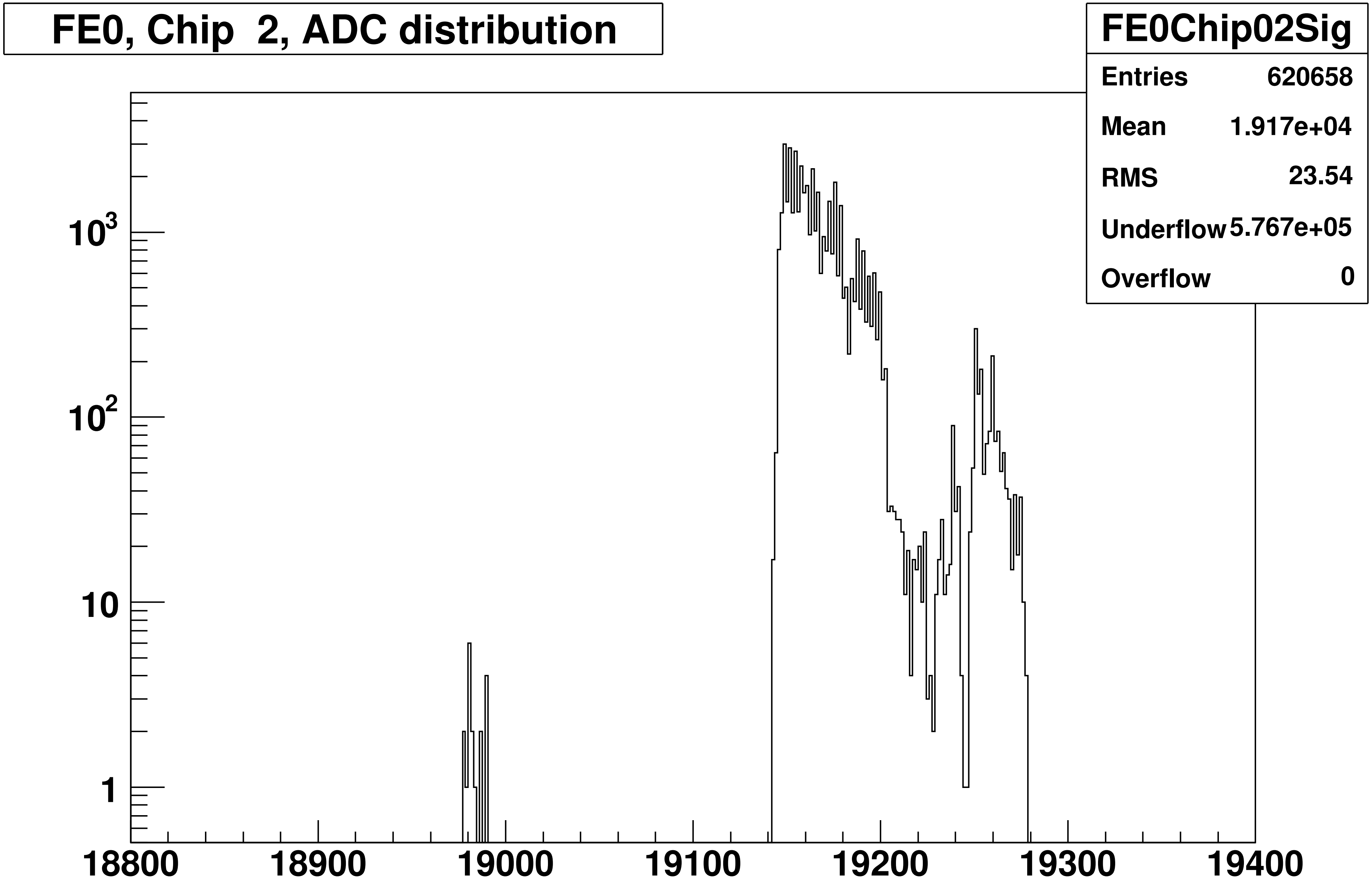

FE0, Chip 2, ADC distribution
FE0Chip02Sig
Entries 620658
Mean 1.917e+04
RMS 23.54
Underflow 5.767e+05
Overflow 0
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10^2
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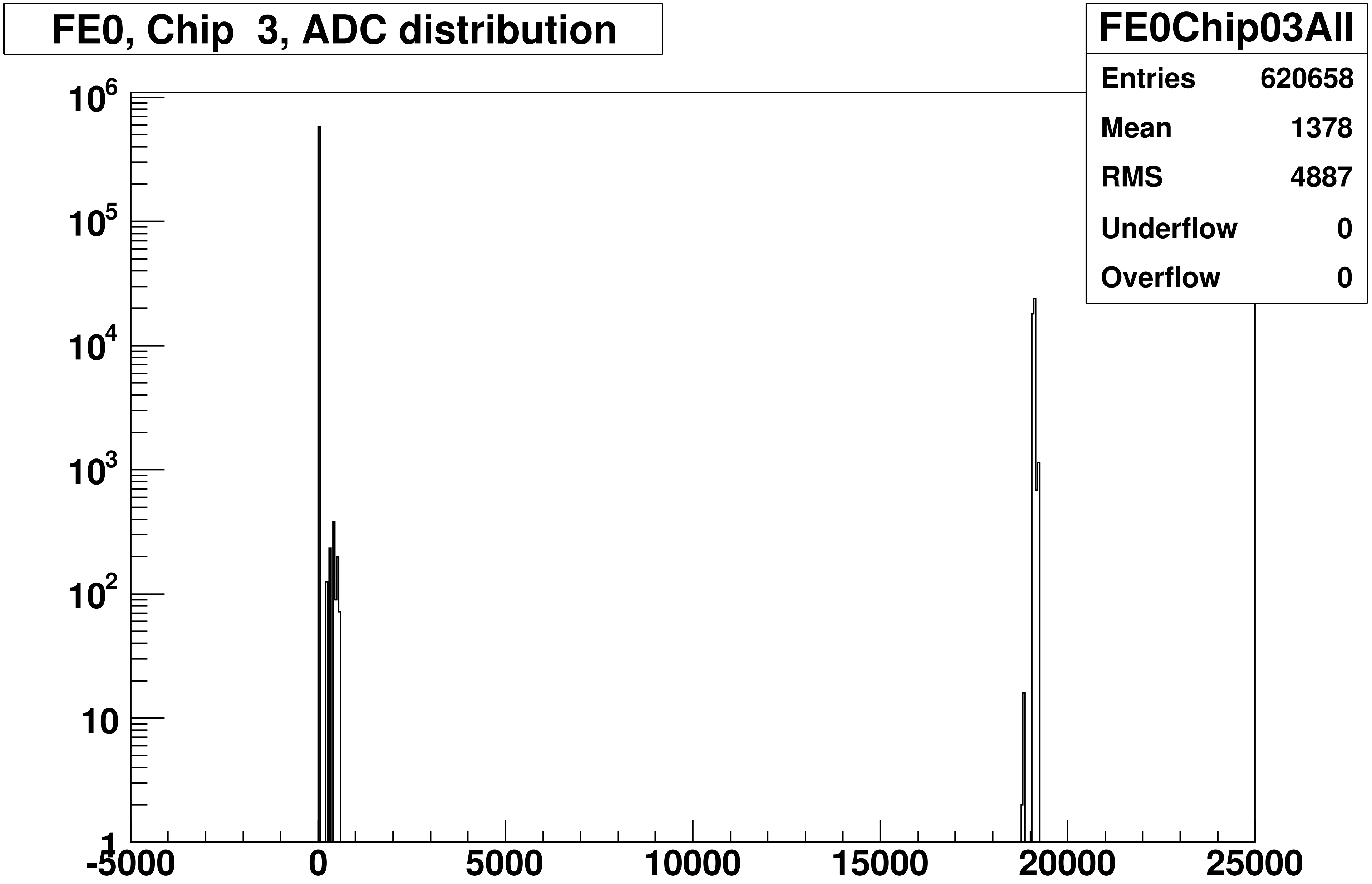
FE0, Chip 3, ADC distribution
FE0Chip03All
Entries 620658
Mean 1378
RMS 4887
Underflow 0
Overflow 0
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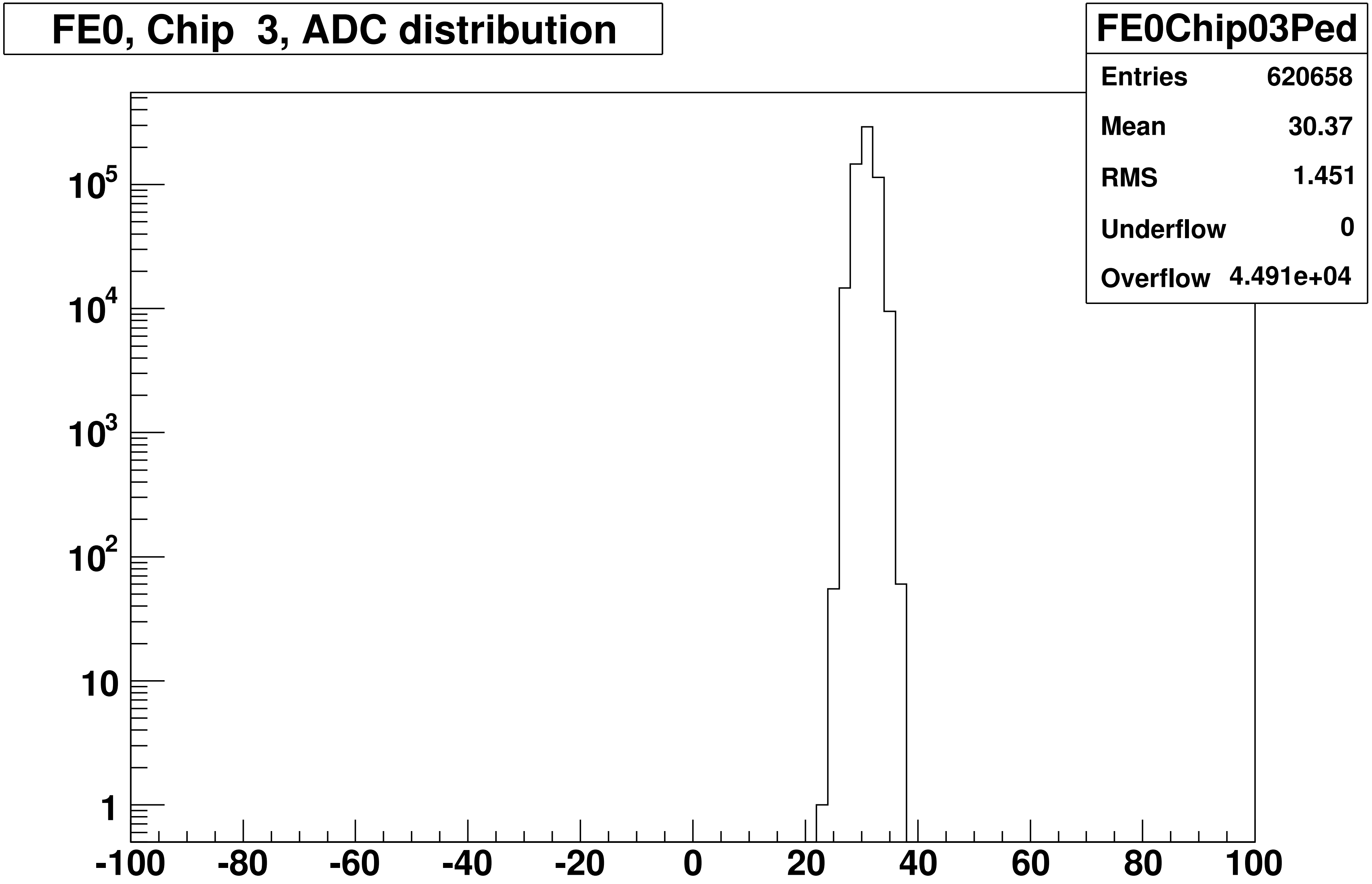
FE0, Chip 3, ADC distribution
FE0Chip03Ped
Entries 620658
Mean 30.37
RMS 1.451
Underflow 0
Overflow 4.491e+04
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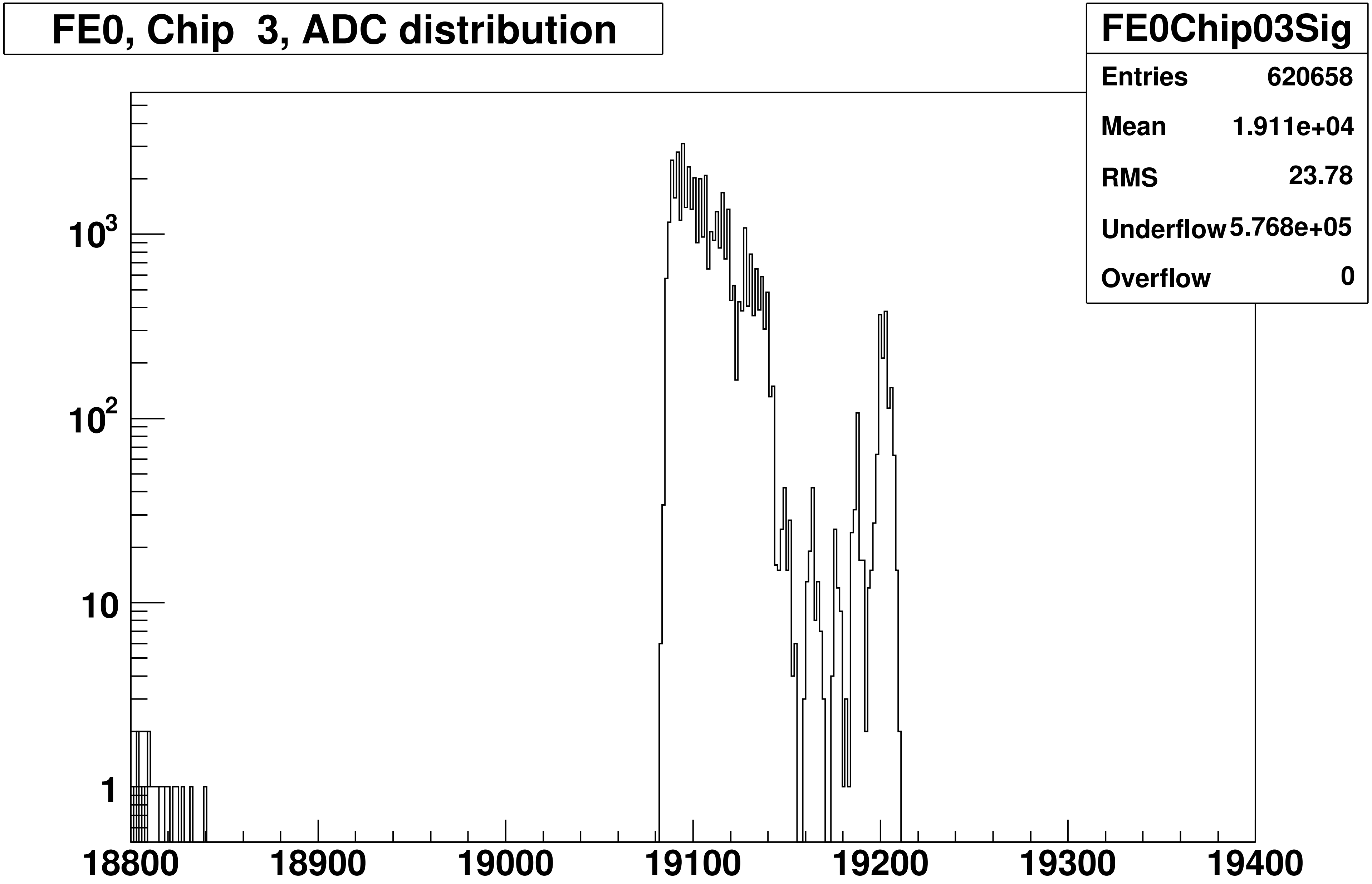
FE0, Chip 3, ADC distribution
FE0Chip03Sig
Entries 620658
Mean 1.911e+04
RMS 23.78
Underflow 5.768e+05
Overflow 0
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10^2
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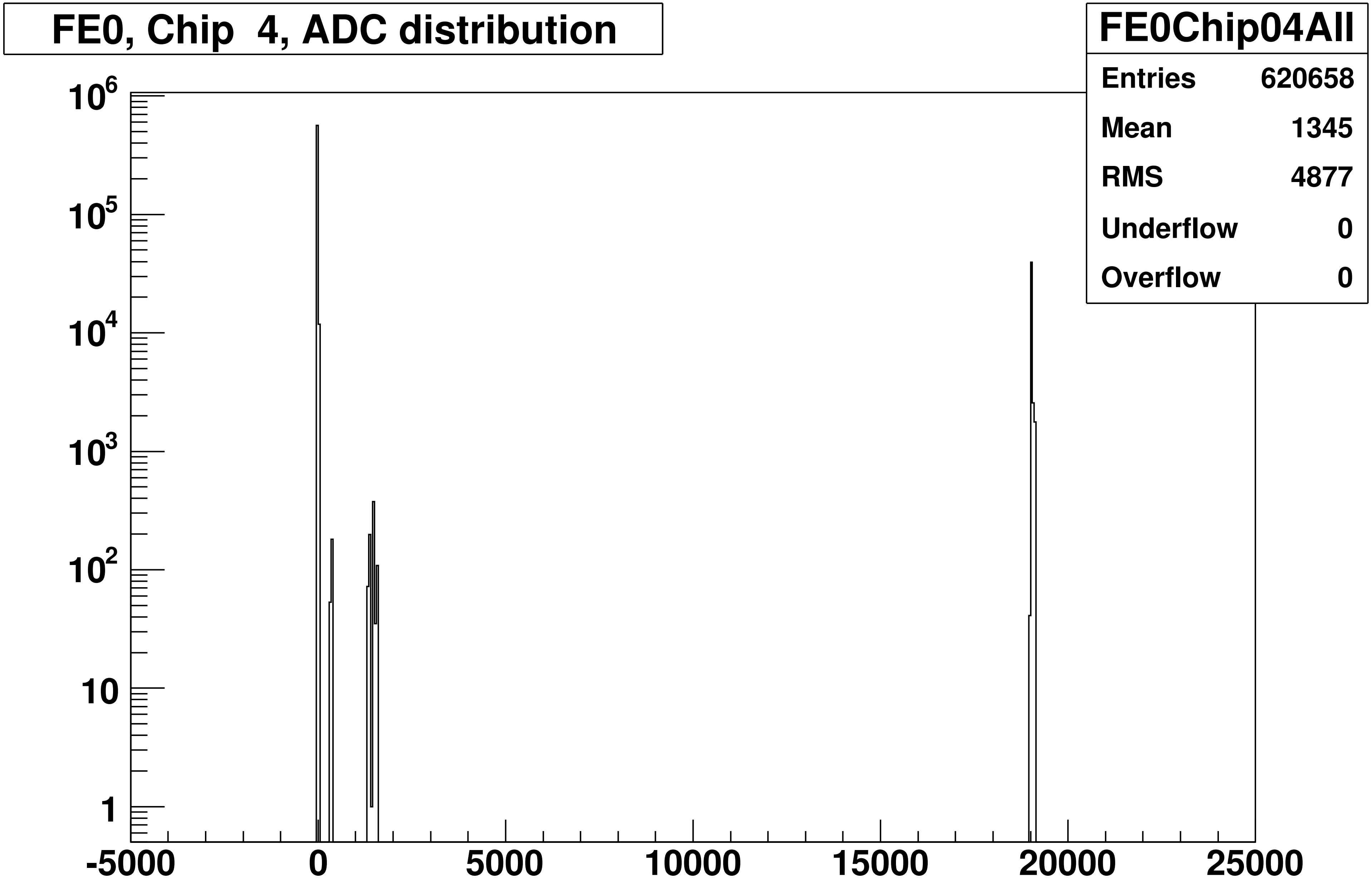
FE0, Chip 4, ADC distribution
FE0Chip04All
Entries 620658
Mean 1345
RMS 4877
Underflow 0
Overflow 0
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10^3
10^2
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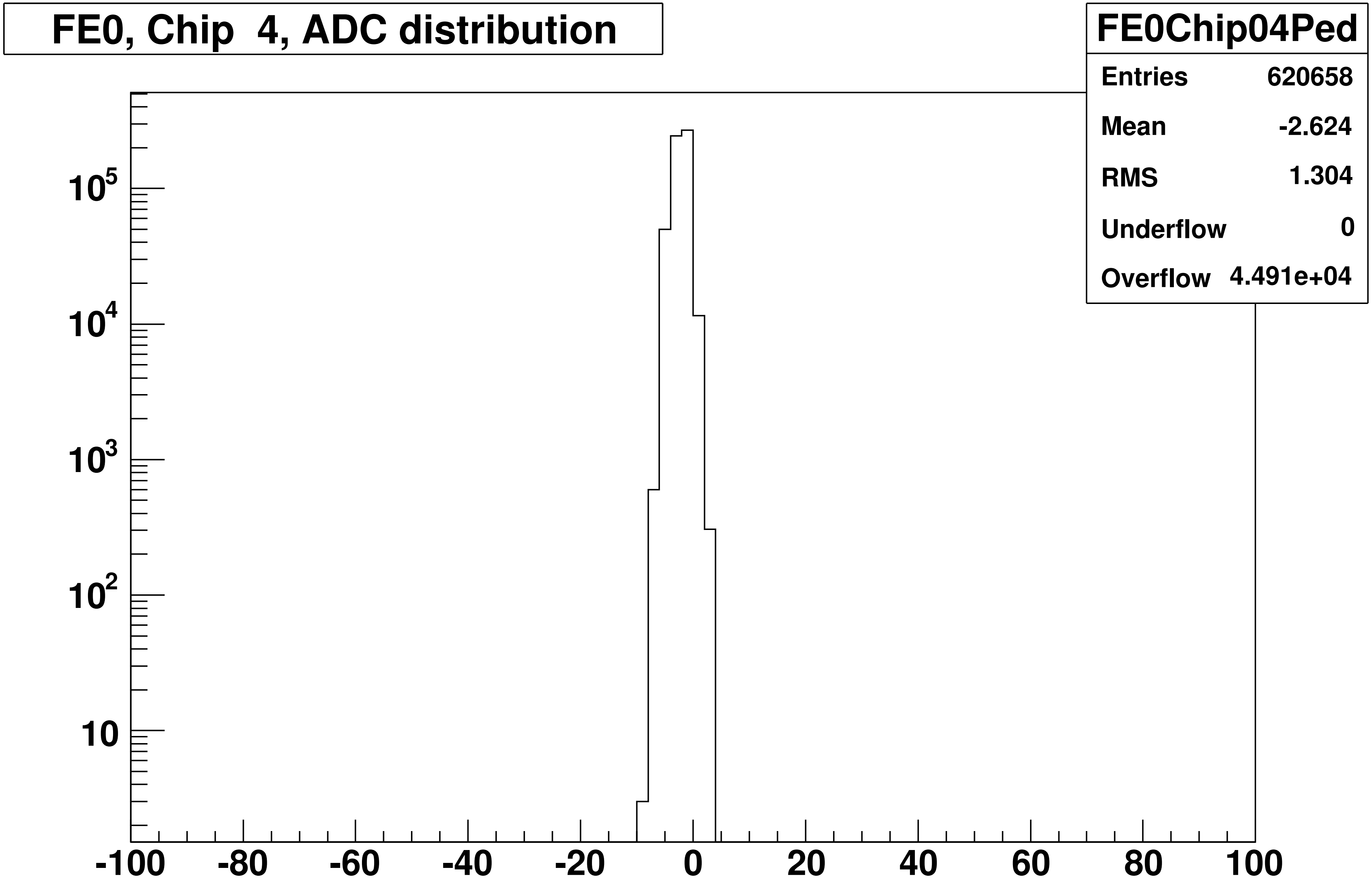
FE0, Chip 4, ADC distribution
FE0Chip04Ped
Entries 620658
Mean -2.624
RMS 1.304
Underflow 0
Overflow 4.491e+04
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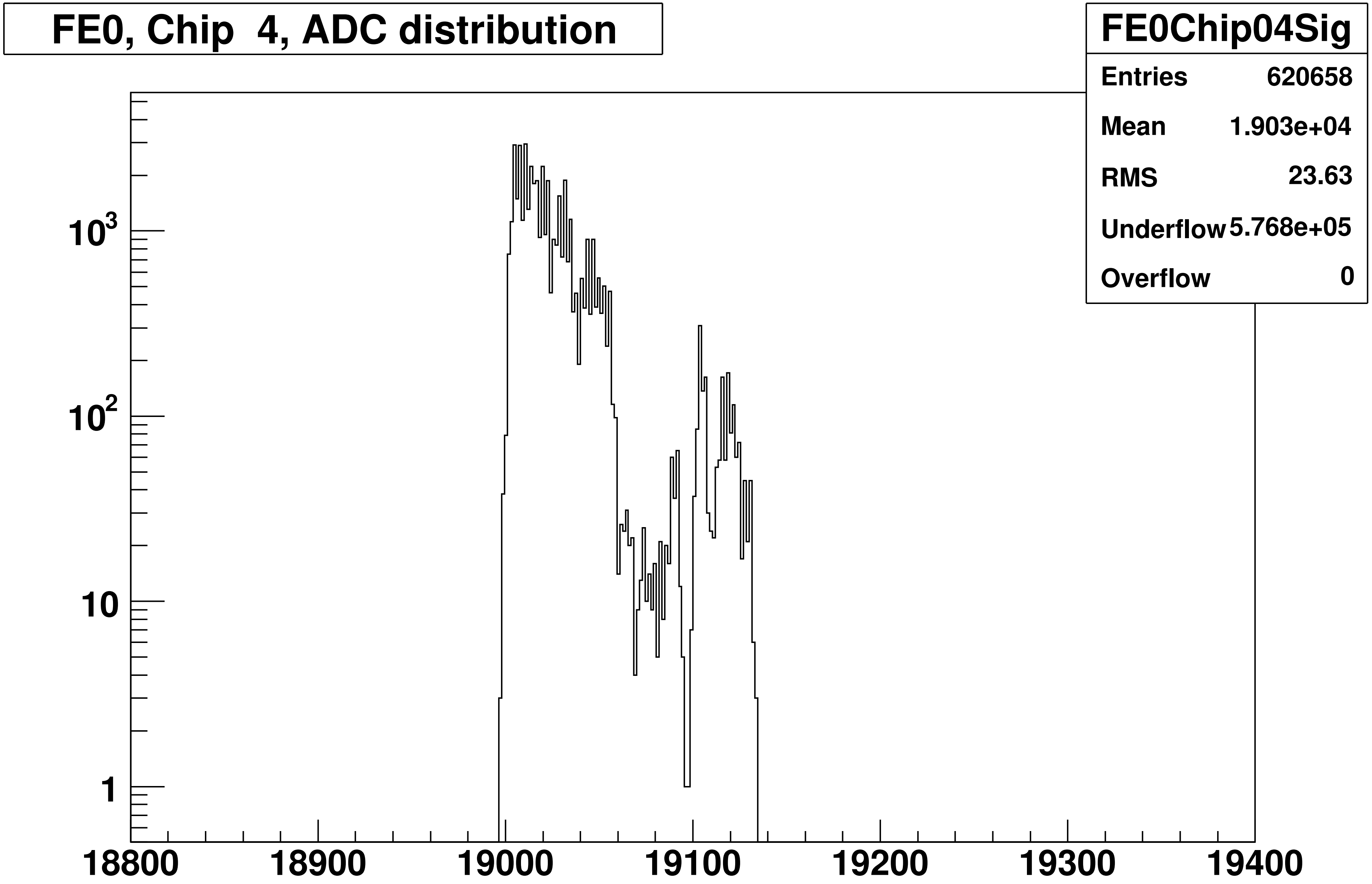

FE0, Chip 4, ADC distribution
FE0Chip04Sig
Entries 620658
Mean 1.903e+04
RMS 23.63
Underflow 5.768e+05
Overflow 0
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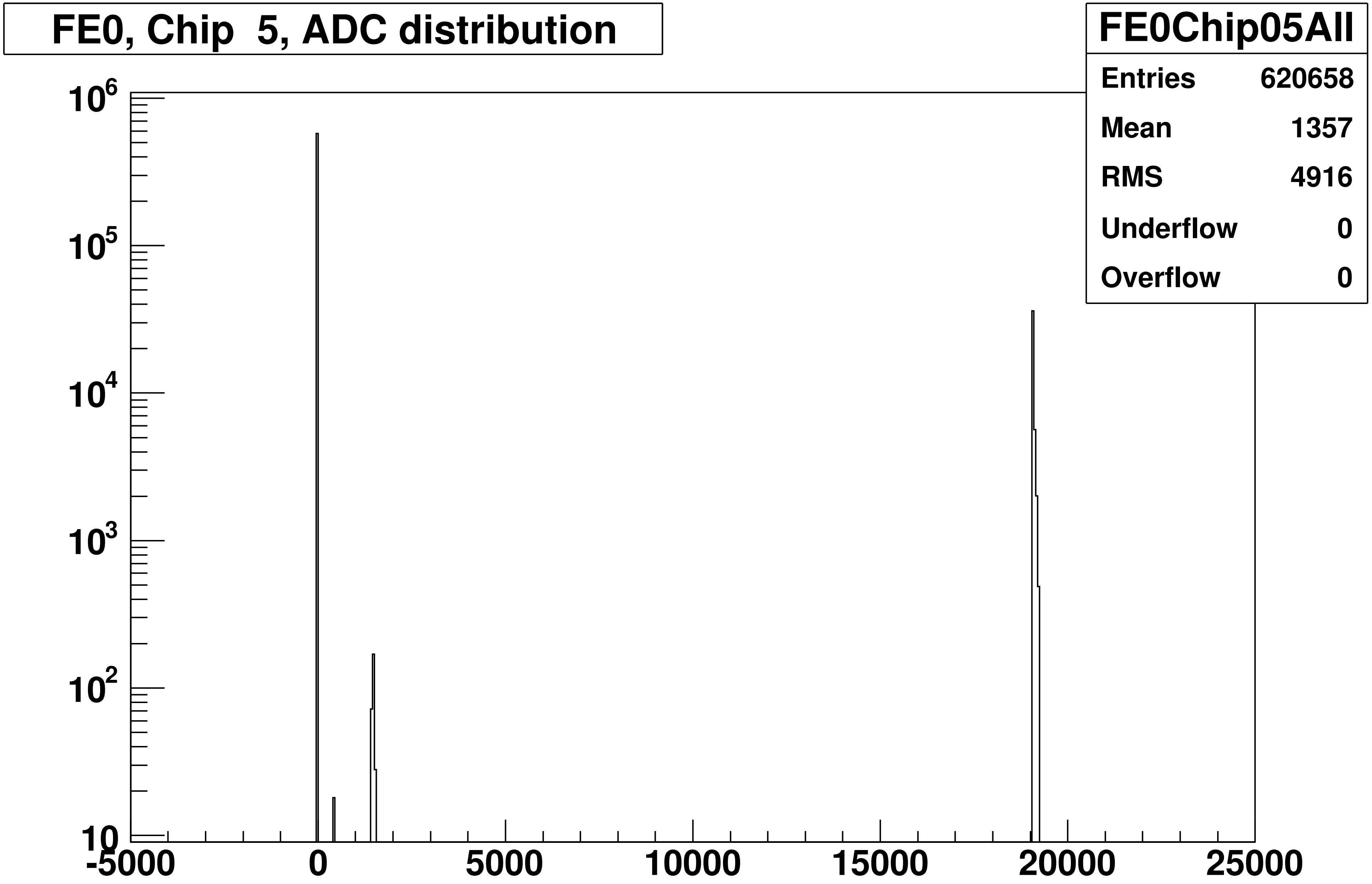
FE0, Chip 5, ADC distribution
FE0Chip05All
Entries 620658
Mean 1357
RMS 4916
Underflow 0
Overflow 0
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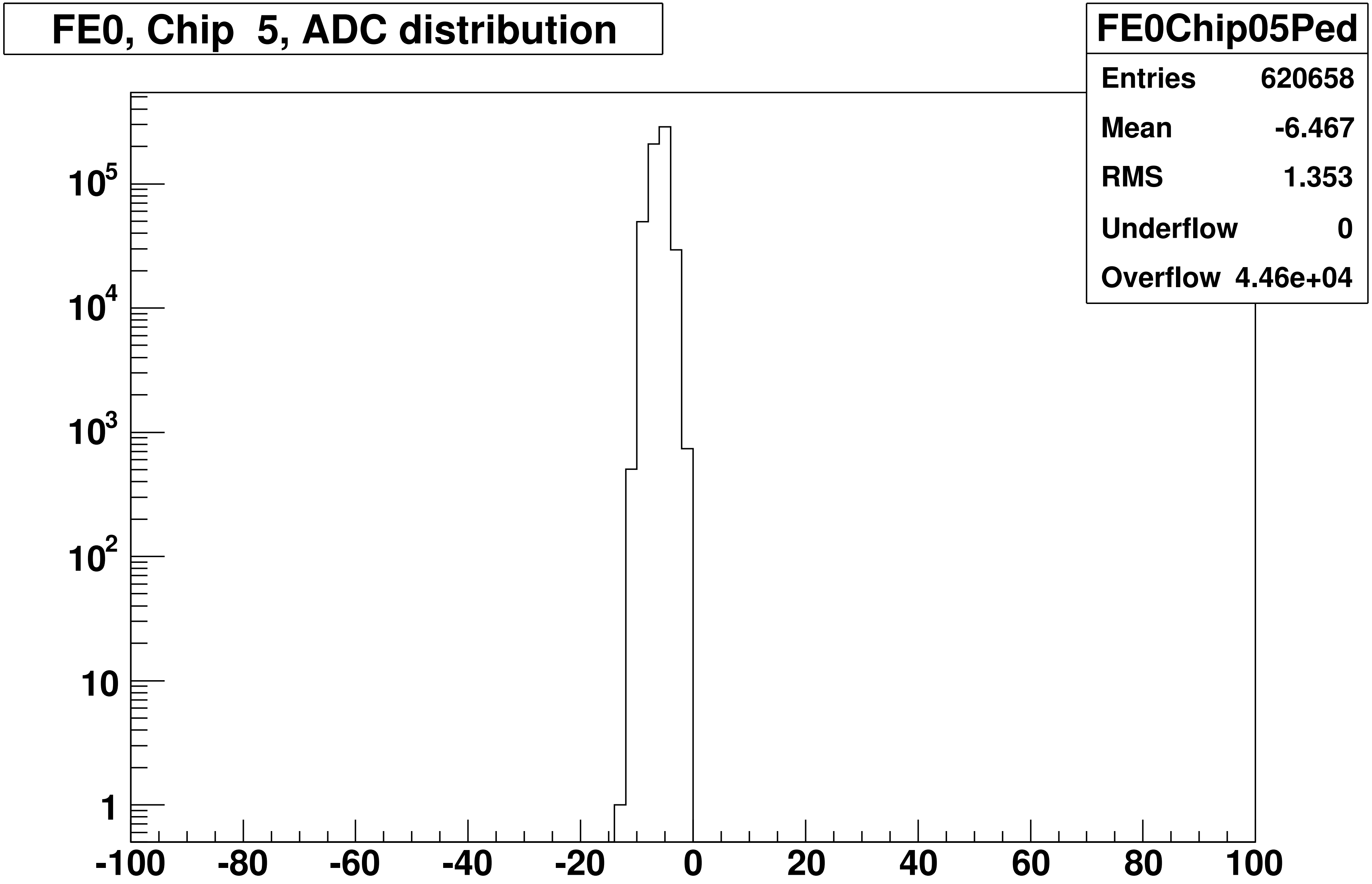
FE0, Chip 5, ADC distribution
FE0Chip05Ped
Entries 620658
Mean -6.467
RMS 1.353
Underflow 0
Overflow 4.46e+04
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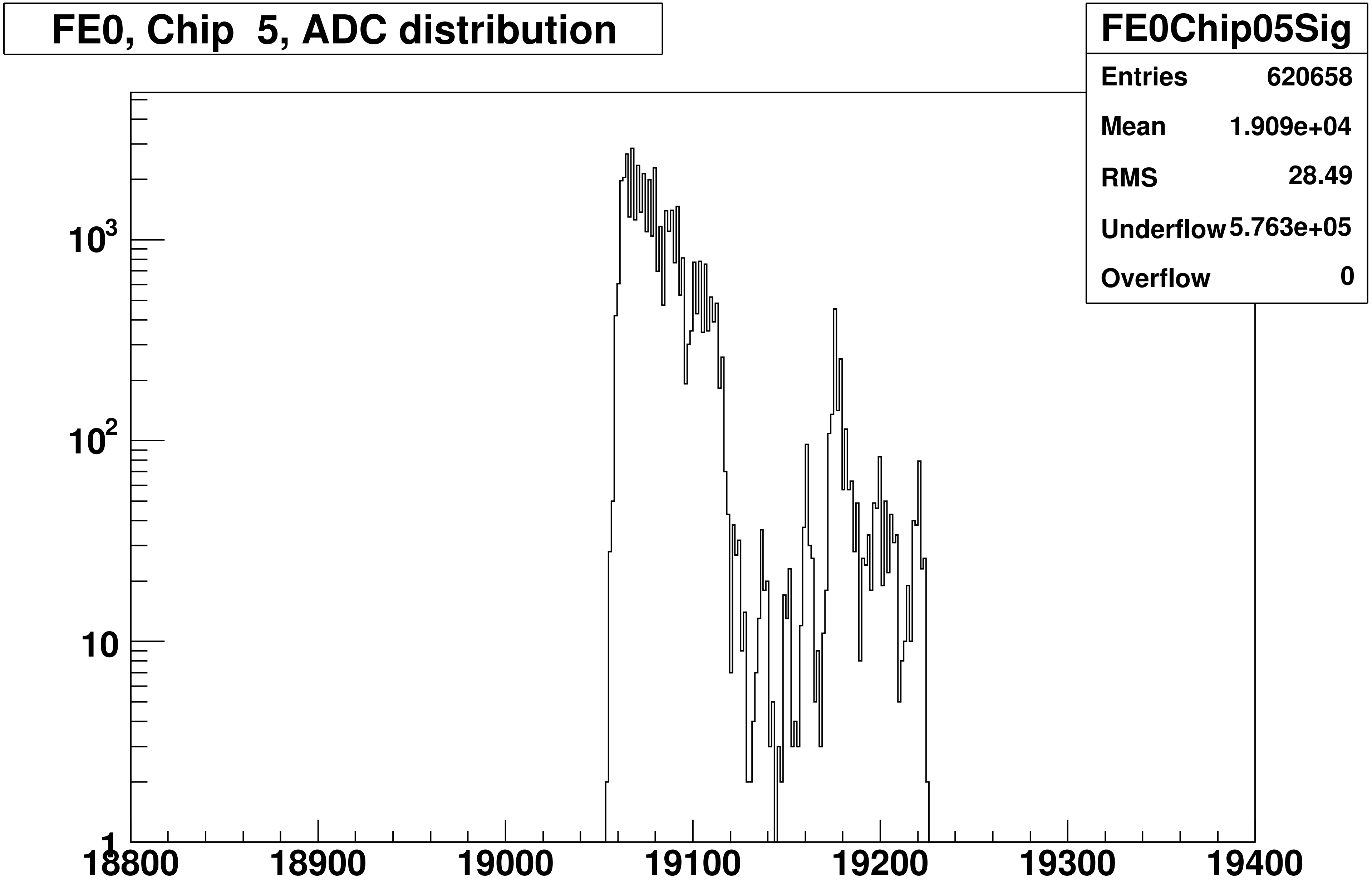
FE0, Chip 5, ADC distribution
FE0Chip05Sig
Entries 620658
Mean 1.909e+04
RMS 28.49
Underflow 5.763e+05
Overflow 0
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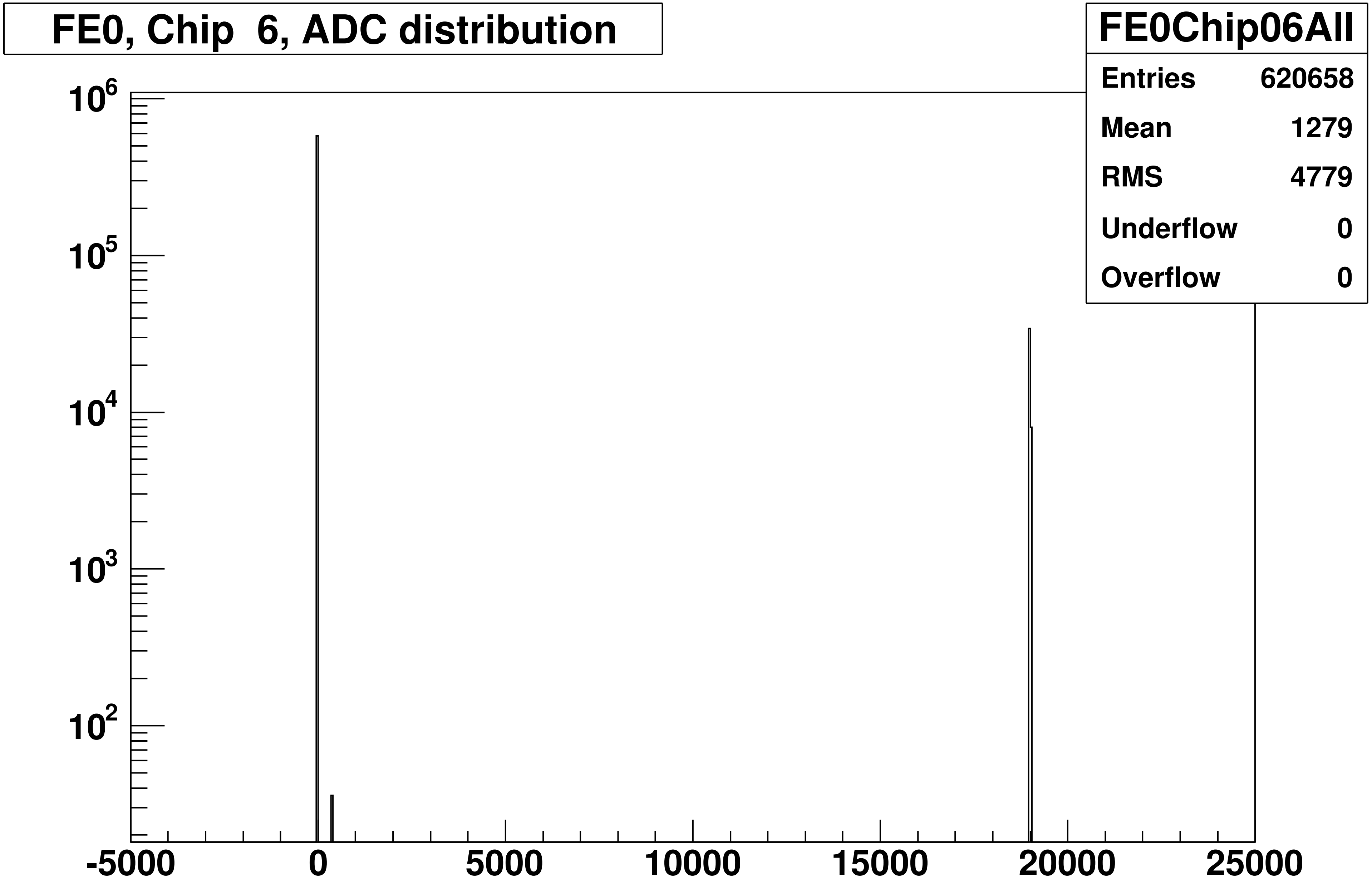
FE0, Chip 6, ADC distribution
FE0Chip06All
Entries 620658
Mean 1279
RMS 4779
Underflow 0
Overflow 0
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10^3
10^2
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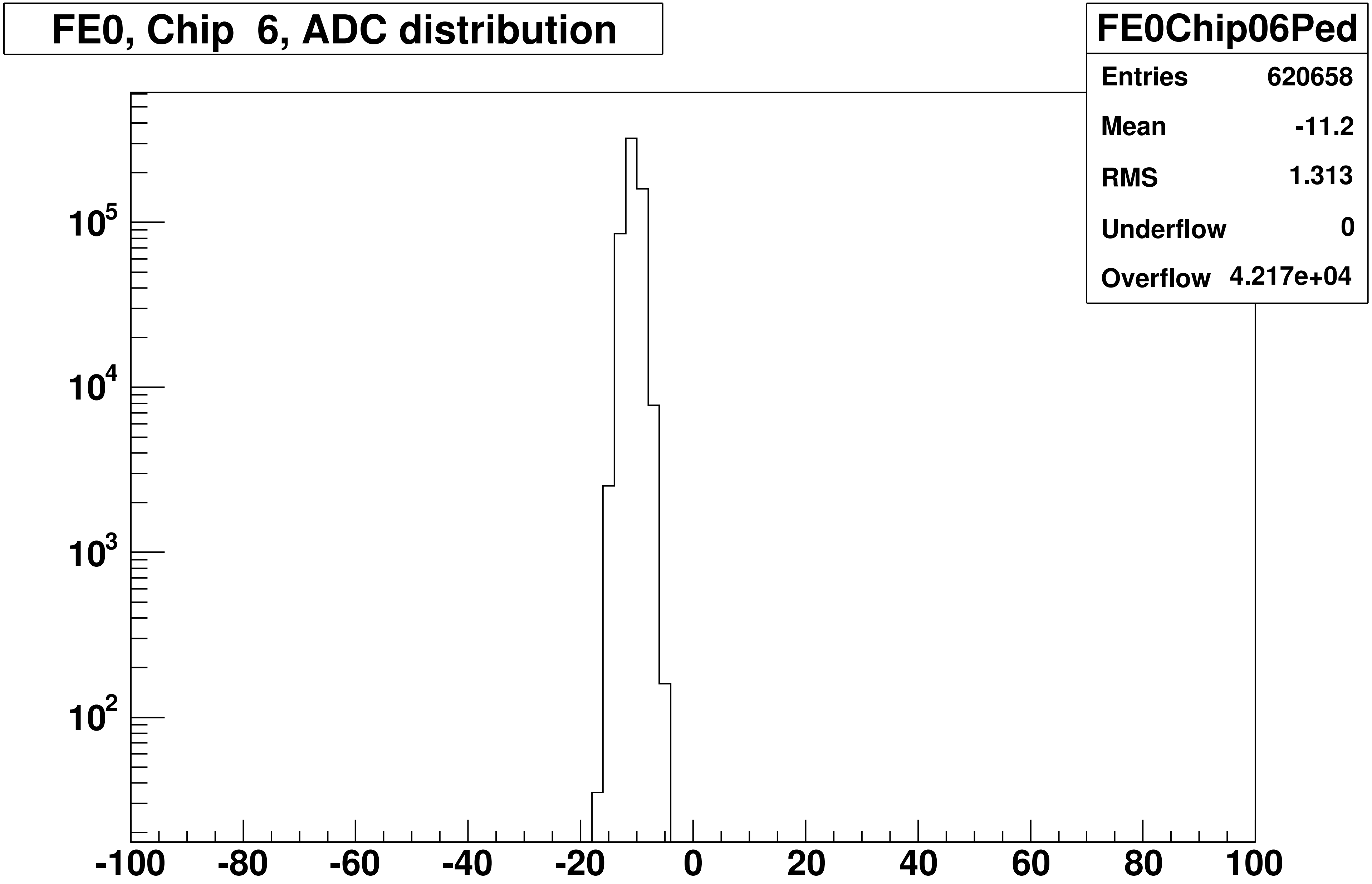
FE0, Chip 6, ADC distribution
FE0Chip06Ped
Entries 620658
Mean -11.2
RMS 1.313
Underflow 0
Overflow 4.217e+04
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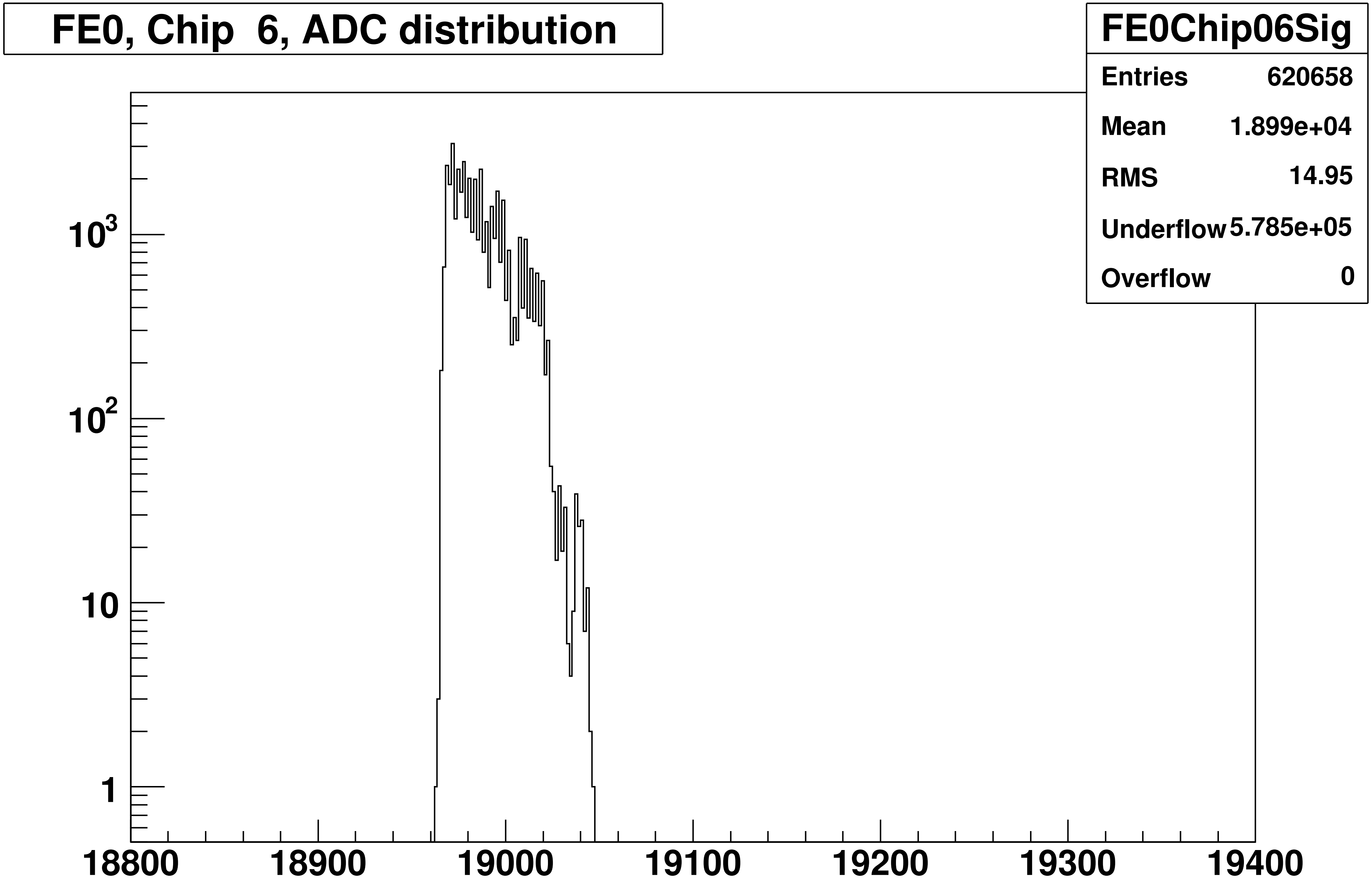
FE0, Chip 6, ADC distribution
FE0Chip06Sig
Entries 620658
Mean 1.899e+04
RMS 14.95
Underflow 5.785e+05
Overflow 0
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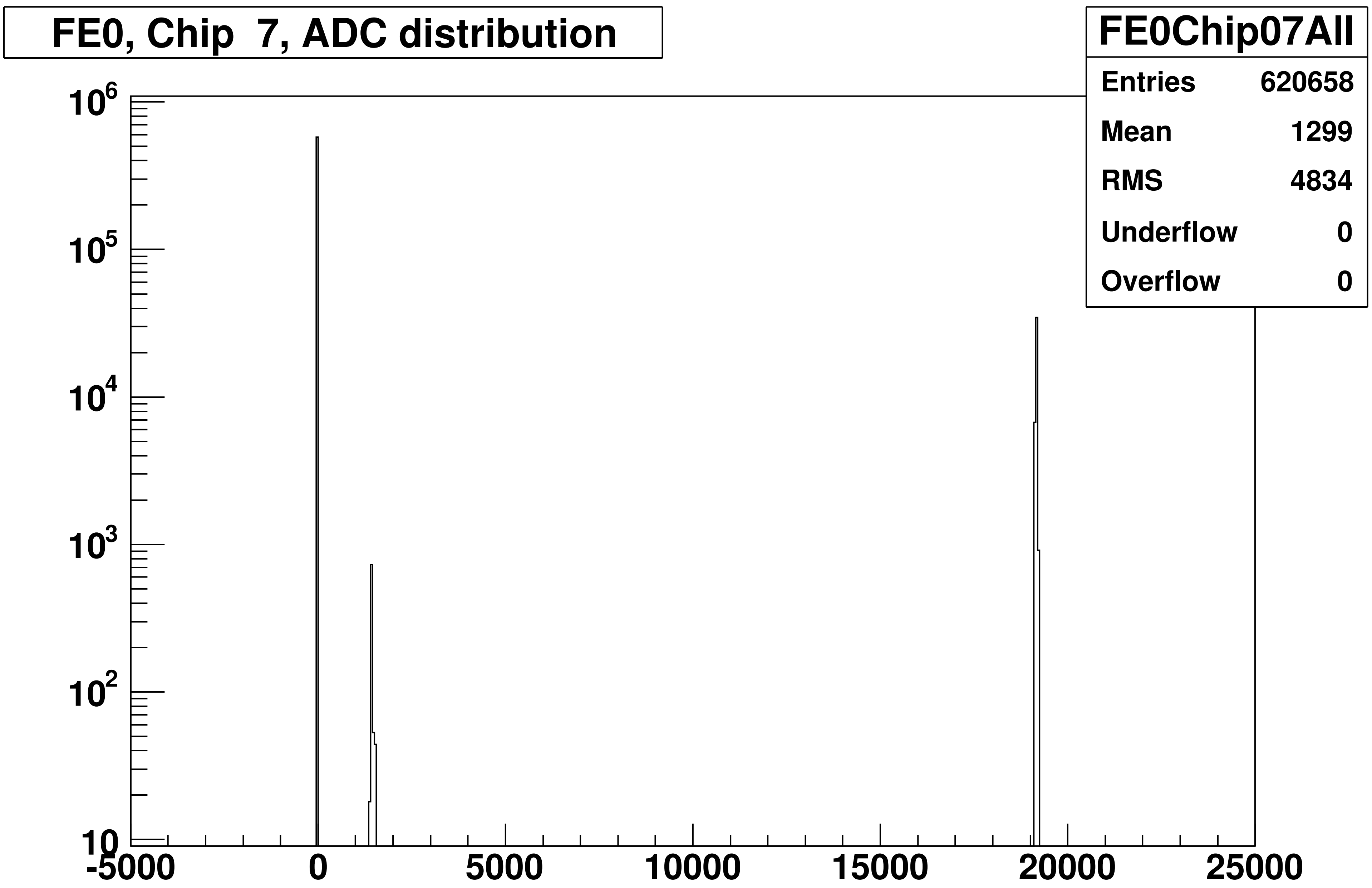
FE0, Chip 7, ADC distribution
FE0Chip07All
Entries 620658
Mean 1299
RMS 4834
Underflow 0
Overflow 0
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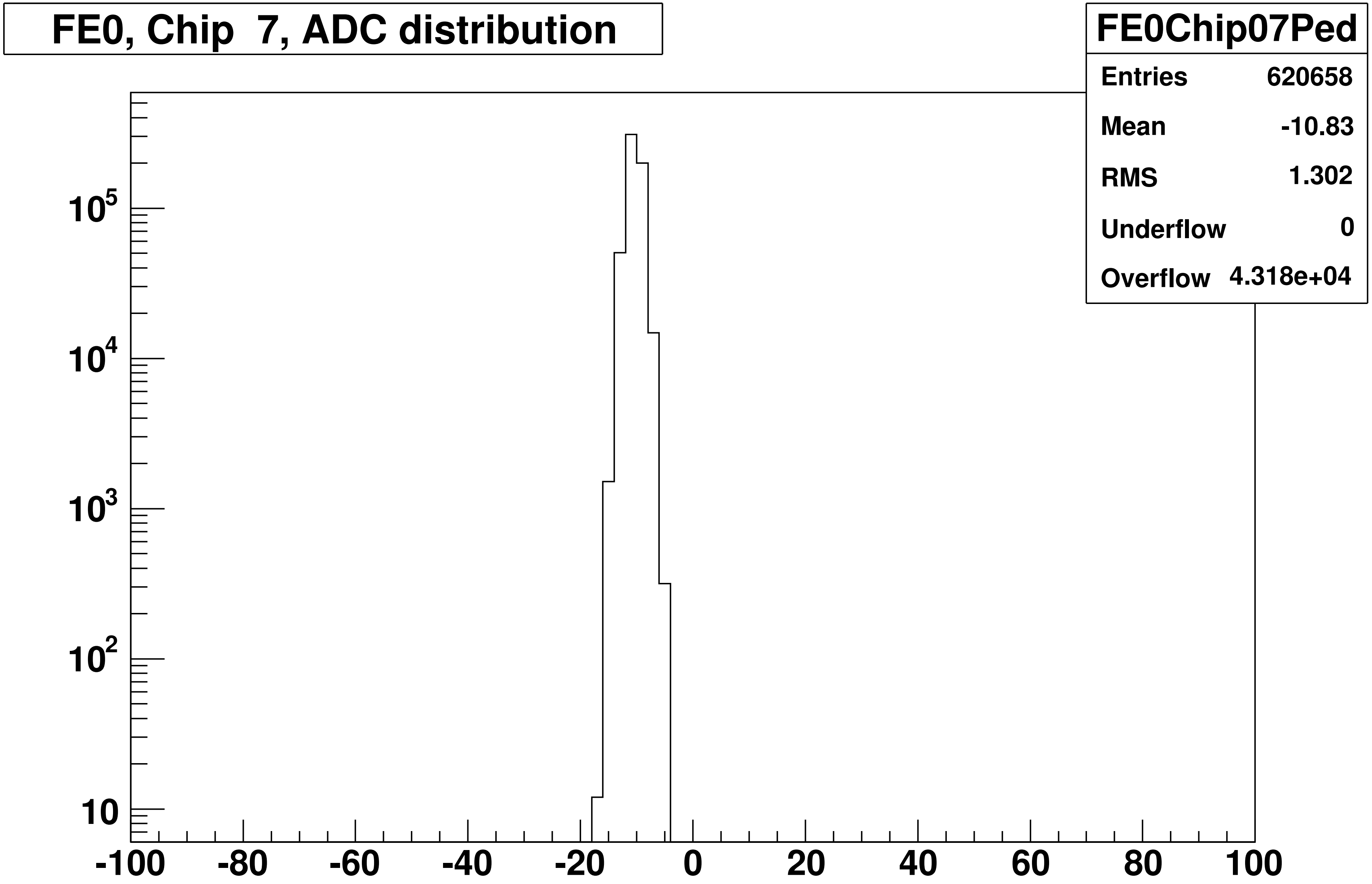

FE0, Chip 7, ADC distribution
FE0Chip07Ped
Entries 620658
Mean -10.83
RMS 1.302
Underflow 0
Overflow 4.318e+04
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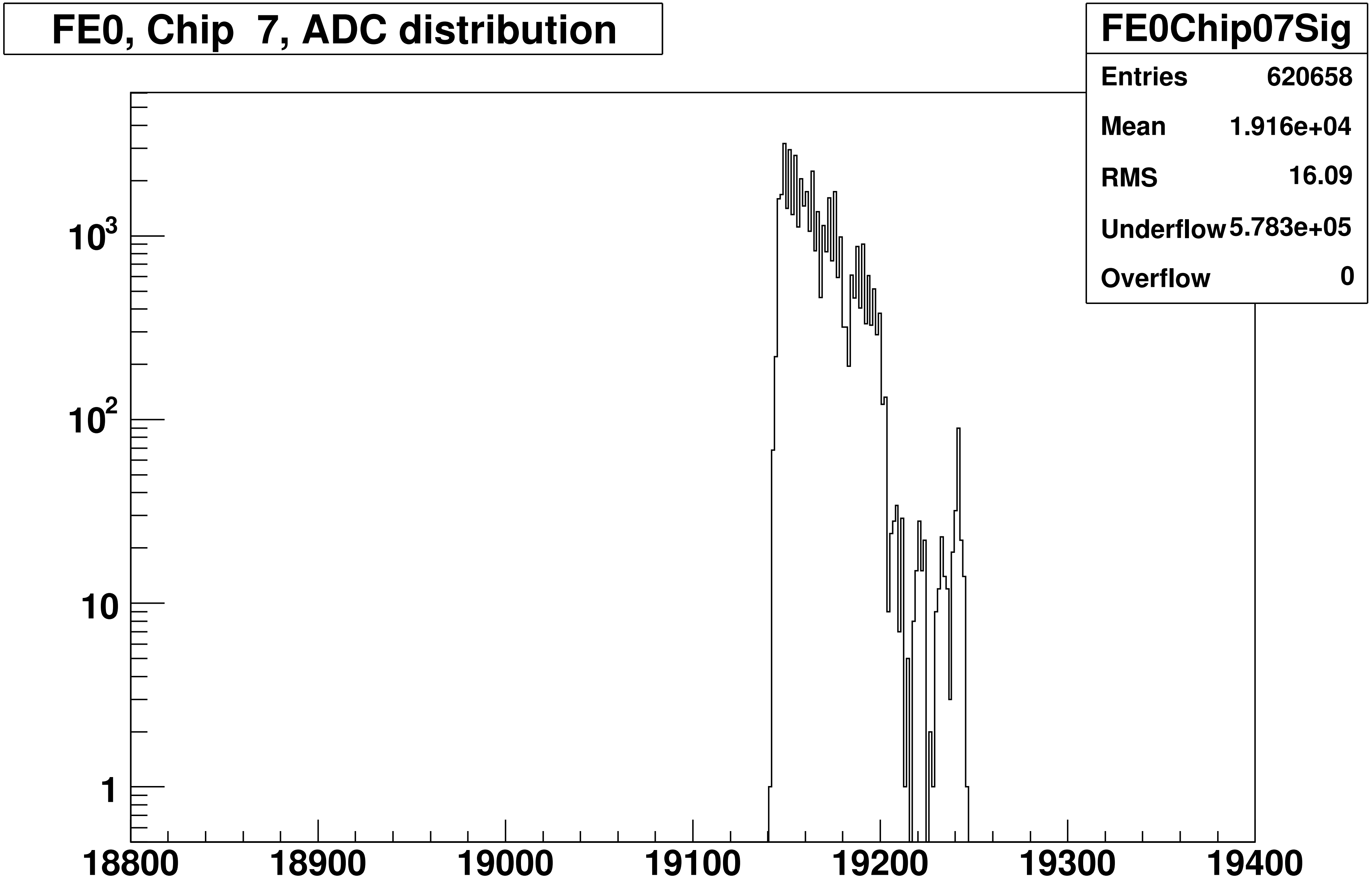
FE0, Chip 7, ADC distribution
FE0Chip07Sig
Entries 620658
Mean 1.916e+04
RMS 16.09
Underflow 5.783e+05
Overflow 0
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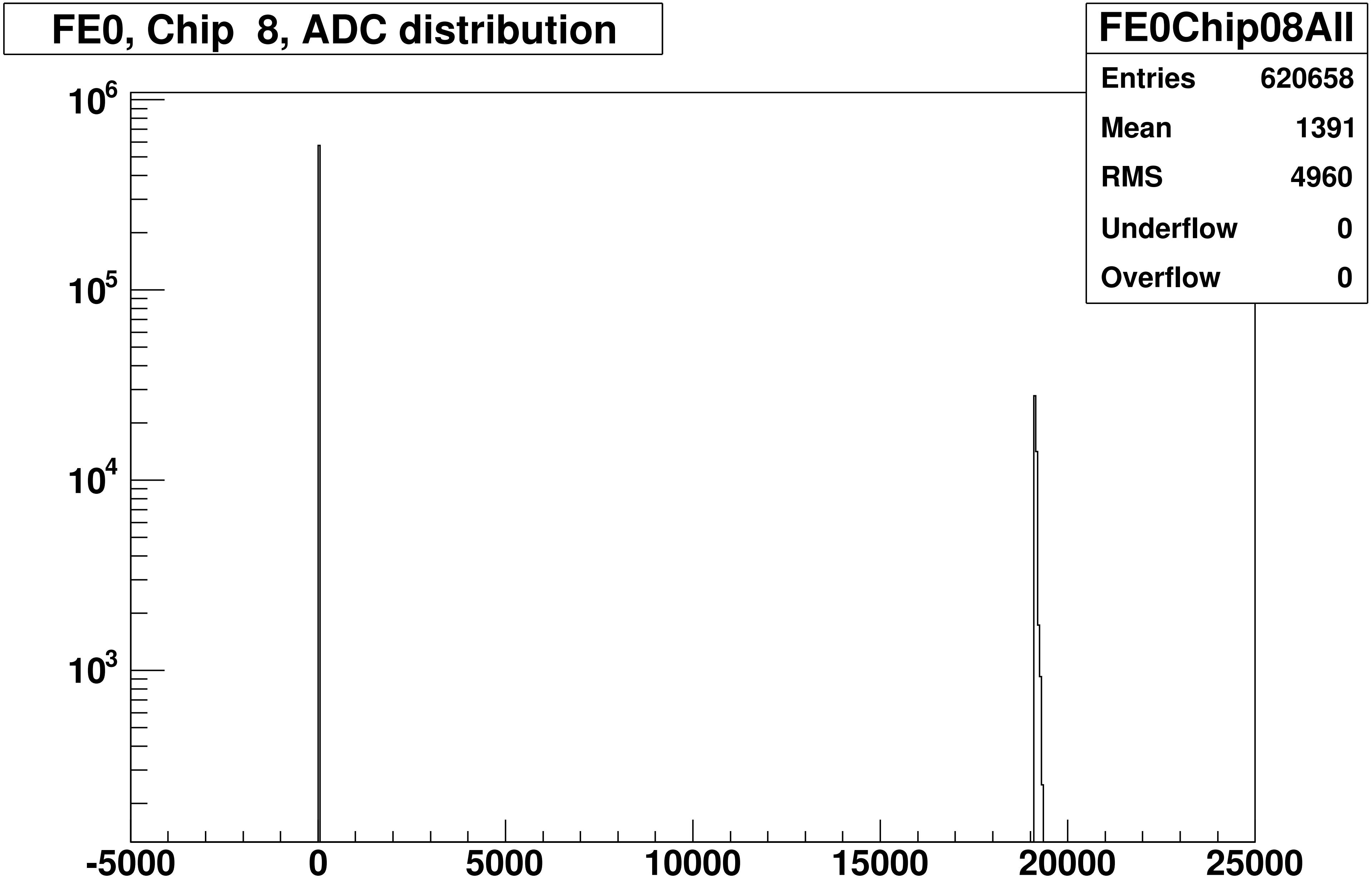
FE0, Chip 8, ADC distribution
FE0Chip08All
Entries 620658
Mean 1391
RMS 4960
Underflow 0
Overflow 0
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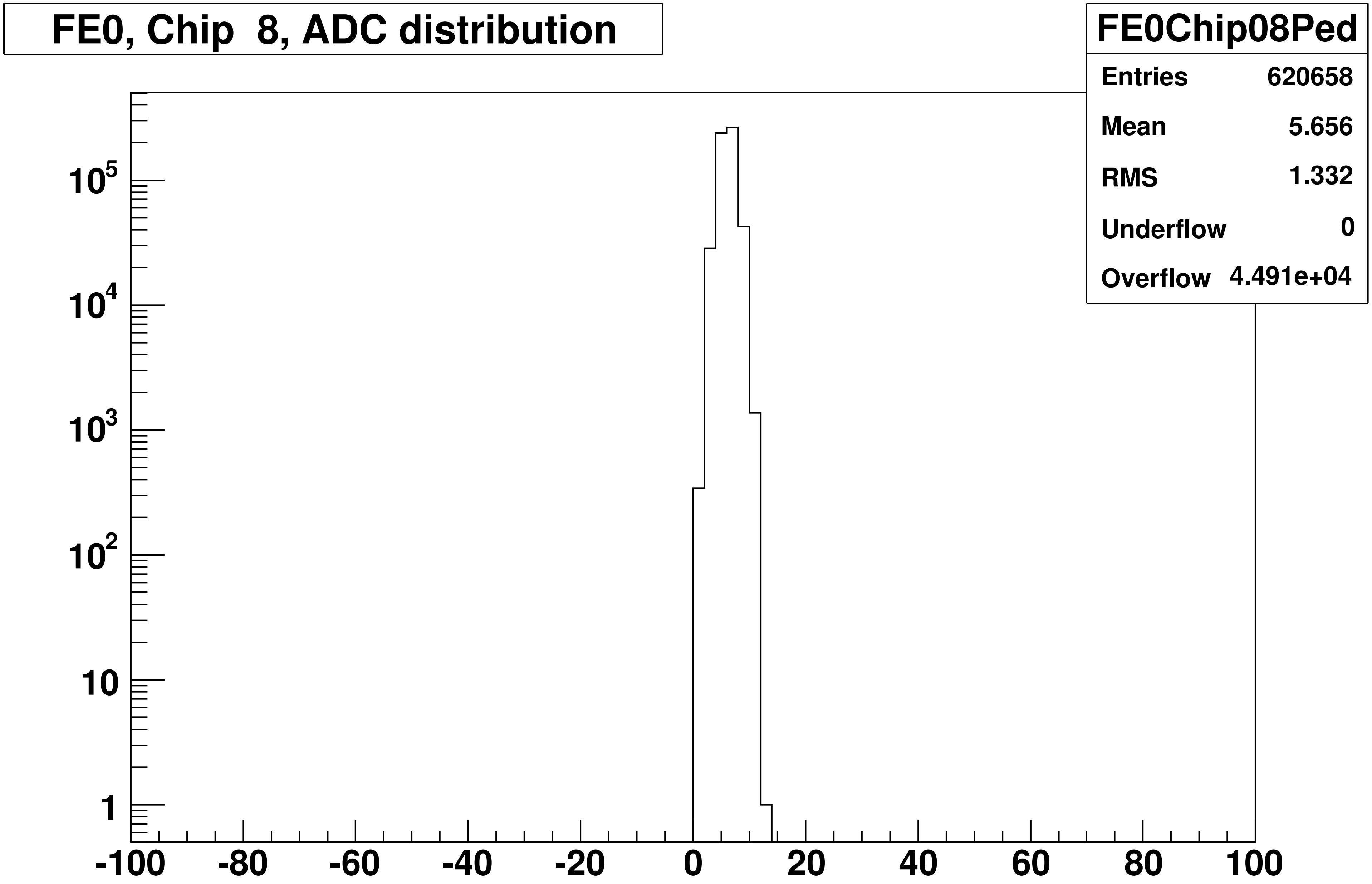
FE0, Chip 8, ADC distribution
FE0Chip08Ped
Entries 620658
Mean 5.656
RMS 1.332
Underflow 0
Overflow 4.491e+04
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10^3
10^2
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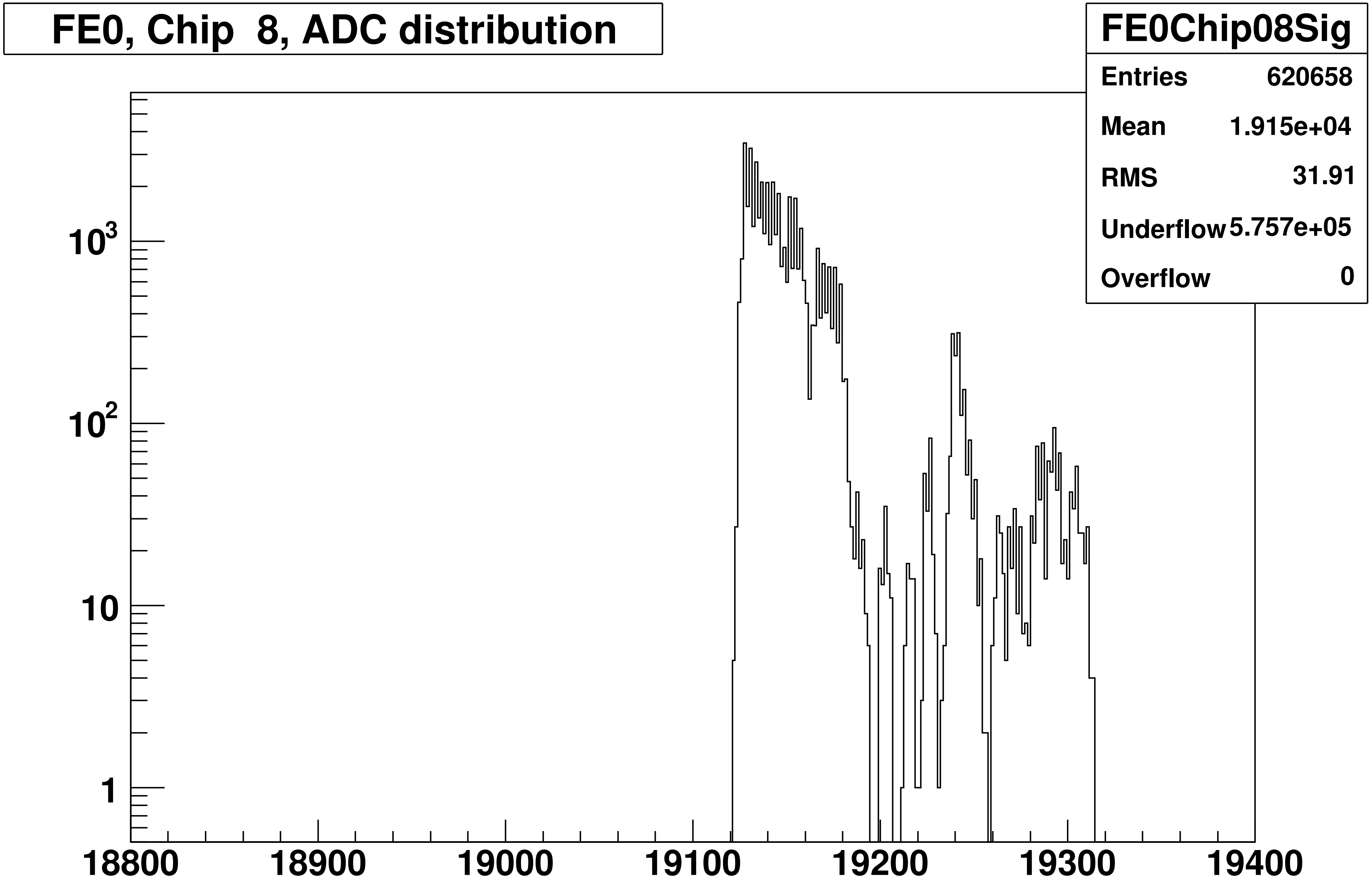
FE0, Chip 8, ADC distribution
FE0Chip08Sig
Entries 620658
Mean 1.915e+04
RMS 31.91
Underflow 5.757e+05
Overflow 0
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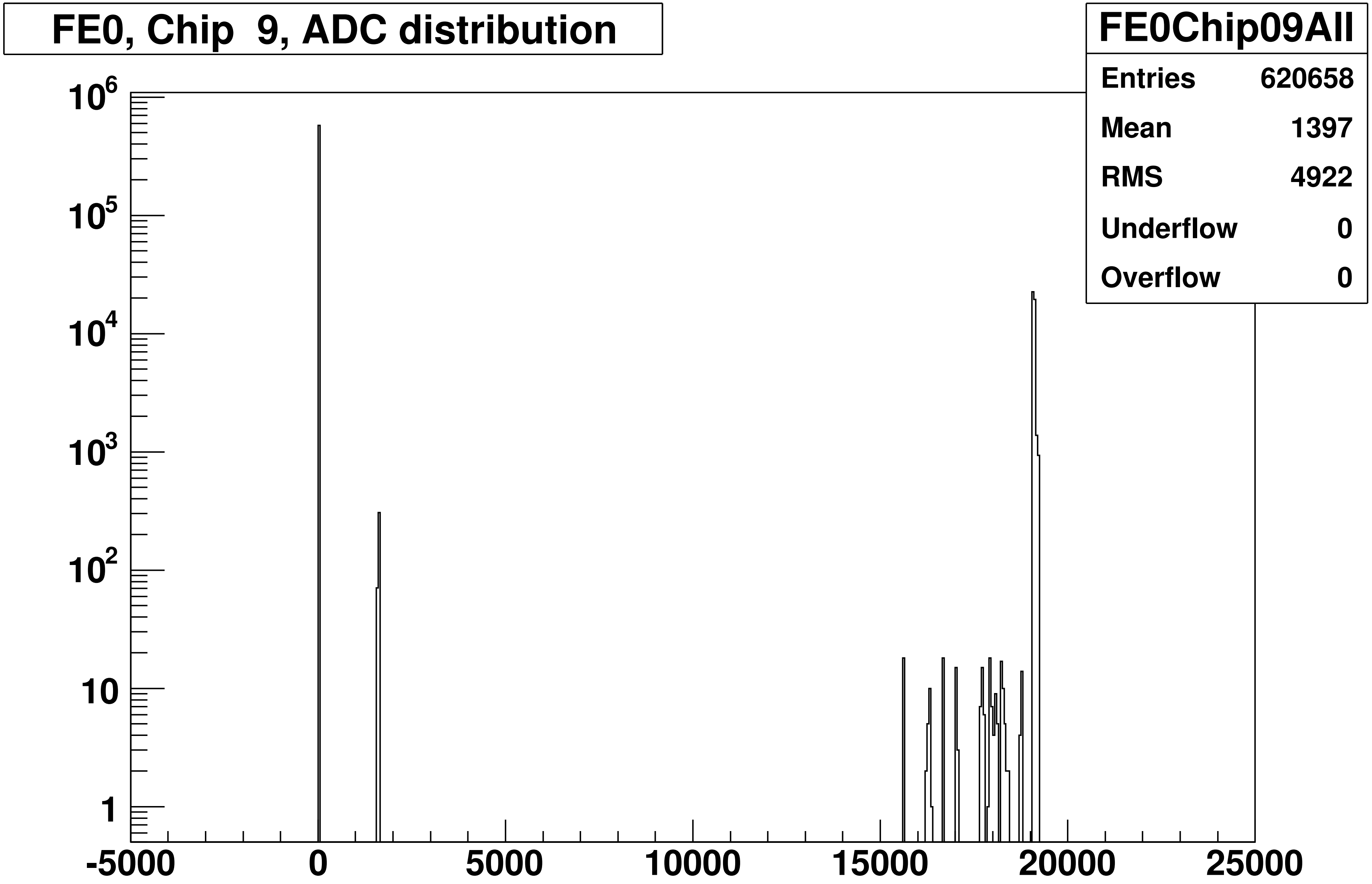
FE0, Chip 9, ADC distribution
FE0Chip09All
Entries 620658
Mean 1397
RMS 4922
Underflow 0
Overflow 0
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10^2
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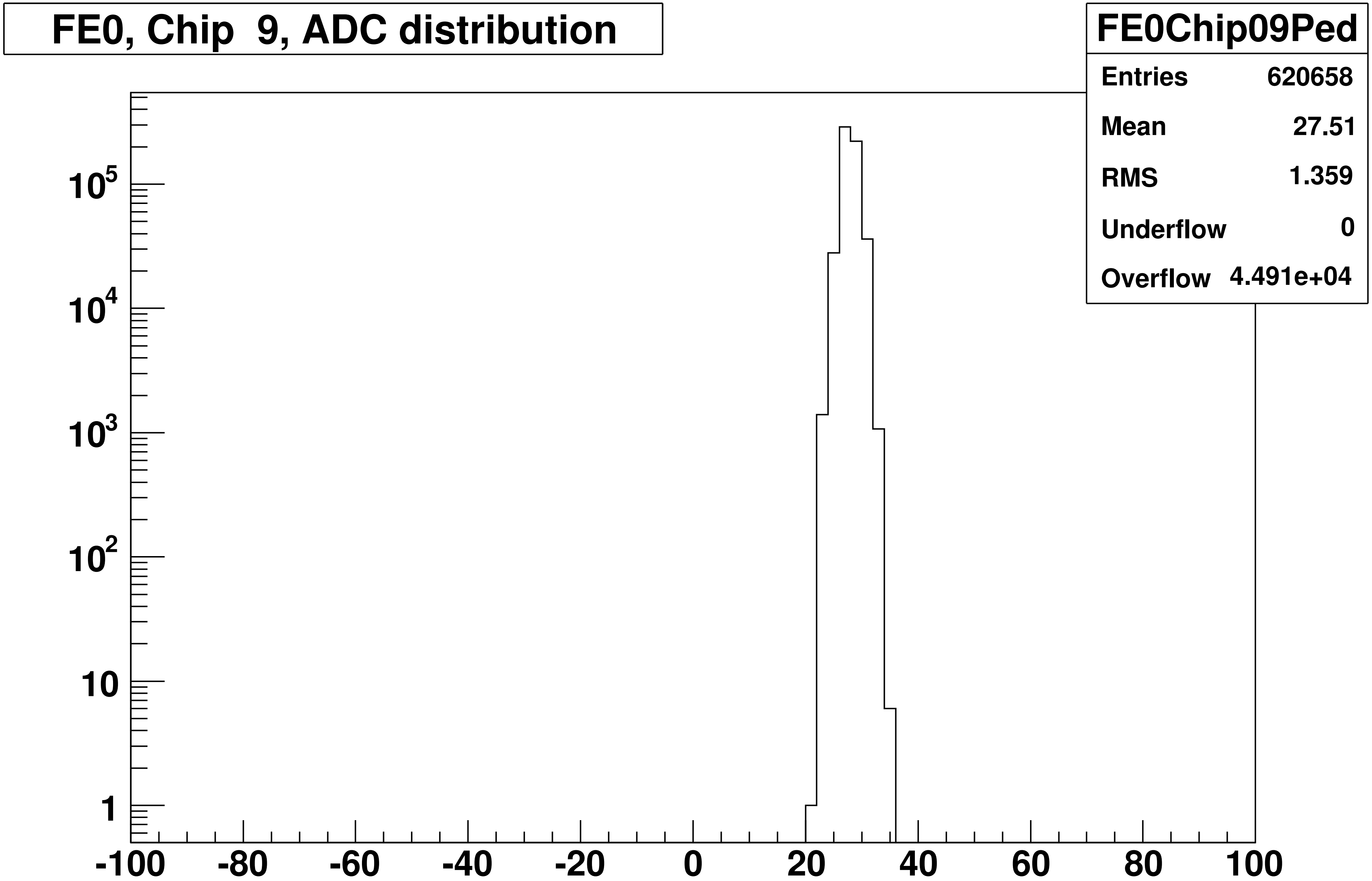
10^4
10^5
10^6


FE0, Chip 9, ADC distribution
FE0Chip09Ped
Entries 620658
Mean 27.51
RMS 1.359
Underflow 0
Overflow 4.491e+04
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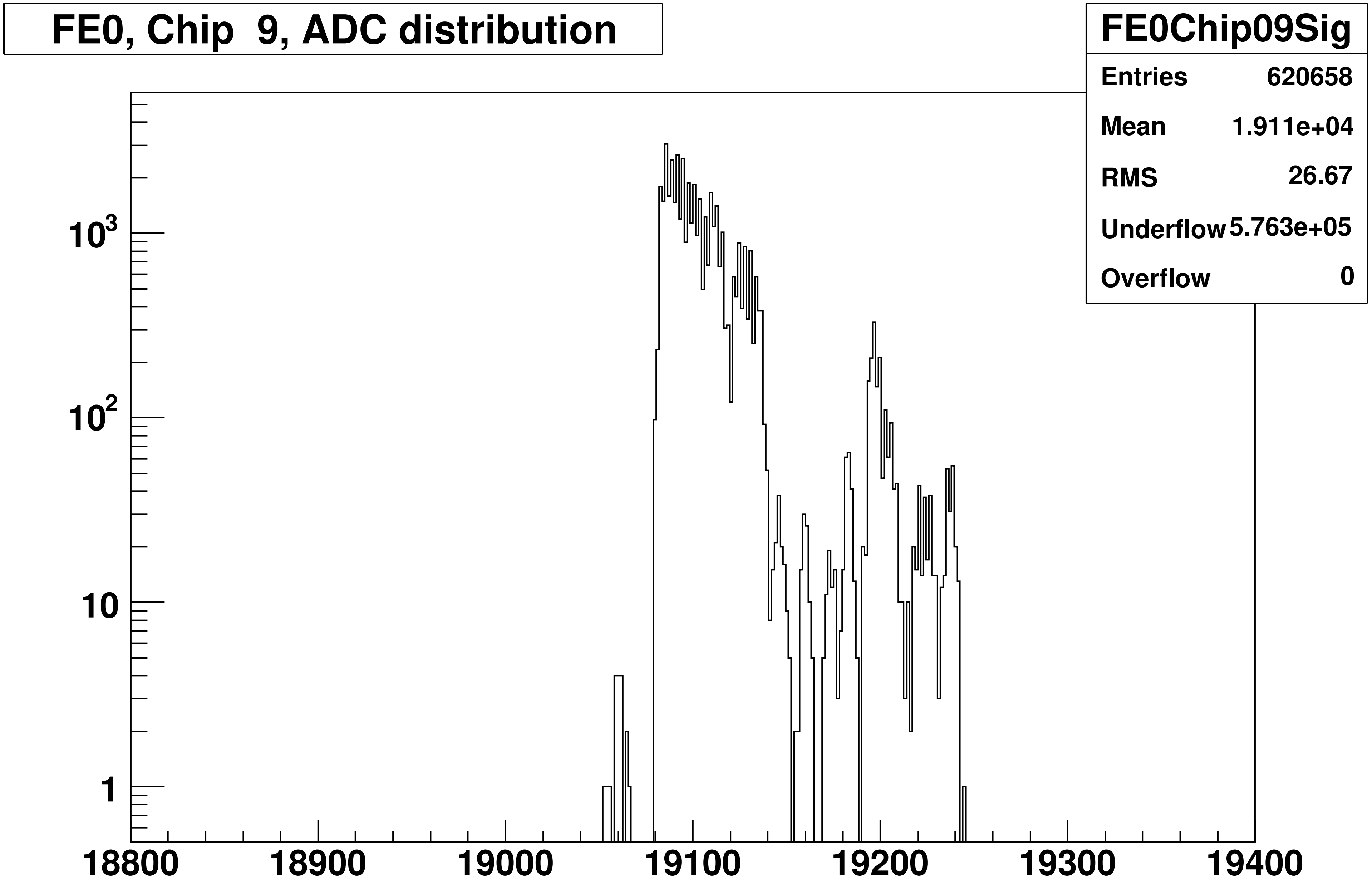
FE0, Chip 9, ADC distribution
FE0Chip09Sig
Entries 620658
Mean 1.911e+04
RMS 26.67
Underflow 5.763e+05
Overflow 0
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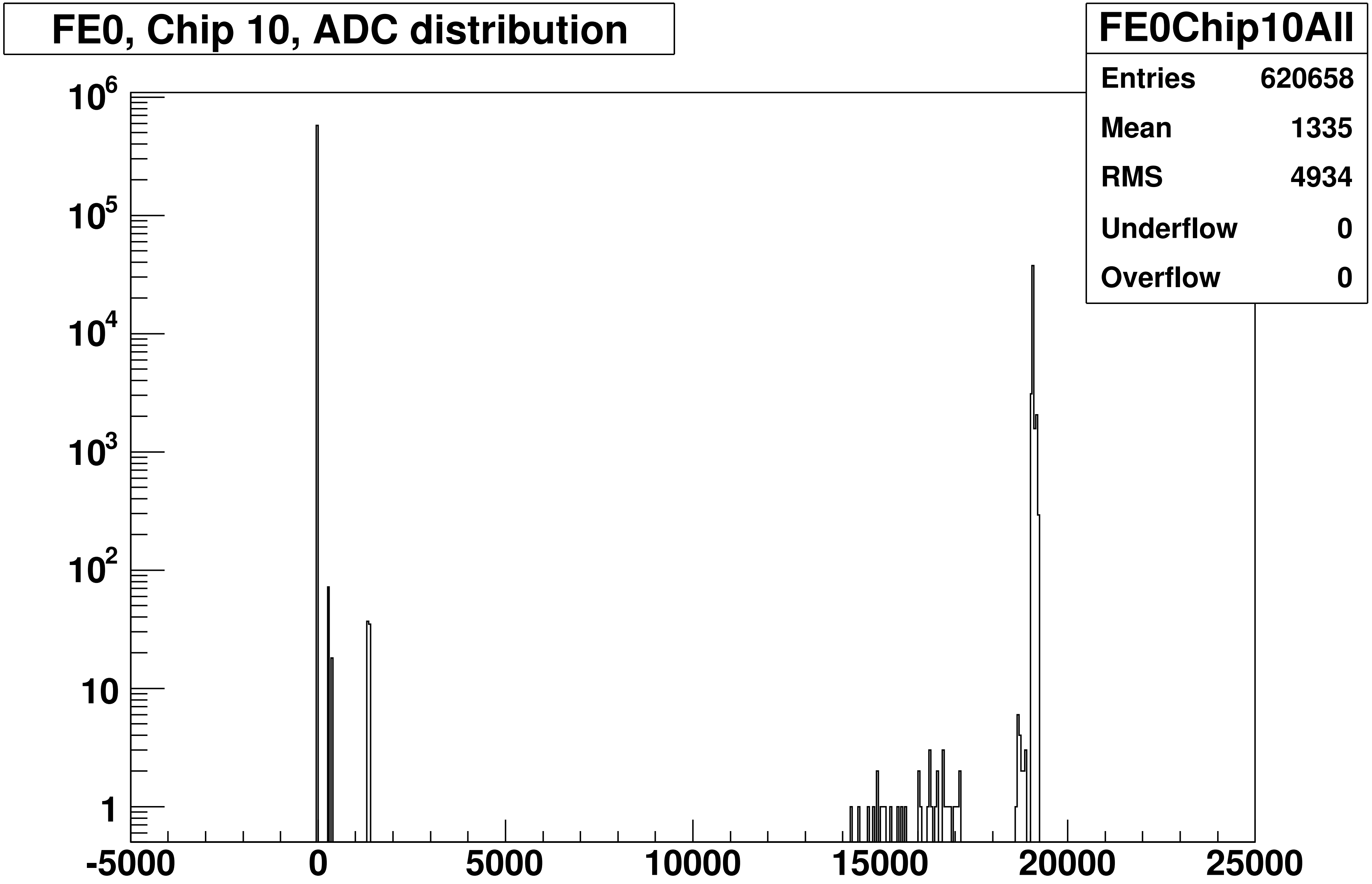
FE0, Chip 10, ADC distribution
FE0Chip10All
Entries 620658
Mean 1335
RMS 4934
Underflow 0
Overflow 0
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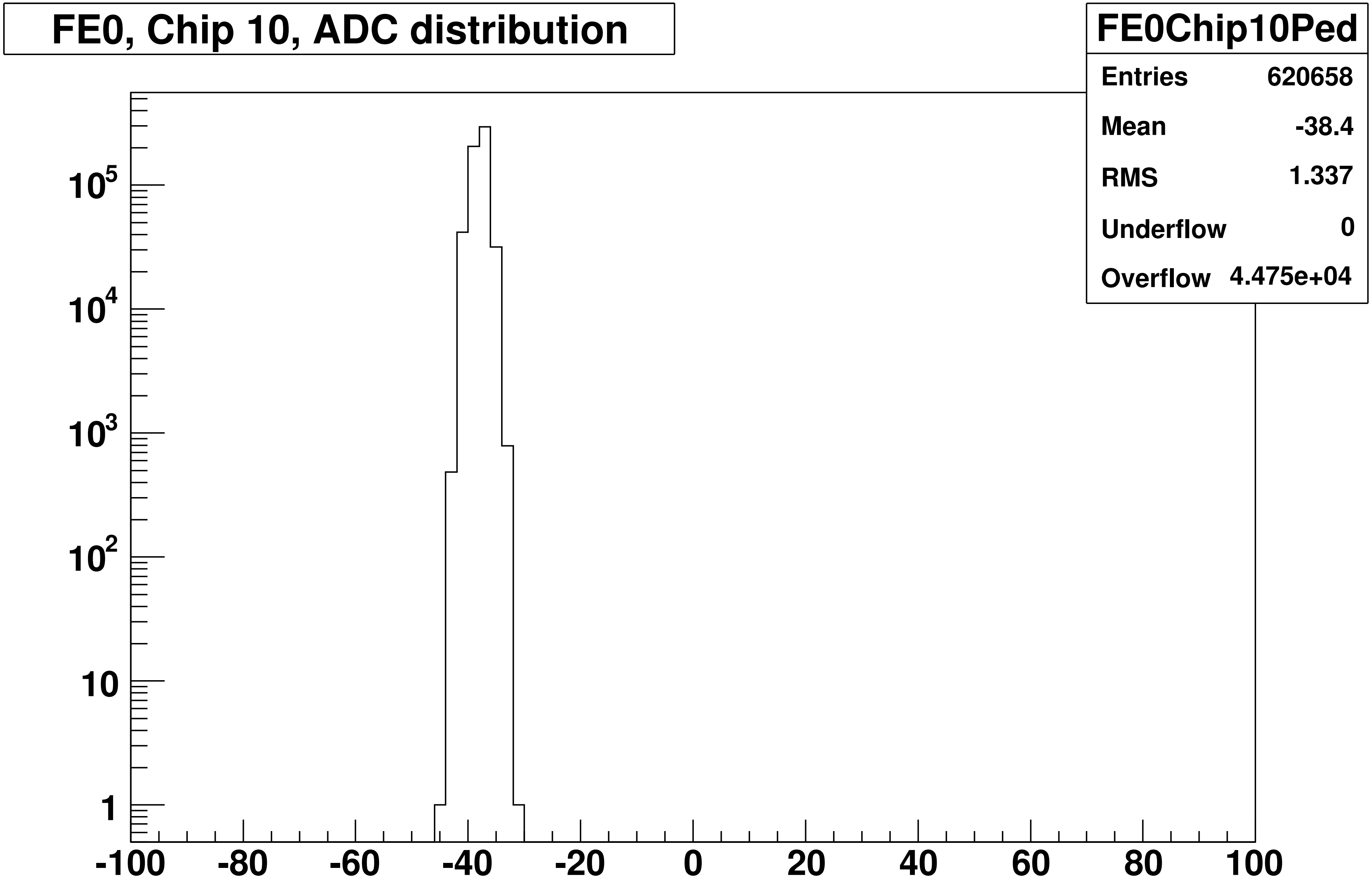
FE0, Chip 10, ADC distribution
FE0Chip10Ped
Entries 620658
Mean -38.4
RMS 1.337
Underflow 0
Overflow 4.475e+04
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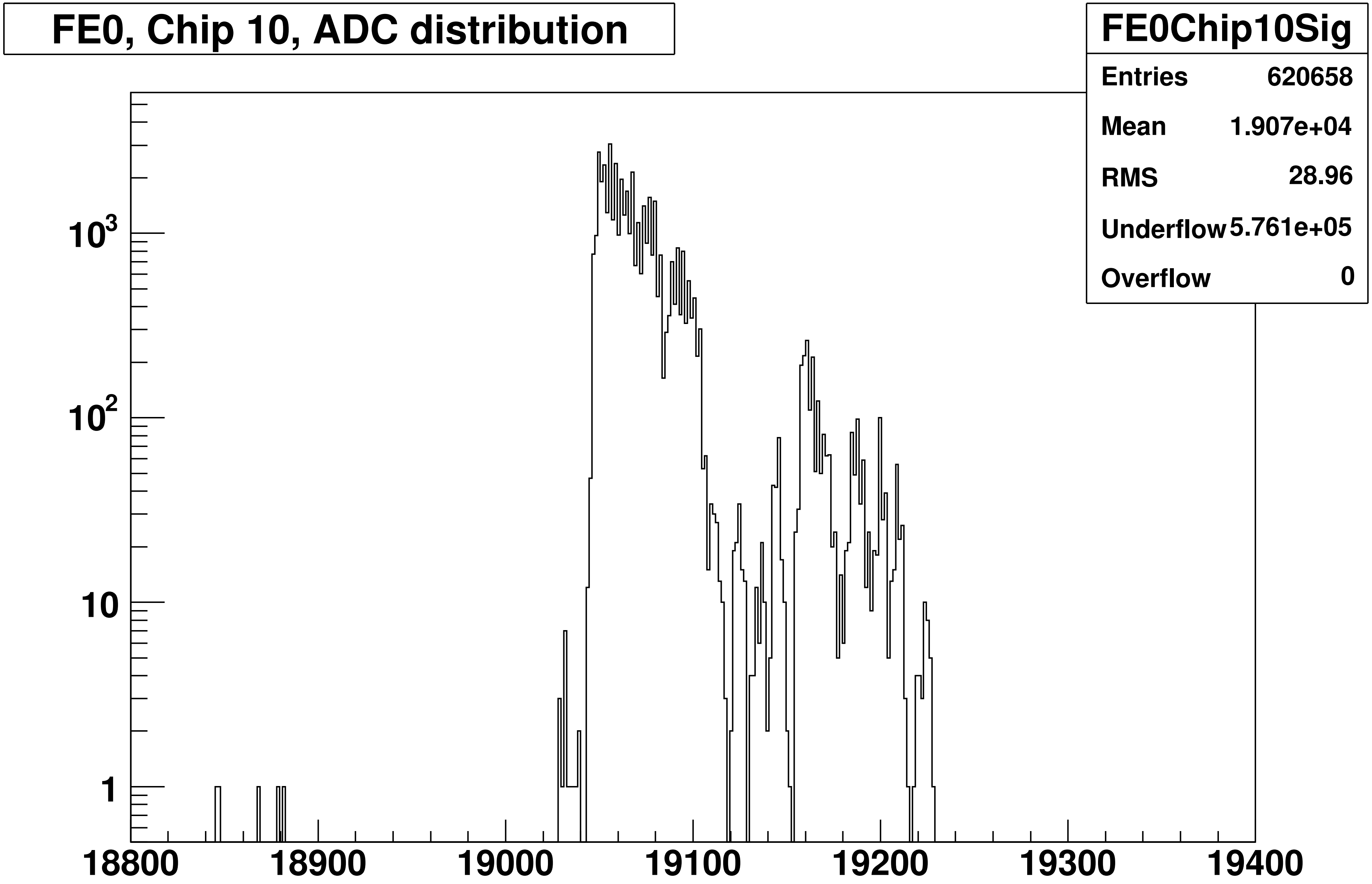
FE0, Chip 10, ADC distribution
FE0Chip10Sig
Entries 620658
Mean 1.907e+04
RMS 28.96
Underflow 5.761e+05
Overflow 0
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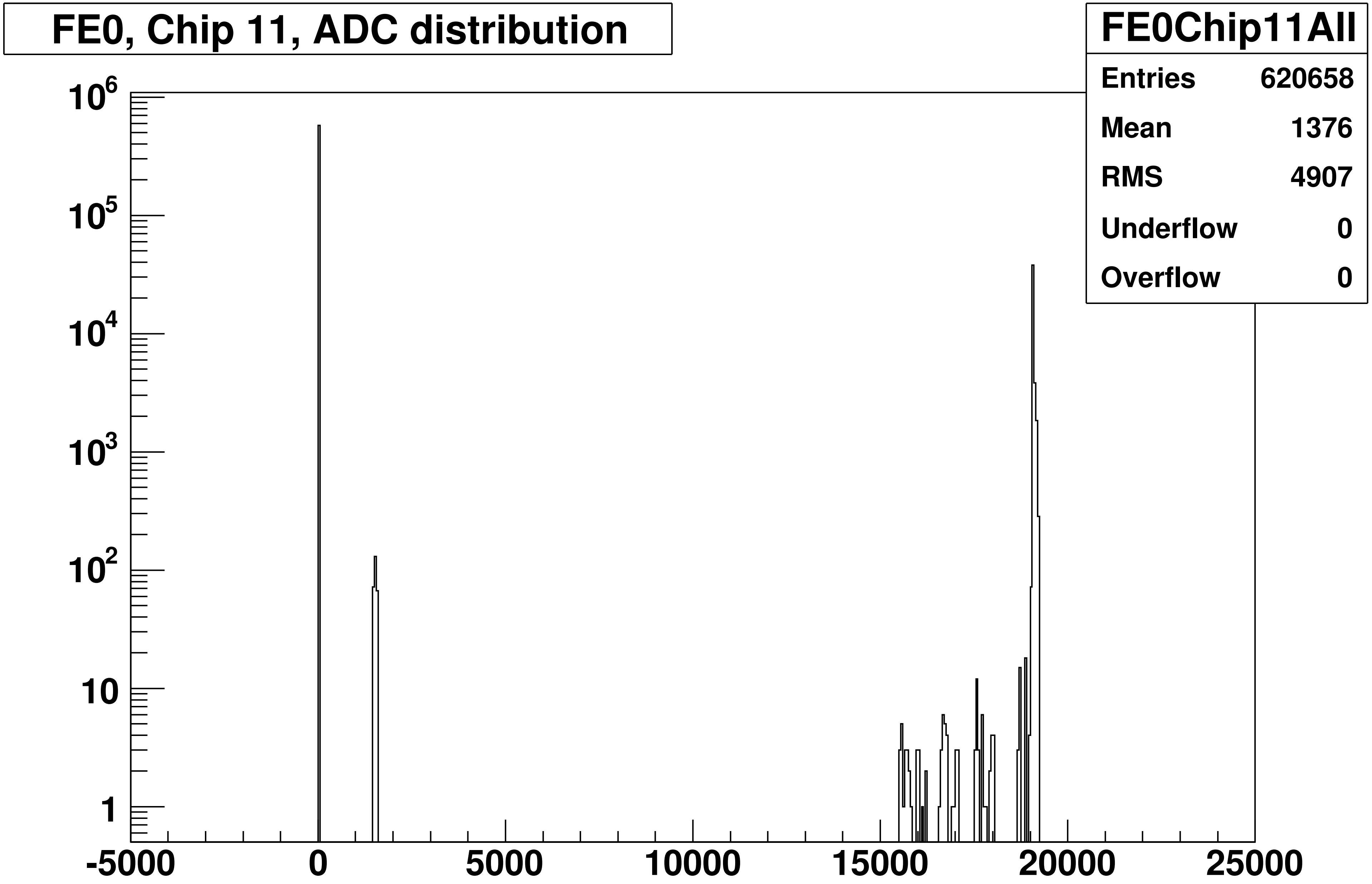
FE0, Chip 11, ADC distribution
FE0Chip11All
Entries 620658
Mean 1376
RMS 4907
Underflow 0
Overflow 0
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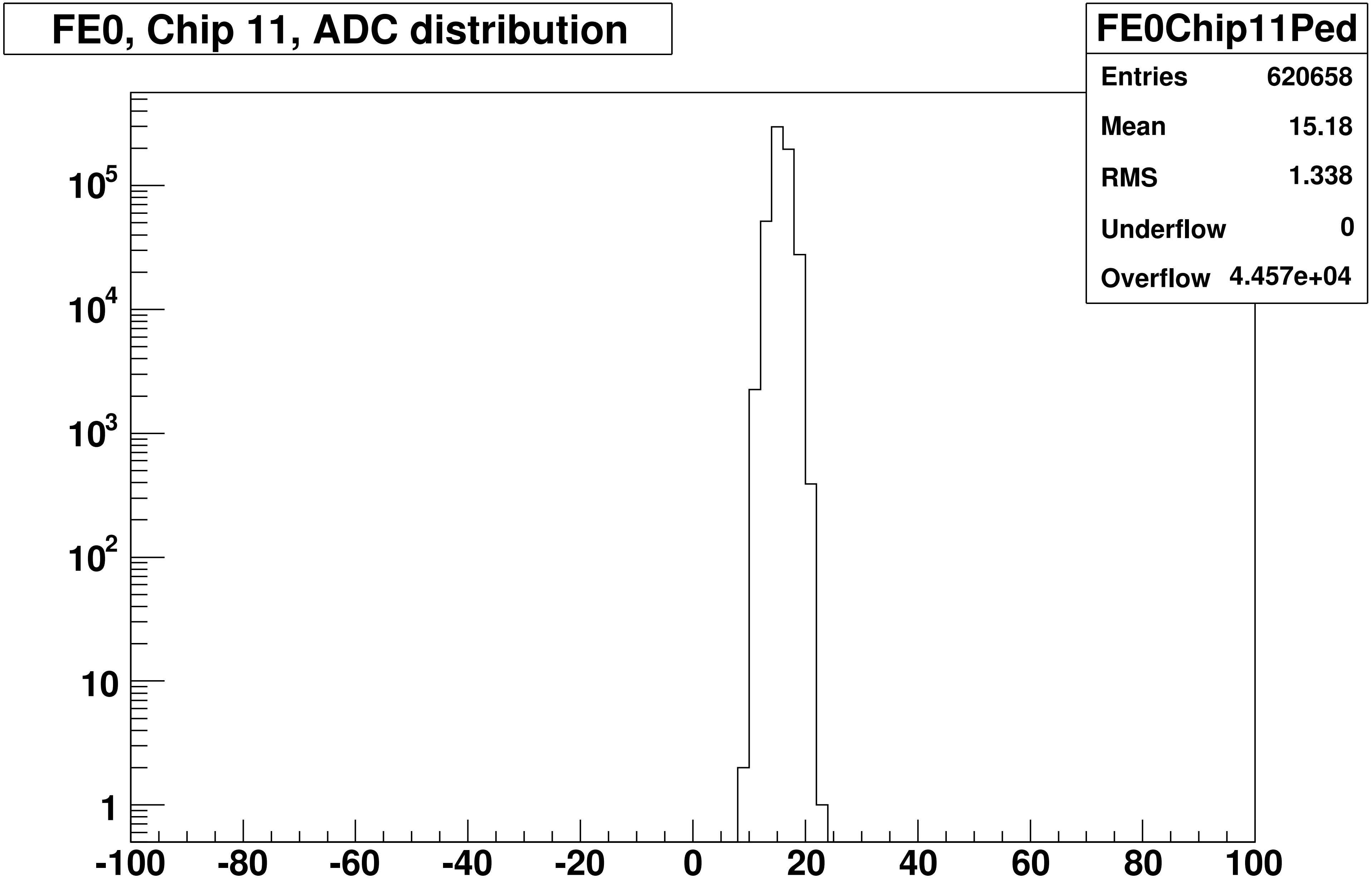
FE0, Chip 11, ADC distribution
FE0Chip11Ped
Entries 620658
Mean 15.18
RMS 1.338
Underflow 0
Overflow 4.457e+04
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10^3
10^2
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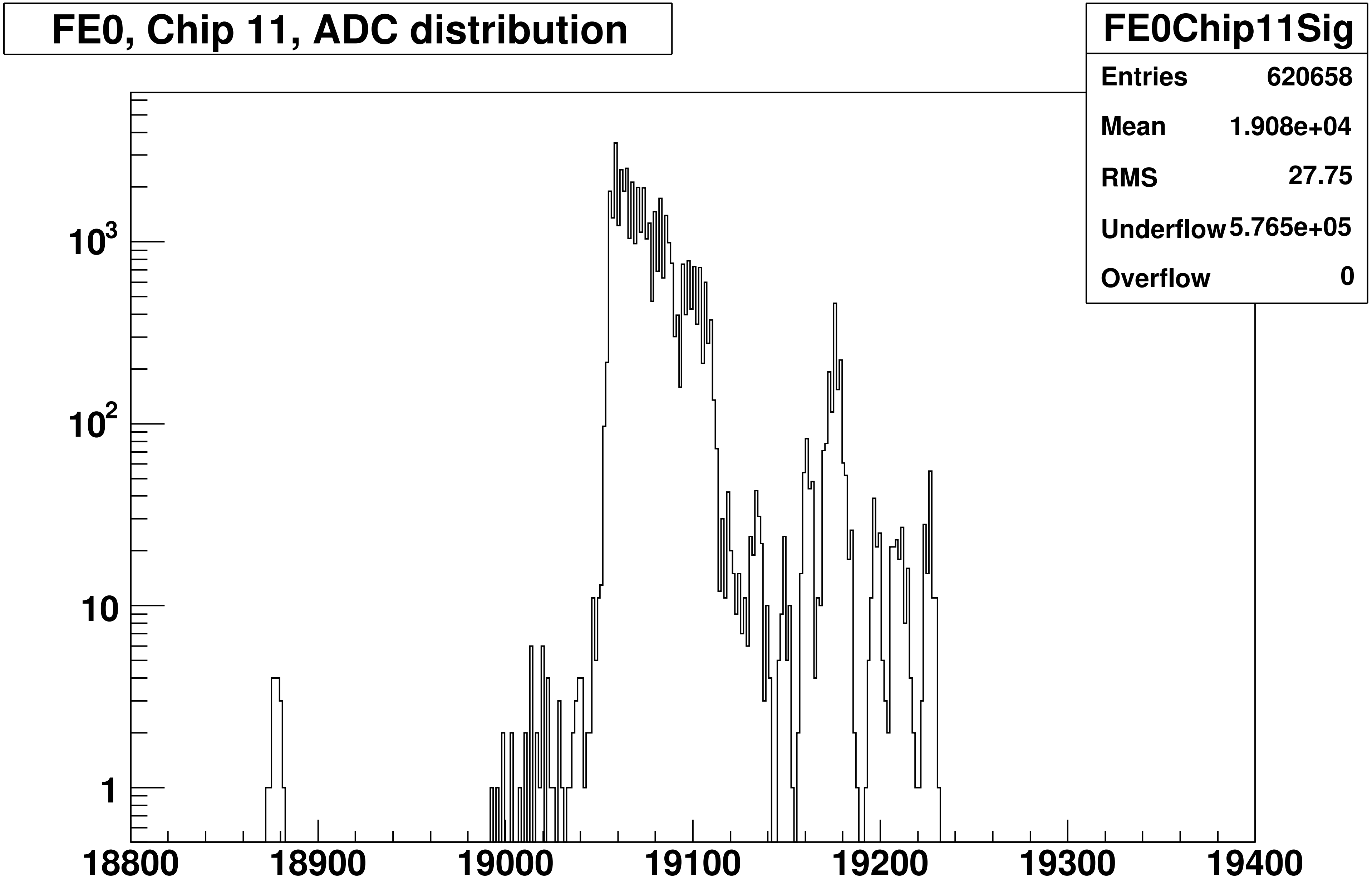
FE0, Chip 11, ADC distribution
FE0Chip11Sig
Entries 620658
Mean 1.908e+04
RMS 27.75
Underflow 5.765e+05
Overflow 0
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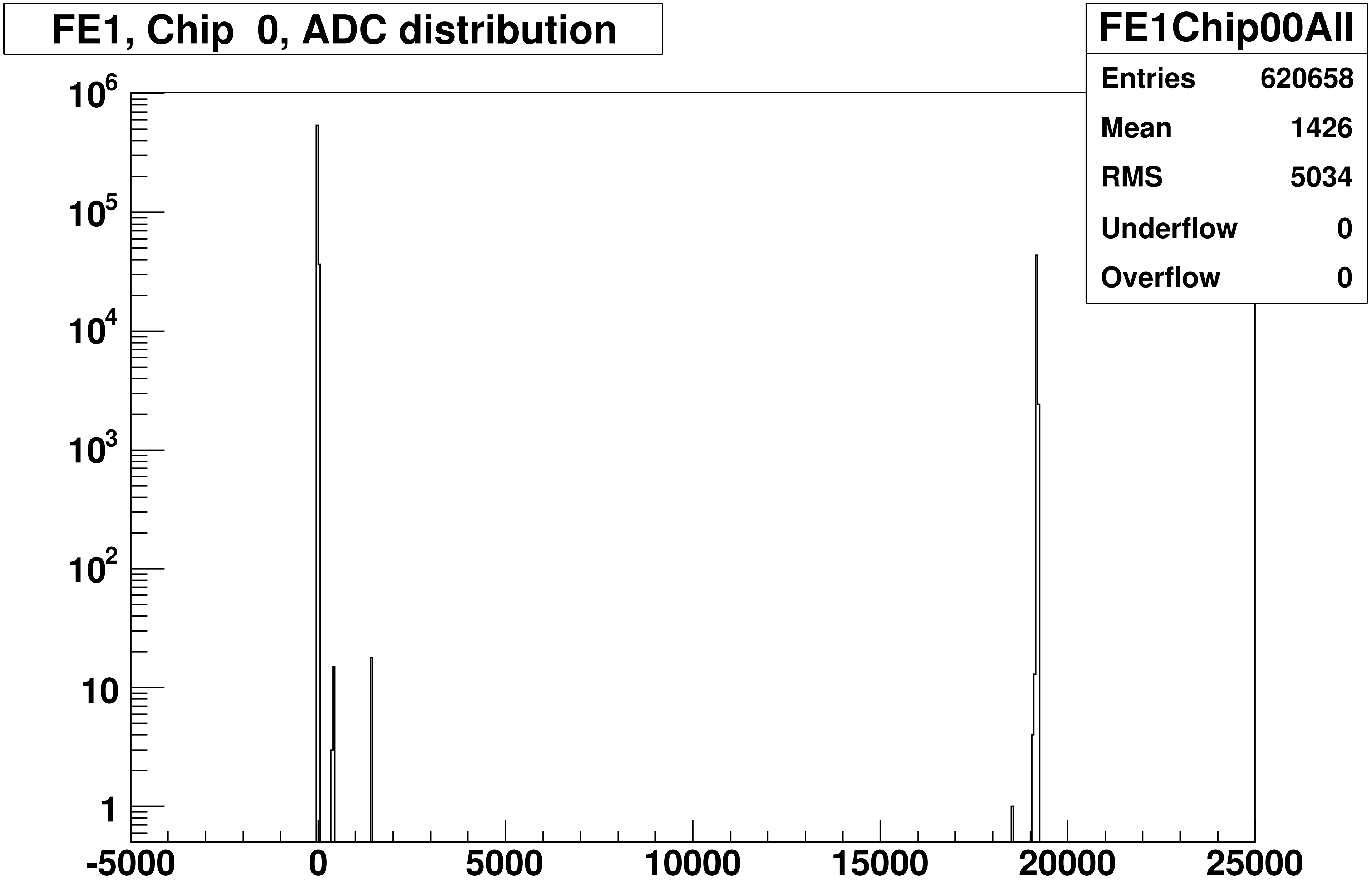
FE1, Chip 0, ADC distribution
FE1Chip00All
Entries 620658
Mean 1426
RMS 5034
Underflow 0
Overflow 0
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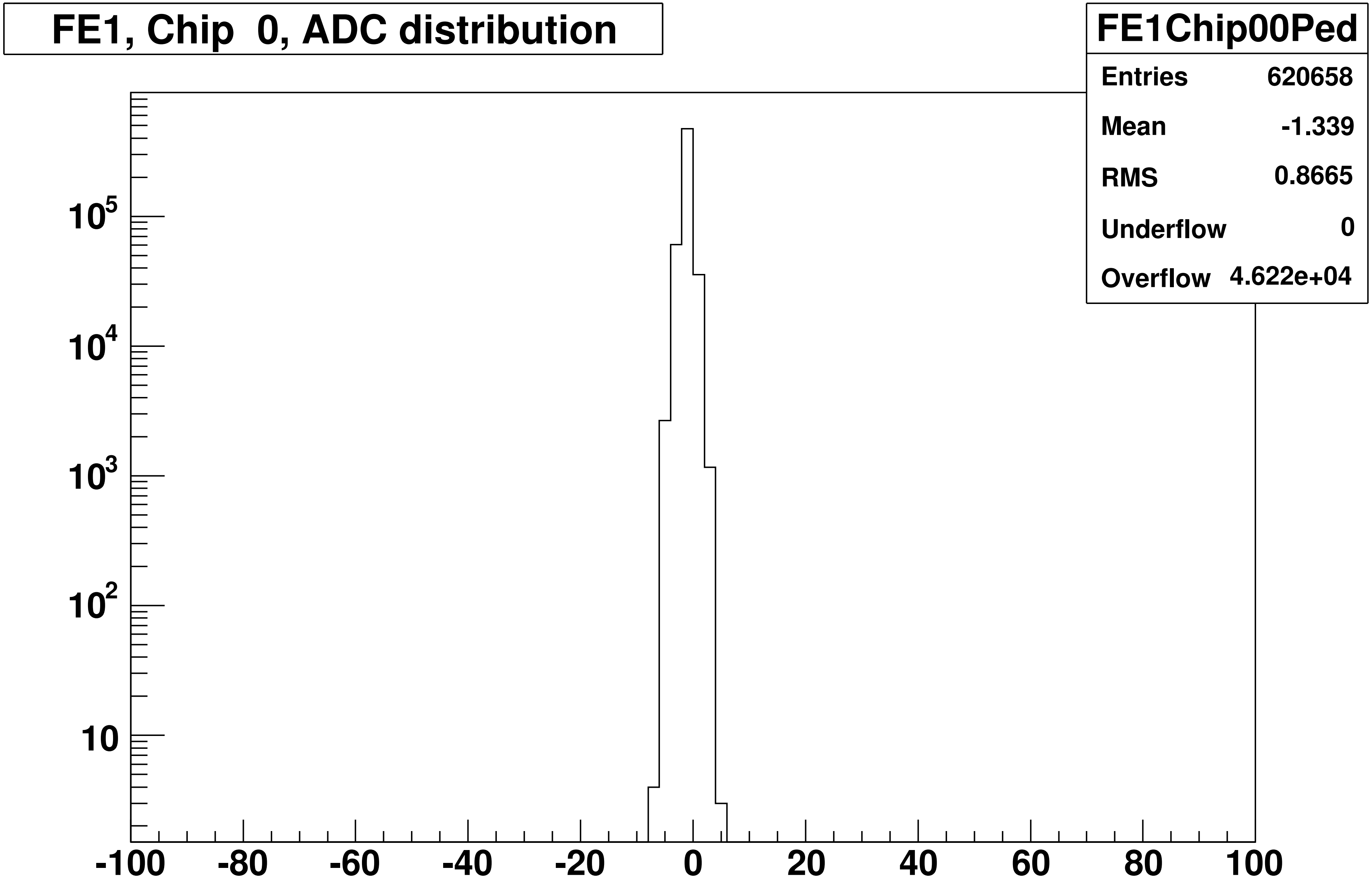
FE1, Chip 0, ADC distribution
FE1Chip00Ped
Entries 620658
Mean -1.339
RMS 0.8665
Underflow 0
Overflow 4.622e+04
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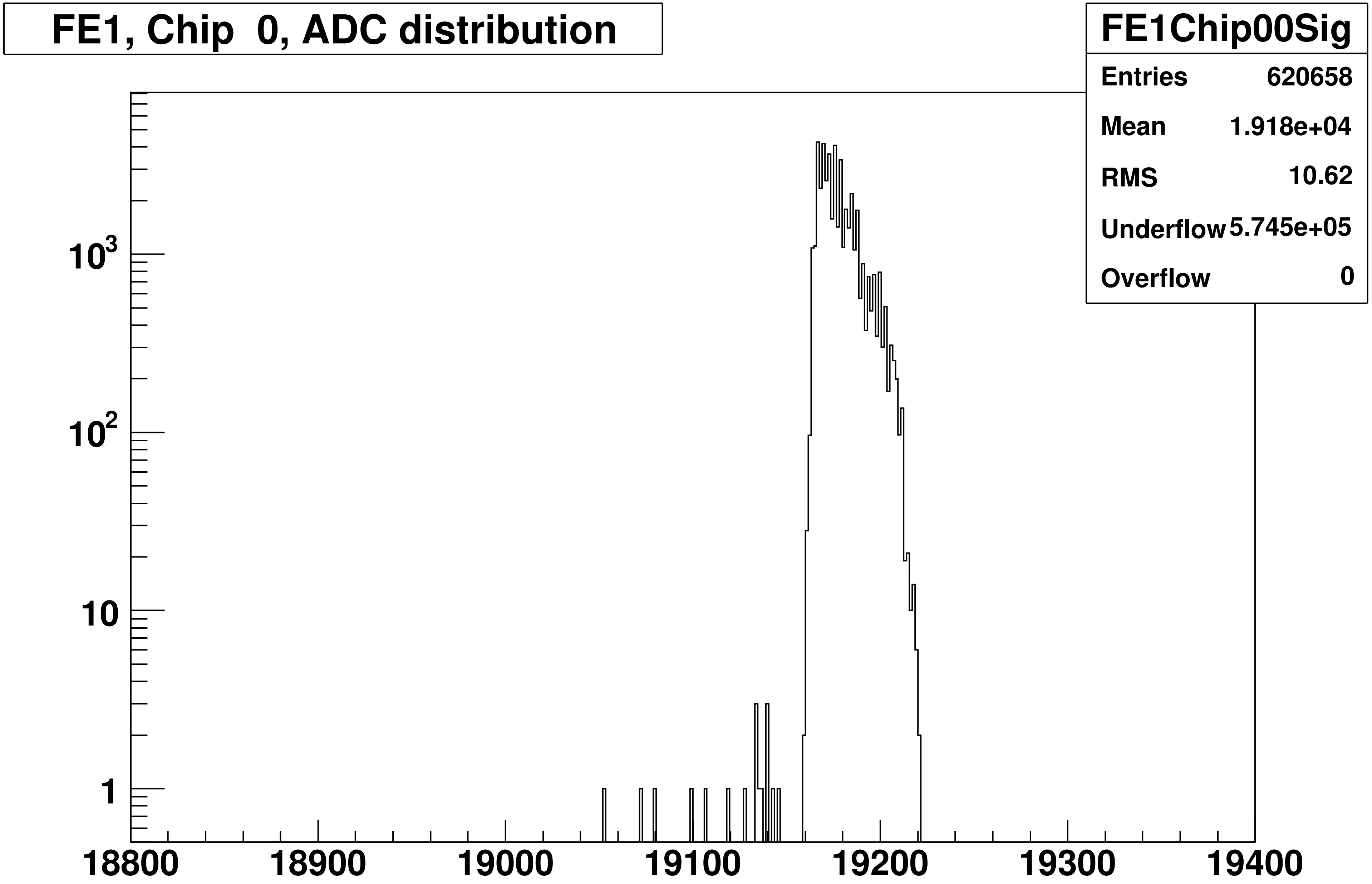
FE1, Chip 0, ADC distribution
FE1Chip00Sig
Entries 620658
Mean 1.918e+04
RMS 10.62
Underflow 5.745e+05
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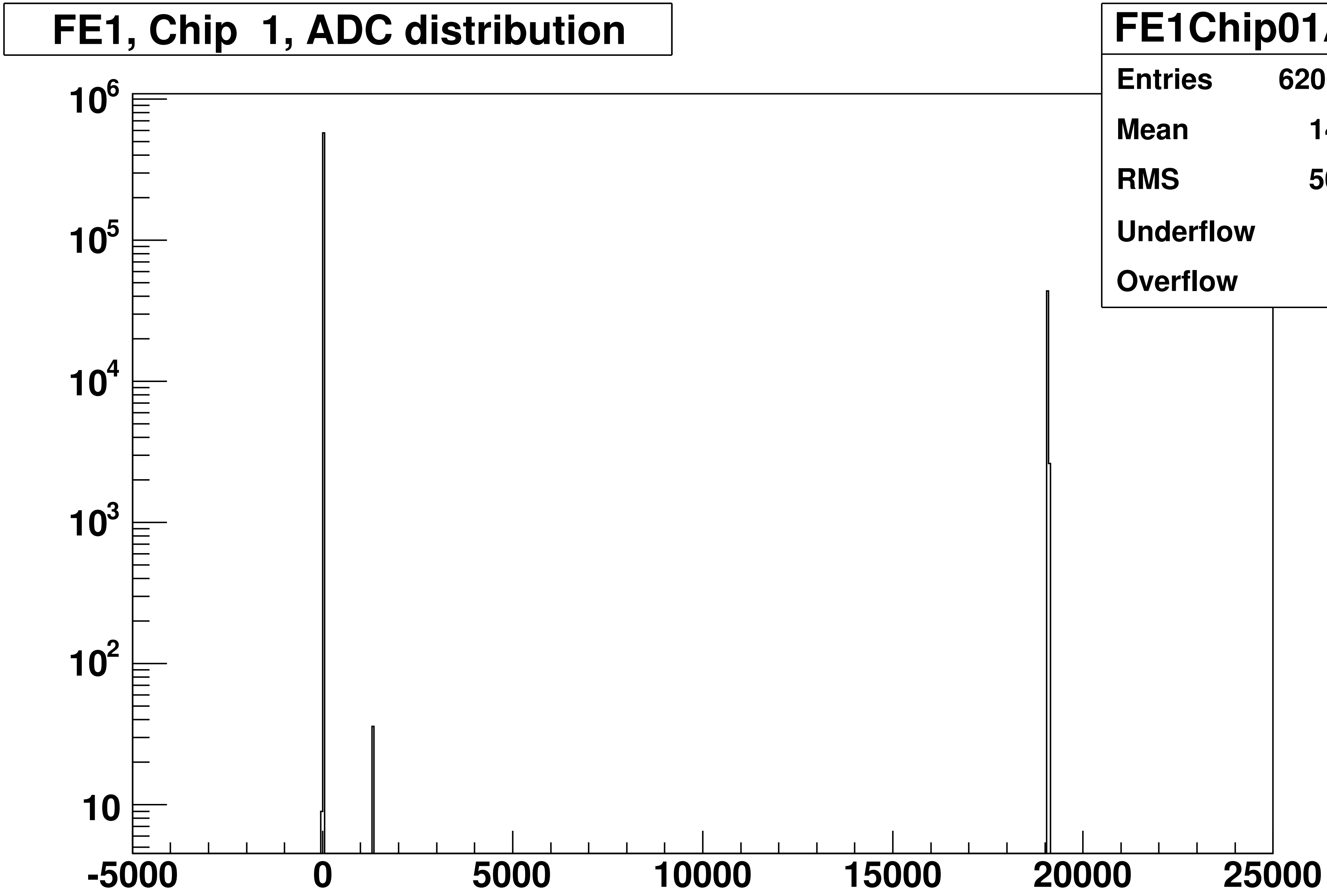
FE1, Chip 1, ADC distribution
FE1Chip01All
Entries 620658
Mean 1426
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Underflow 0
Overflow 0
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10^2
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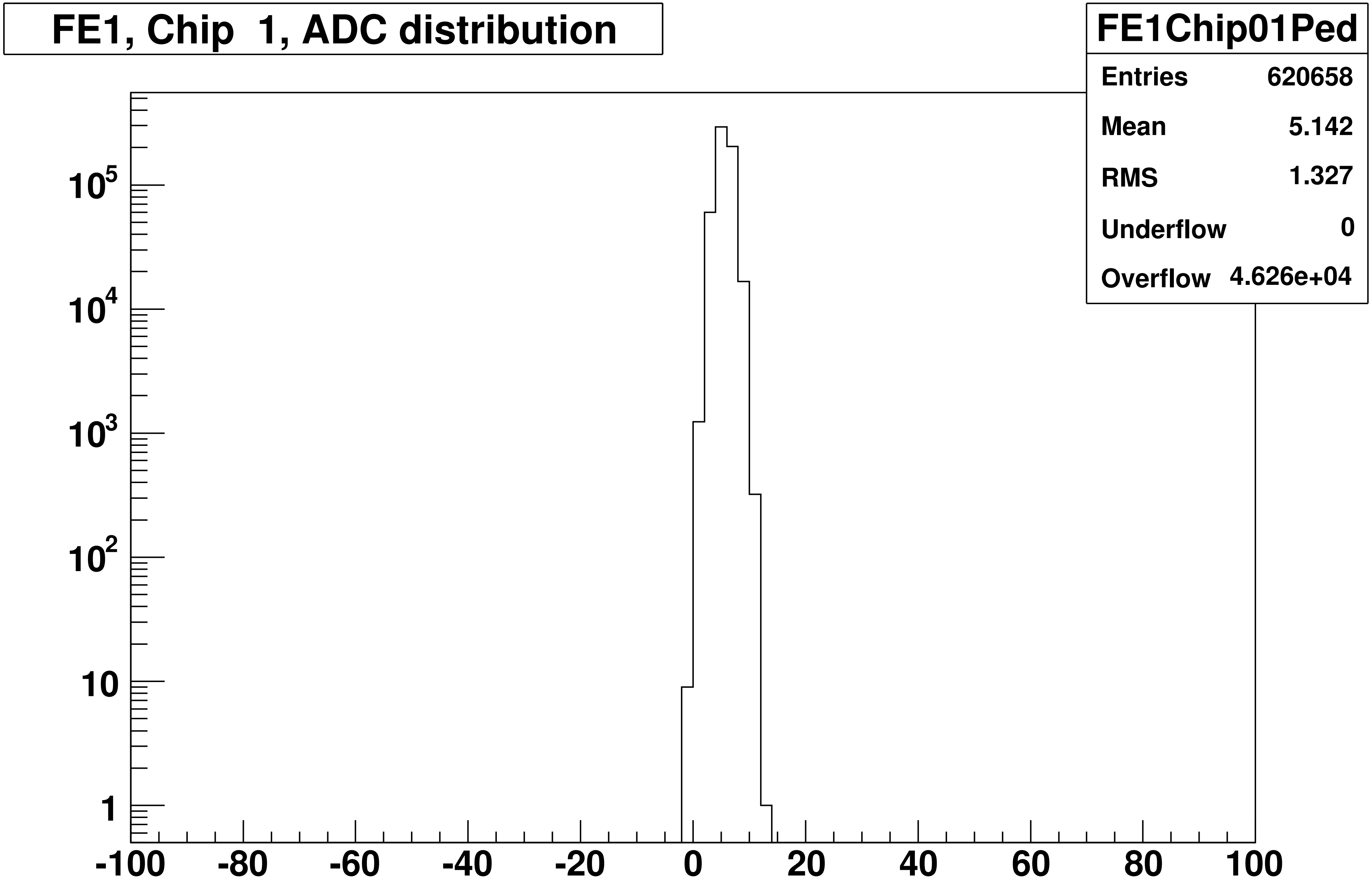
20000
25000


FE1, Chip 1, ADC distribution
FE1Chip01Ped
Entries 620658
Mean 5.142
RMS 1.327
Underflow 0
Overflow 4.626e+04
10^5
10^4
10^3
10^2
10
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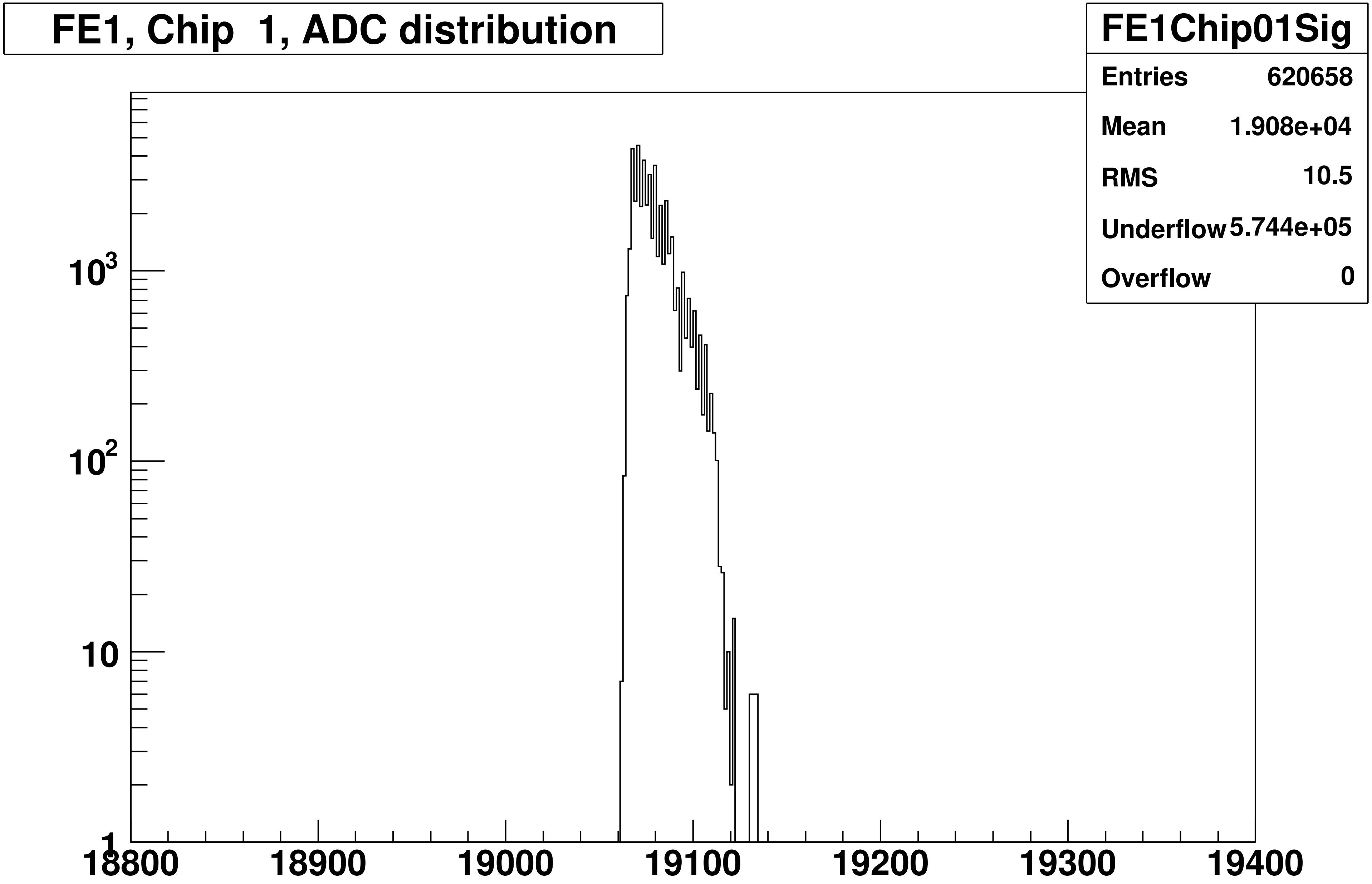

FE1, Chip 1, ADC distribution
FE1Chip01Sig
Entries 620658
Mean 1.908e+04
RMS 10.5
Underflow 5.744e+05
Overflow 0
10^3
10^2
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1
18800
18900
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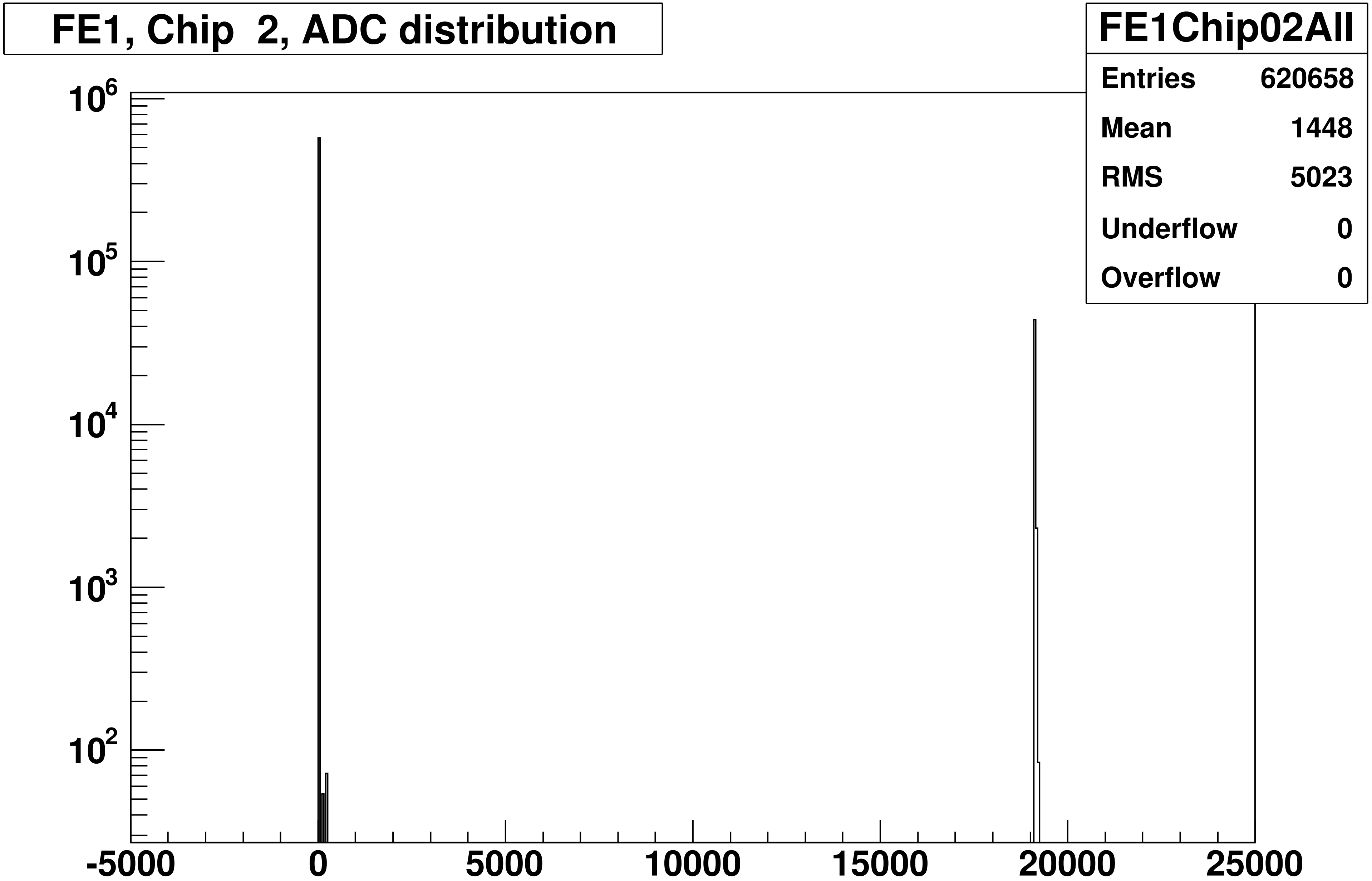
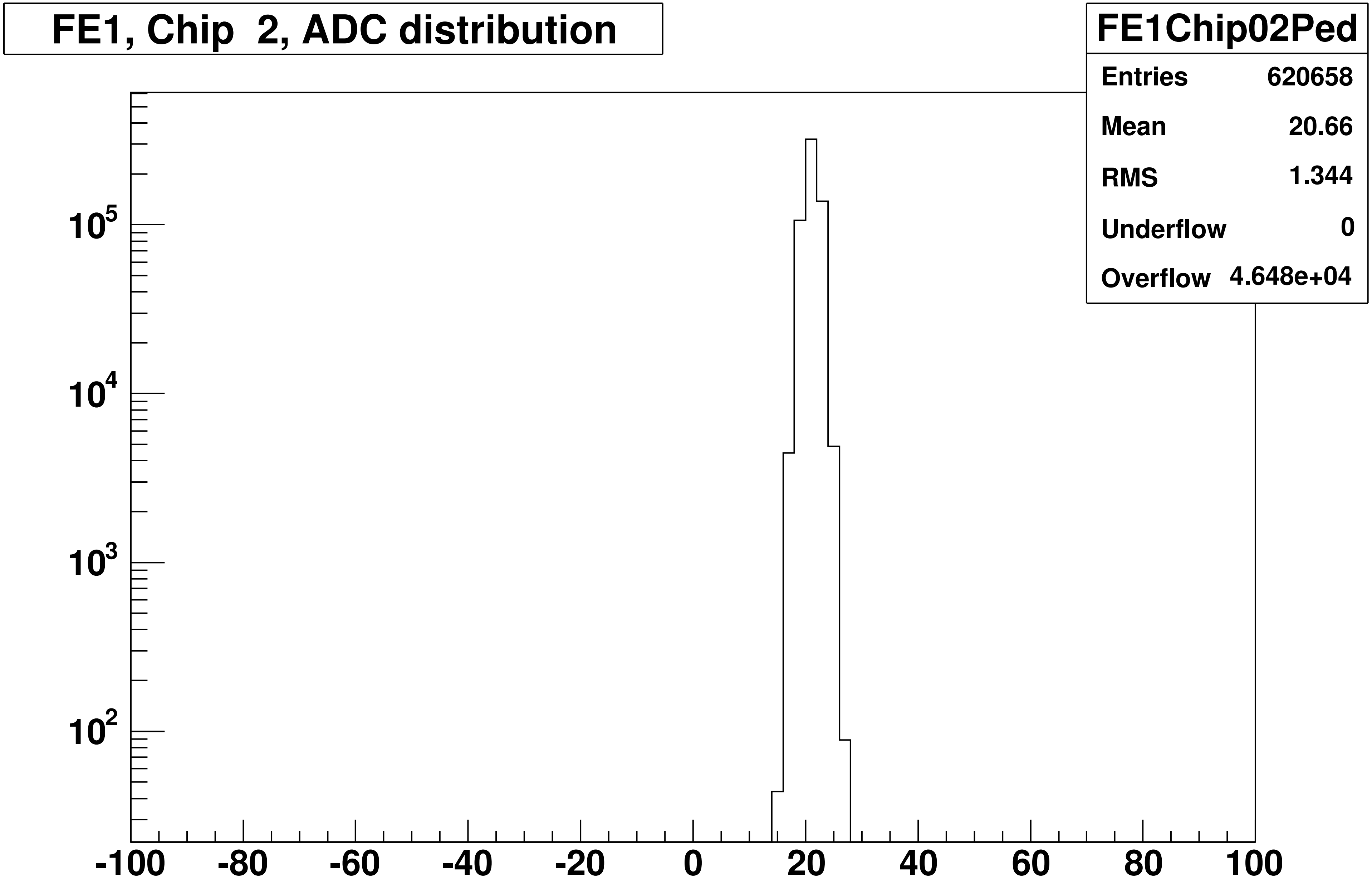
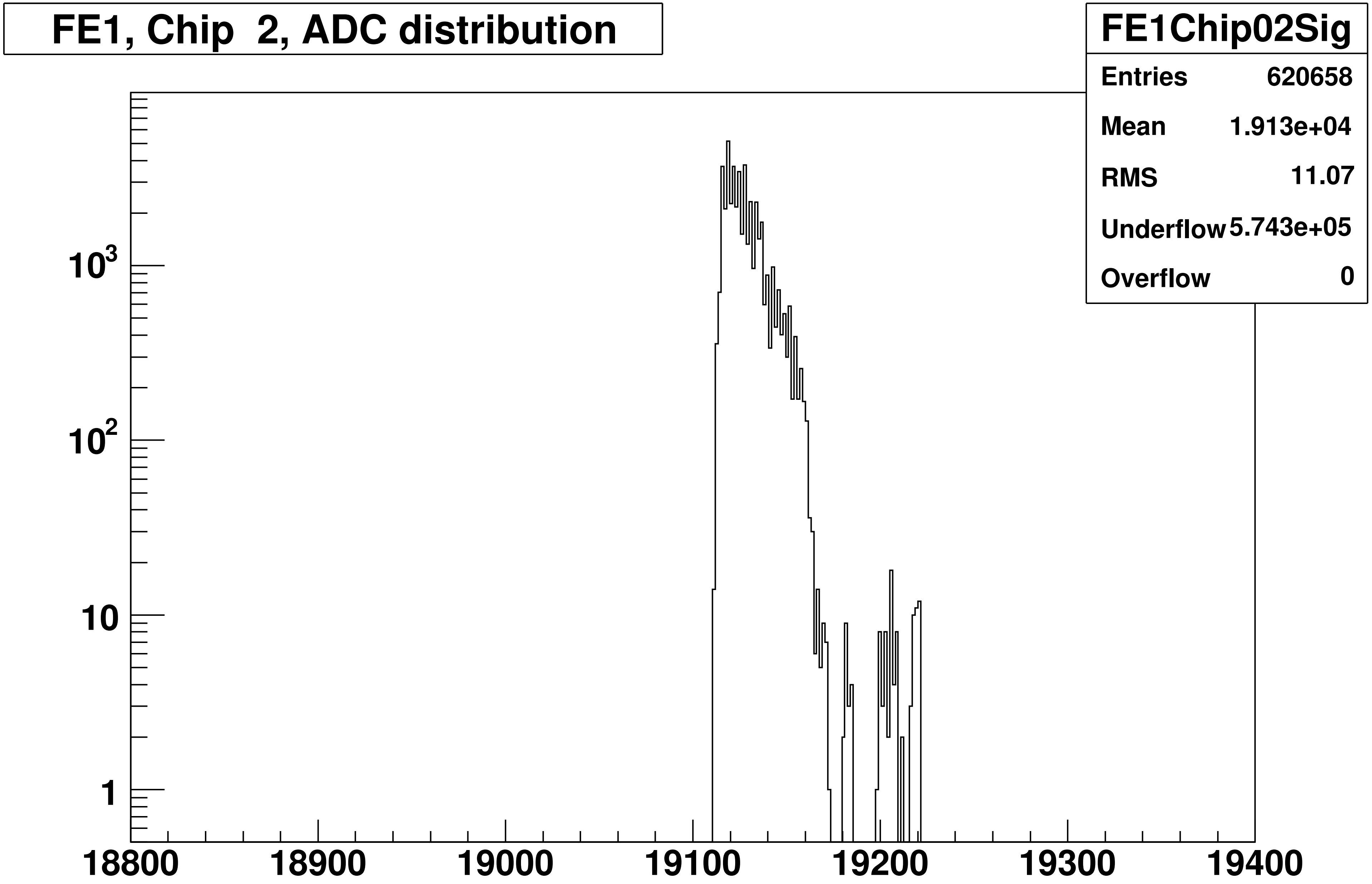

FE1, Chip 2, ADC distribution
FE1Chip02All
Entries 620658
Mean 1448
RMS 5023
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 2, ADC distribution
FE1Chip02Ped
Entries 620658
Mean 20.66
RMS 1.344
Underflow 0
Overflow 4.648e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE1, Chip 2, ADC distribution
FE1Chip02Sig
Entries 620658
Mean 1.913e+04
RMS 11.07
Underflow 5.743e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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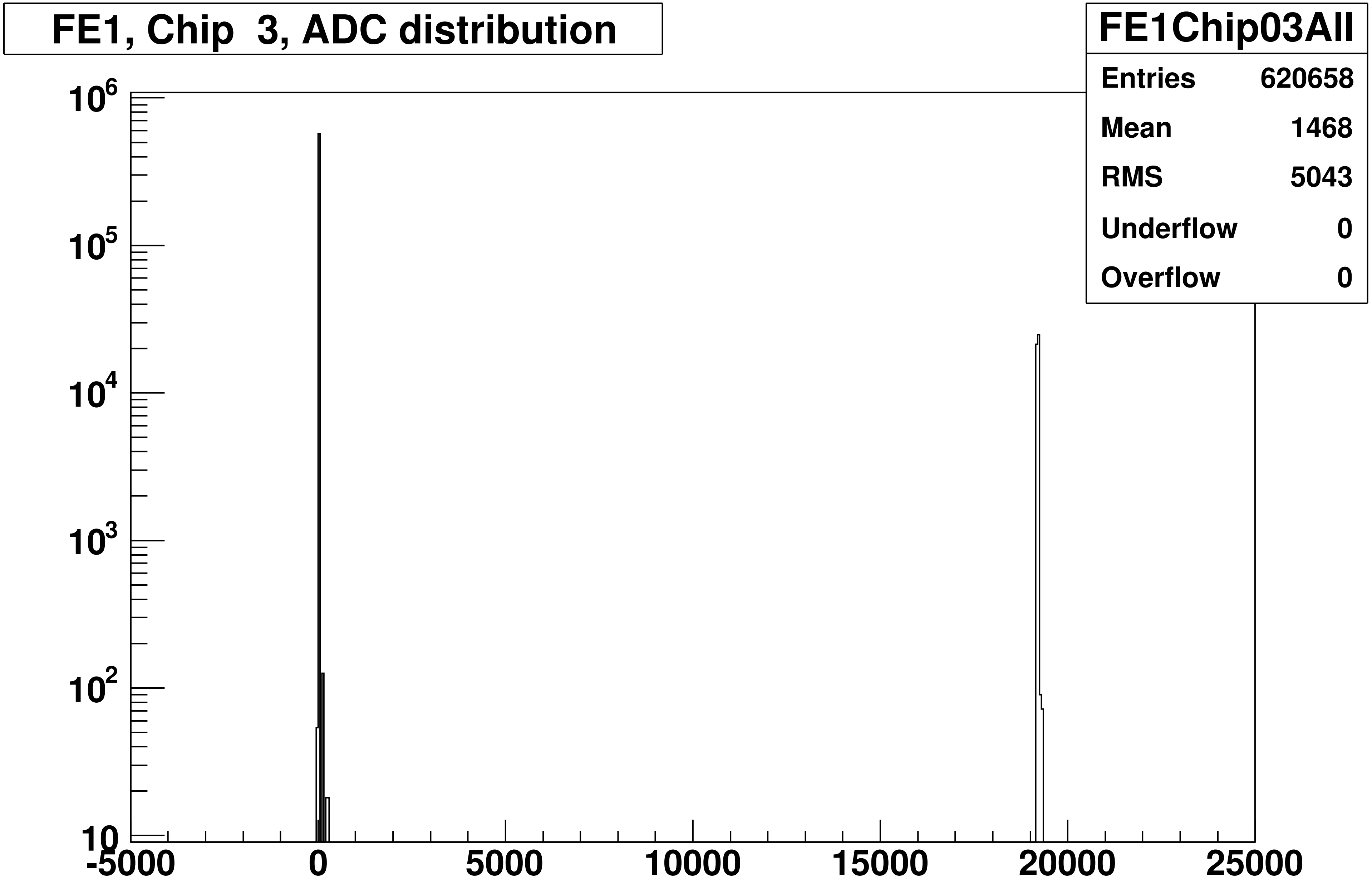
19300
19400



FE1, Chip 3, ADC distribution
FE1Chip03All
Entries 620658
Mean 1468
RMS 5043
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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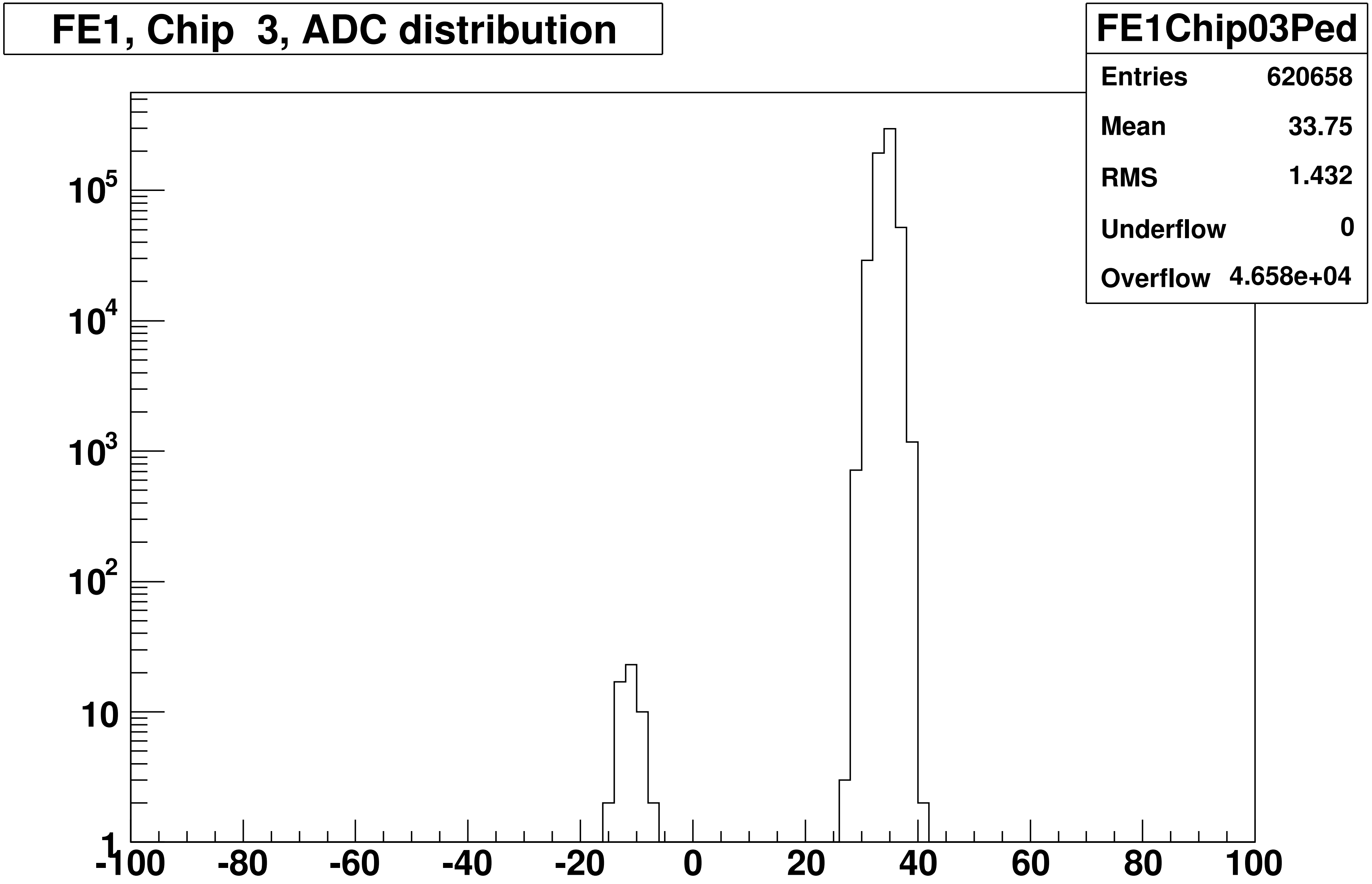
5000
10000
15000
20000
25000


FE1, Chip 3, ADC distribution
FE1Chip03Ped
Entries 620658
Mean 33.75
RMS 1.432
Underflow 0
Overflow 4.658e+04
10^5
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10^3
10^2
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1
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80
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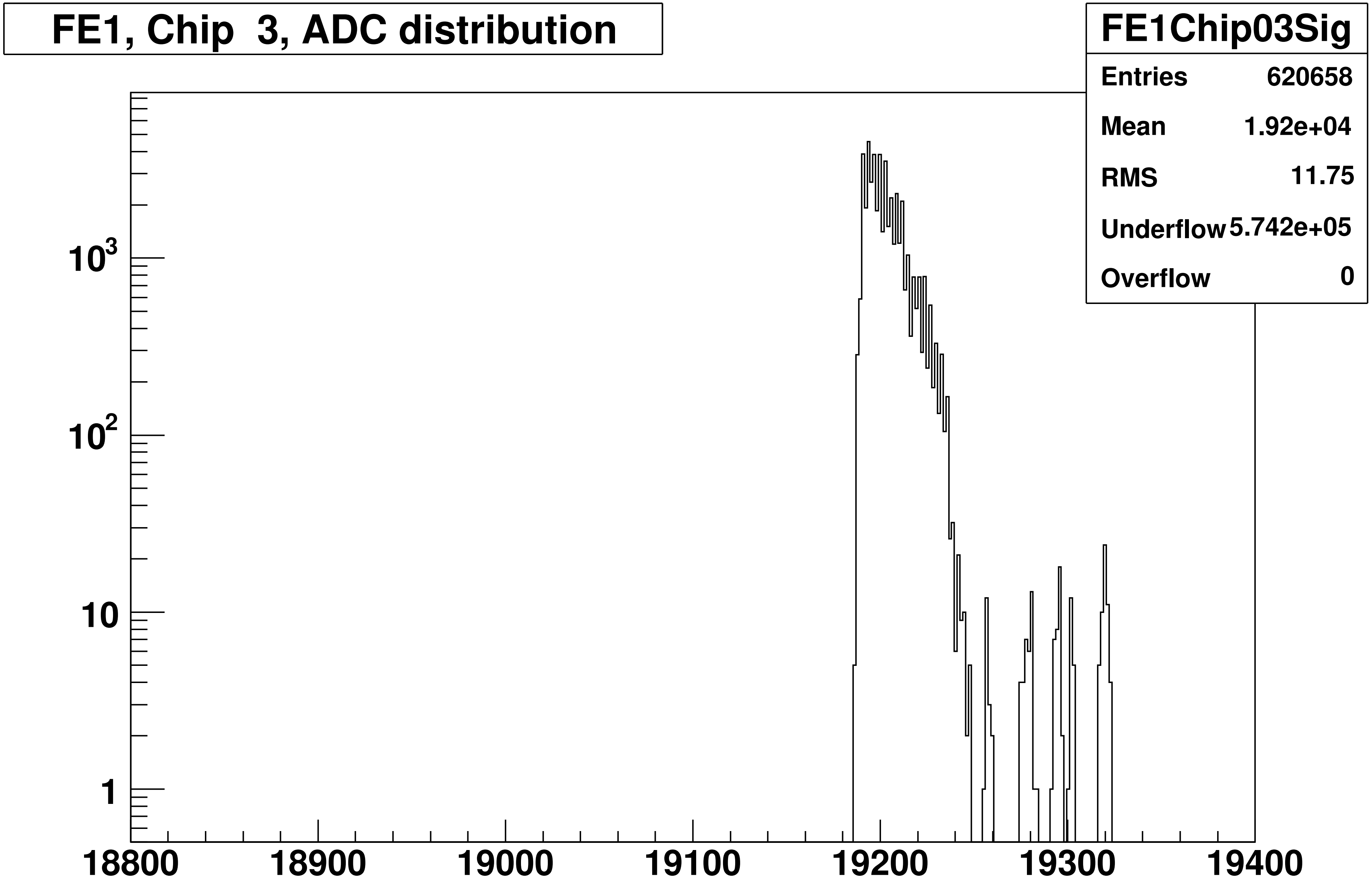
FE1, Chip 3, ADC distribution
FE1Chip03Sig
Entries 620658
Mean 1.92e+04
RMS 11.75
Underflow 5.742e+05
Overflow 0
10^3
10^2
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1
18800
18900
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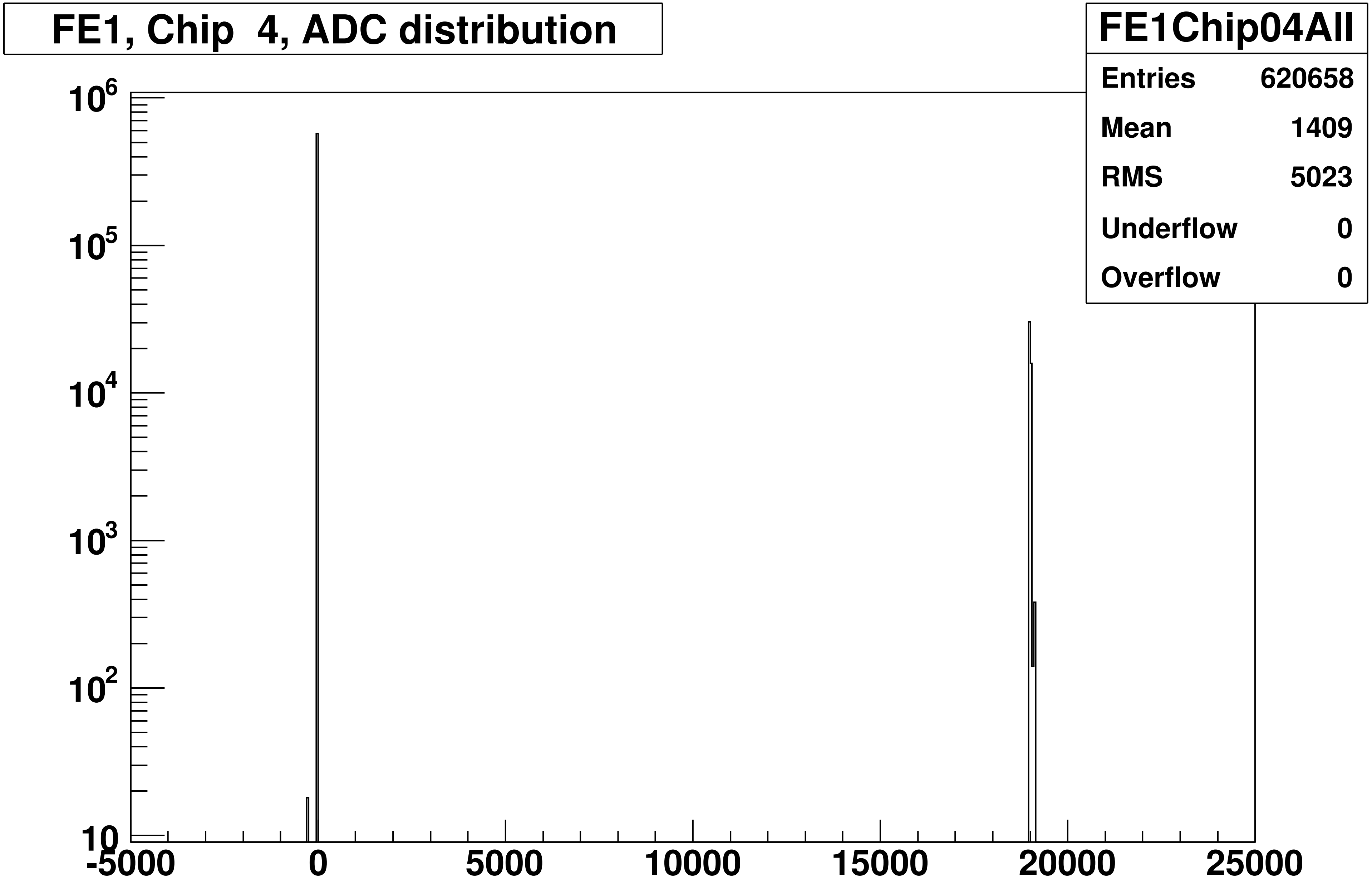

FE1, Chip 4, ADC distribution
FE1Chip04All
Entries 620658
Mean 1409
RMS 5023
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
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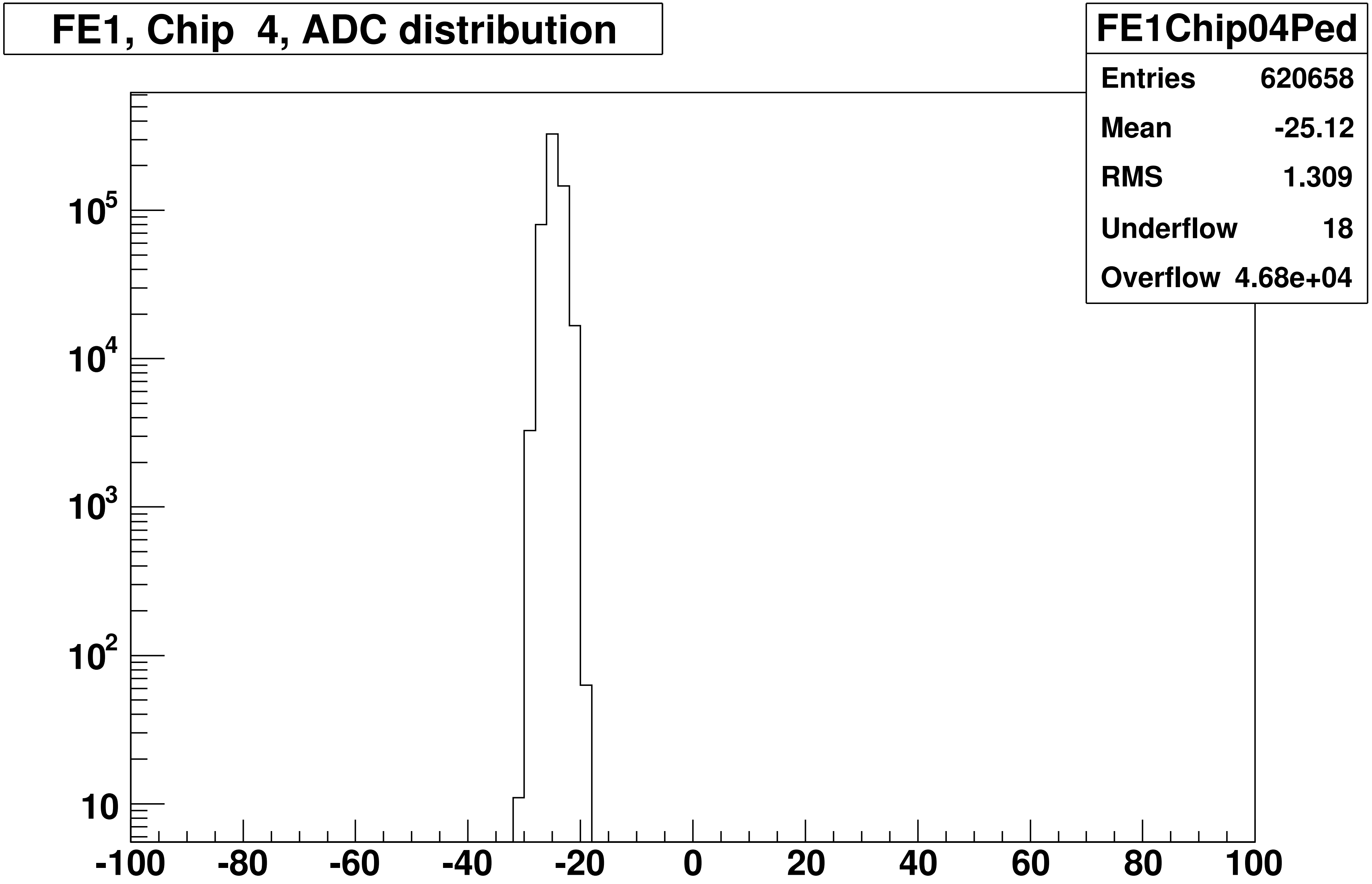
20000
25000


FE1, Chip 4, ADC distribution
FE1Chip04Ped
Entries 620658
Mean -25.12
RMS 1.309
Underflow 18
Overflow 4.68e+04
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10^3
10^2
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80
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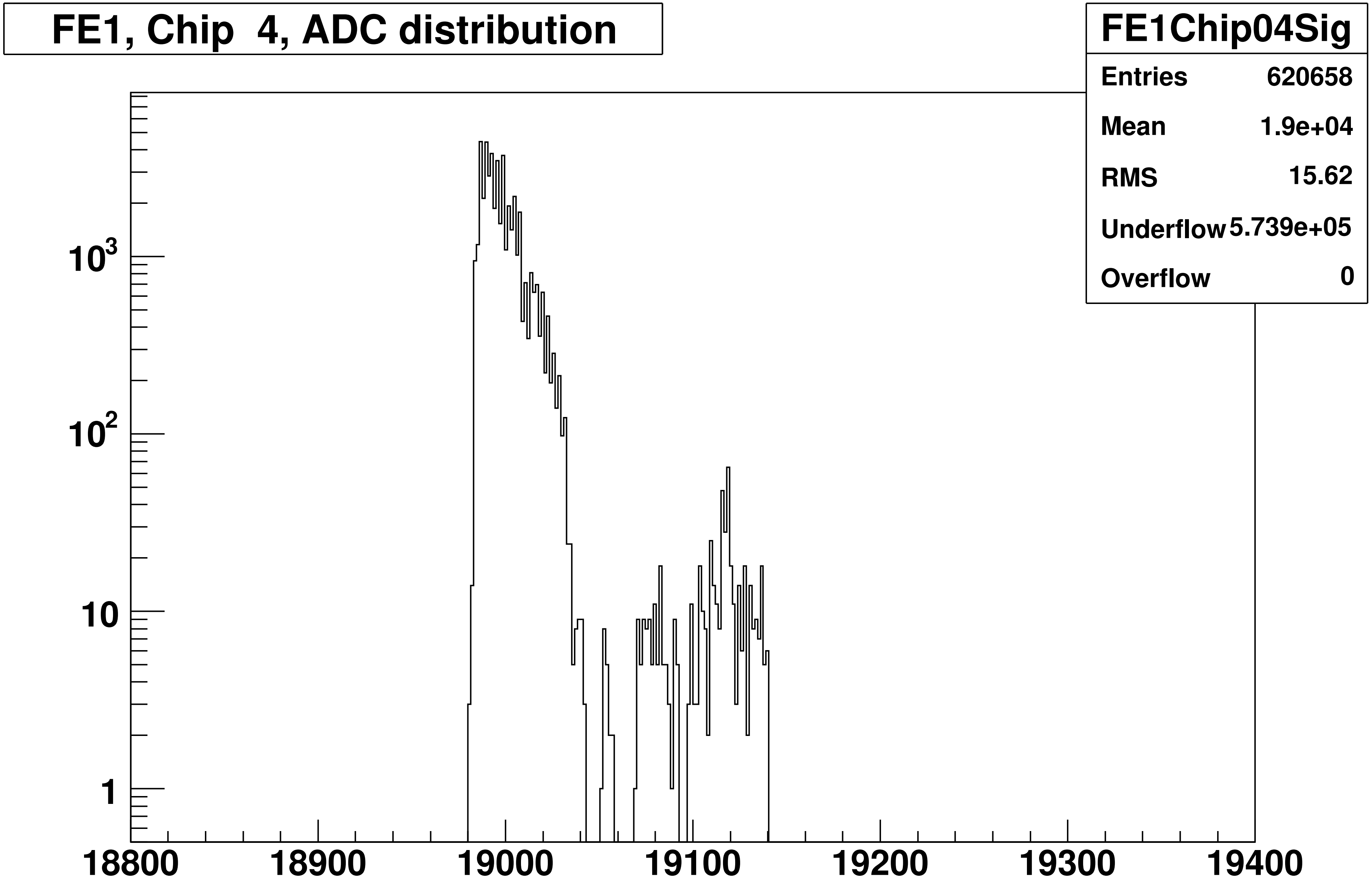
FE1, Chip 4, ADC distribution
FE1Chip04Sig
Entries 620658
Mean 1.9e+04
RMS 15.62
Underflow 5.739e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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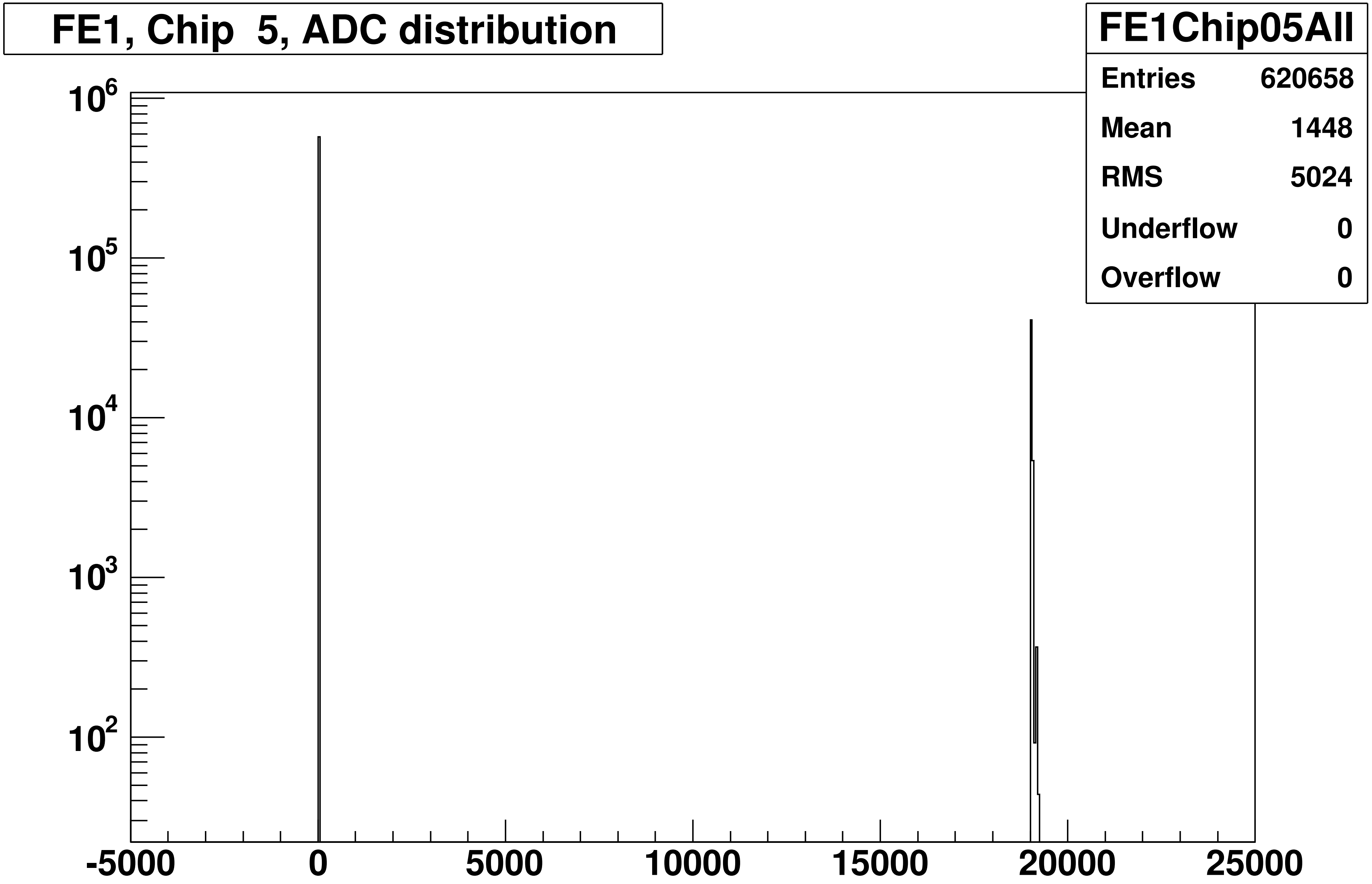
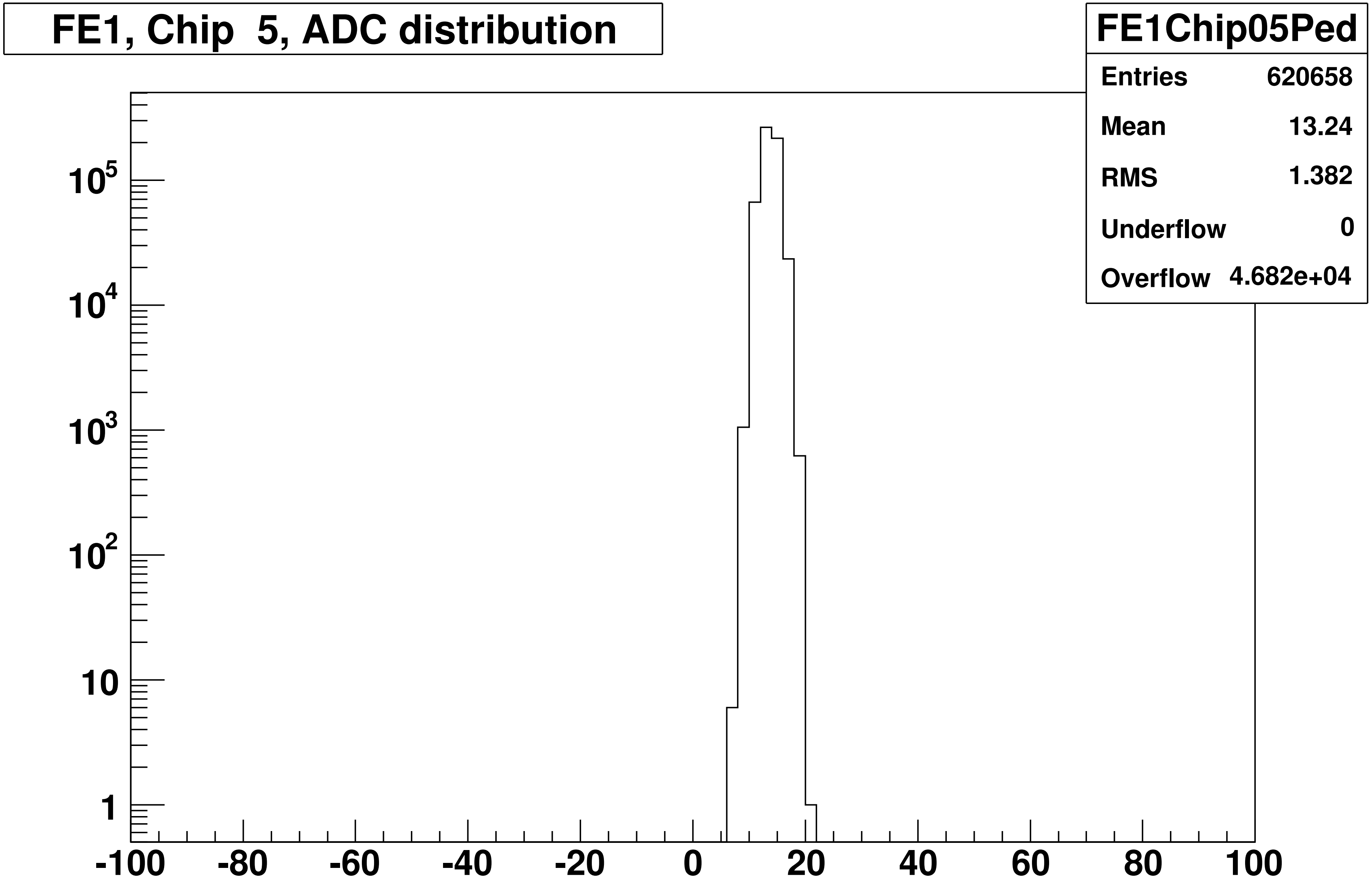
19300
19400


FE1, Chip 5, ADC distribution
FE1Chip05All
Entries 620658
Mean 1448
RMS 5024
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 5, ADC distribution
FE1Chip05Ped
Entries 620658
Mean 13.24
RMS 1.382
Underflow 0
Overflow 4.682e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
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-20
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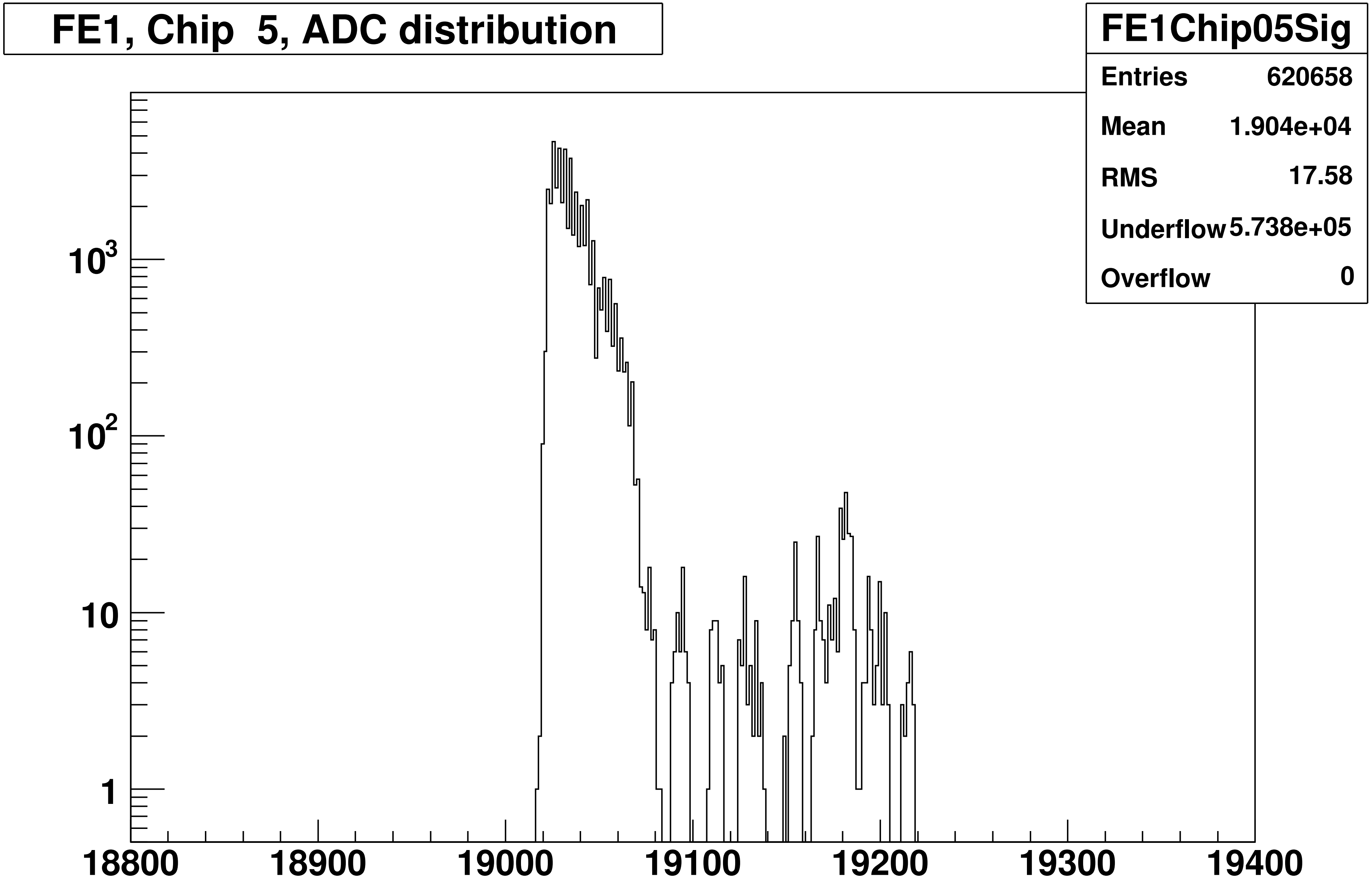
60
80
100


FE1, Chip 5, ADC distribution
FE1Chip05Sig
Entries 620658
Mean 1.904e+04
RMS 17.58
Underflow 5.738e+05
Overflow 0
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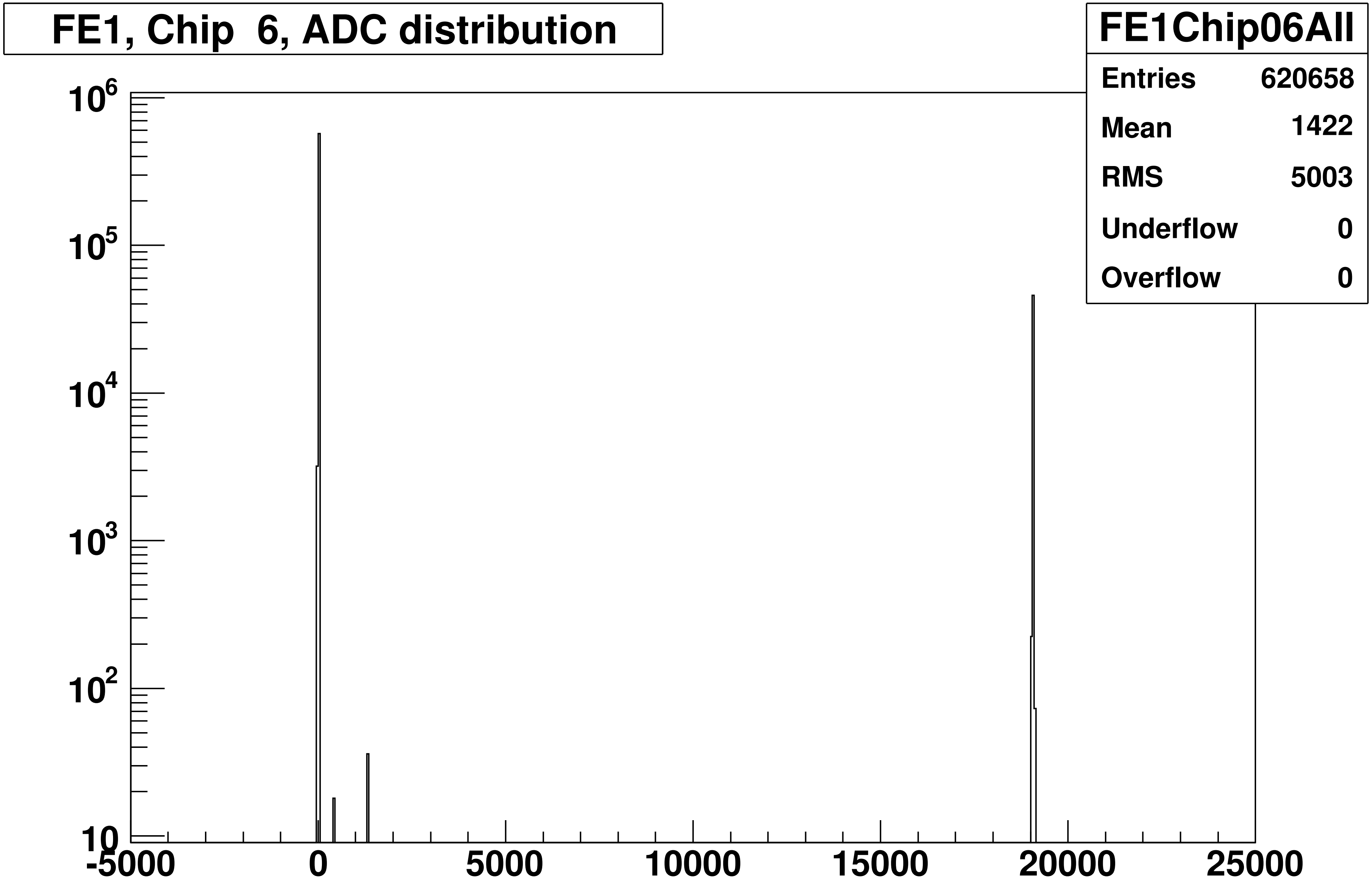
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 6, ADC distribution
FE1Chip06All
Entries 620658
Mean 1422
RMS 5003
Underflow 0
Overflow 0
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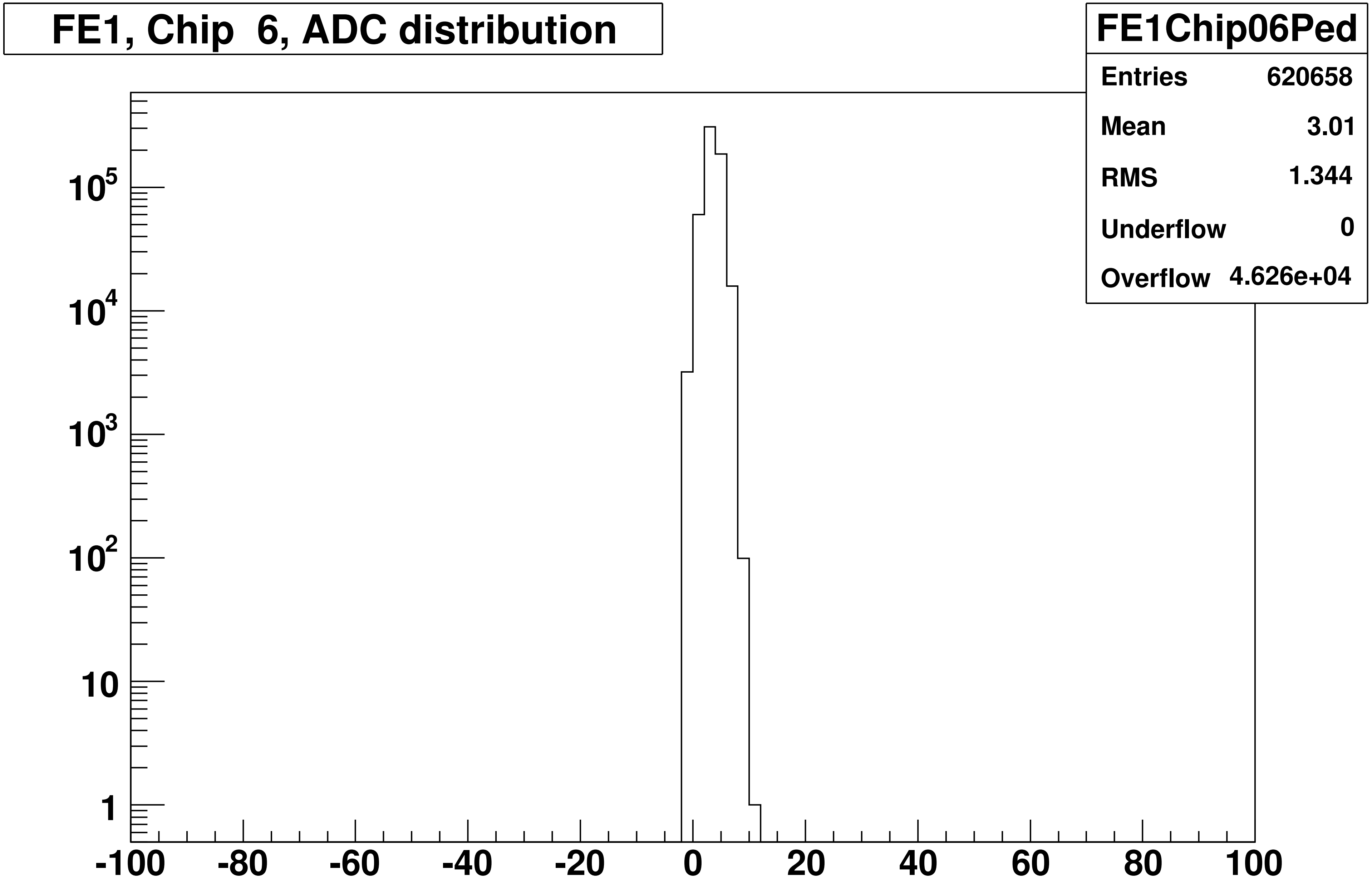
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE1, Chip 6, ADC distribution
FE1Chip06Ped
Entries 620658
Mean 3.01
RMS 1.344
Underflow 0
Overflow 4.626e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
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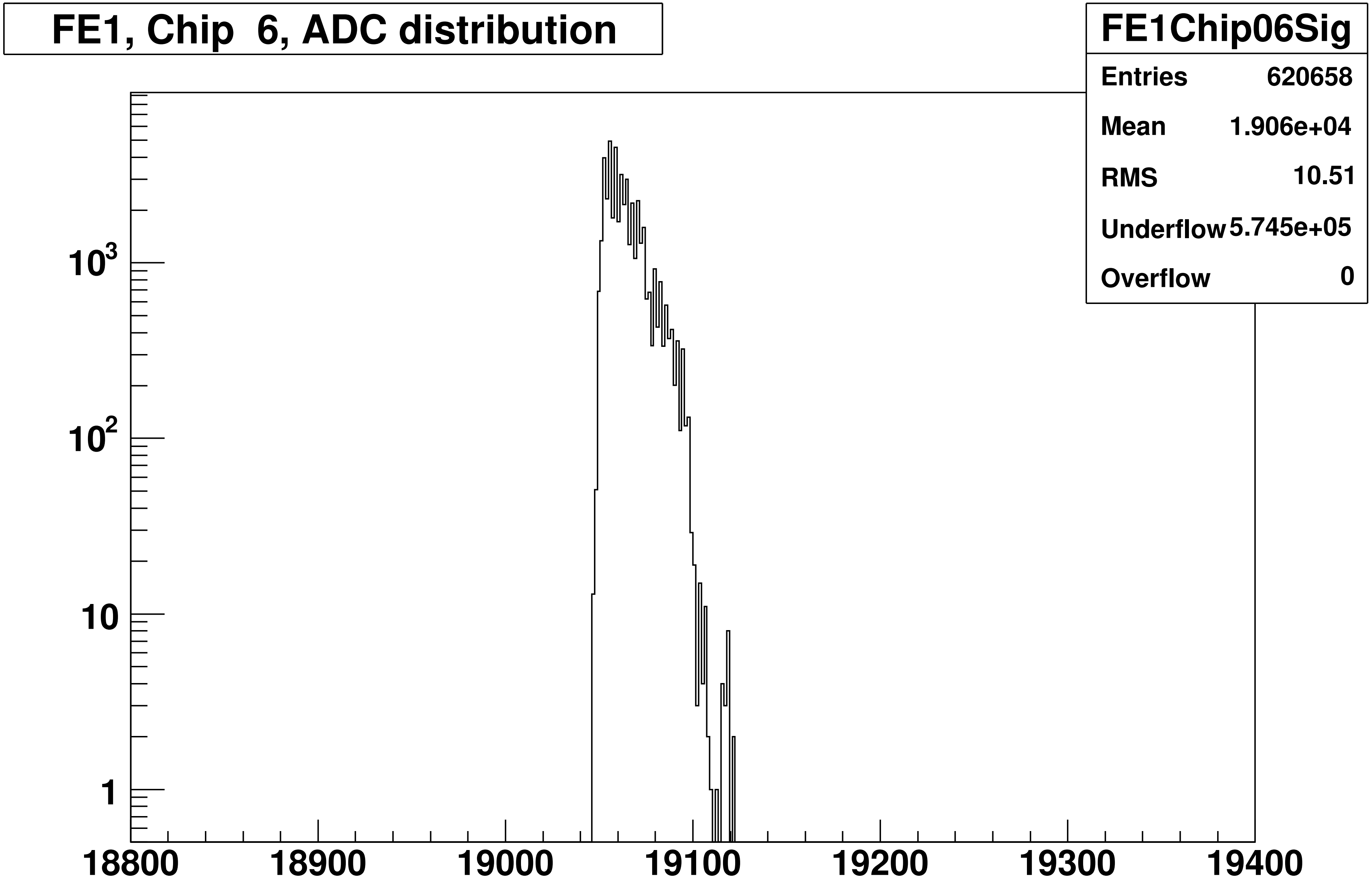
80
100


FE1, Chip 6, ADC distribution
FE1Chip06Sig
Entries 620658
Mean 1.906e+04
RMS 10.51
Underflow 5.745e+05
Overflow 0
10^3
10^2
10
1
18800
18900
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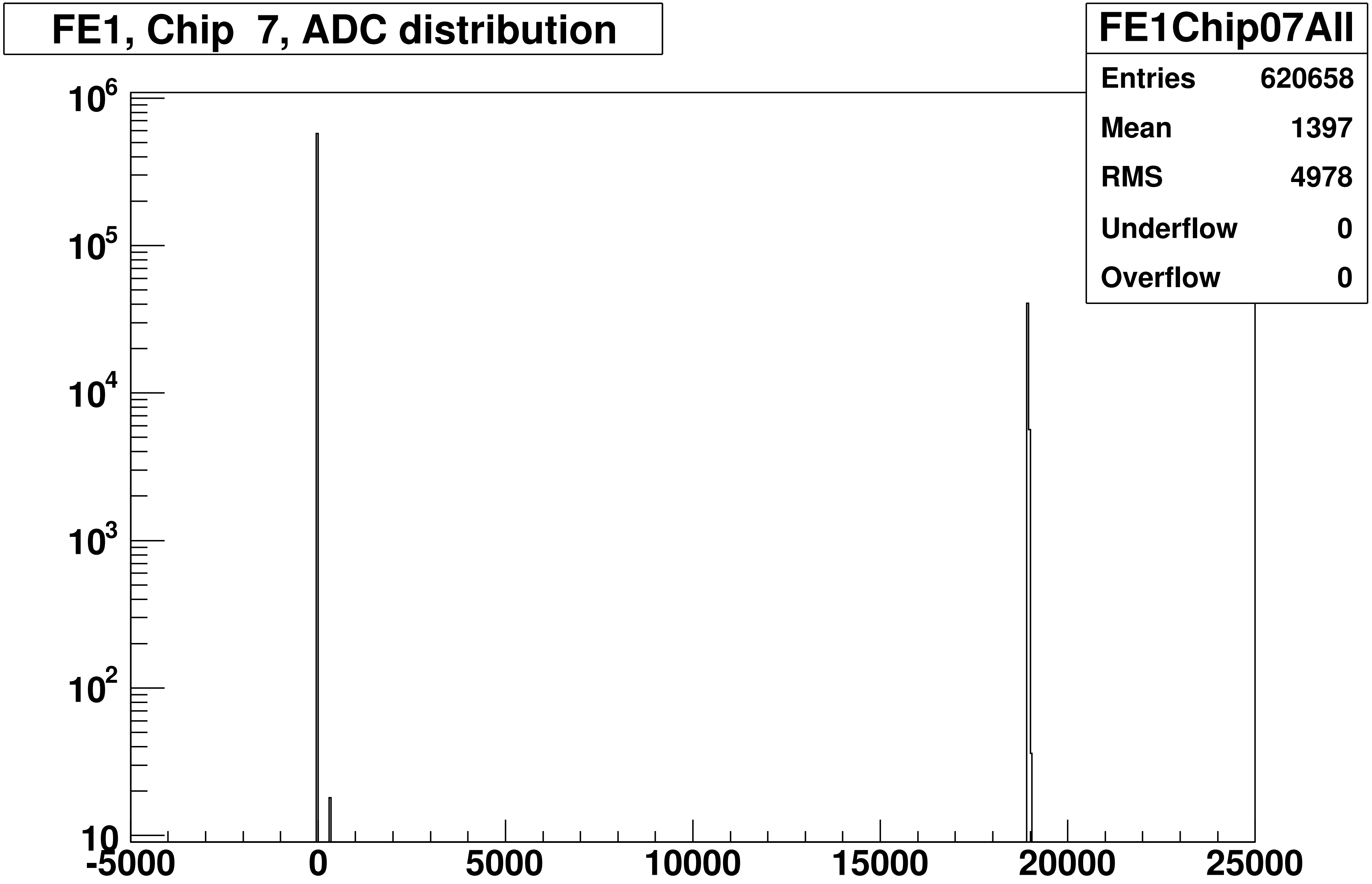
19300
19400



FE1, Chip 7, ADC distribution
FE1Chip07All
Entries 620658
Mean 1397
RMS 4978
Underflow 0
Overflow 0
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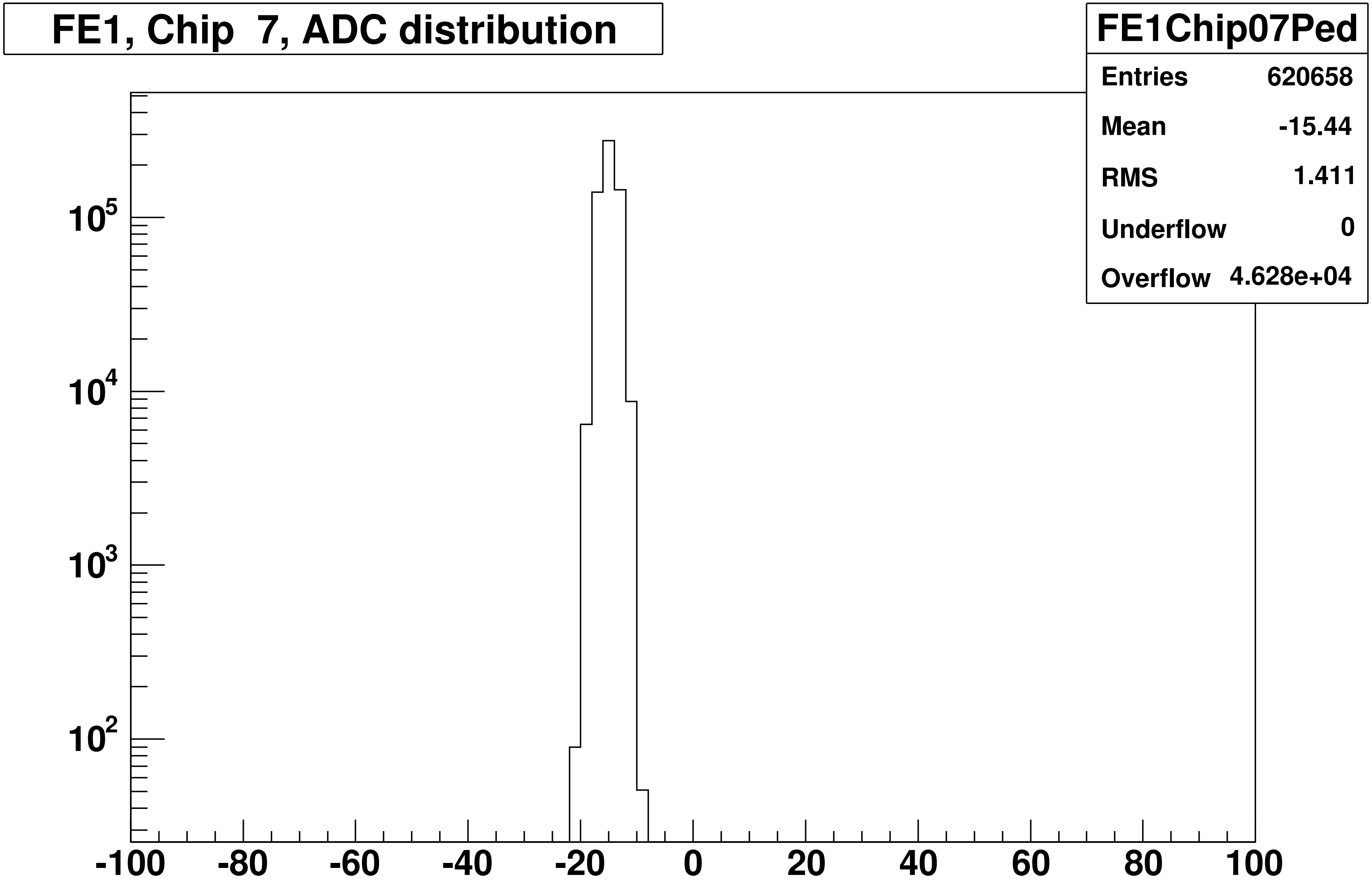
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 7, ADC distribution
FE1Chip07Ped
Entries 620658
Mean -15.44
RMS 1.411
Underflow 0
Overflow 4.628e+04
10^5
10^4
10^3
10^2
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-20
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40
60
80
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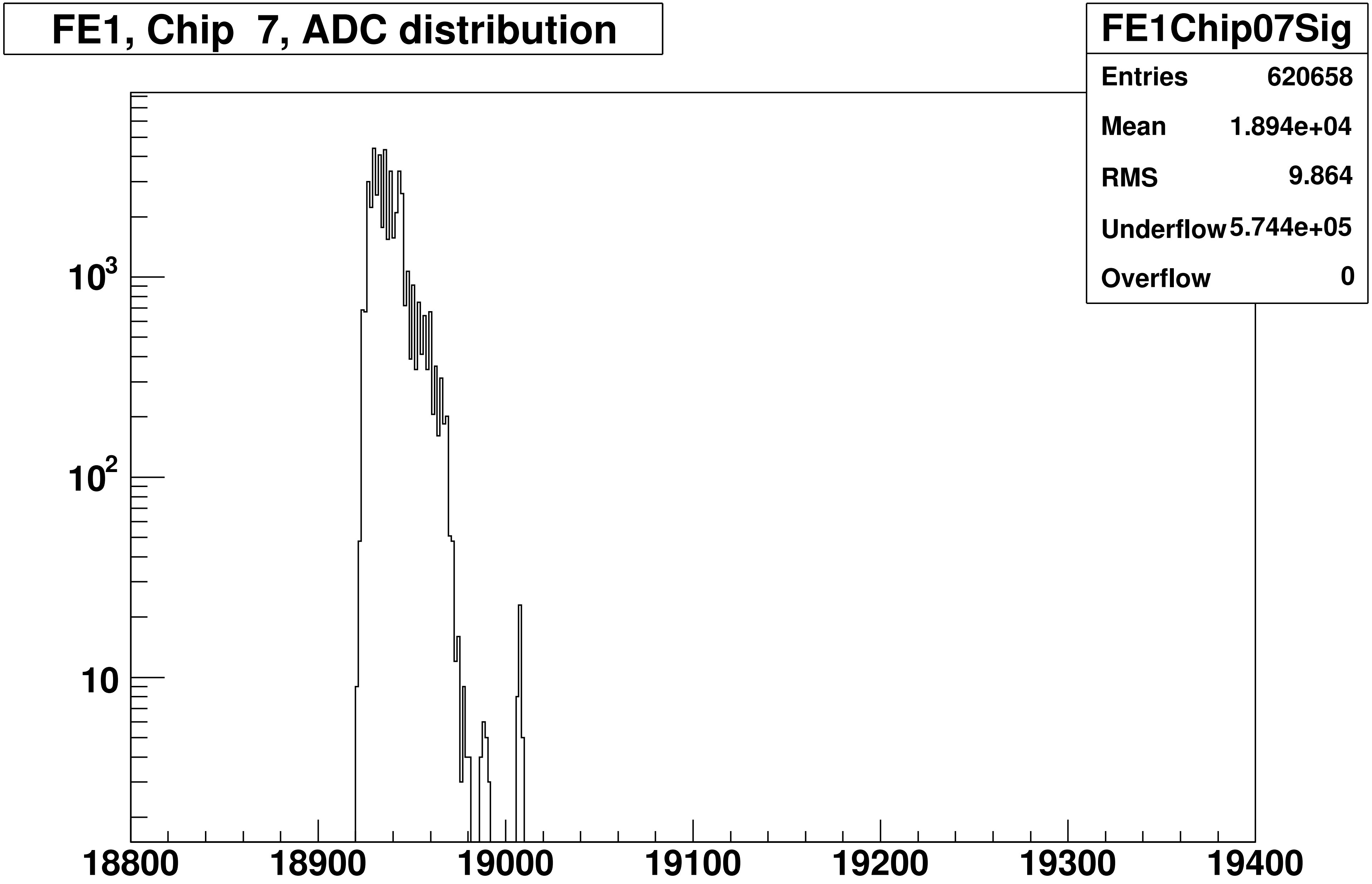
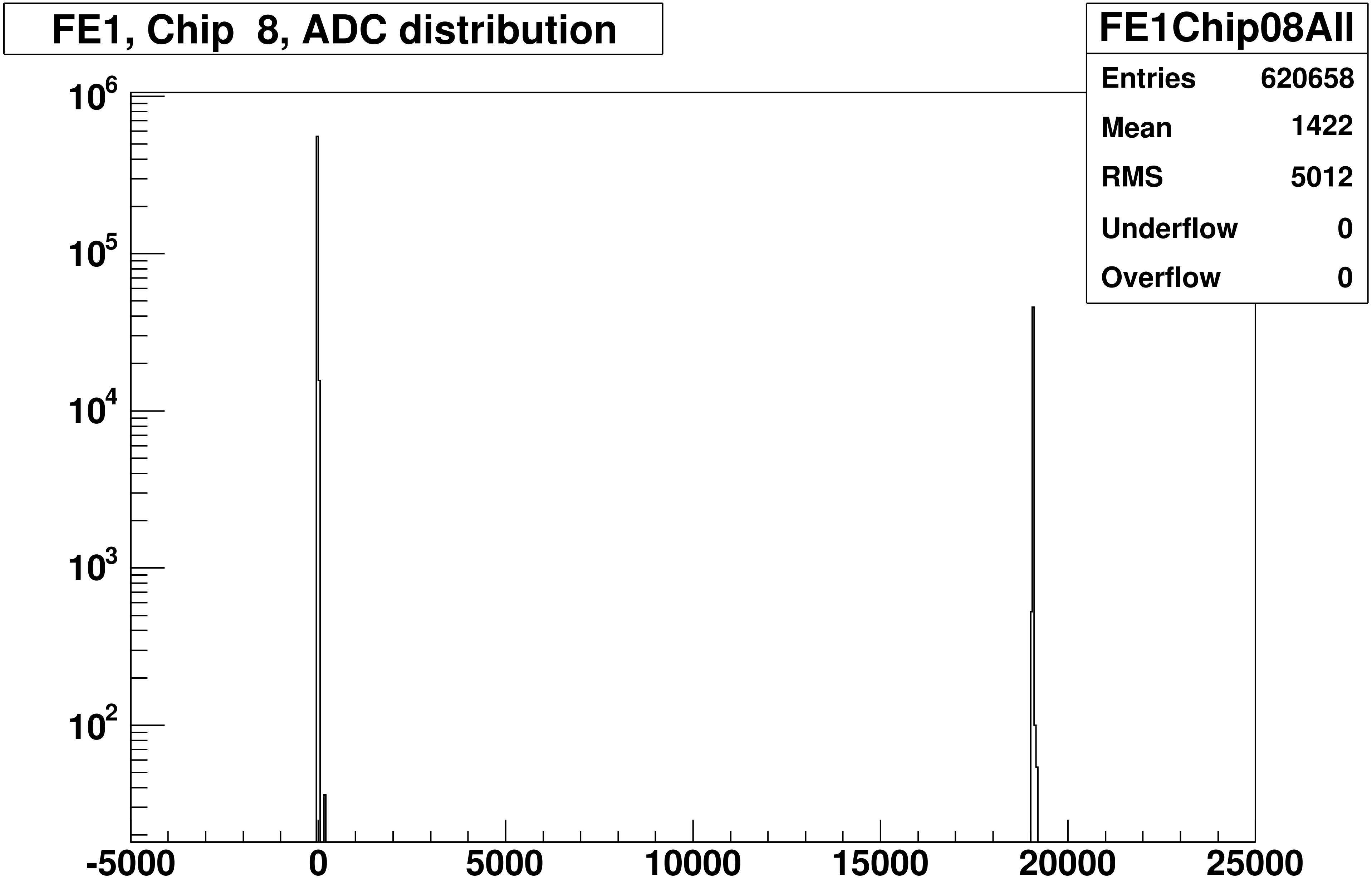

FE1, Chip 7, ADC distribution
FE1Chip07Sig
Entries 620658
Mean 1.894e+04
RMS 9.864
Underflow 5.744e+05
Overflow 0
10
10^2
10^3
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 8, ADC distribution
FE1Chip08All
Entries 620658
Mean 1422
RMS 5012
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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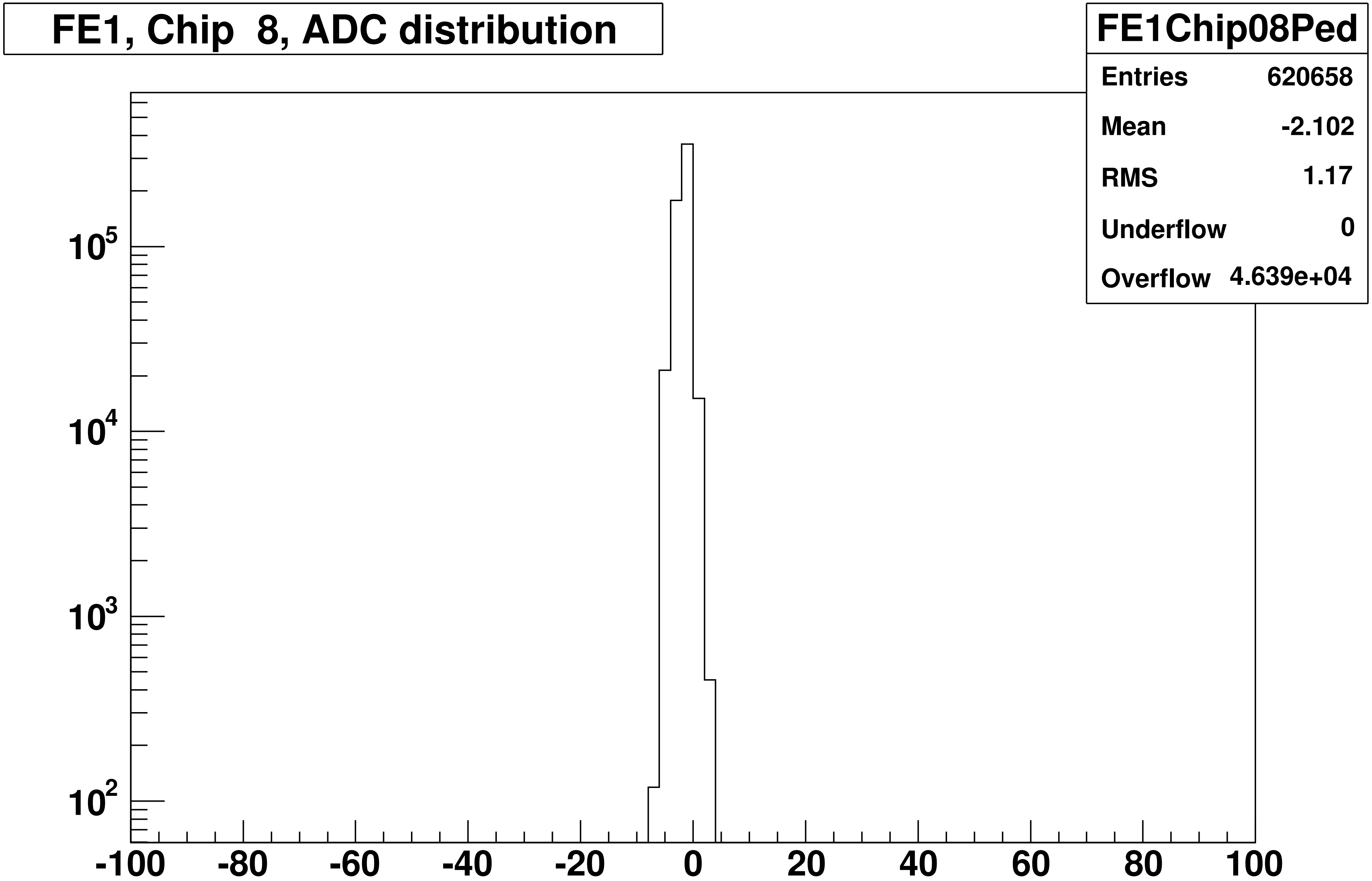
10000
15000
20000
25000


FE1, Chip 8, ADC distribution
FE1Chip08Ped
Entries 620658
Mean -2.102
RMS 1.17
Underflow 0
Overflow 4.639e+04
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10^3
10^2
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-20
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40
60
80
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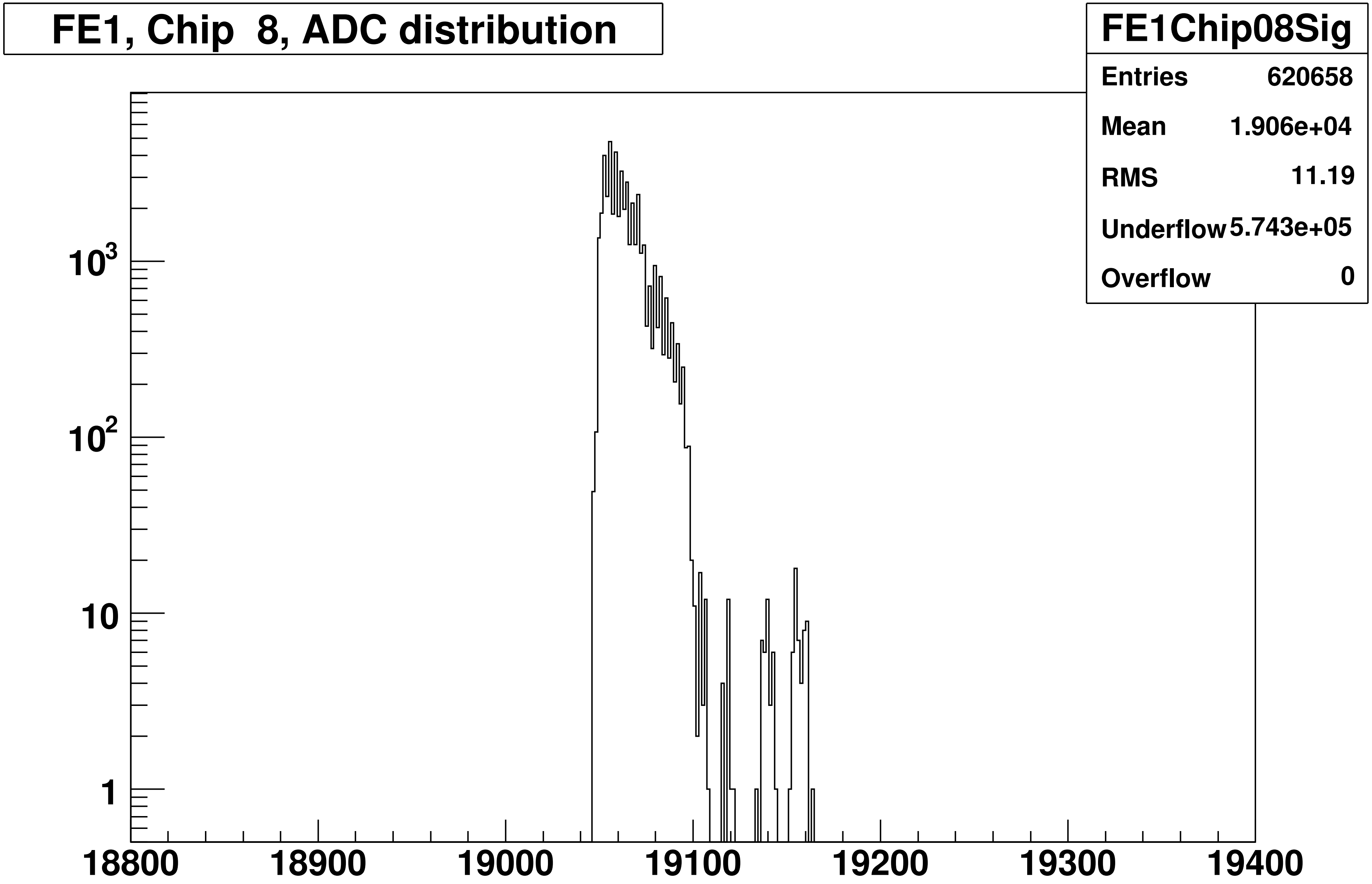
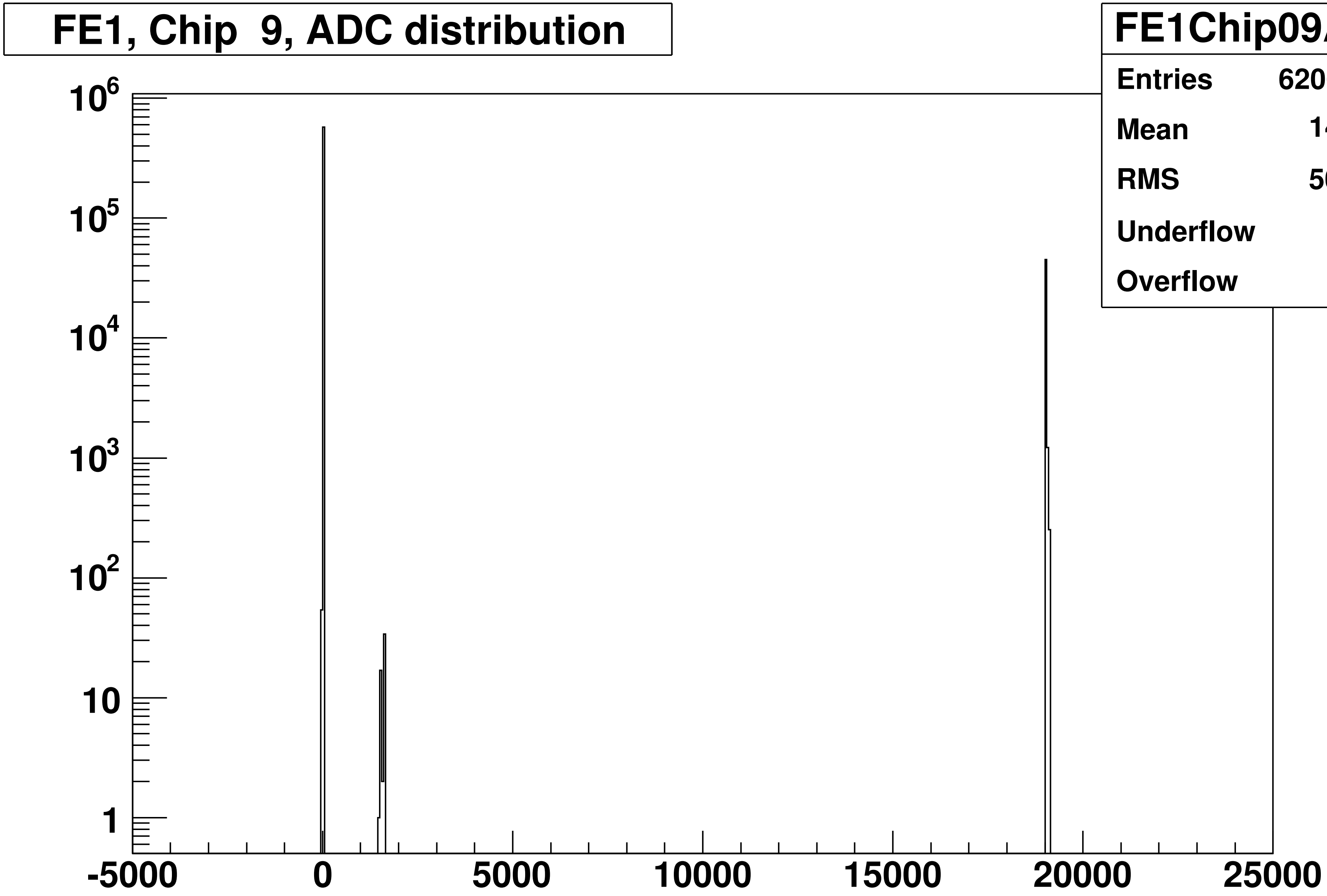
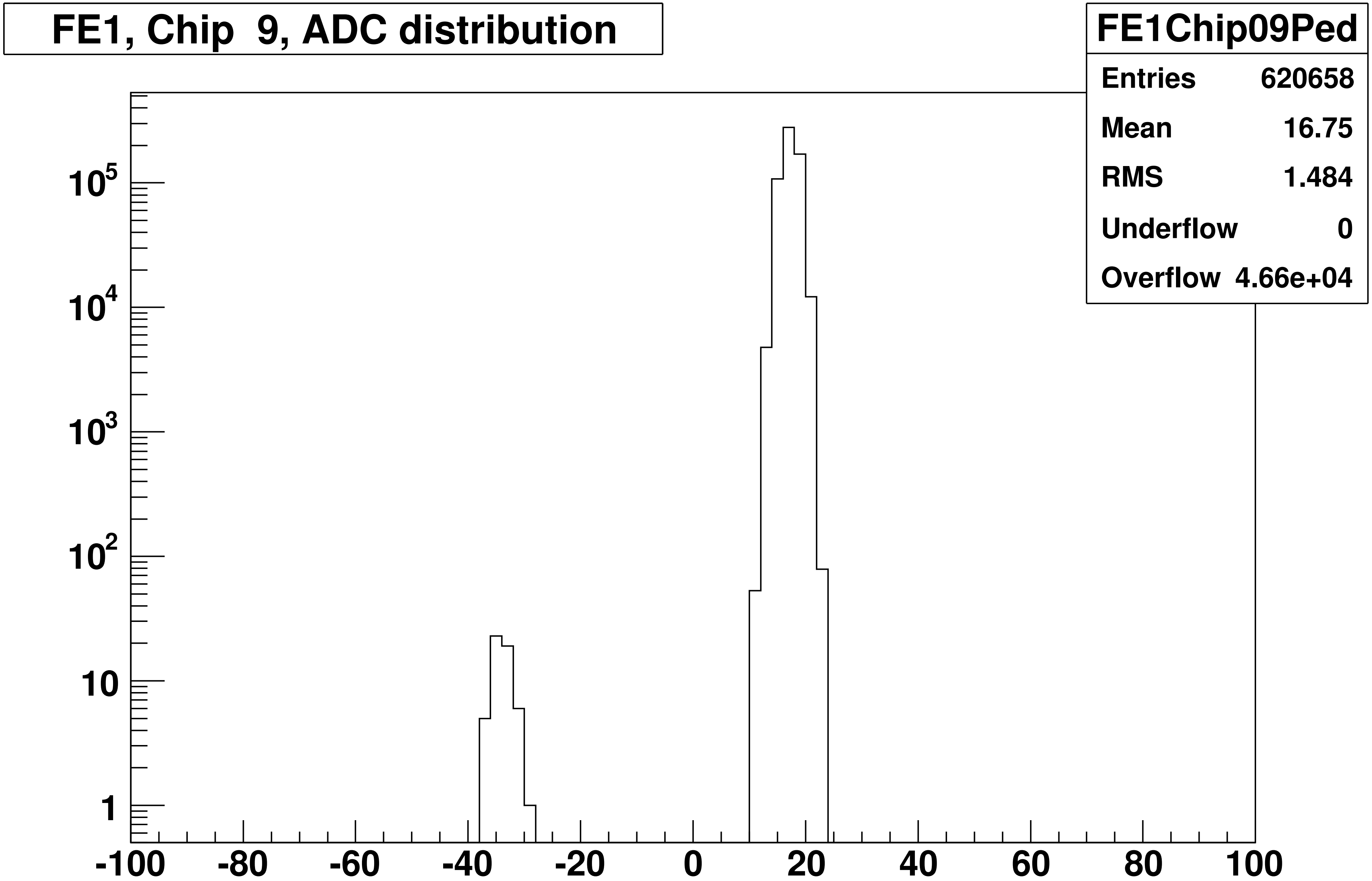

FE1, Chip 8, ADC distribution
FE1Chip08Sig
Entries 620658
Mean 1.906e+04
RMS 11.19
Underflow 5.743e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 9, ADC distribution
FE1Chip09All
Entries 620658
Mean 1442
RMS 5006
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE1, Chip 9, ADC distribution
FE1Chip09Ped
Entries 620658
Mean 16.75
RMS 1.484
Underflow 0
Overflow 4.66e+04
10^5
10^4
10^3
10^2
10
1
-100
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-20
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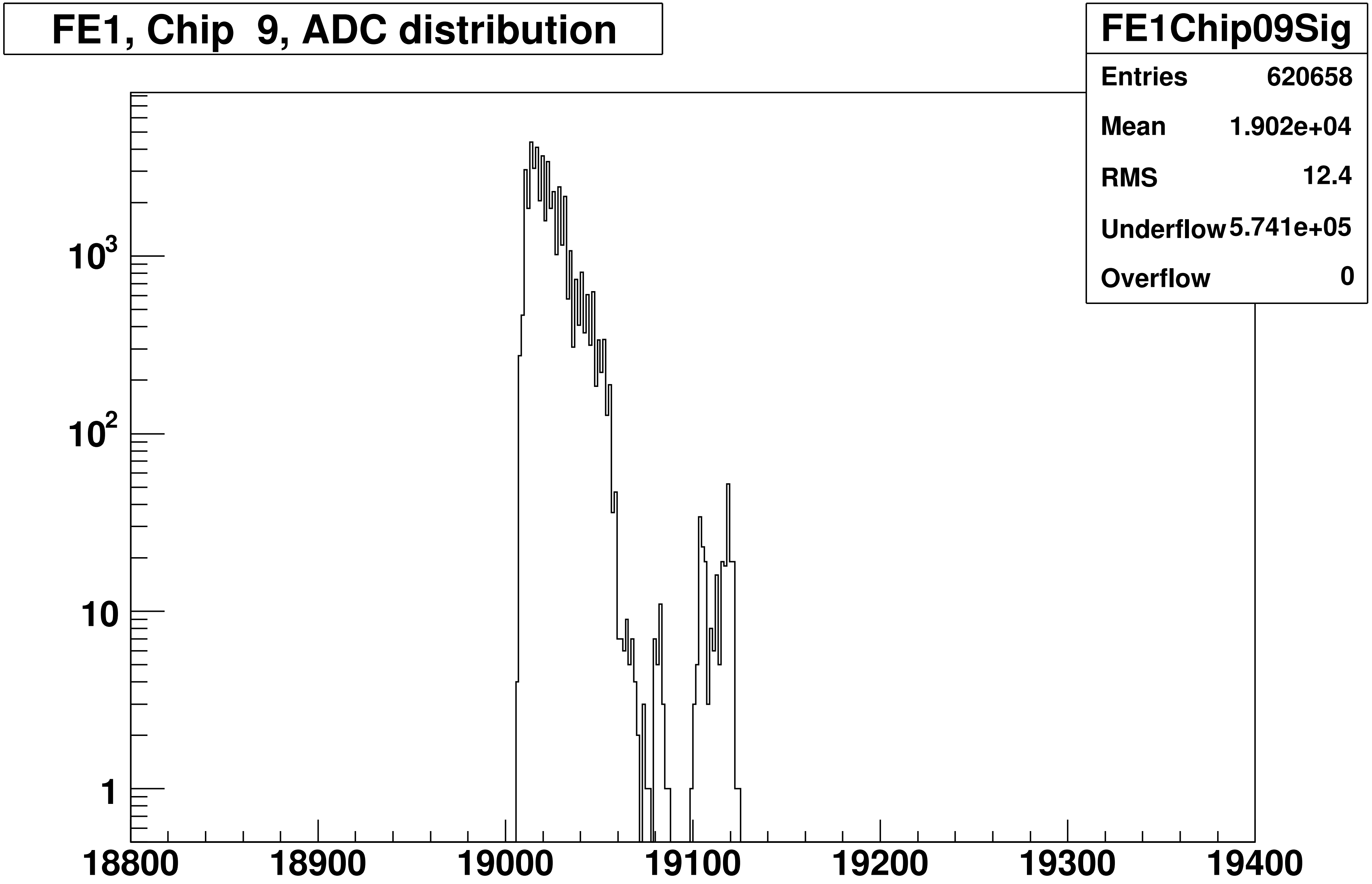

FE1, Chip 9, ADC distribution
FE1Chip09Sig
Entries 620658
Mean 1.902e+04
RMS 12.4
Underflow 5.741e+05
Overflow 0
10^3
10^2
10
1
18800
18900
19000
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19200
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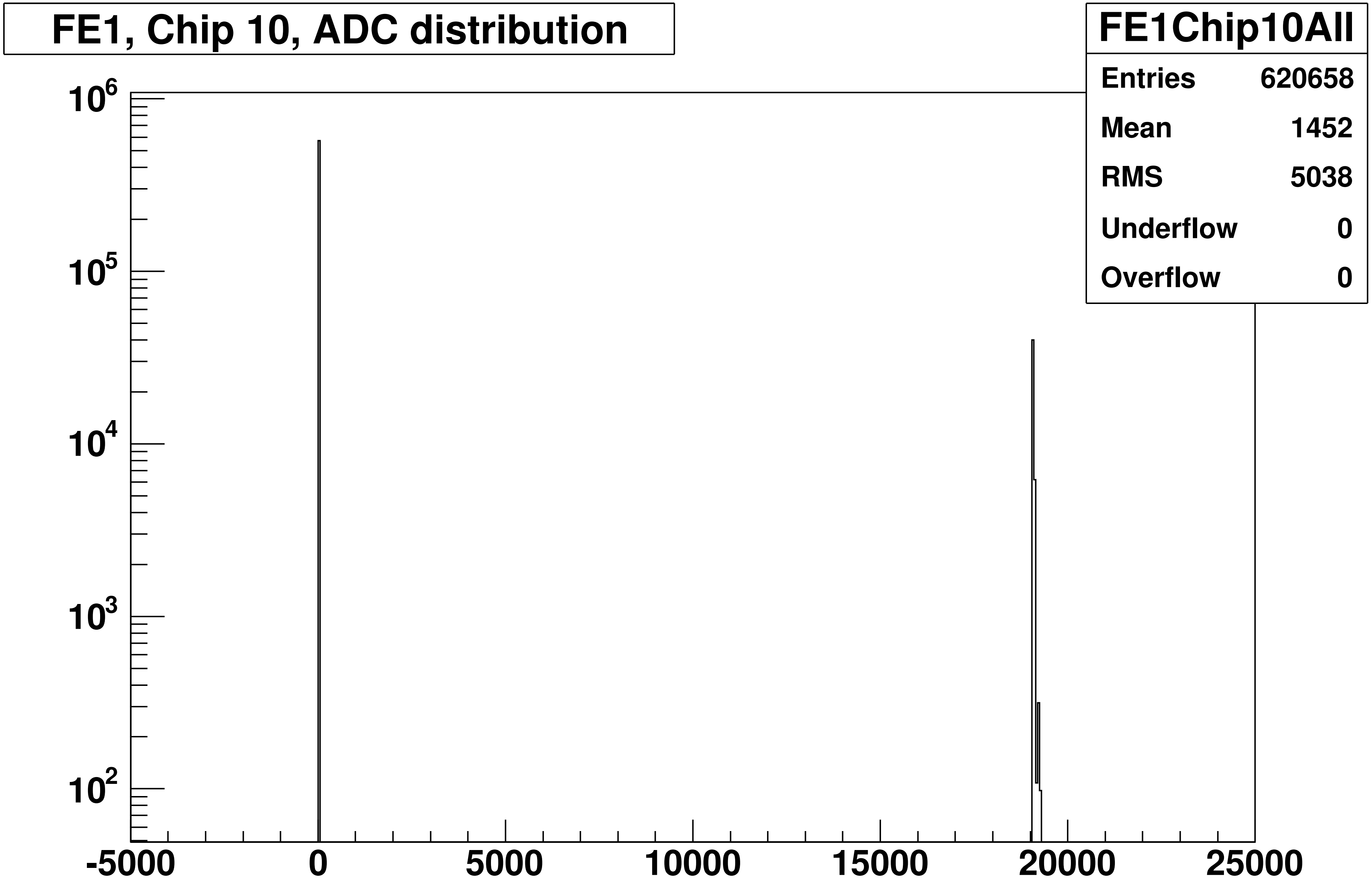
19400



FE1, Chip 10, ADC distribution
FE1Chip10All
Entries 620658
Mean 1452
RMS 5038
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
-5000
0
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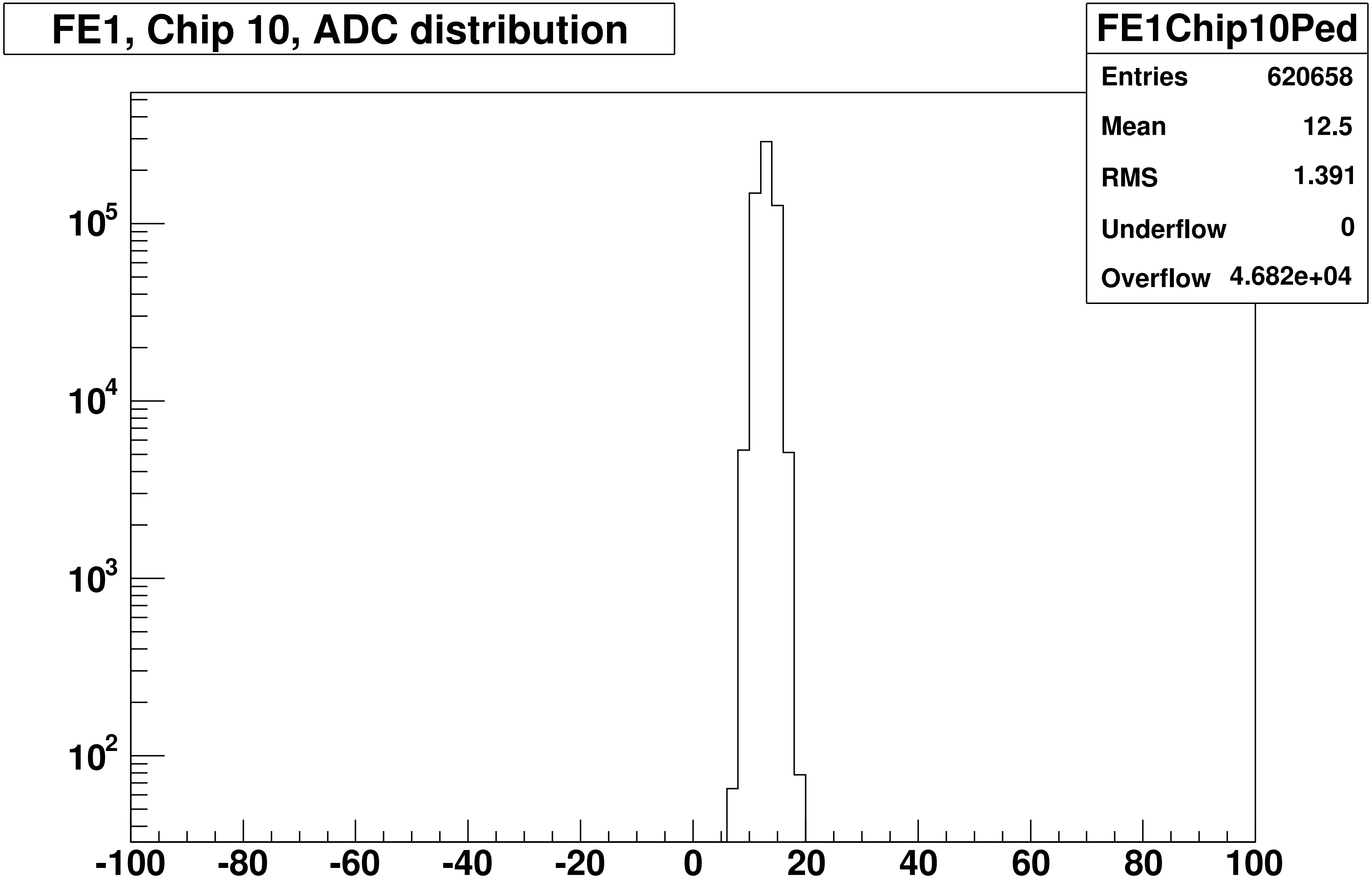
10000
15000
20000
25000


FE1, Chip 10, ADC distribution
FE1Chip10Ped
Entries 620658
Mean 12.5
RMS 1.391
Underflow 0
Overflow 4.682e+04
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10^4
10^3
10^2
-100
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-60
-40
-20
0
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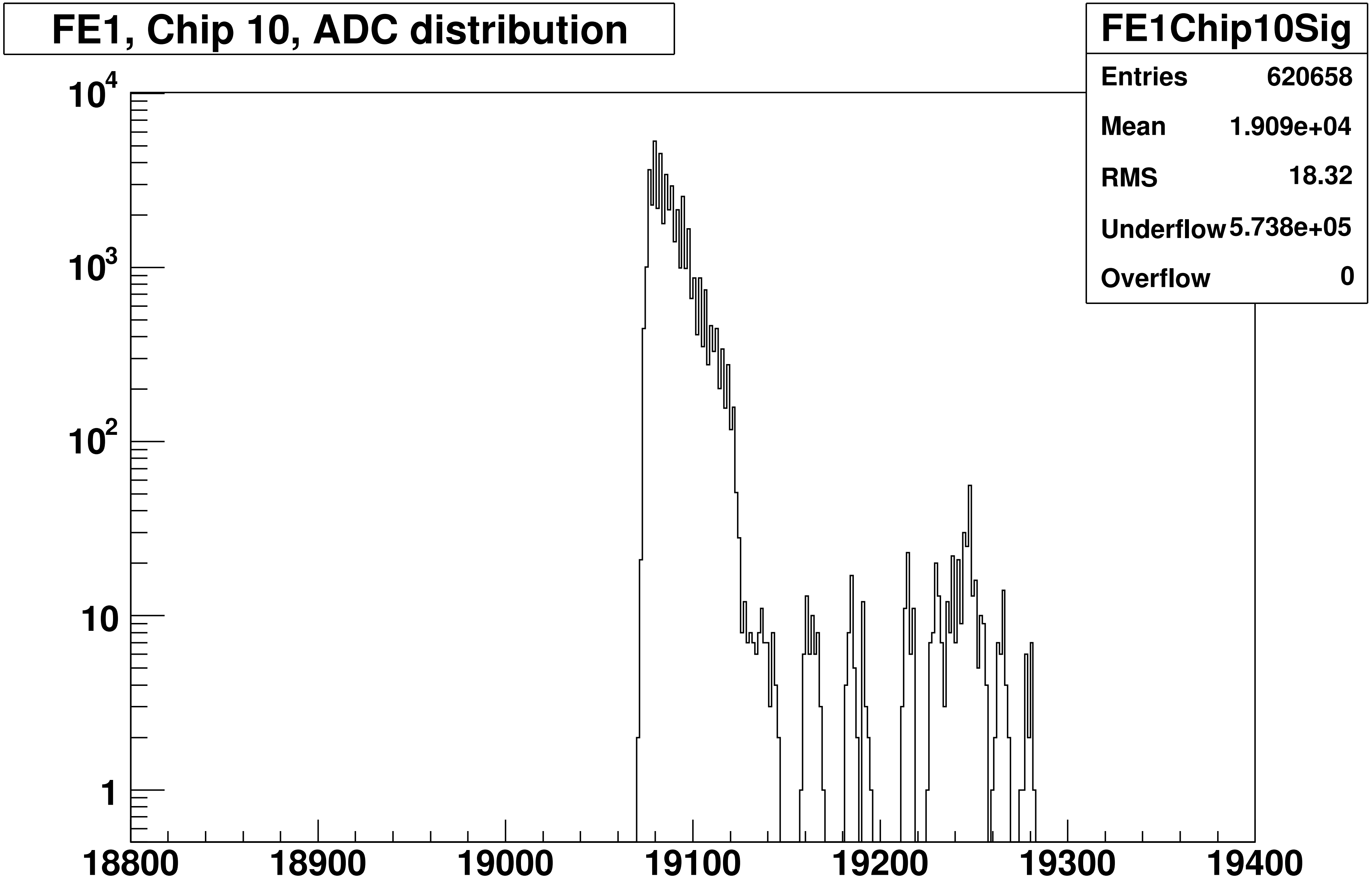
60
80
100


FE1, Chip 10, ADC distribution
FE1Chip10Sig
Entries 620658
Mean 1.909e+04
RMS 18.32
Underflow 5.738e+05
Overflow 0
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10^3
10^2
10
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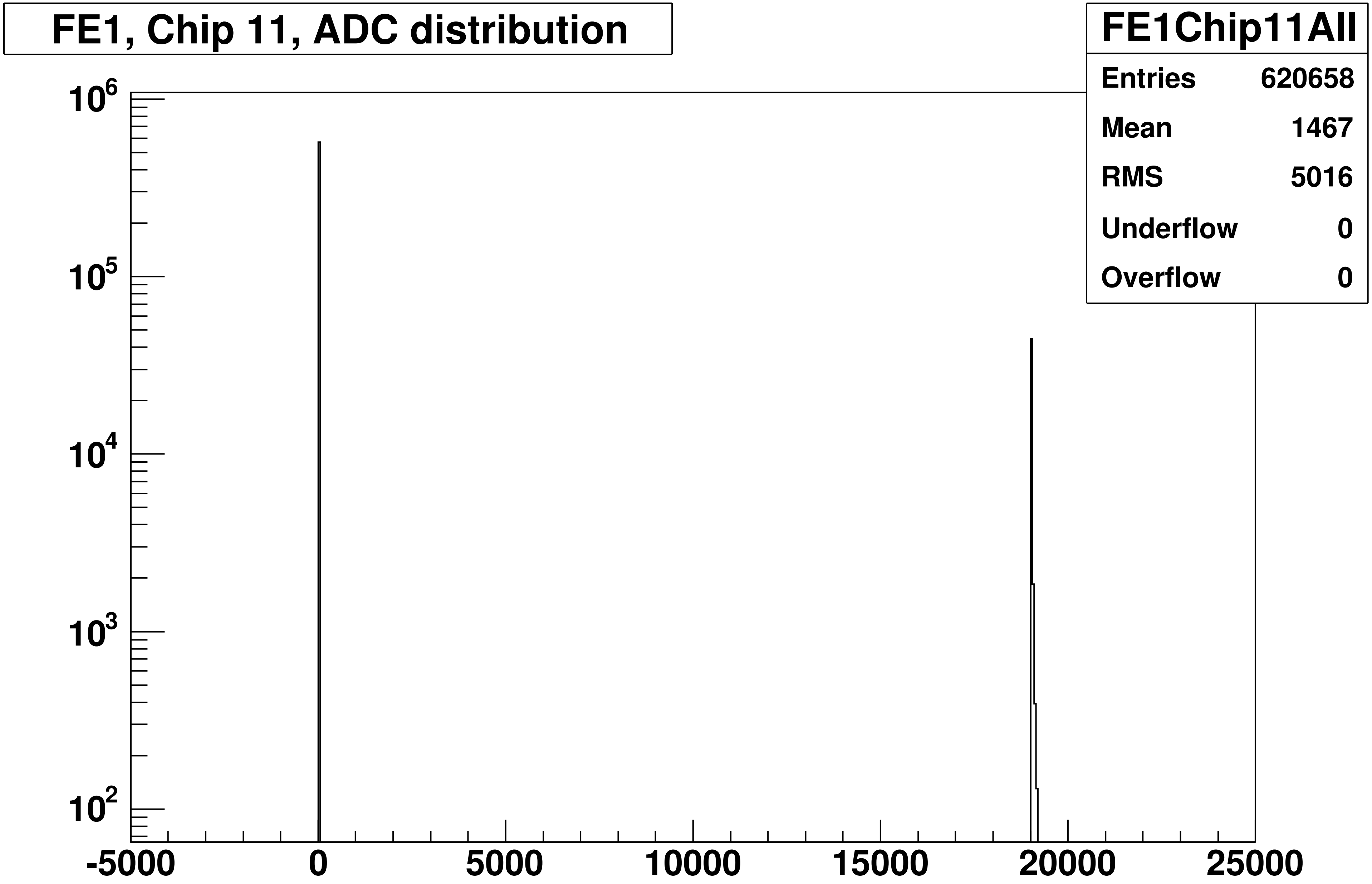
18800
18900
19000
19100
19200
19300
19400


FE1, Chip 11, ADC distribution
FE1Chip11All
Entries 620658
Mean 1467
RMS 5016
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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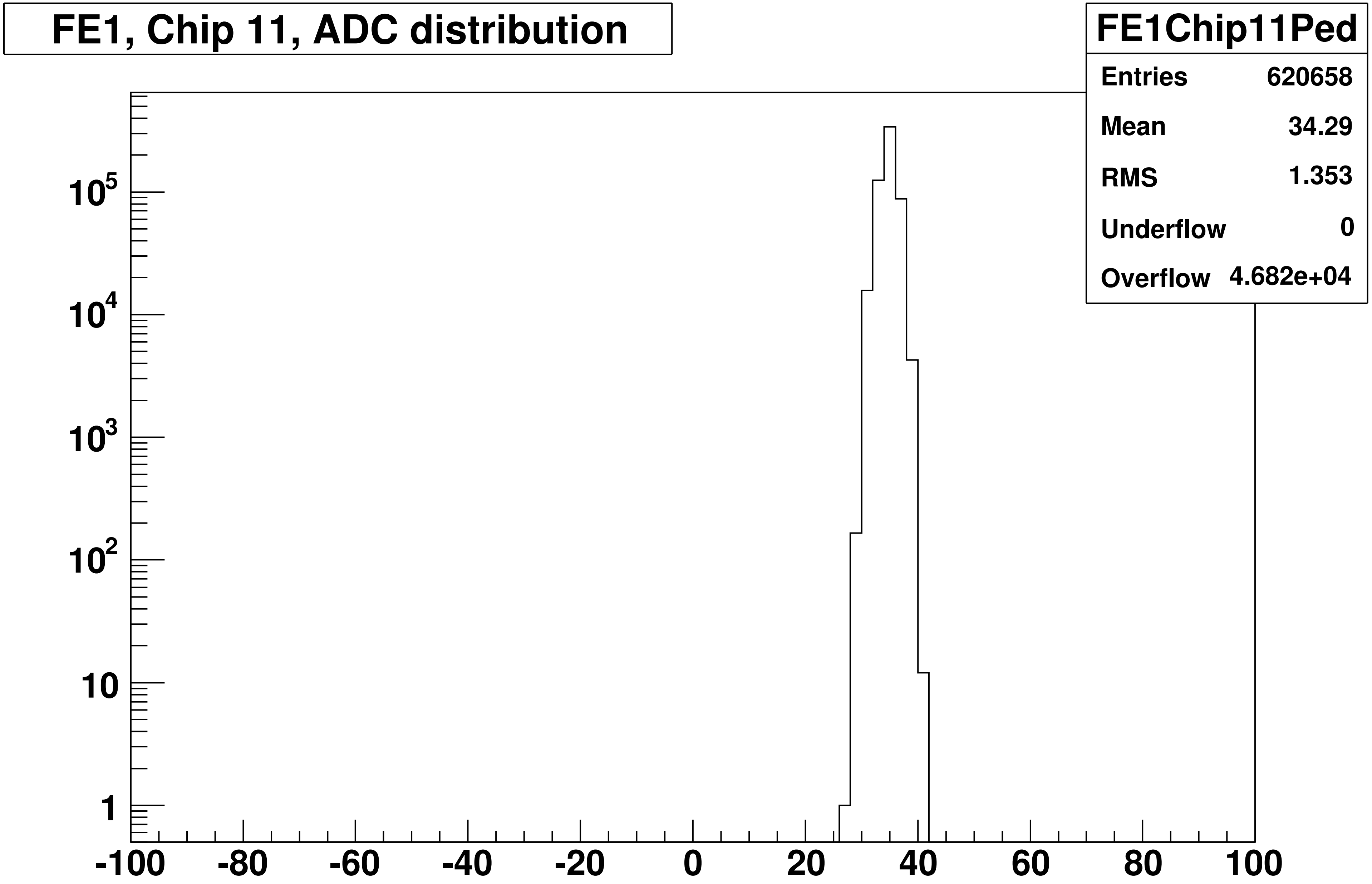
10000
15000
20000
25000



FE1, Chip 11, ADC distribution
FE1Chip11Ped
Entries 620658
Mean 34.29
RMS 1.353
Underflow 0
Overflow 4.682e+04
10^5
10^4
10^3
10^2
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1
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-20
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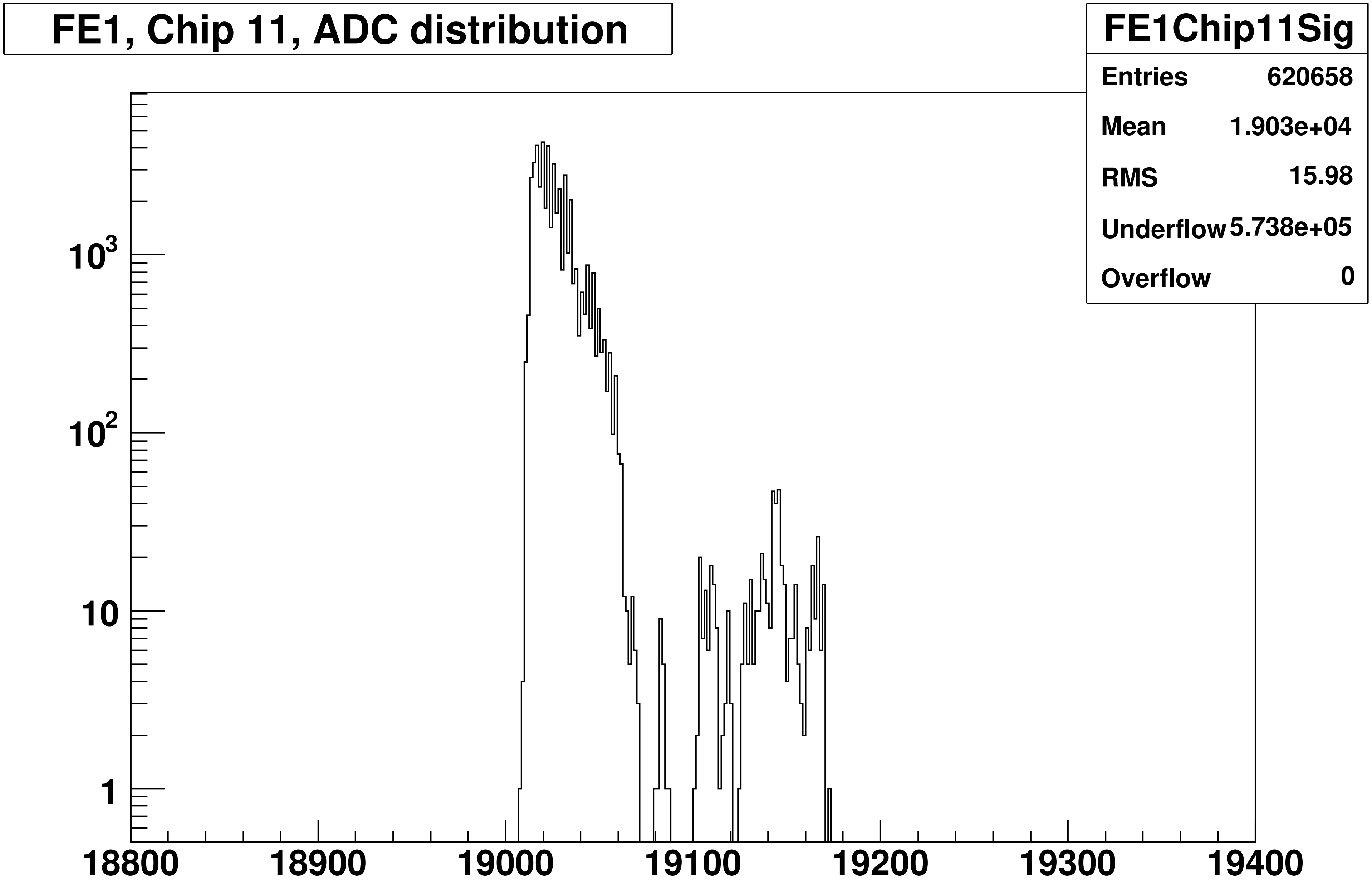
80
100


FE1, Chip 11, ADC distribution
FE1Chip11Sig
Entries 620658
Mean 1.903e+04
RMS 15.98
Underflow 5.738e+05
Overflow 0
10^3
10^2
10
1
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18900
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19200
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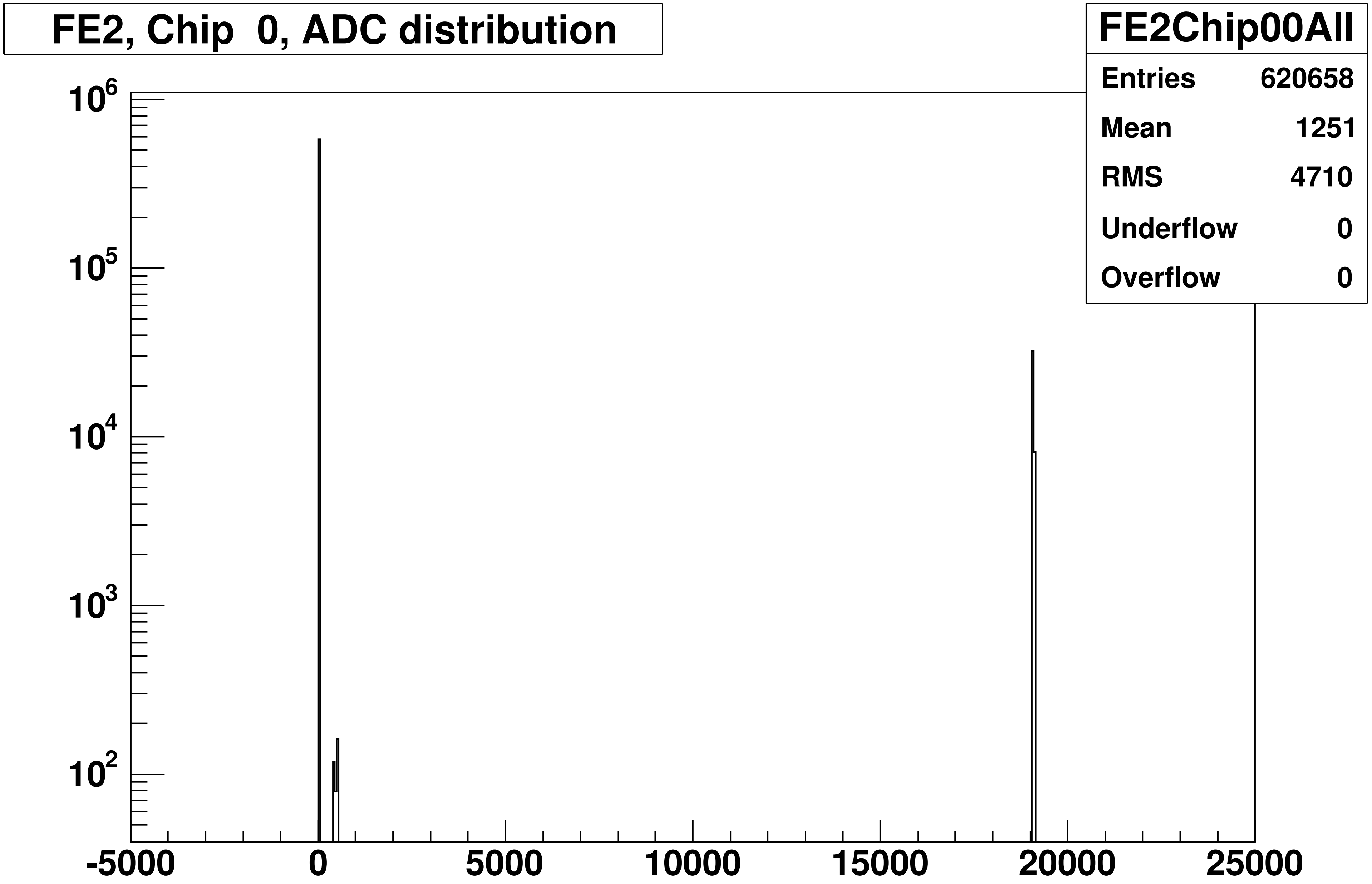
19400


FE2, Chip 0, ADC distribution
FE2Chip00All
Entries 620658
Mean 1251
RMS 4710
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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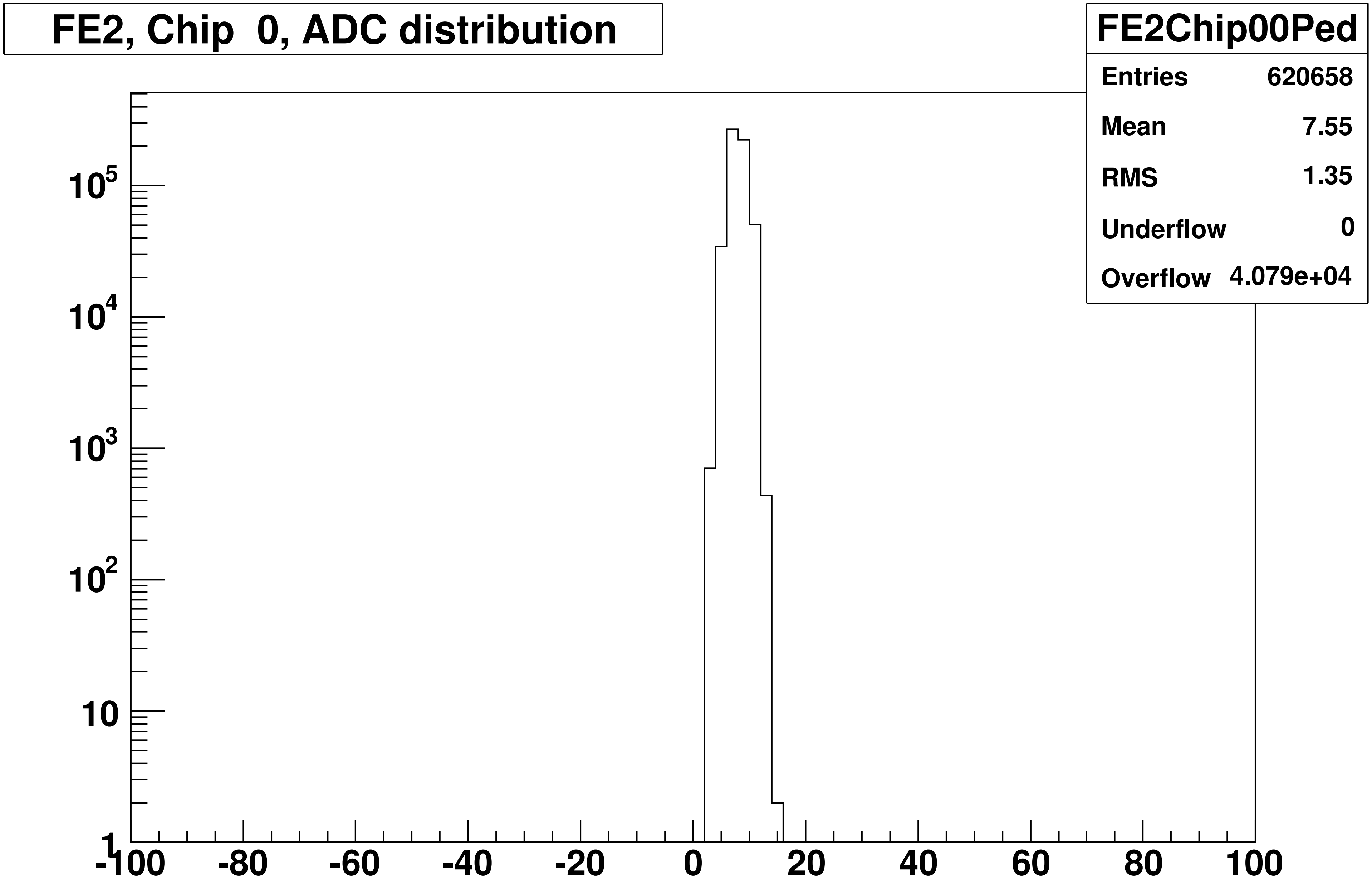
0
5000
10000
15000
20000
25000


FE2, Chip 0, ADC distribution
FE2Chip00Ped
Entries 620658
Mean 7.55
RMS 1.35
Underflow 0
Overflow 4.079e+04
10^5
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10^3
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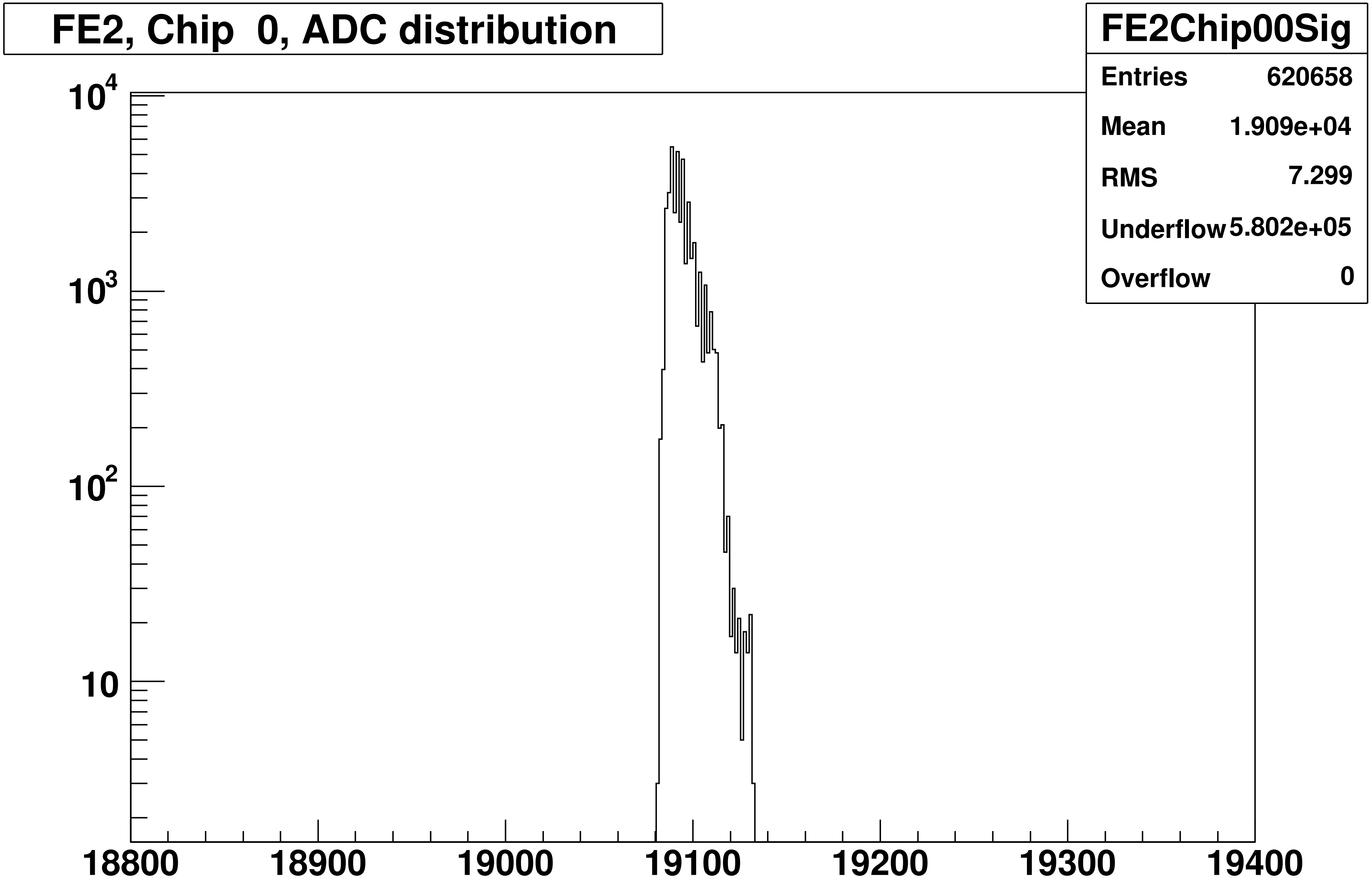
60
80
100


FE2, Chip 0, ADC distribution
FE2Chip00Sig
Entries 620658
Mean 1.909e+04
RMS 7.299
Underflow 5.802e+05
Overflow 0
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10^3
10^2
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18800
18900
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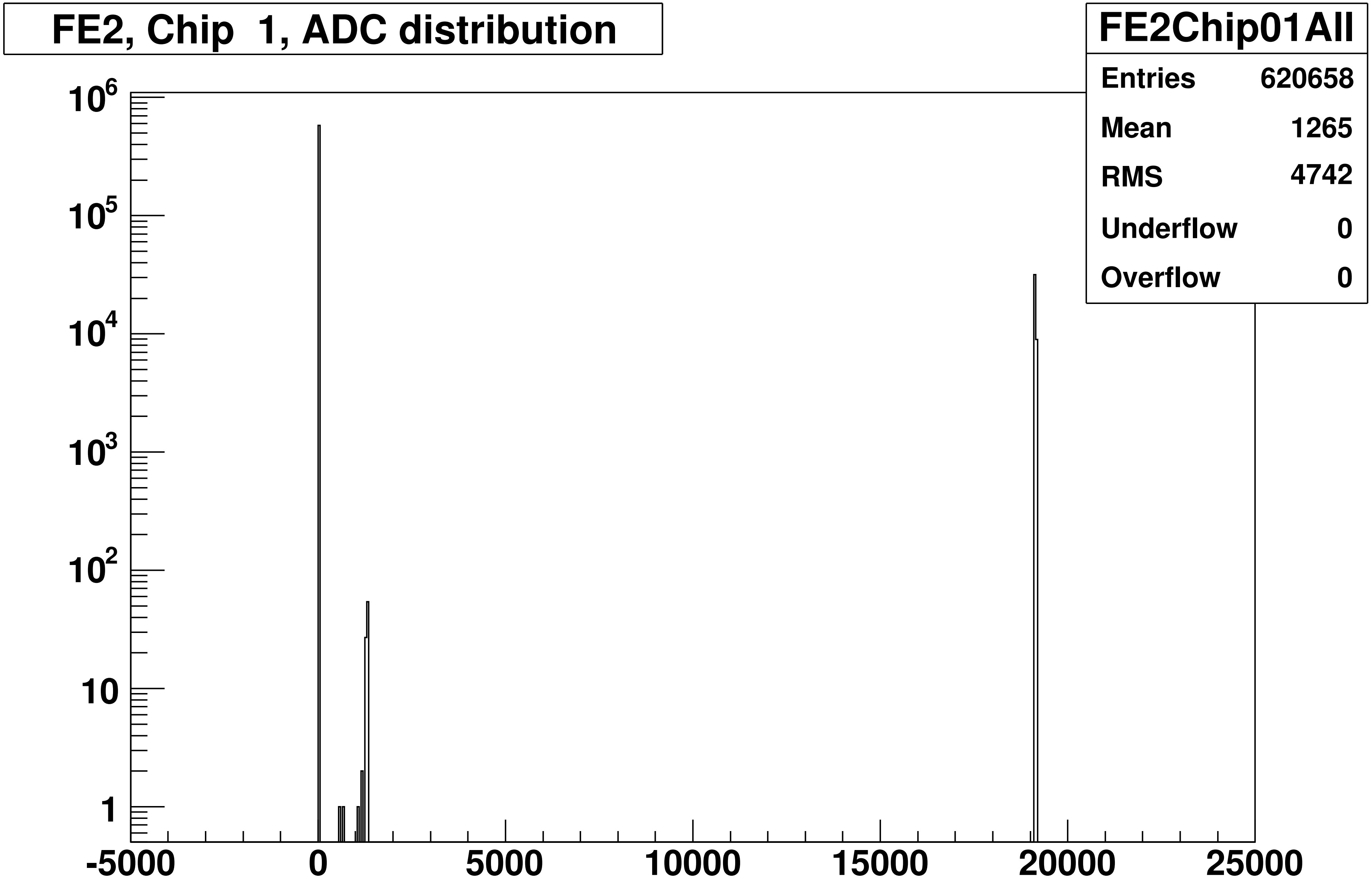
19400



FE2, Chip 1, ADC distribution
FE2Chip01All
Entries 620658
Mean 1265
RMS 4742
Underflow 0
Overflow 0
10^6
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10^4
10^3
10^2
10
1
-5000
0
5000
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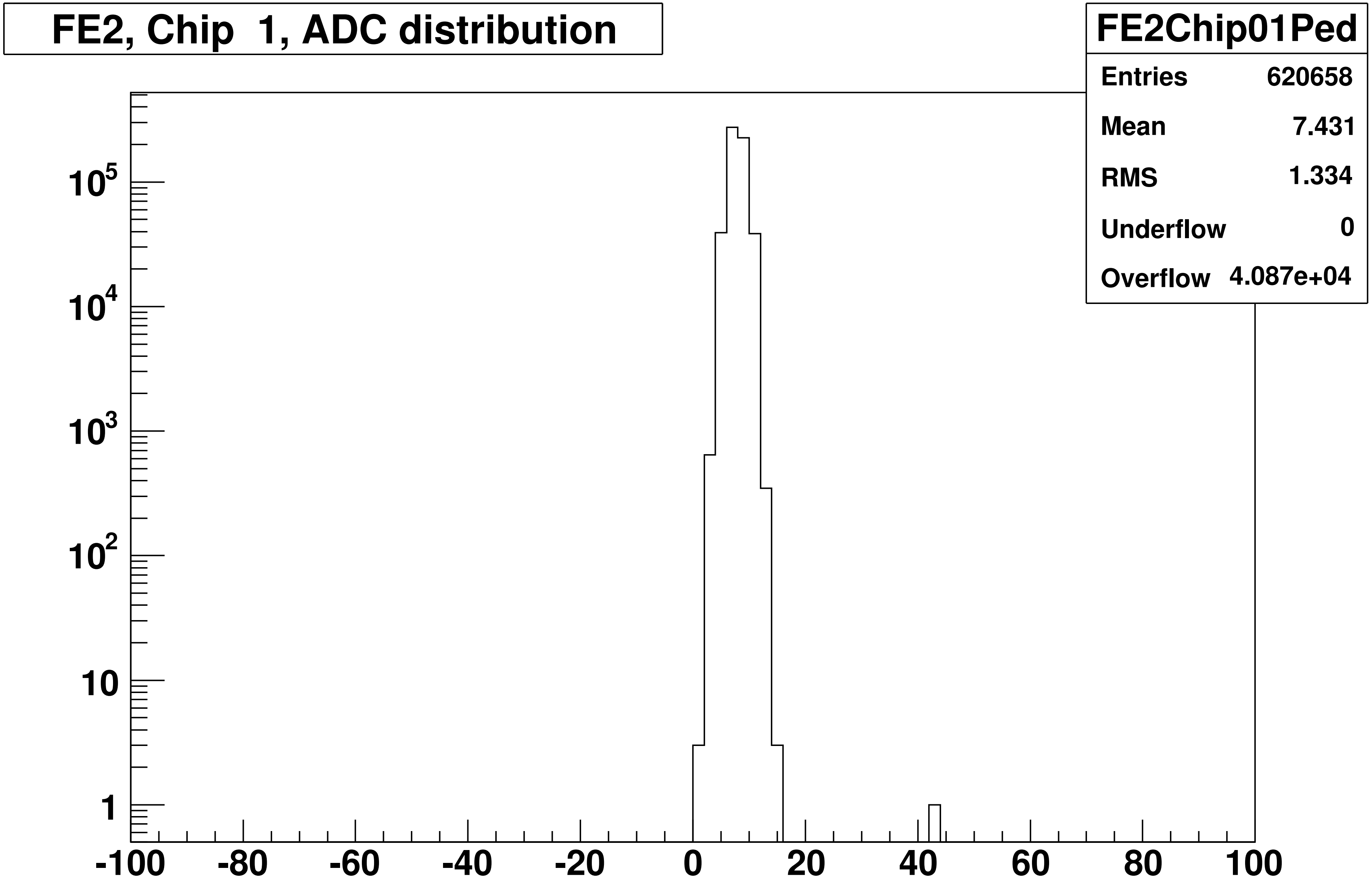
25000


FE2, Chip 1, ADC distribution
FE2Chip01Ped
Entries 620658
Mean 7.431
RMS 1.334
Underflow 0
Overflow 4.087e+04
10^5
10^4
10^3
10^2
10
1
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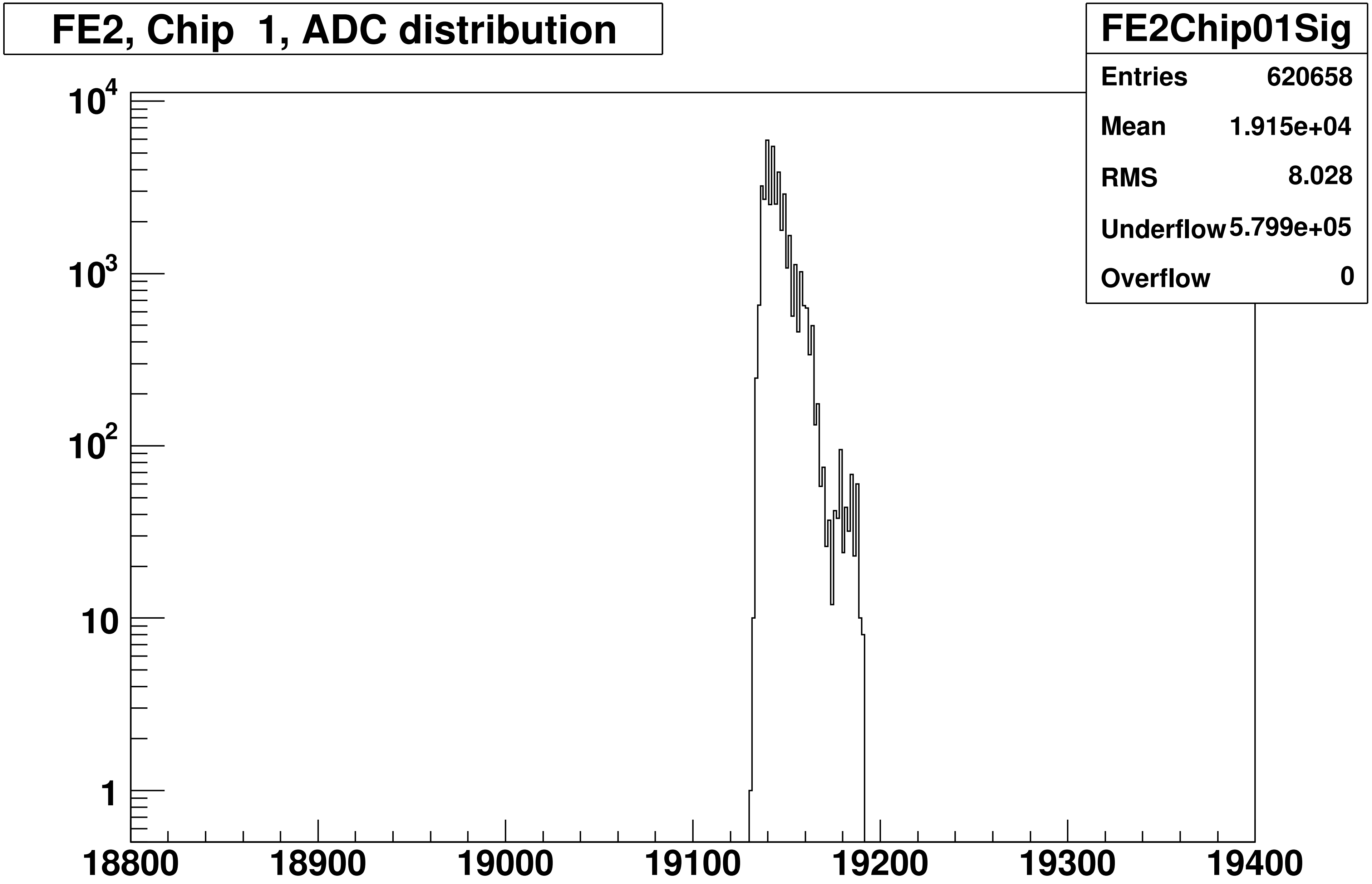

FE2, Chip 1, ADC distribution
FE2Chip01Sig
Entries 620658
Mean 1.915e+04
RMS 8.028
Underflow 5.799e+05
Overflow 0
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10^2
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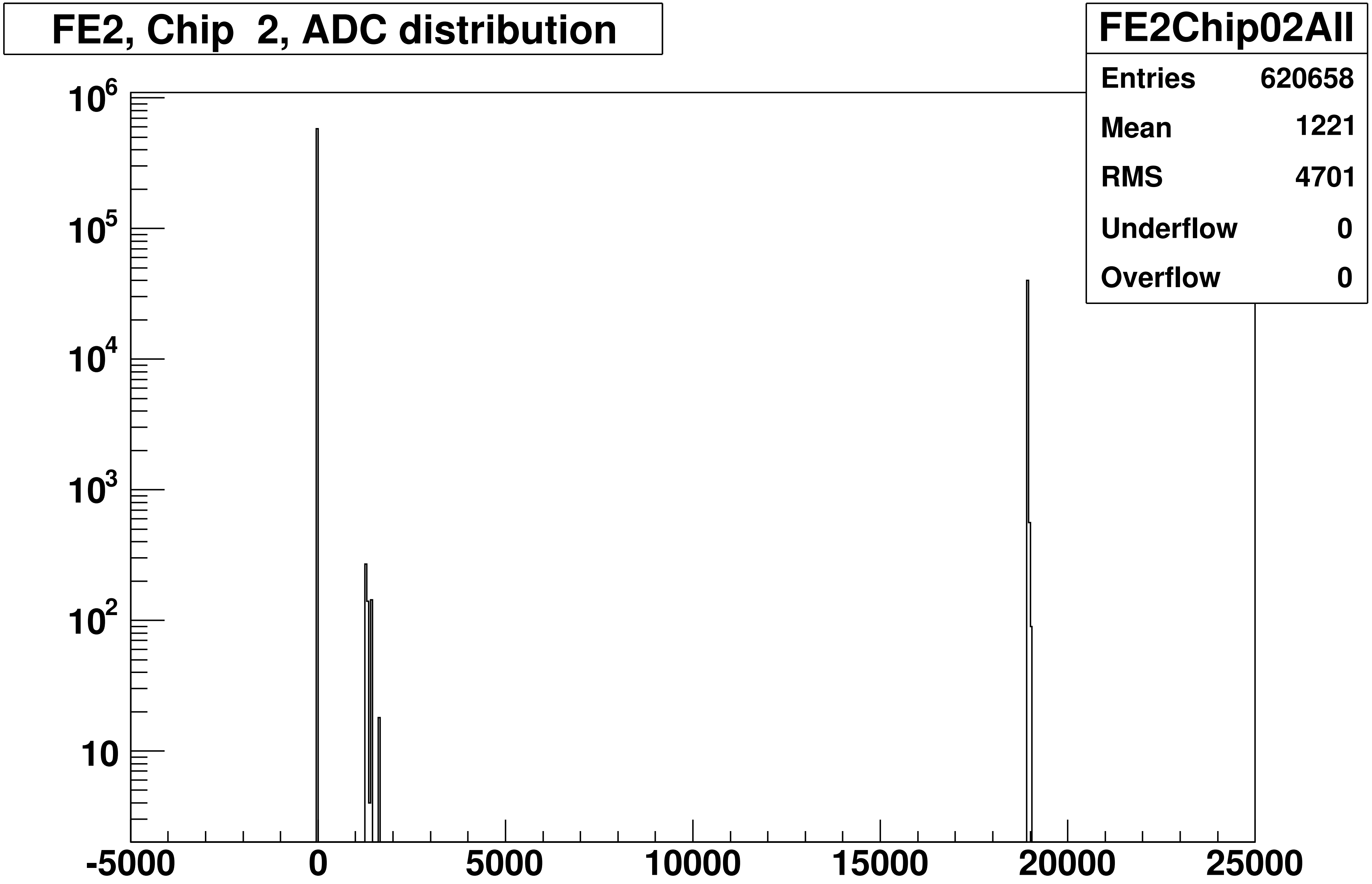
FE2, Chip 2, ADC distribution
FE2Chip02All
Entries 620658
Mean 1221
RMS 4701
Underflow 0
Overflow 0
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5000
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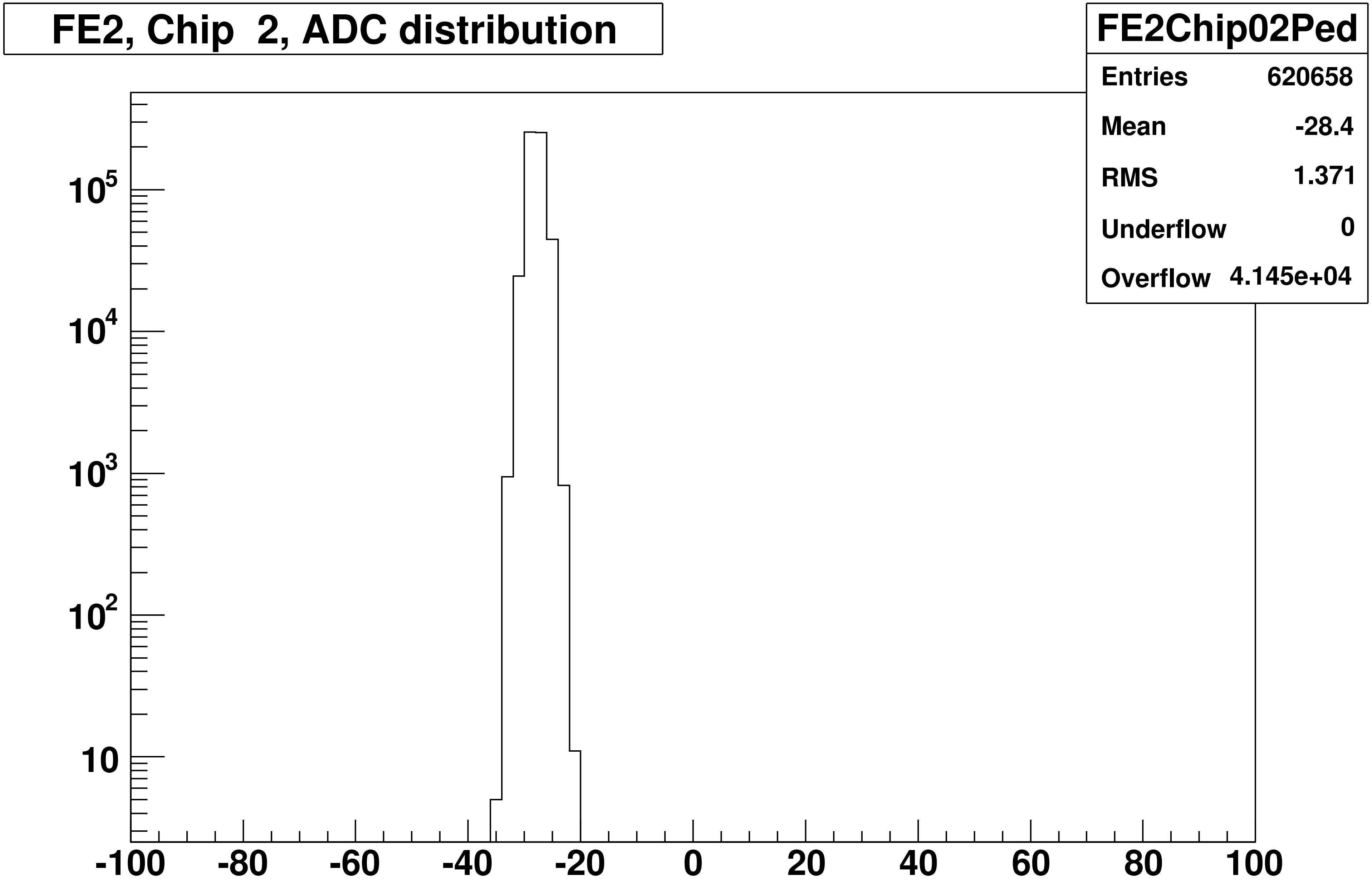
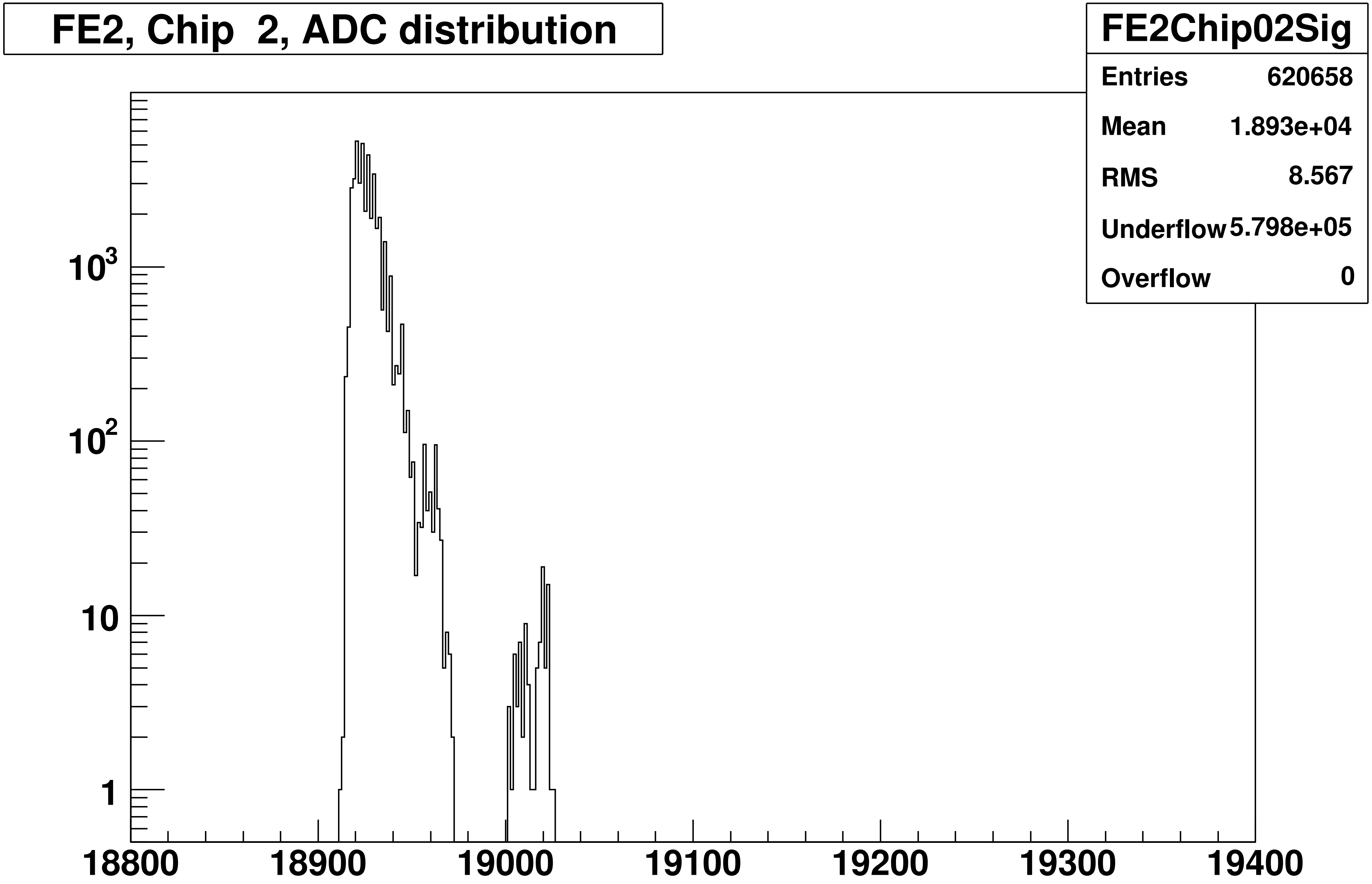
10^4
10^5
10^6



FE2, Chip 2, ADC distribution
FE2Chip02Ped
Entries 620658
Mean -28.4
RMS 1.371
Underflow 0
Overflow 4.145e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 2, ADC distribution
FE2Chip02Sig
Entries 620658
Mean 1.893e+04
RMS 8.567
Underflow 5.798e+05
Overflow 0
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10^2
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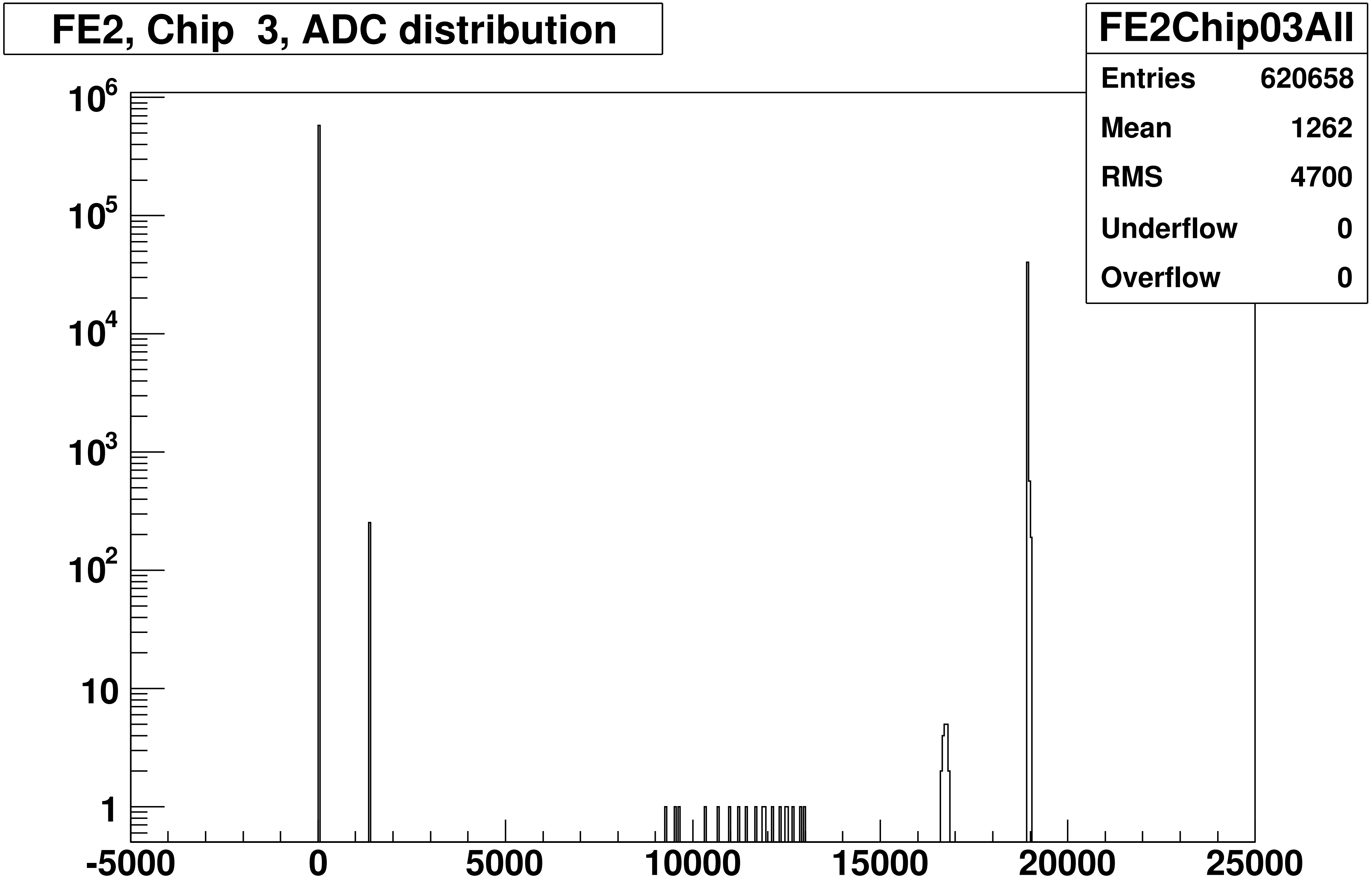
FE2, Chip 3, ADC distribution
FE2Chip03All
Entries 620658
Mean 1262
RMS 4700
Underflow 0
Overflow 0
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10^2
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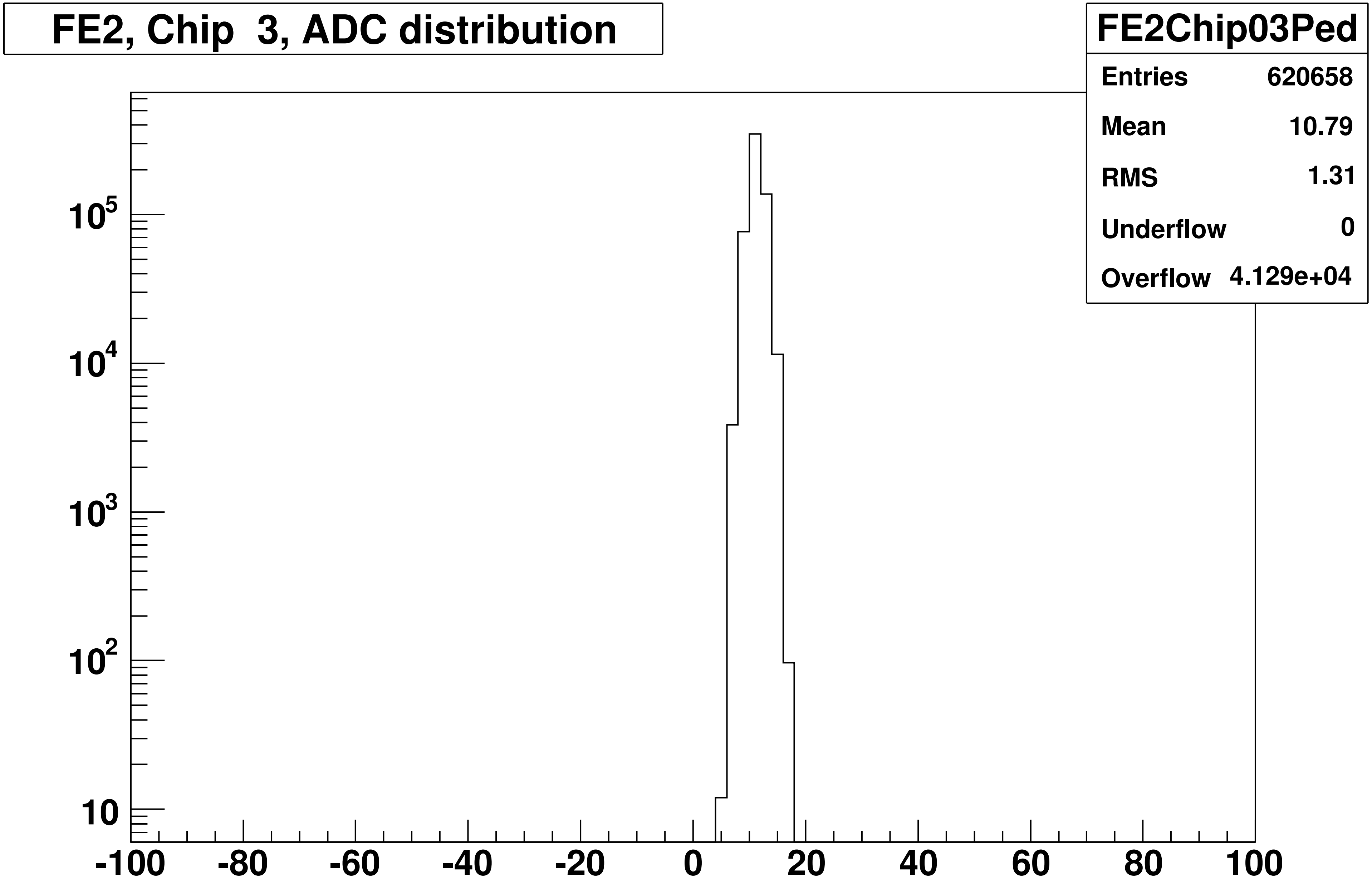
FE2, Chip 3, ADC distribution
FE2Chip03Ped
Entries 620658
Mean 10.79
RMS 1.31
Underflow 0
Overflow 4.129e+04
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10^4
10^3
10^2
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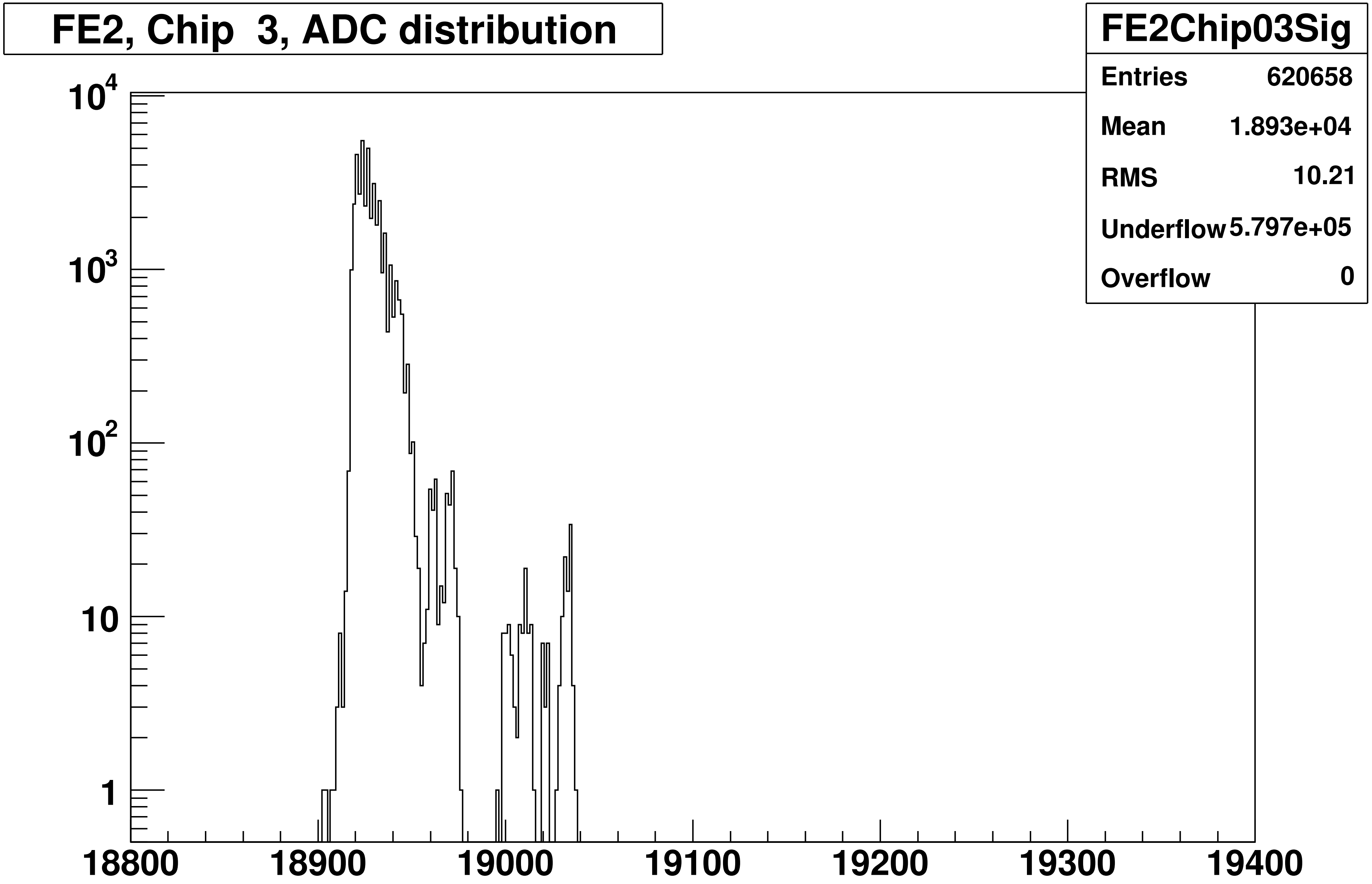
FE2, Chip 3, ADC distribution
FE2Chip03Sig
Entries 620658
Mean 1.893e+04
RMS 10.21
Underflow 5.797e+05
Overflow 0
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10^2
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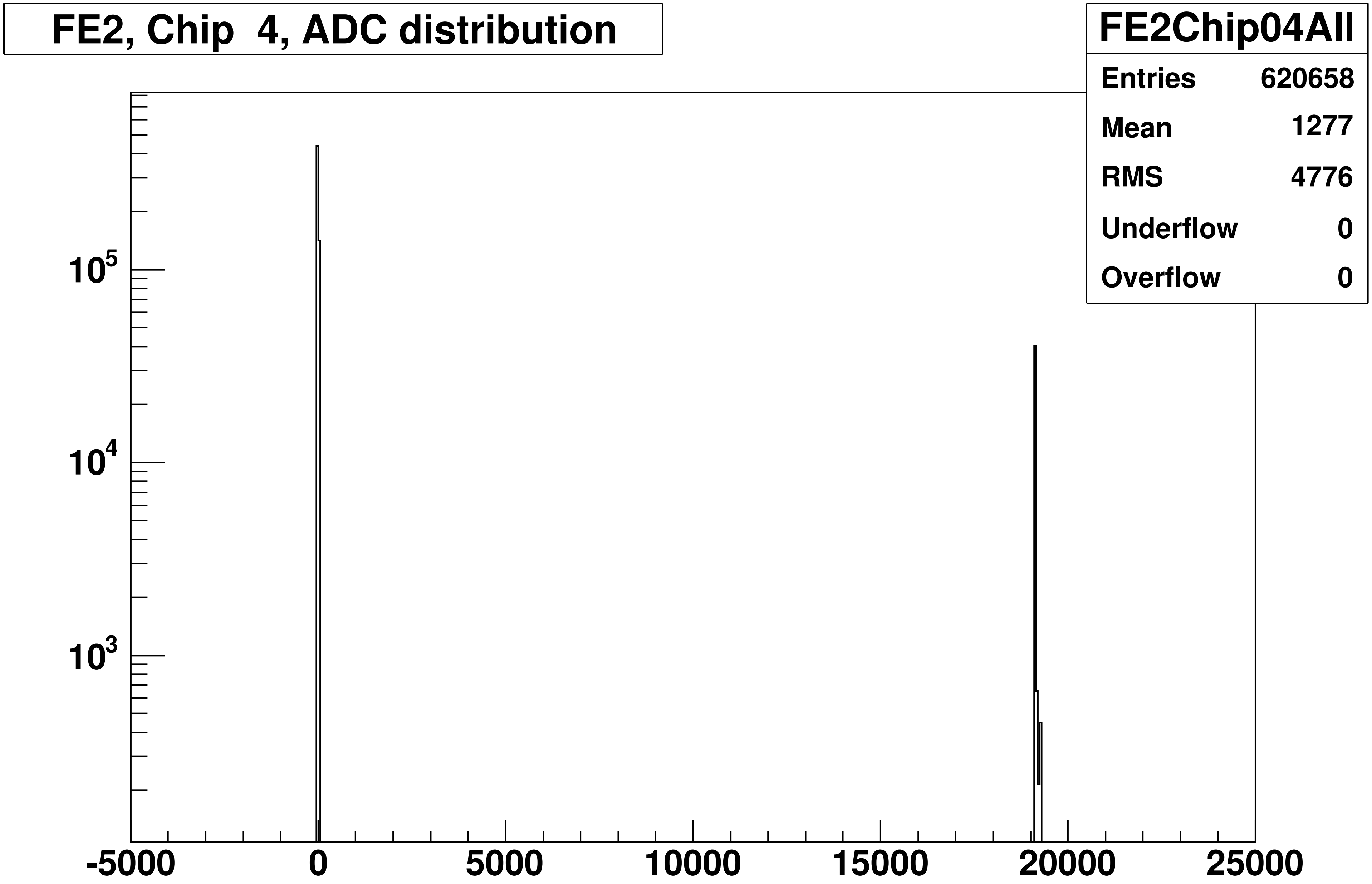
FE2, Chip 4, ADC distribution
FE2Chip04All
Entries 620658
Mean 1277
RMS 4776
Underflow 0
Overflow 0
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10^4
10^3
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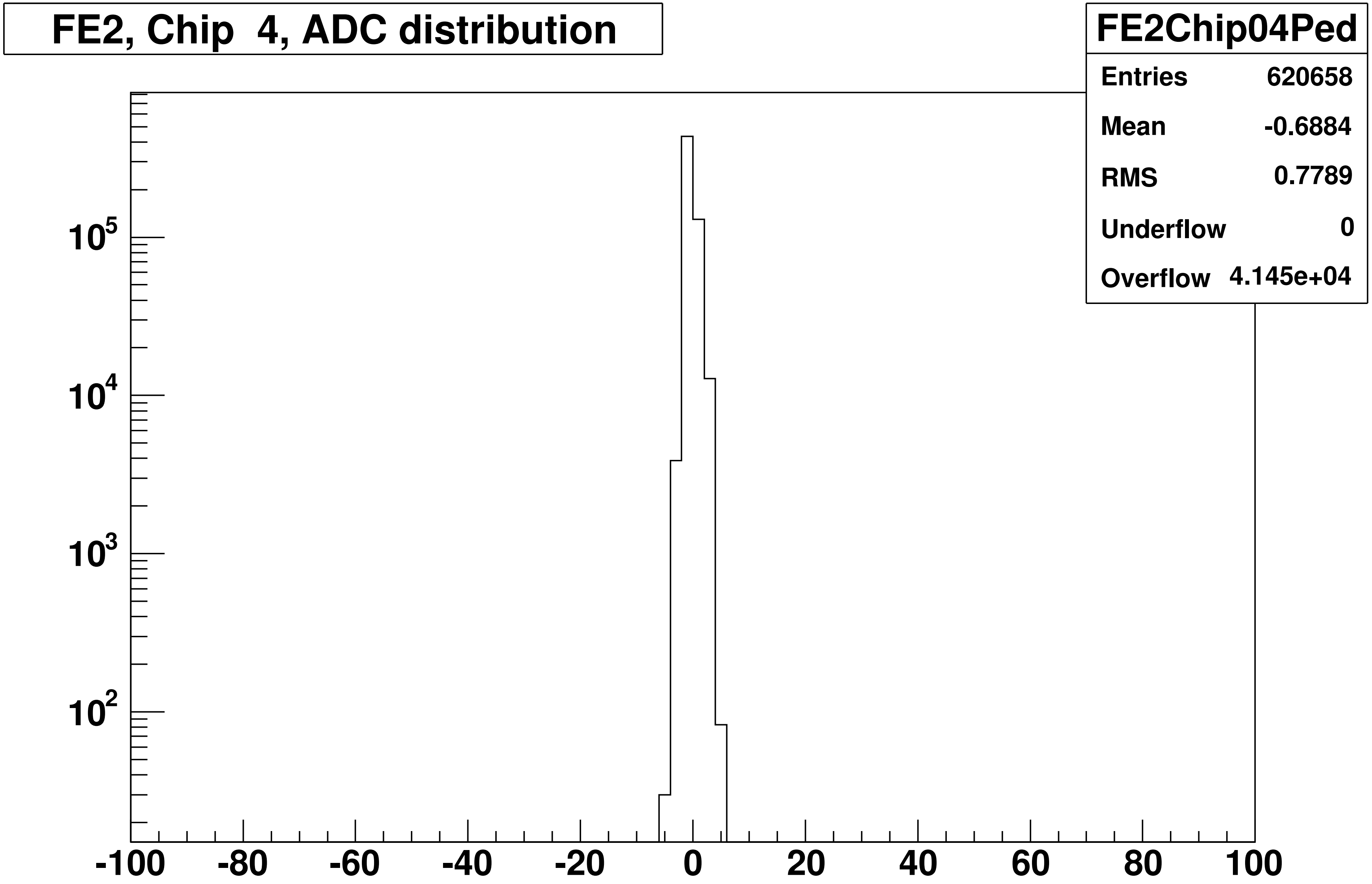
25000


FE2, Chip 4, ADC distribution
FE2Chip04Ped
Entries 620658
Mean -0.6884
RMS 0.7789
Underflow 0
Overflow 4.145e+04
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80
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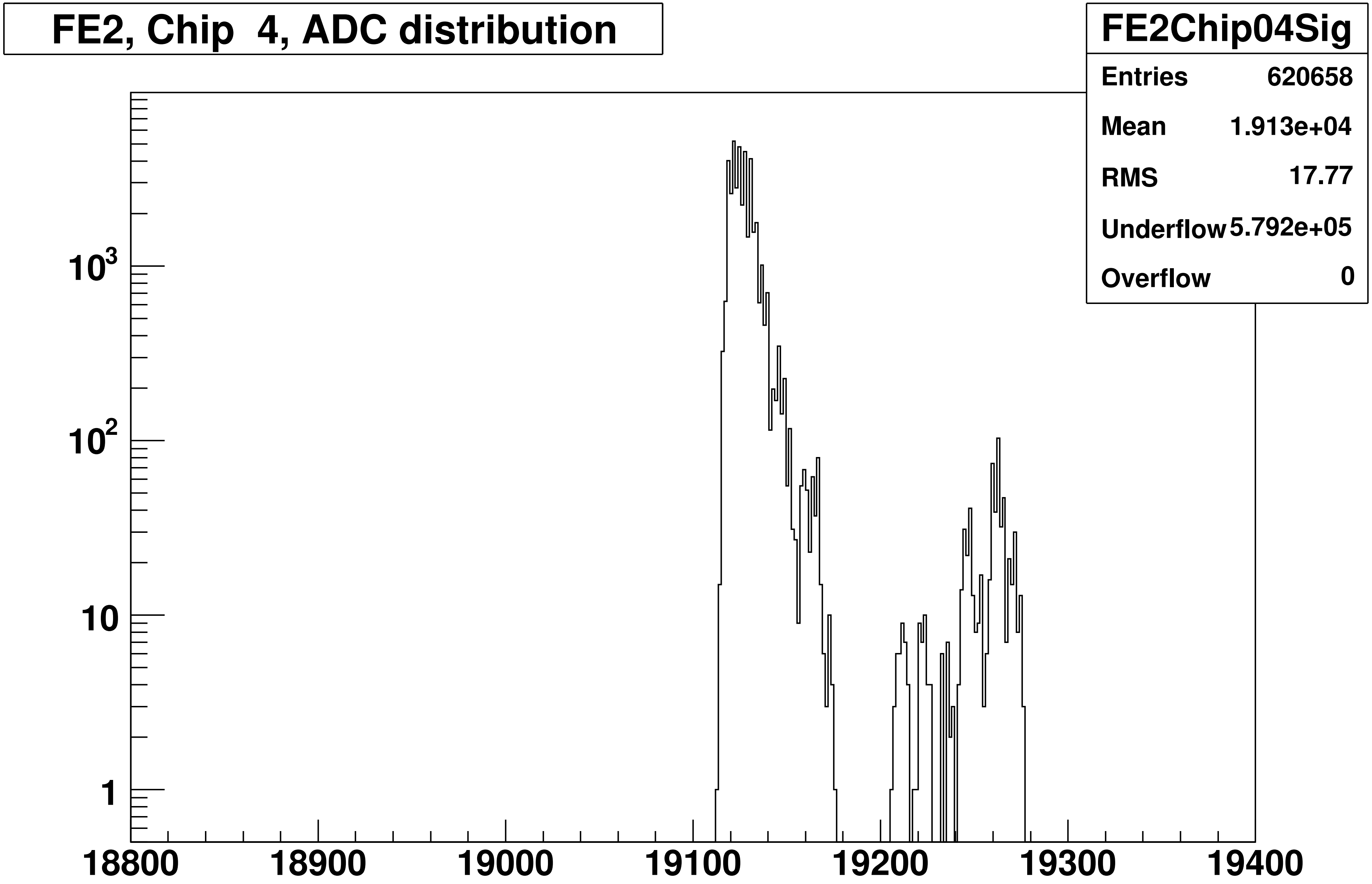
FE2, Chip 4, ADC distribution
FE2Chip04Sig
Entries 620658
Mean 1.913e+04
RMS 17.77
Underflow 5.792e+05
Overflow 0
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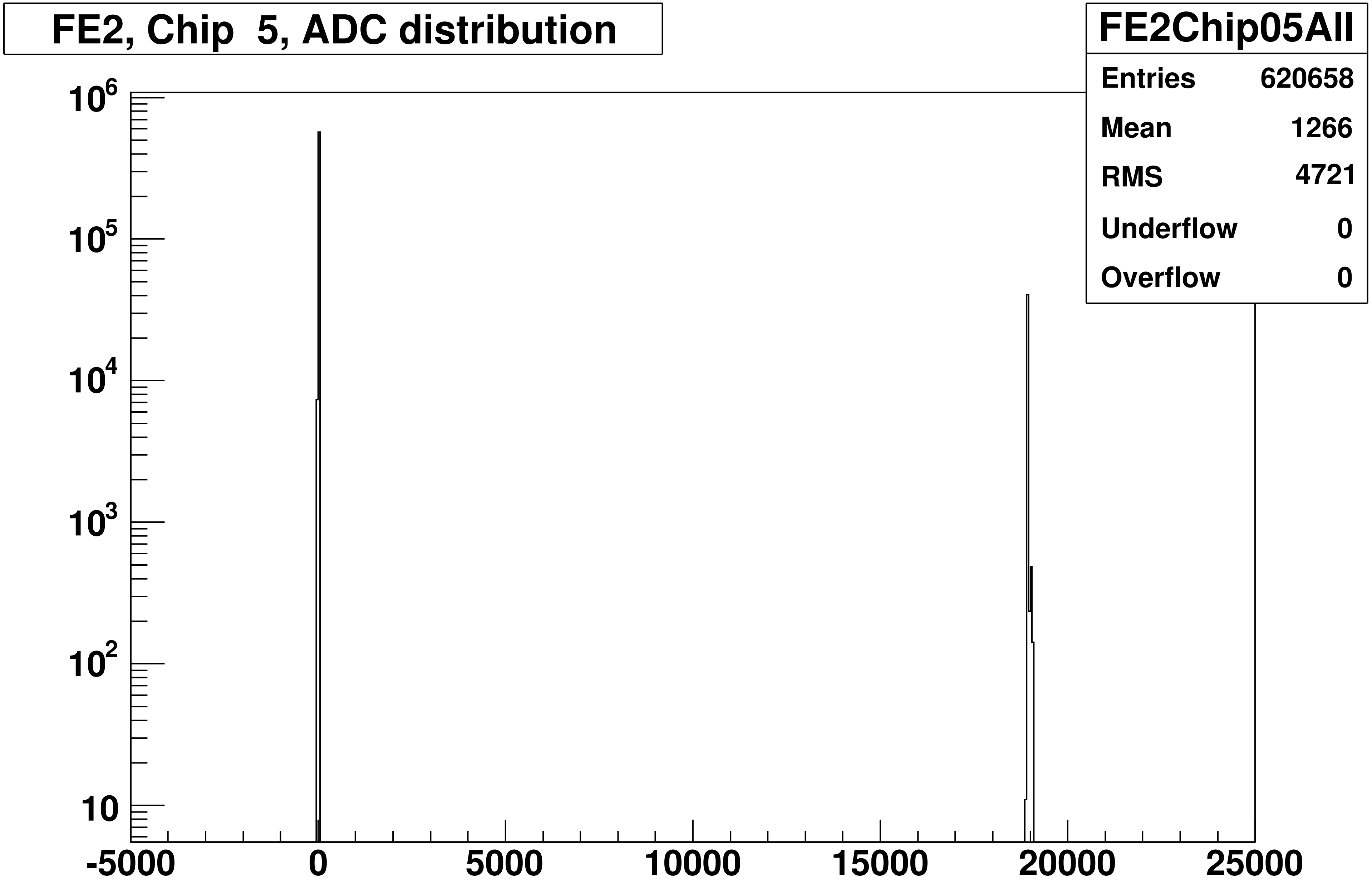

FE2, Chip 5, ADC distribution
FE2Chip05All
Entries 620658
Mean 1266
RMS 4721
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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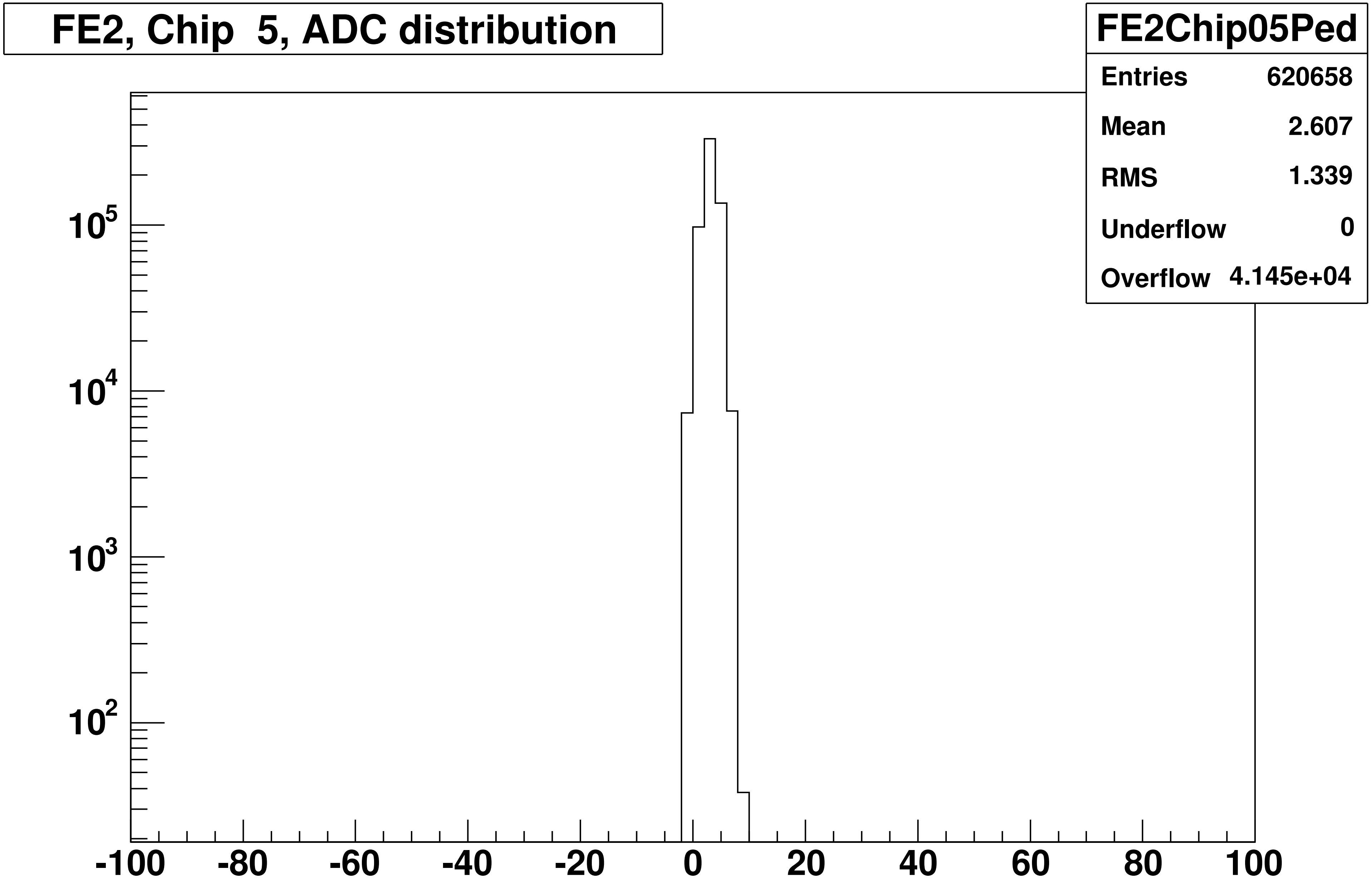
20000
25000


FE2, Chip 5, ADC distribution
FE2Chip05Ped
Entries 620658
Mean 2.607
RMS 1.339
Underflow 0
Overflow 4.145e+04
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10^2
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80
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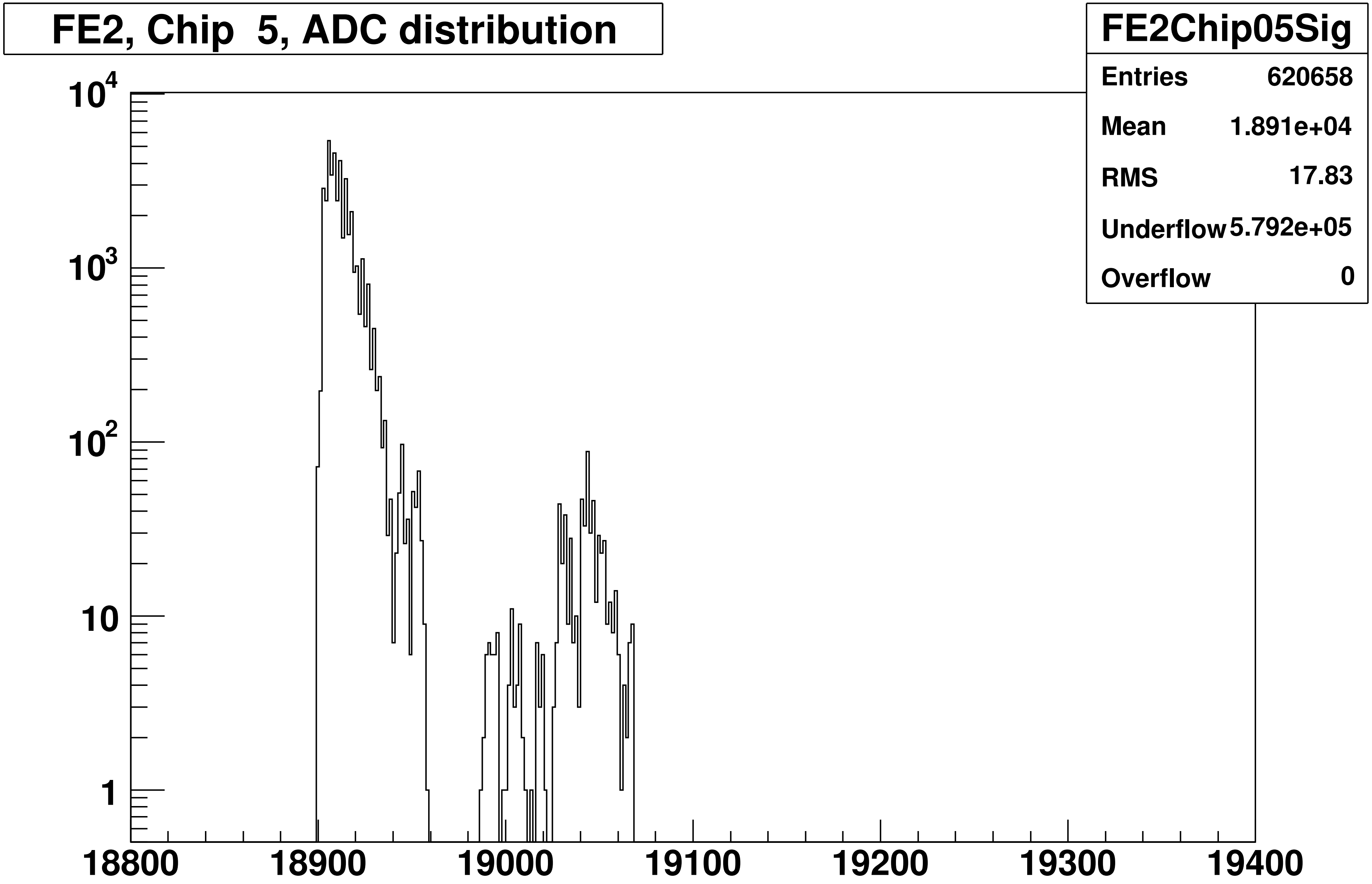
FE2, Chip 5, ADC distribution
FE2Chip05Sig
Entries 620658
Mean 1.891e+04
RMS 17.83
Underflow 5.792e+05
Overflow 0
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10^3
10^2
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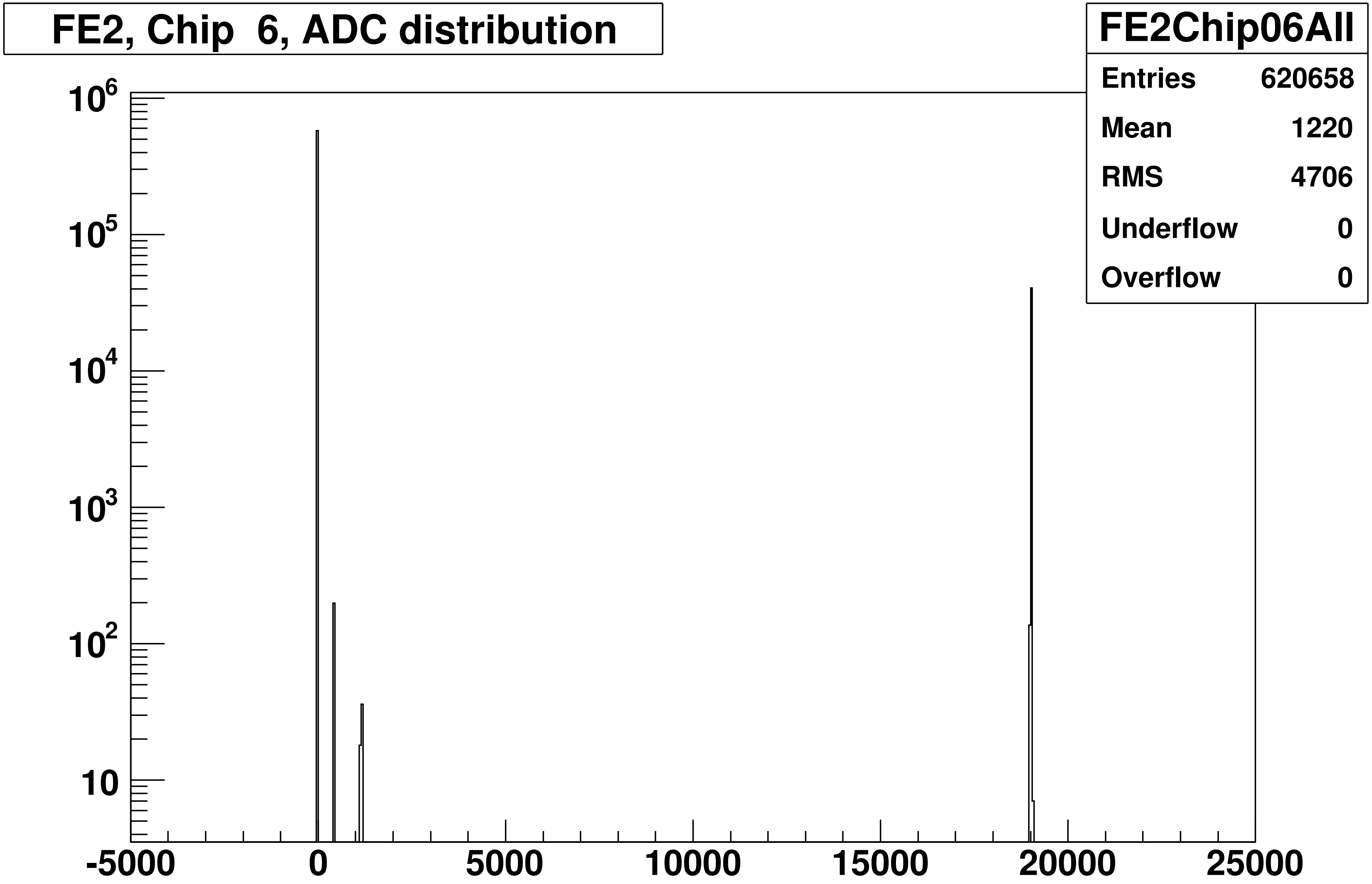
FE2, Chip 6, ADC distribution
FE2Chip06All
Entries 620658
Mean 1220
RMS 4706
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
-5000
0
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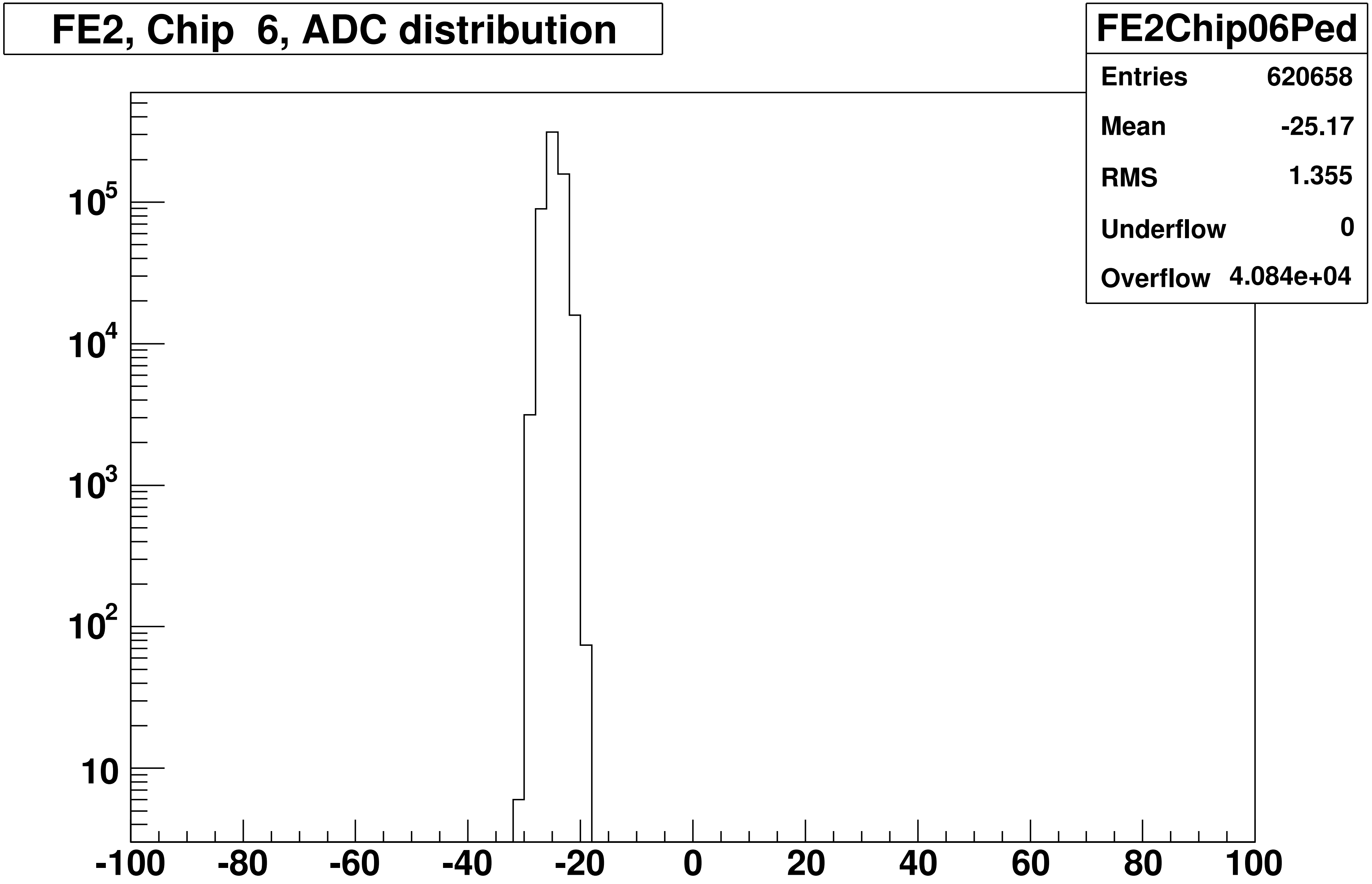
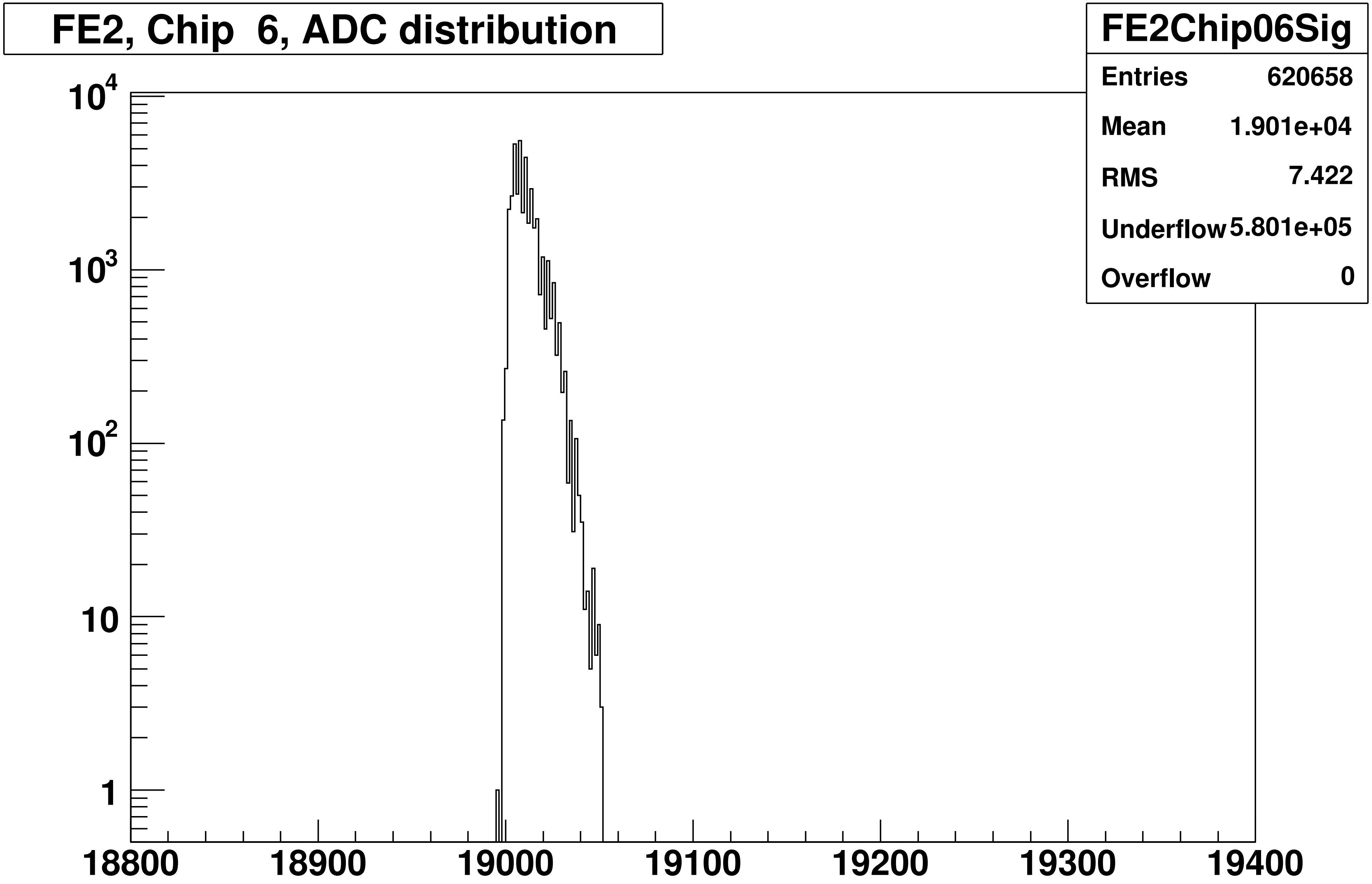
10000
15000
20000
25000


FE2, Chip 6, ADC distribution
FE2Chip06Ped
Entries 620658
Mean -25.17
RMS 1.355
Underflow 0
Overflow 4.084e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 6, ADC distribution
FE2Chip06Sig
Entries 620658
Mean 1.901e+04
RMS 7.422
Underflow 5.801e+05
Overflow 0
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10^3
10^2
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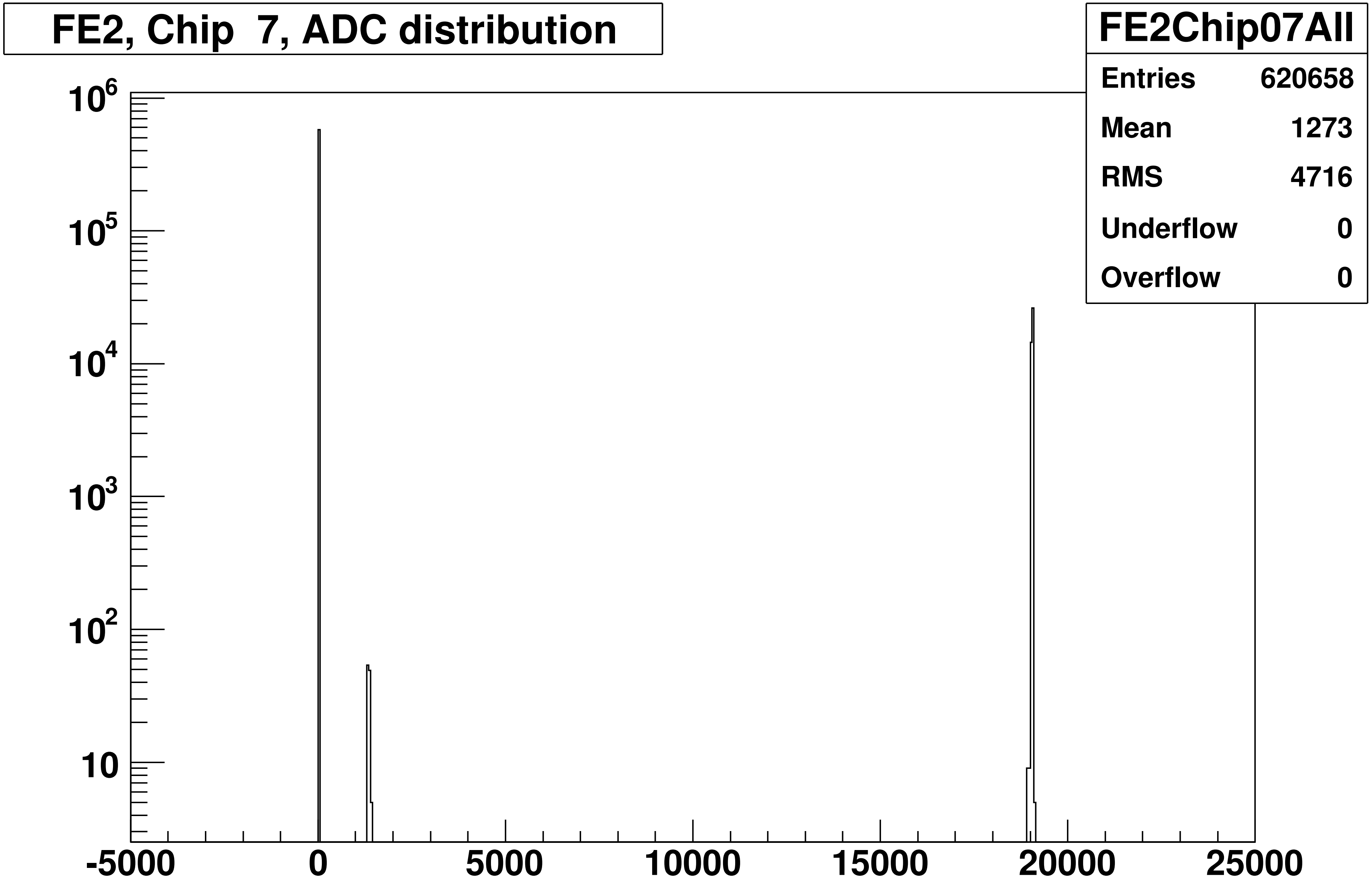
FE2, Chip 7, ADC distribution
FE2Chip07All
Entries 620658
Mean 1273
RMS 4716
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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20000
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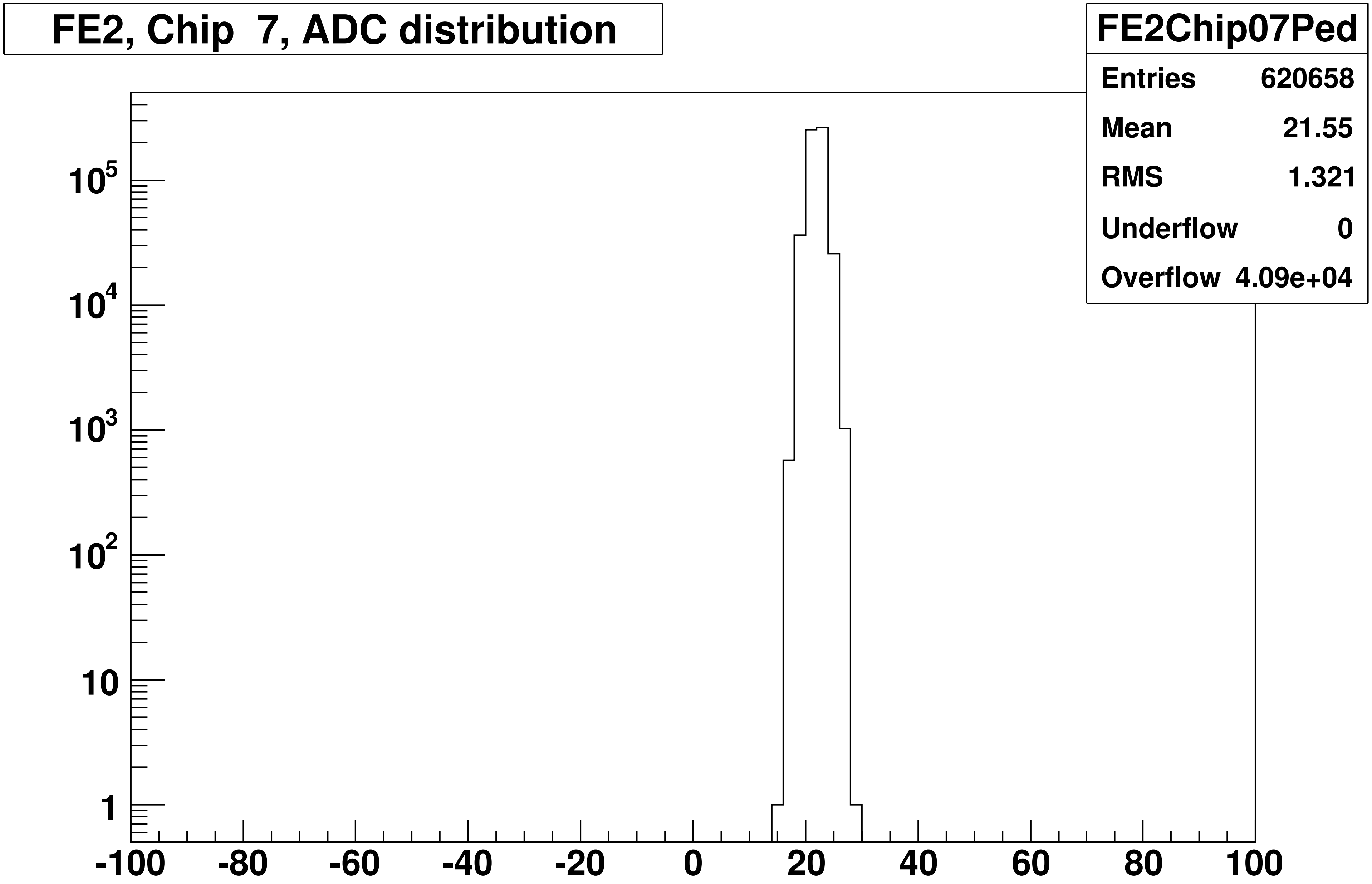
FE2, Chip 7, ADC distribution
FE2Chip07Ped
Entries 620658
Mean 21.55
RMS 1.321
Underflow 0
Overflow 4.09e+04
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10^4
10^3
10^2
10
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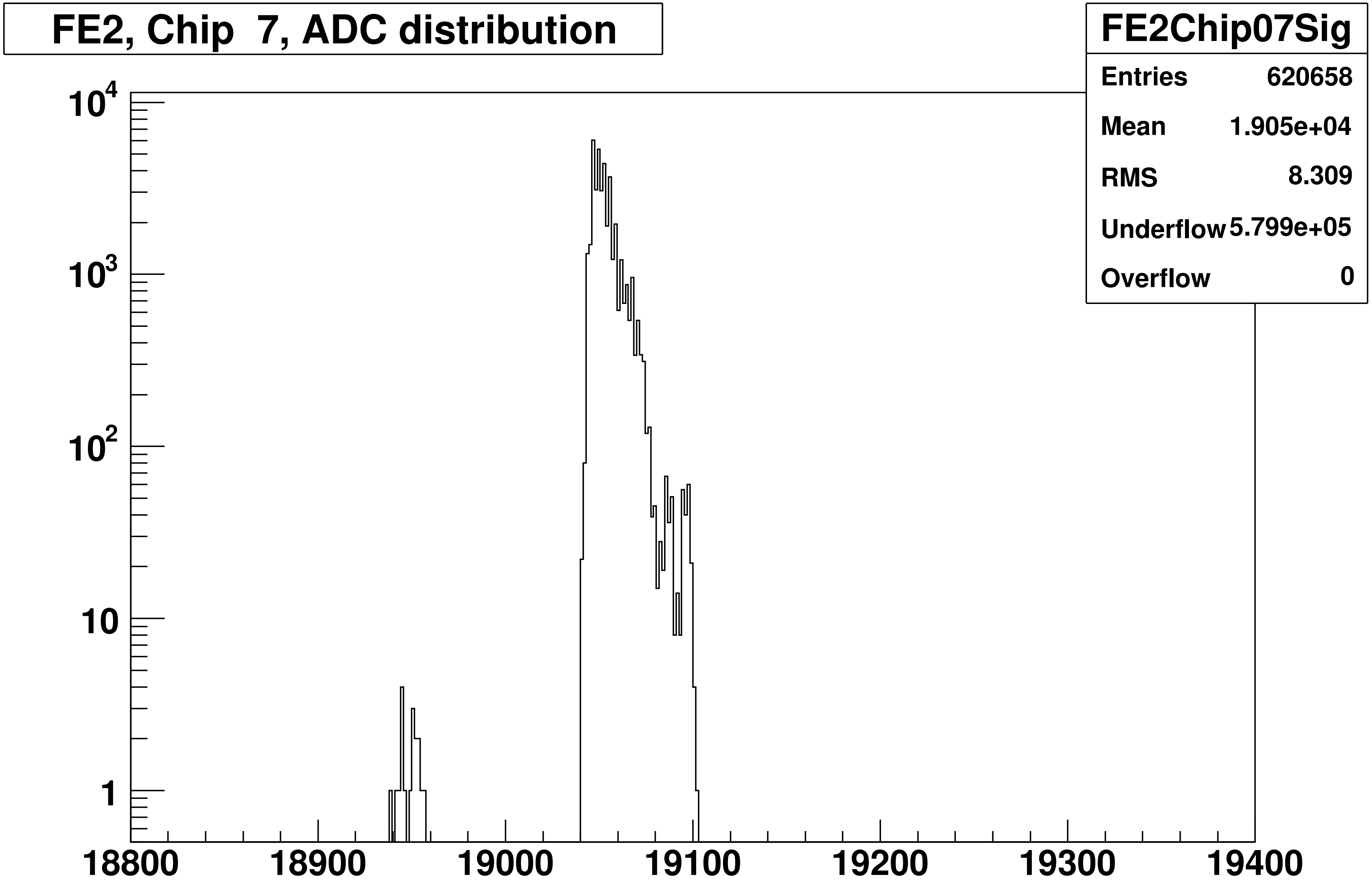
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE2, Chip 7, ADC distribution
FE2Chip07Sig
Entries 620658
Mean 1.905e+04
RMS 8.309
Underflow 5.799e+05
Overflow 0
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10^3
10^2
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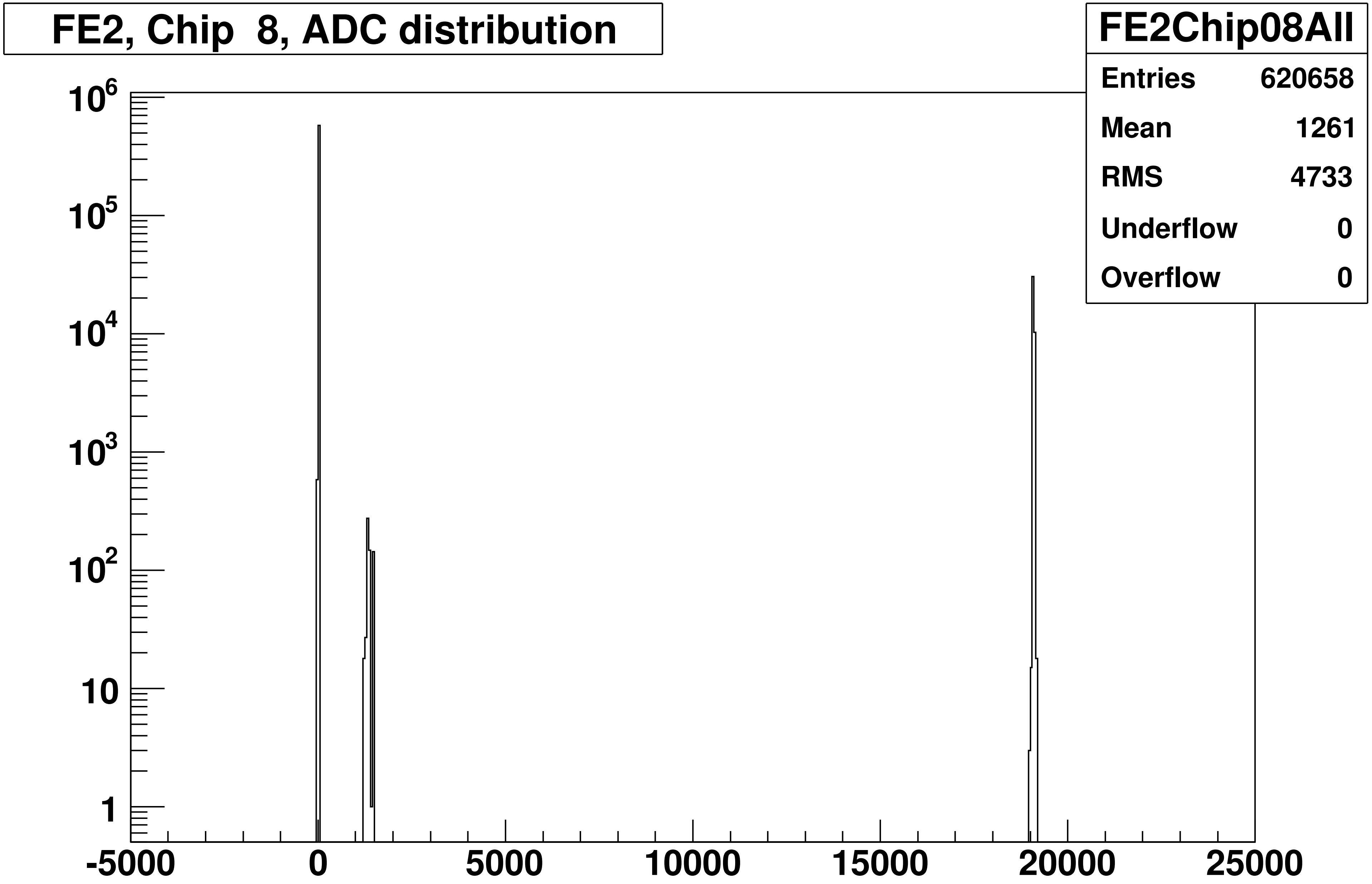

FE2, Chip 8, ADC distribution
FE2Chip08All
Entries 620658
Mean 1261
RMS 4733
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
0
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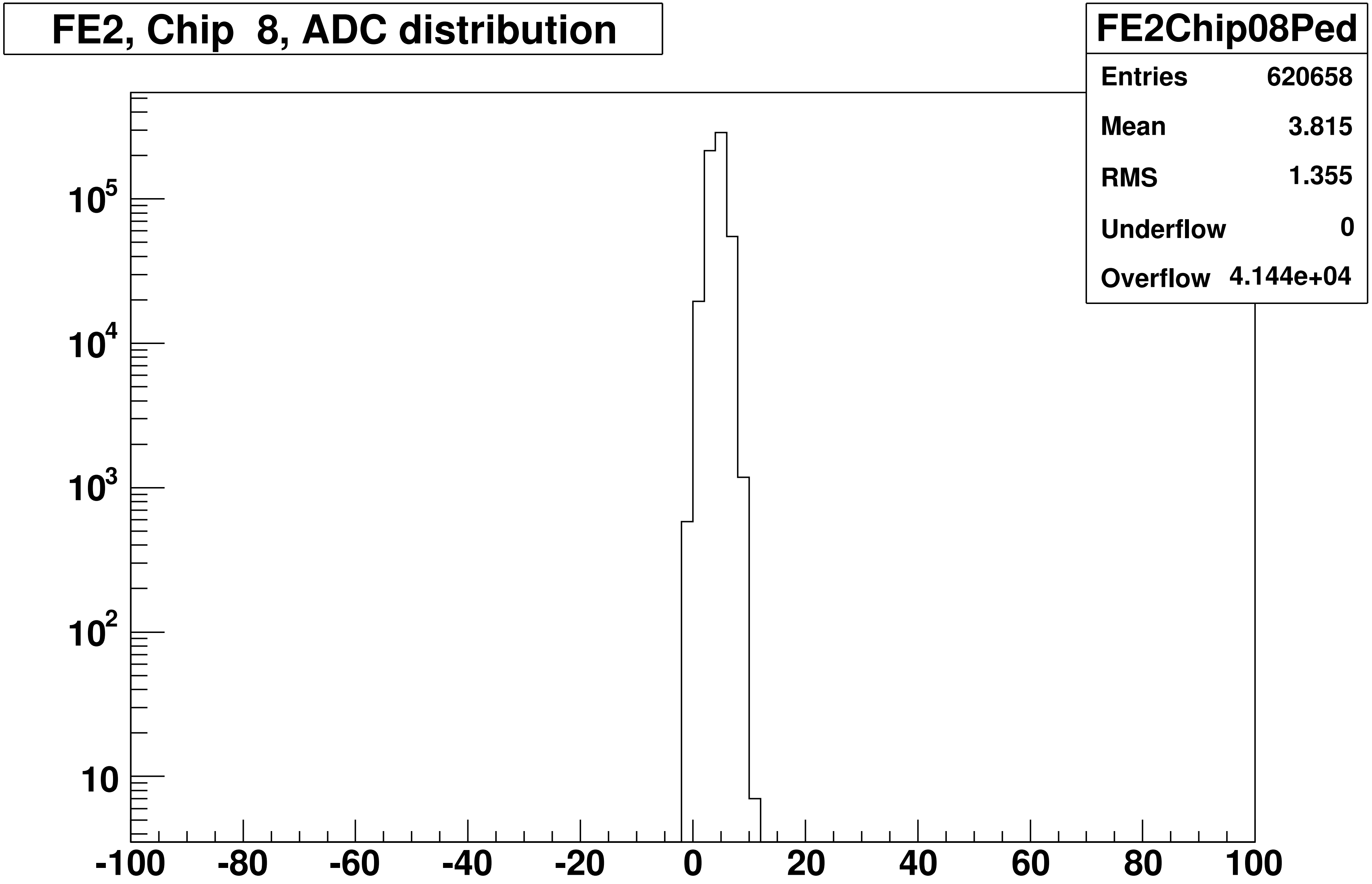
10000
15000
20000
25000


FE2, Chip 8, ADC distribution
FE2Chip08Ped
Entries 620658
Mean 3.815
RMS 1.355
Underflow 0
Overflow 4.144e+04
10
10^2
10^3
10^4
10^5
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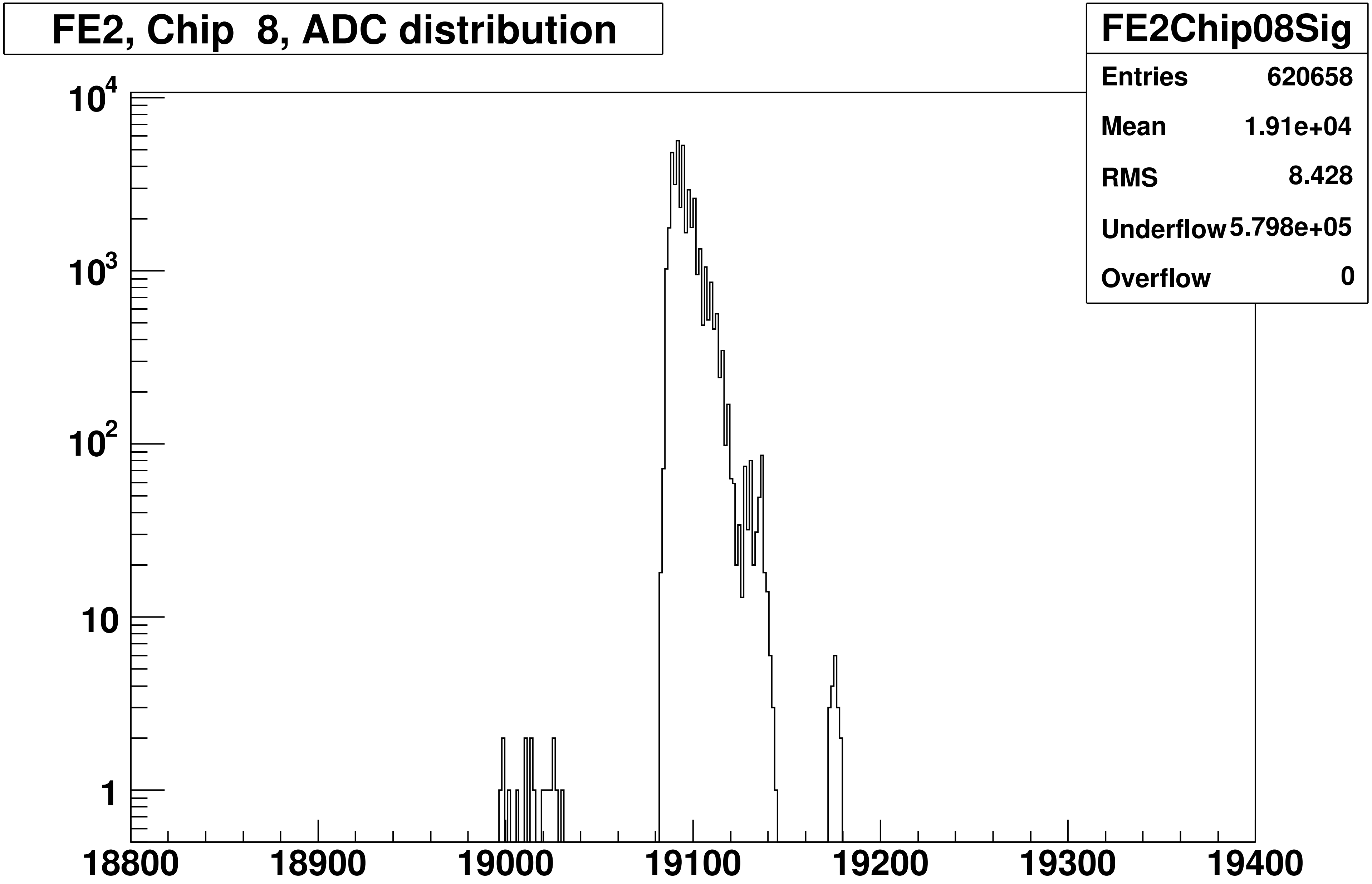
-80
-60
-40
-20
0
20
40
60
80
100


FE2, Chip 8, ADC distribution
FE2Chip08Sig
Entries 620658
Mean 1.91e+04
RMS 8.428
Underflow 5.798e+05
Overflow 0
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10^3
10^2
10
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18900
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19200
19300
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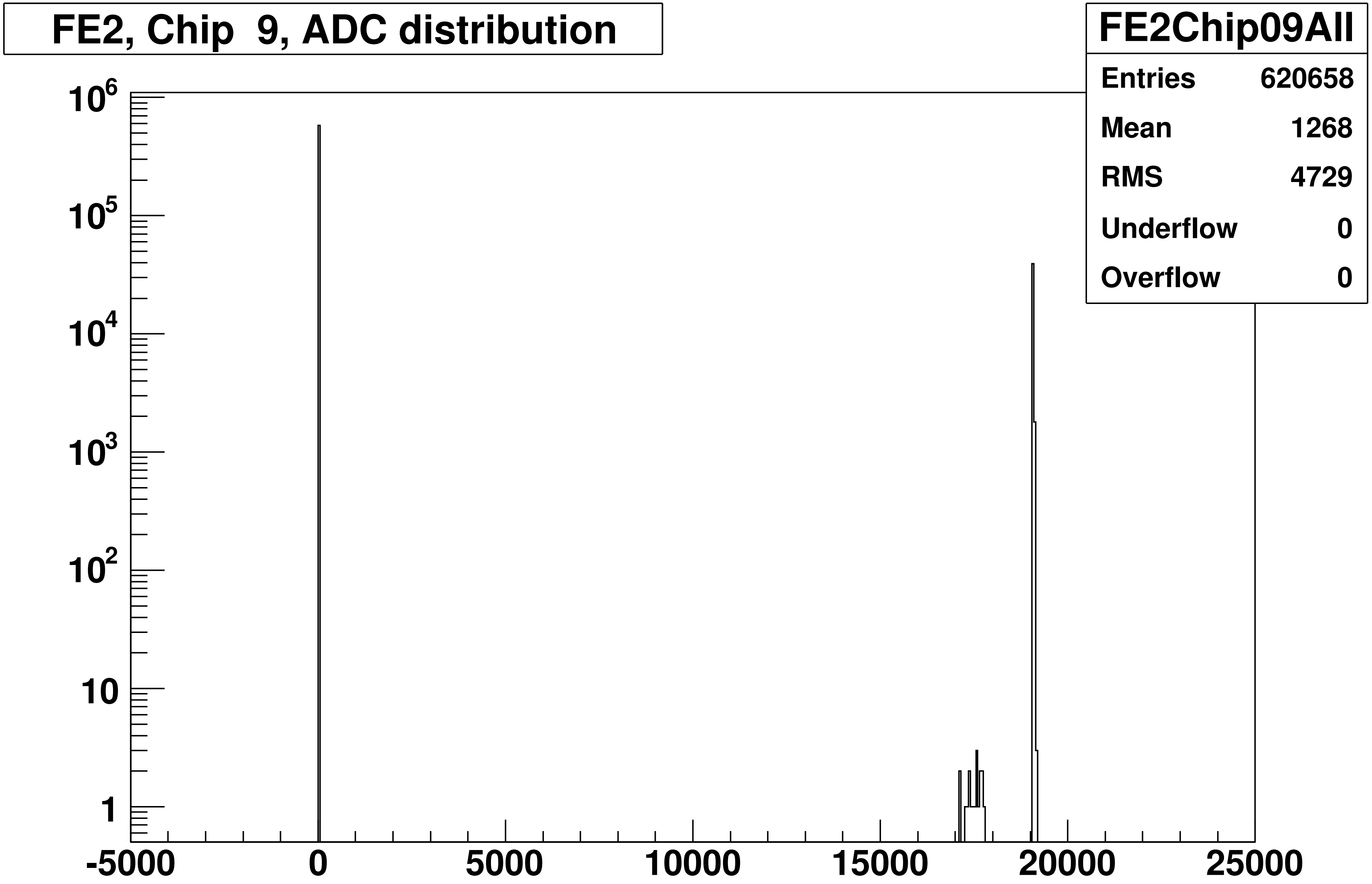

FE2, Chip 9, ADC distribution
FE2Chip09All
Entries 620658
Mean 1268
RMS 4729
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
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-5000
0
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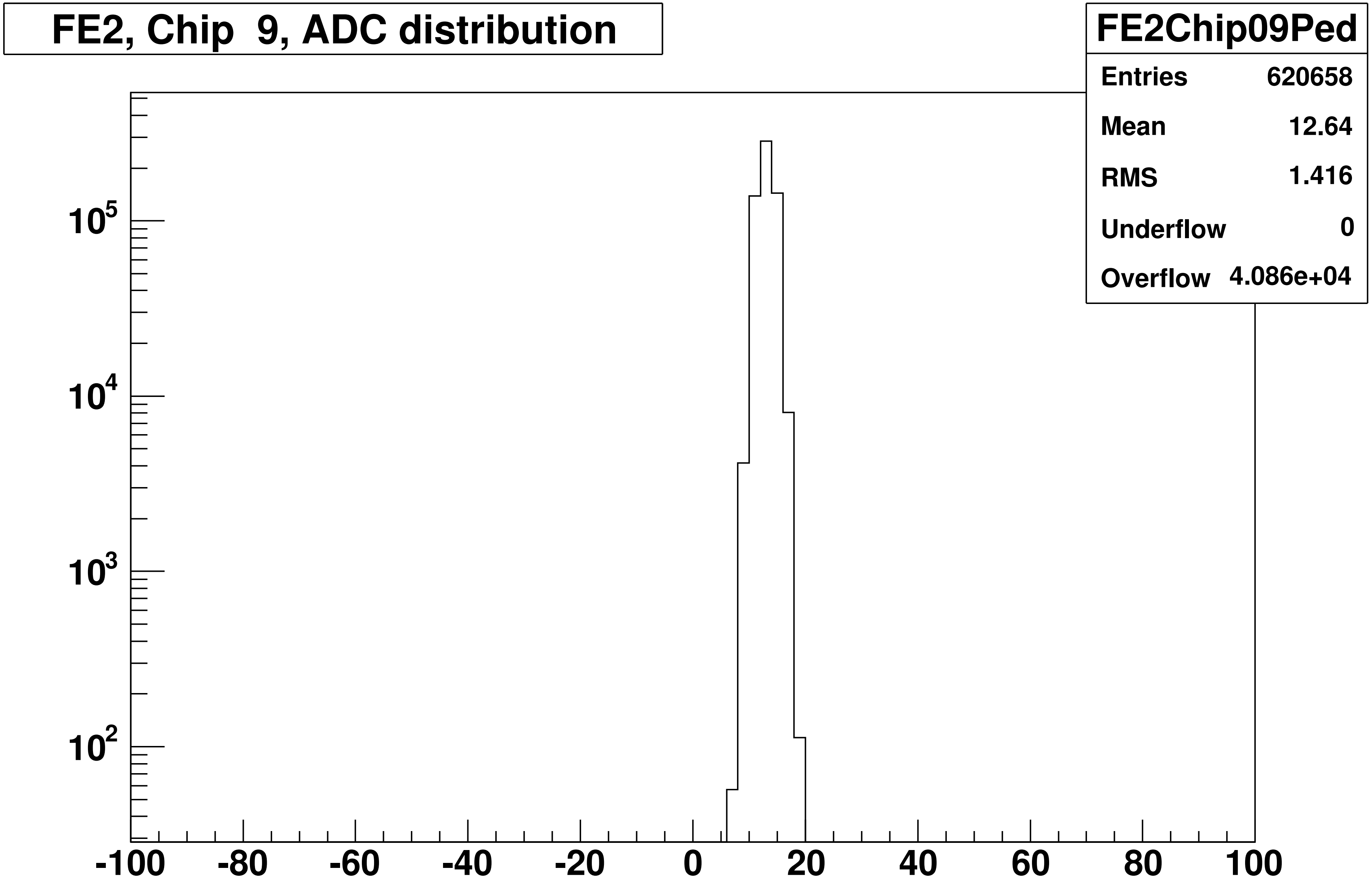
10000
15000
20000
25000


FE2, Chip 9, ADC distribution
FE2Chip09Ped
Entries 620658
Mean 12.64
RMS 1.416
Underflow 0
Overflow 4.086e+04
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10^3
10^2
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-20
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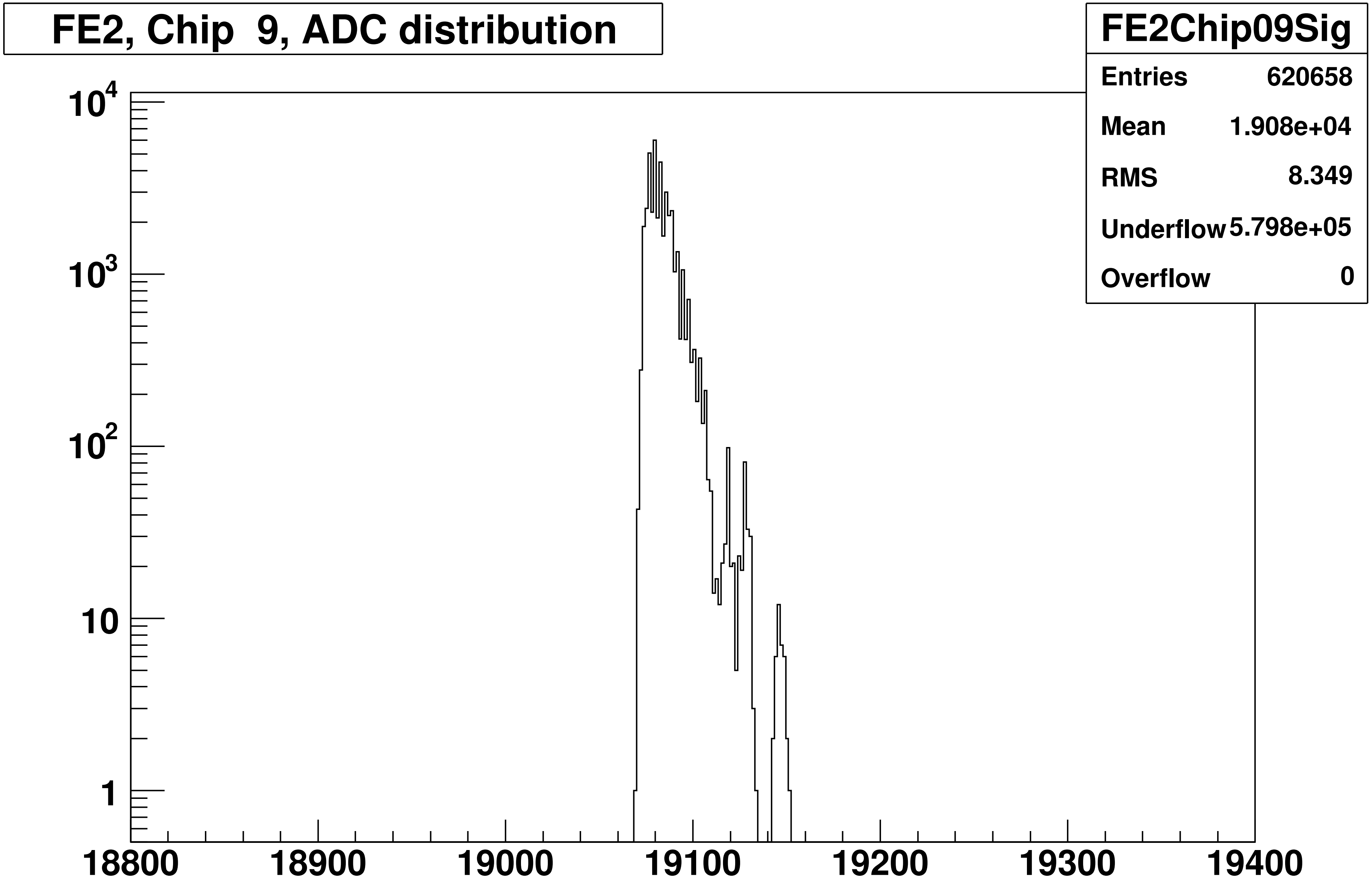
FE2, Chip 9, ADC distribution
FE2Chip09Sig
Entries 620658
Mean 1.908e+04
RMS 8.349
Underflow 5.798e+05
Overflow 0
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10^3
10^2
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18900
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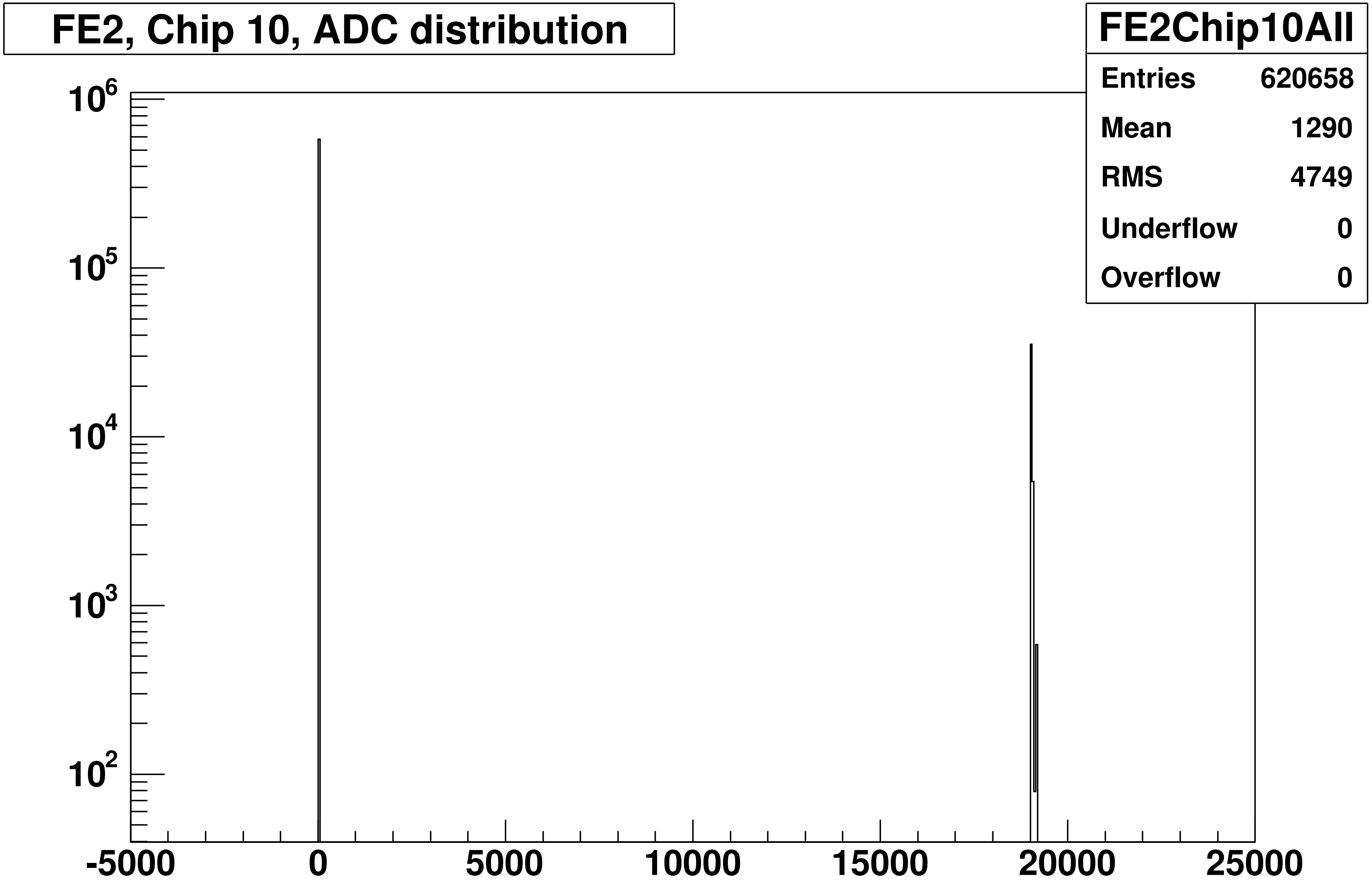

FE2, Chip 10, ADC distribution
FE2Chip10All
Entries 620658
Mean 1290
RMS 4749
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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0
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10000
15000
20000
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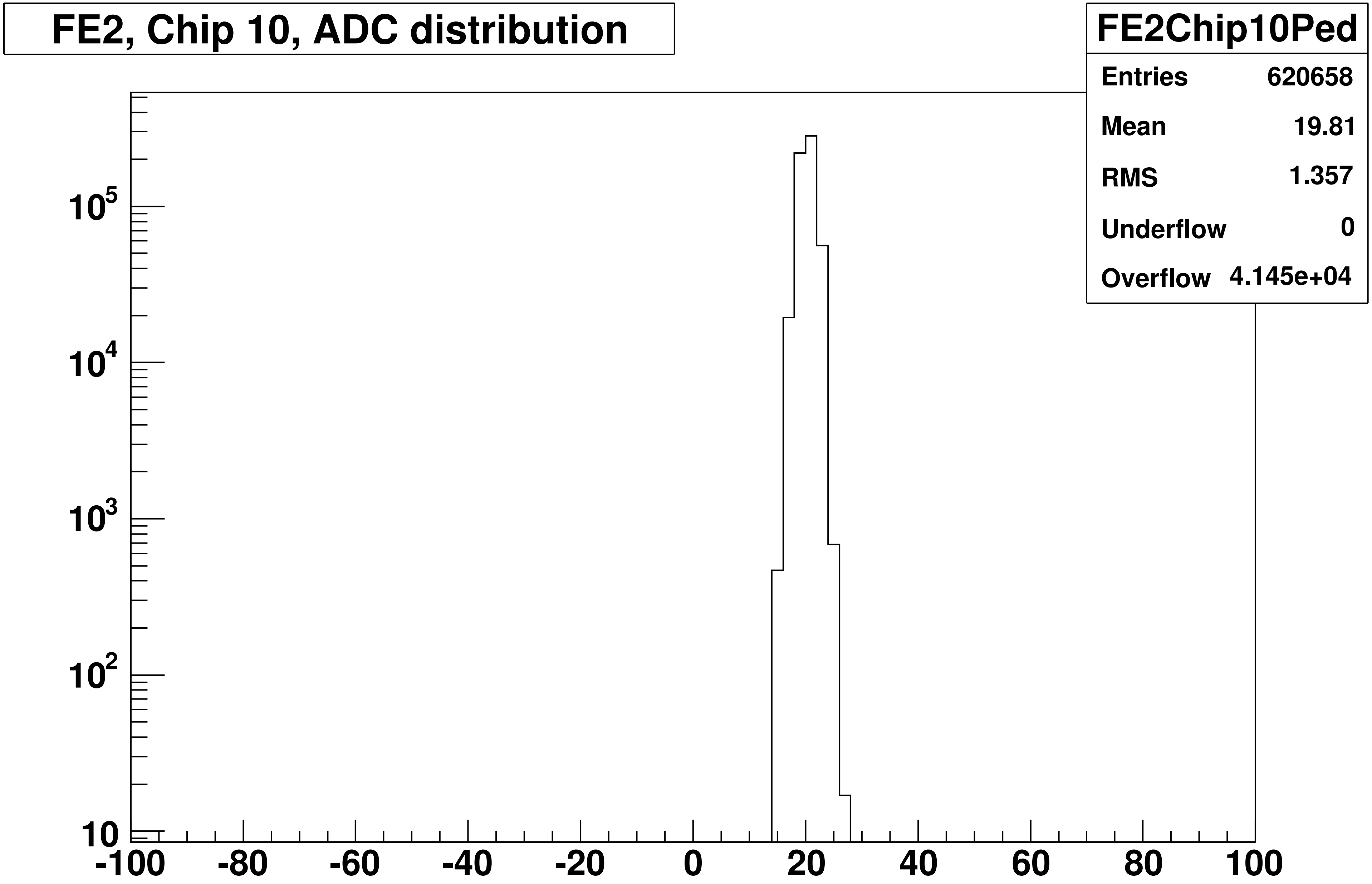
FE2, Chip 10, ADC distribution
FE2Chip10Ped
Entries 620658
Mean 19.81
RMS 1.357
Underflow 0
Overflow 4.145e+04
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10^3
10^4
10^5
-100
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-40
-20
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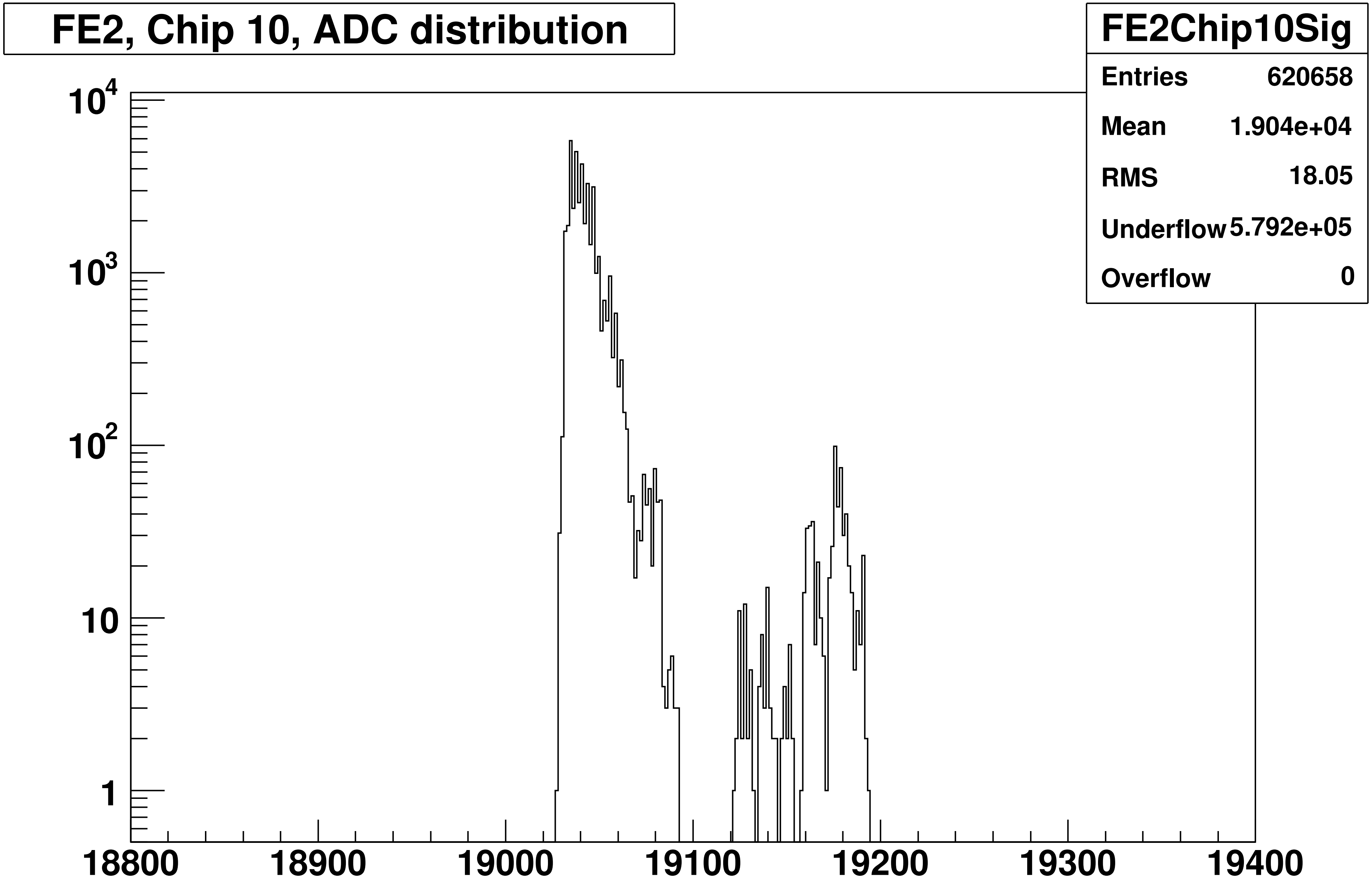
FE2, Chip 10, ADC distribution
FE2Chip10Sig
Entries 620658
Mean 1.904e+04
RMS 18.05
Underflow 5.792e+05
Overflow 0
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10^3
10^2
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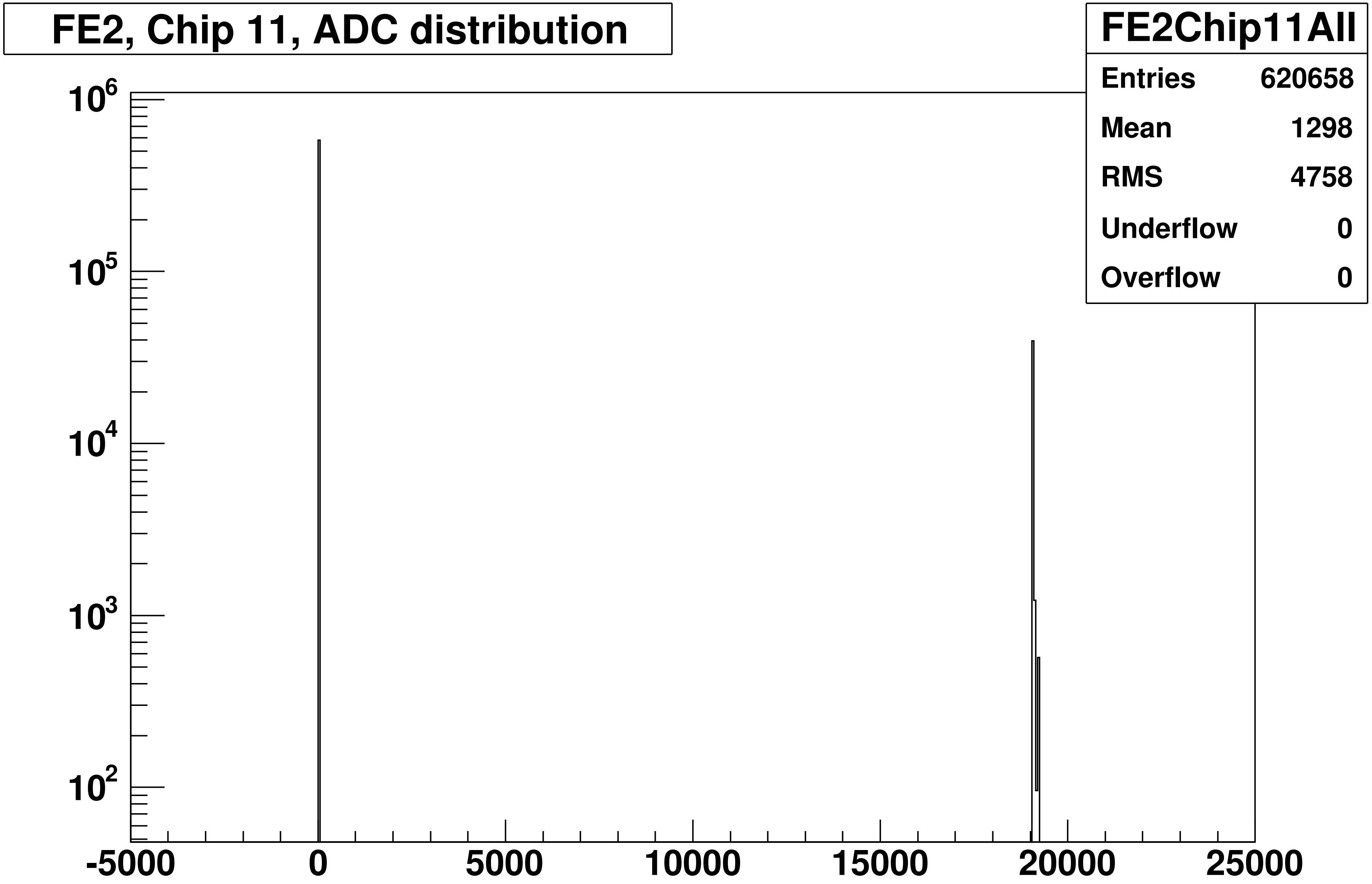
FE2, Chip 11, ADC distribution
FE2Chip11All
Entries 620658
Mean 1298
RMS 4758
Underflow 0
Overflow 0
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10^5
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10^3
10^2
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20000
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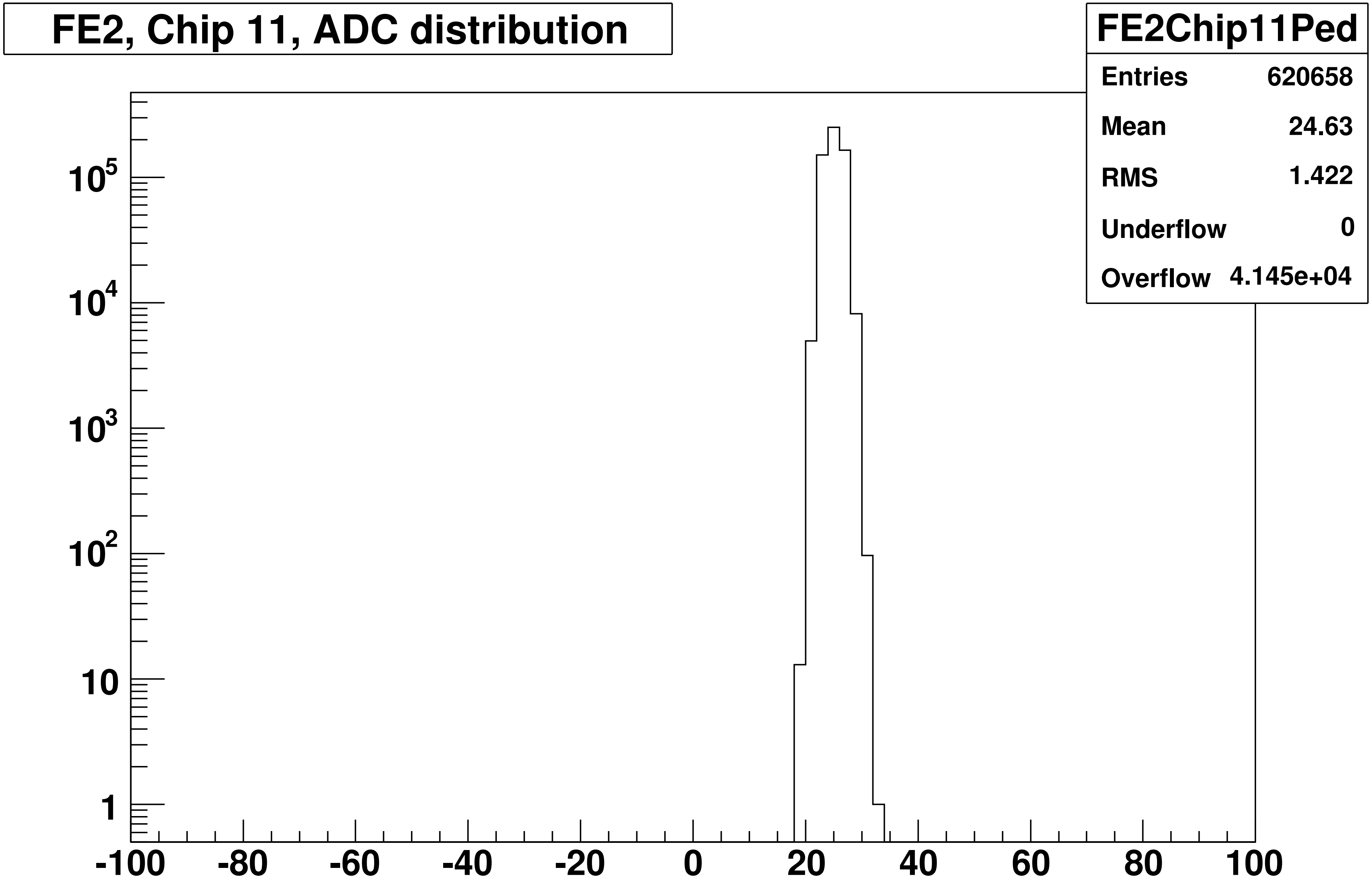
FE2, Chip 11, ADC distribution
FE2Chip11Ped
Entries 620658
Mean 24.63
RMS 1.422
Underflow 0
Overflow 4.145e+04
10^5
10^4
10^3
10^2
10
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-20
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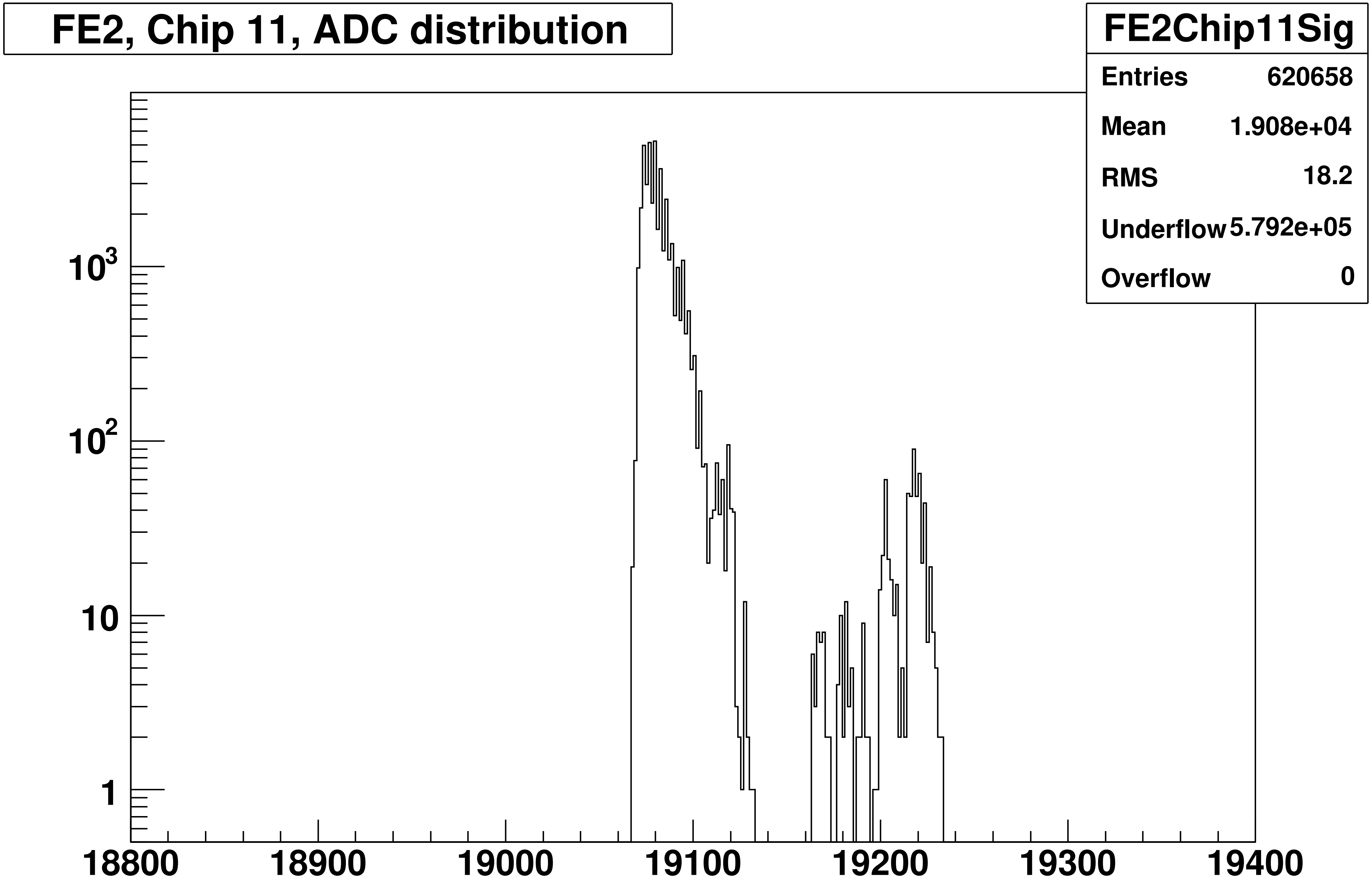
FE2, Chip 11, ADC distribution
FE2Chip11Sig
Entries 620658
Mean 1.908e+04
RMS 18.2
Underflow 5.792e+05
Overflow 0
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10^2
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18900
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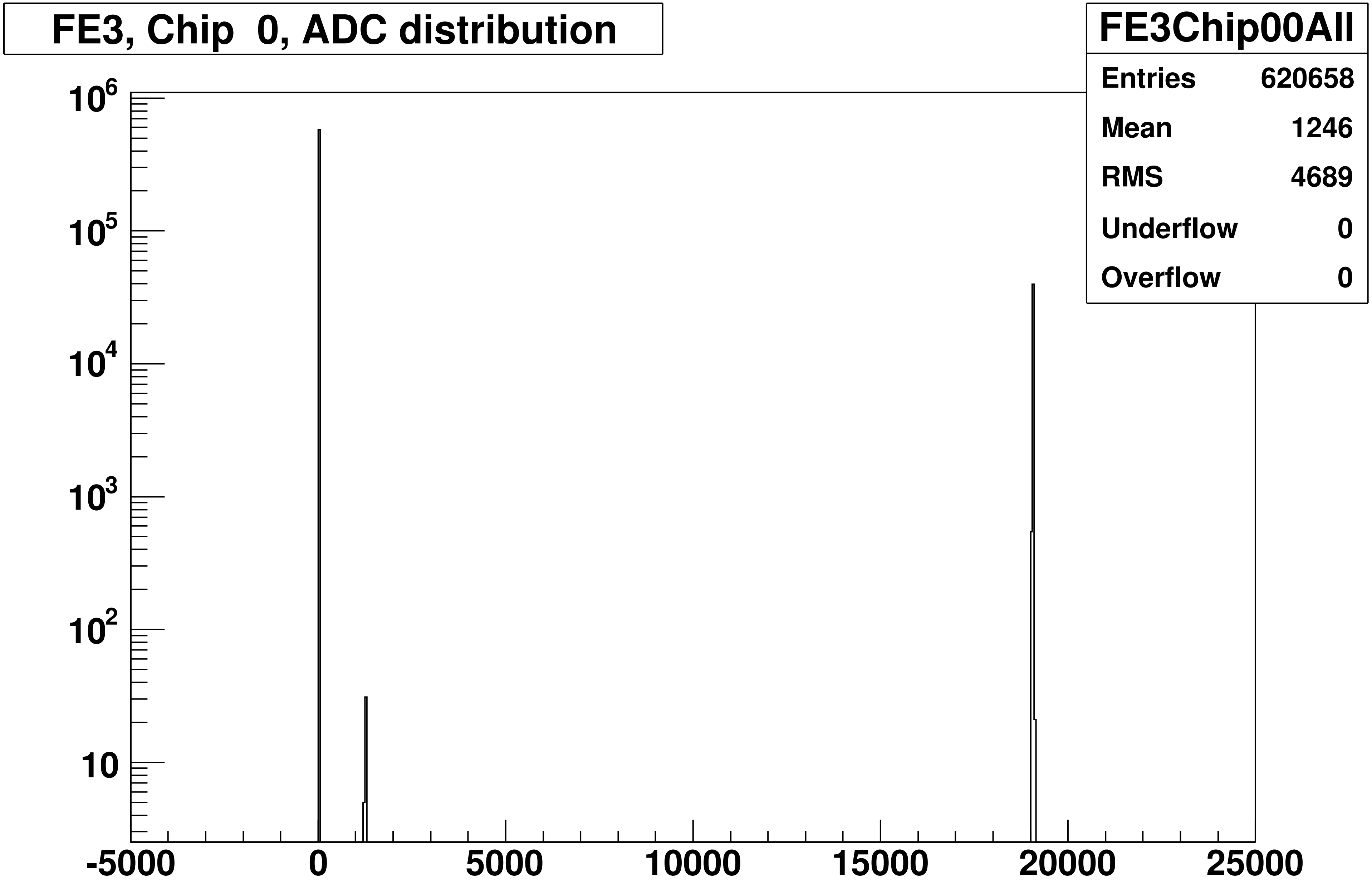
19400


FE3, Chip 0, ADC distribution
FE3Chip00All
Entries 620658
Mean 1246
RMS 4689
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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10000
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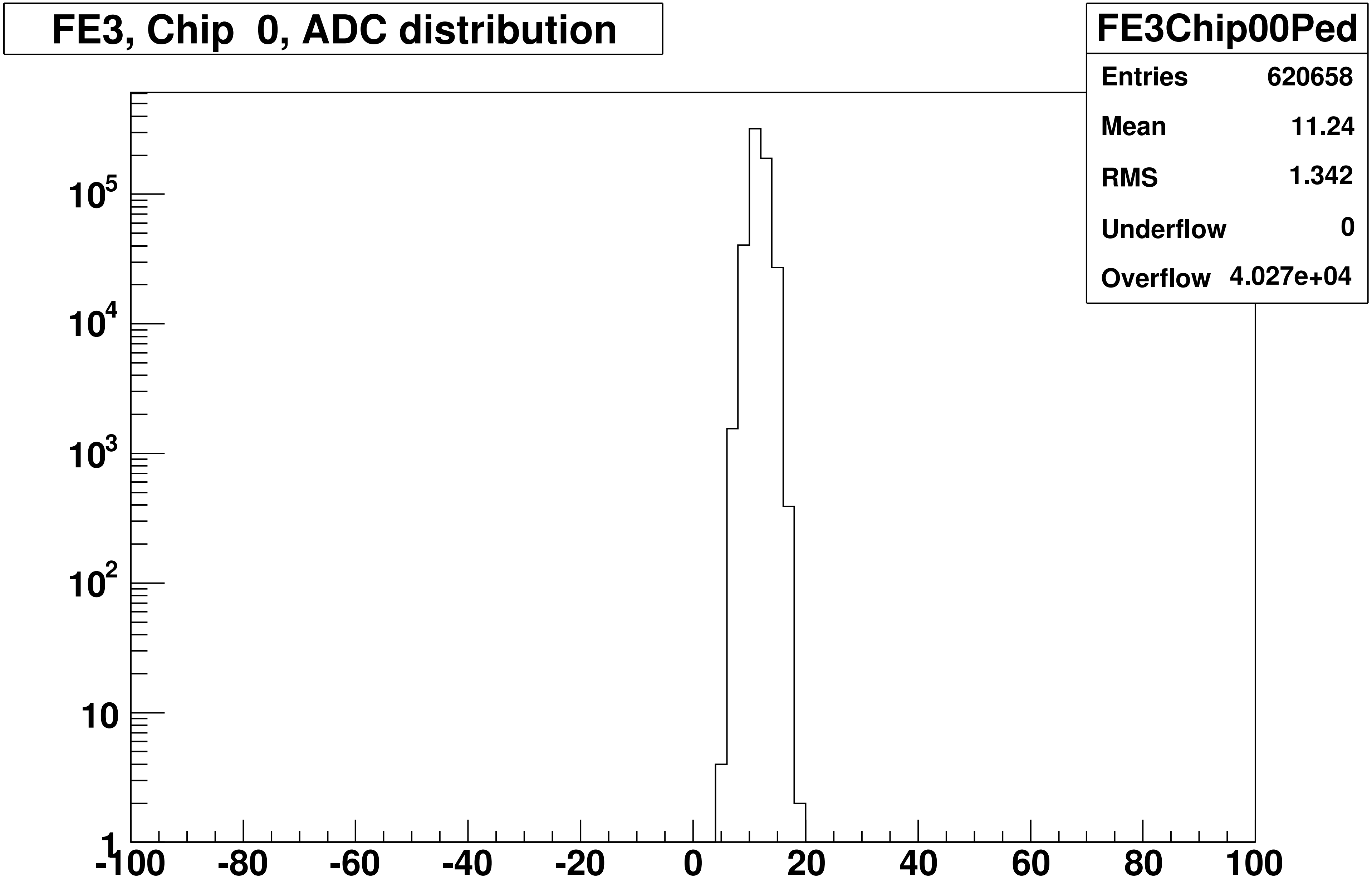

FE3, Chip 0, ADC distribution
FE3Chip00Ped
Entries 620658
Mean 11.24
RMS 1.342
Underflow 0
Overflow 4.027e+04
10^5
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10^3
10^2
10
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-20
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80
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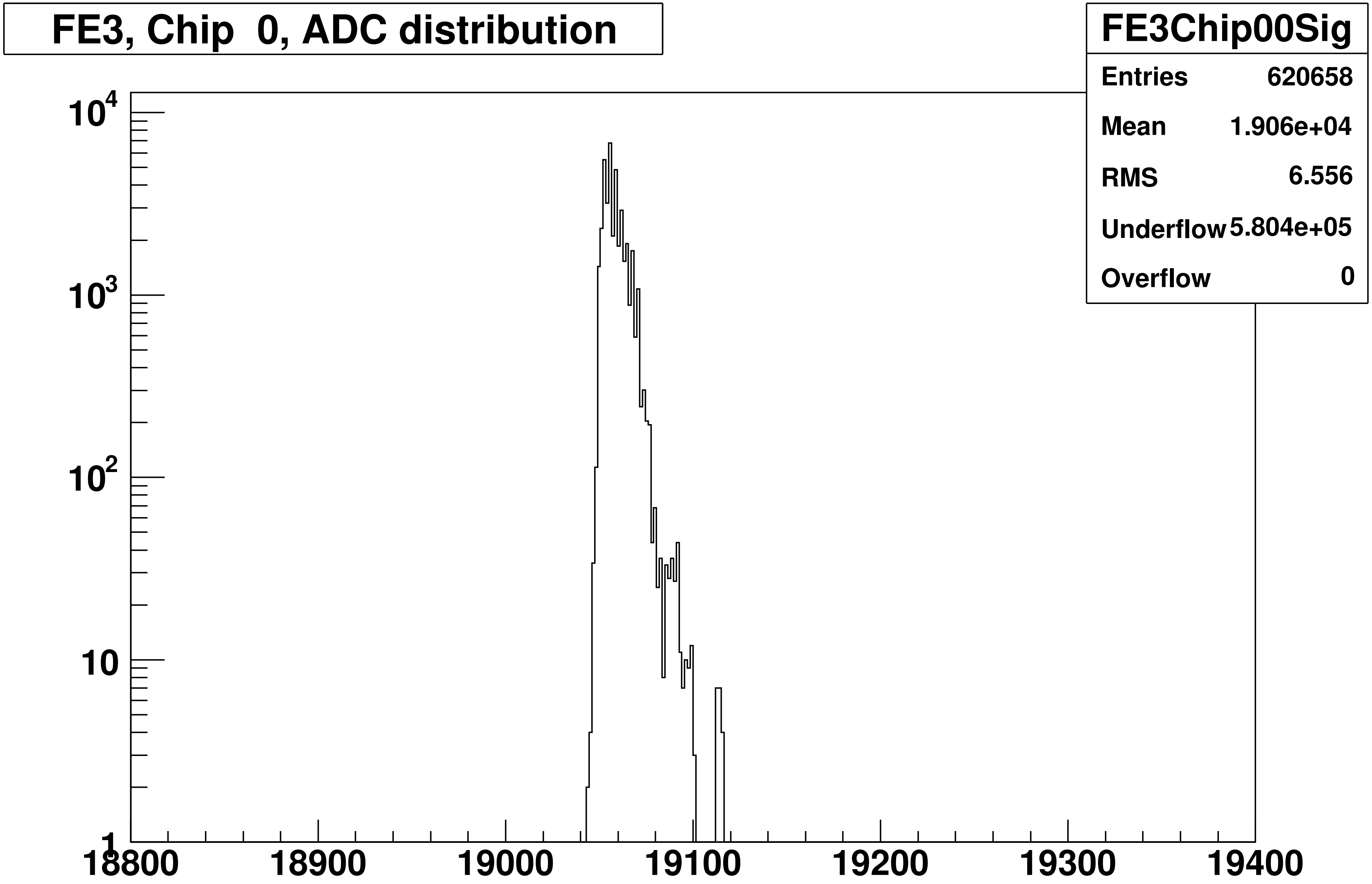
FE3, Chip 0, ADC distribution
FE3Chip00Sig
Entries 620658
Mean 1.906e+04
RMS 6.556
Underflow 5.804e+05
Overflow 0
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10^3
10^2
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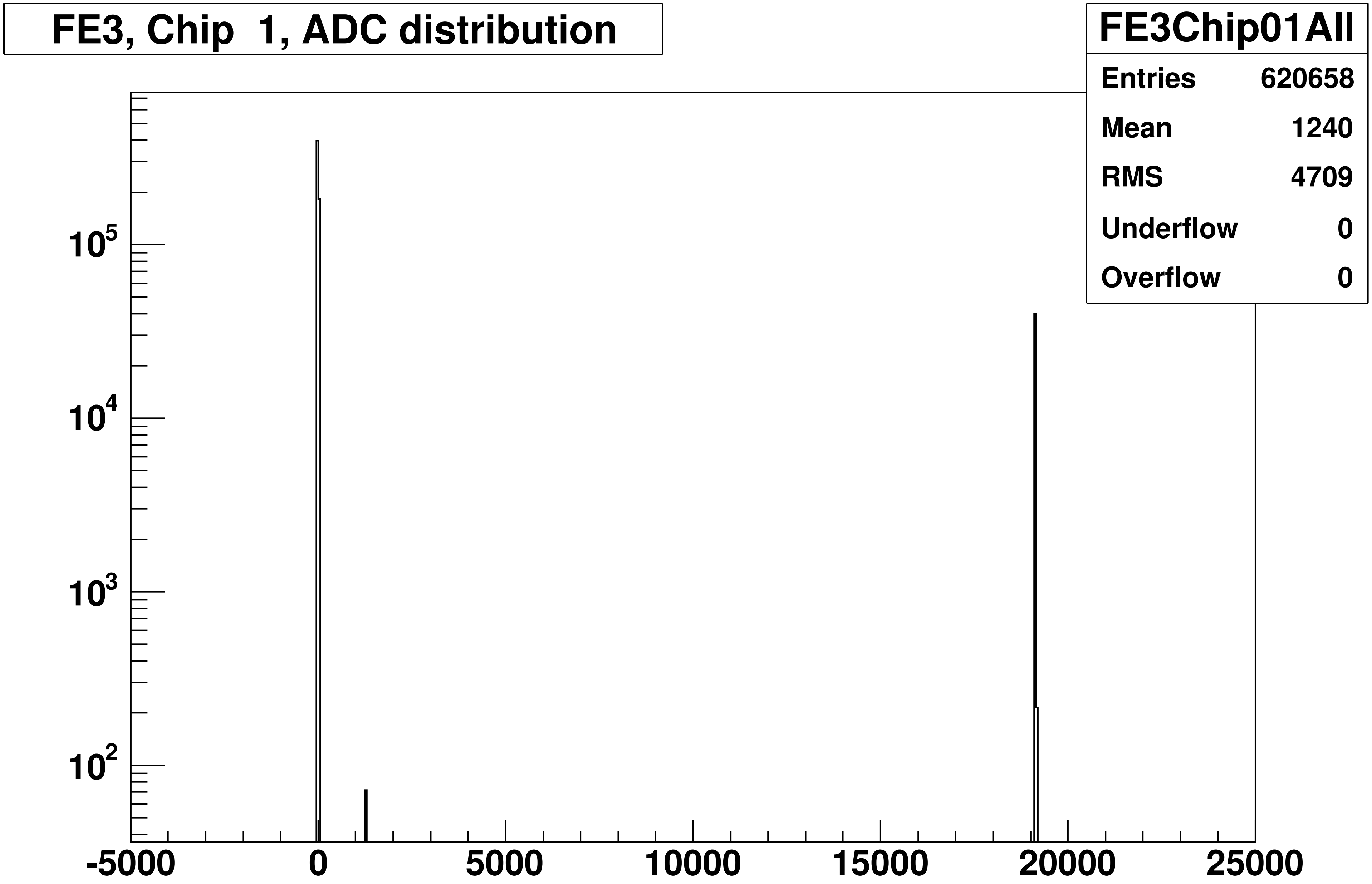

FE3, Chip 1, ADC distribution
FE3Chip01All
Entries 620658
Mean 1240
RMS 4709
Underflow 0
Overflow 0
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10^4
10^3
10^2
-5000
0
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10000
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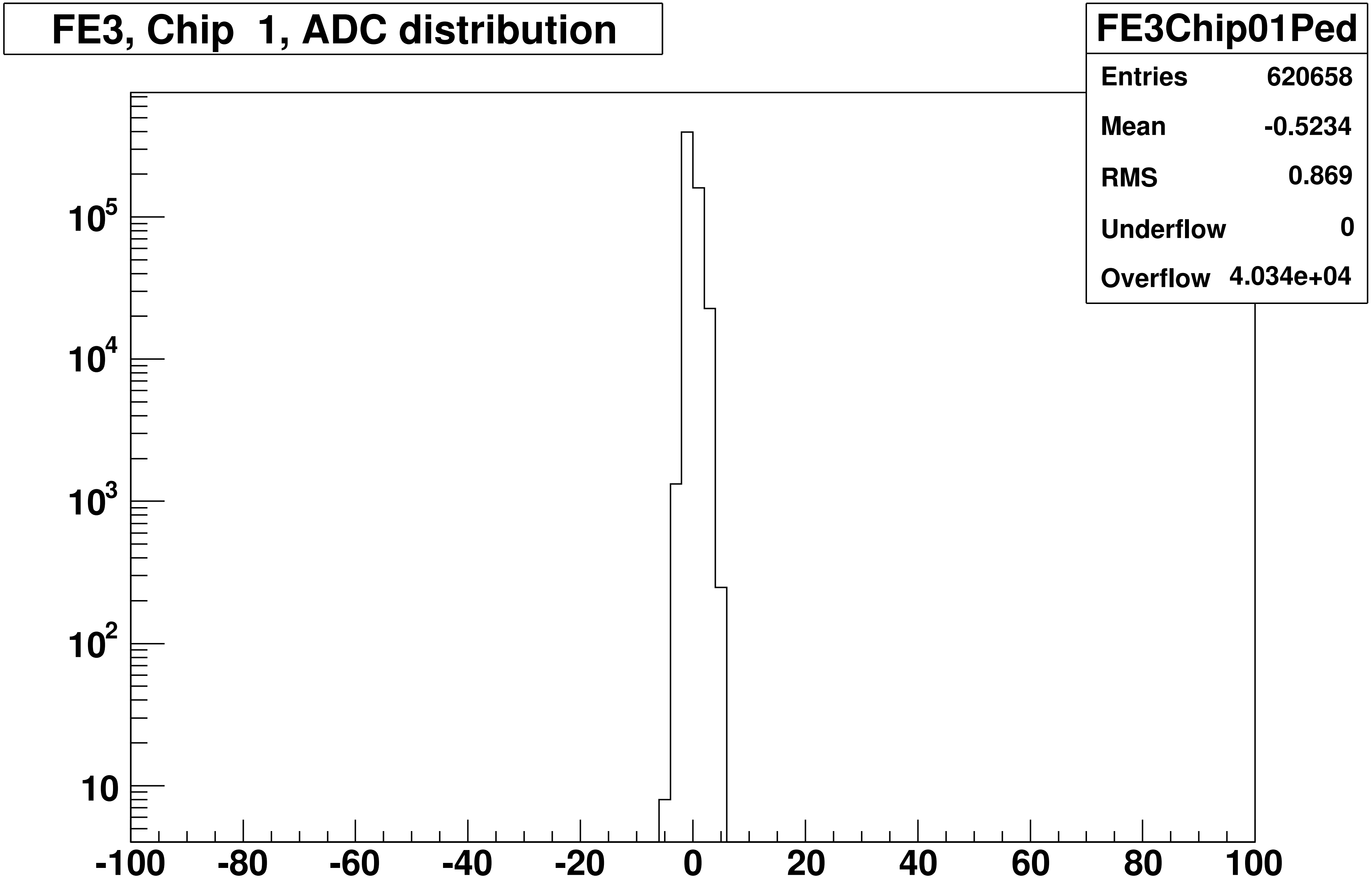
20000
25000


FE3, Chip 1, ADC distribution
FE3Chip01Ped
Entries 620658
Mean -0.5234
RMS 0.869
Underflow 0
Overflow 4.034e+04
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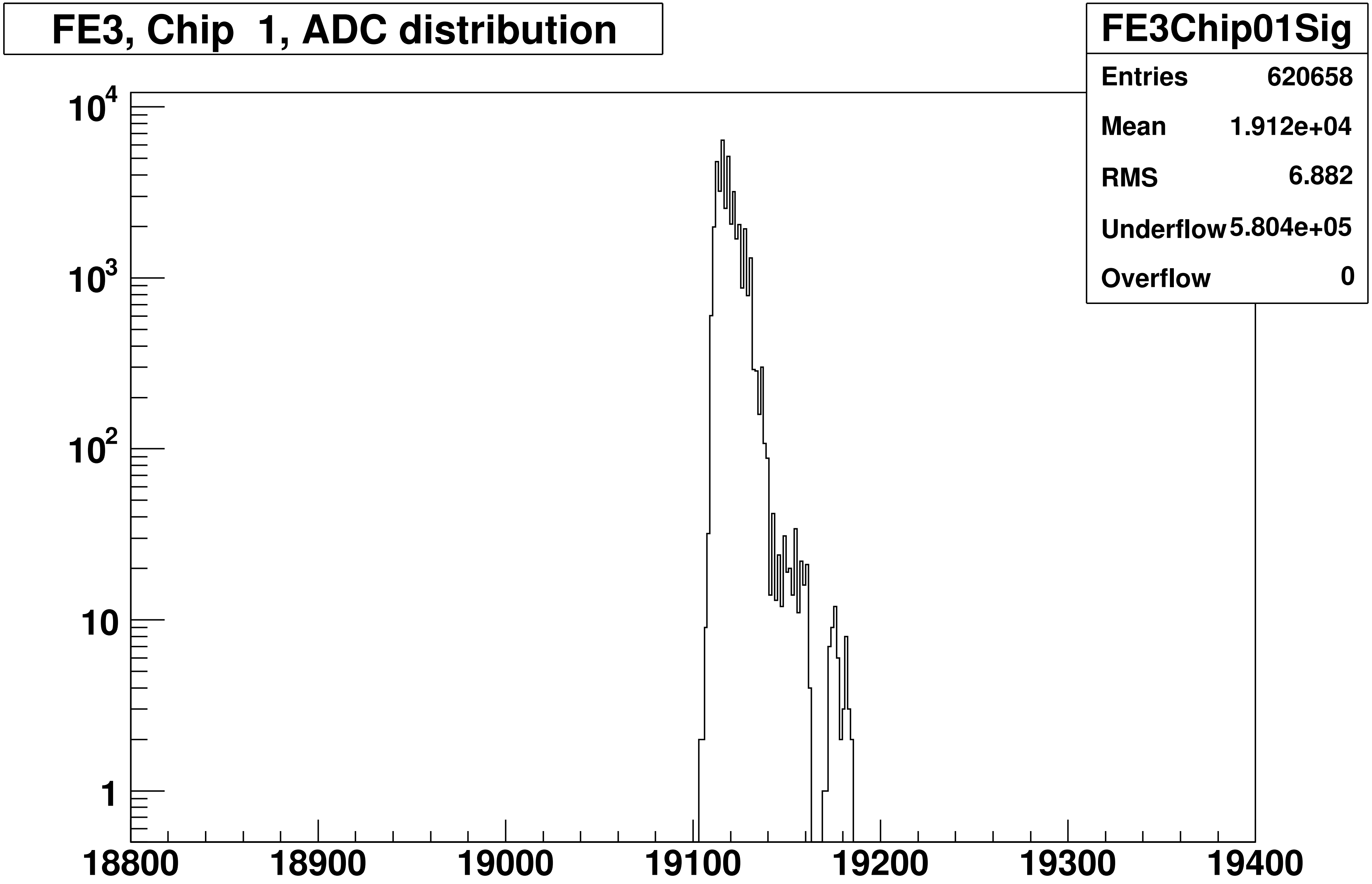
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 1, ADC distribution
FE3Chip01Sig
Entries 620658
Mean 1.912e+04
RMS 6.882
Underflow 5.804e+05
Overflow 0
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10^3
10^2
10
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18900
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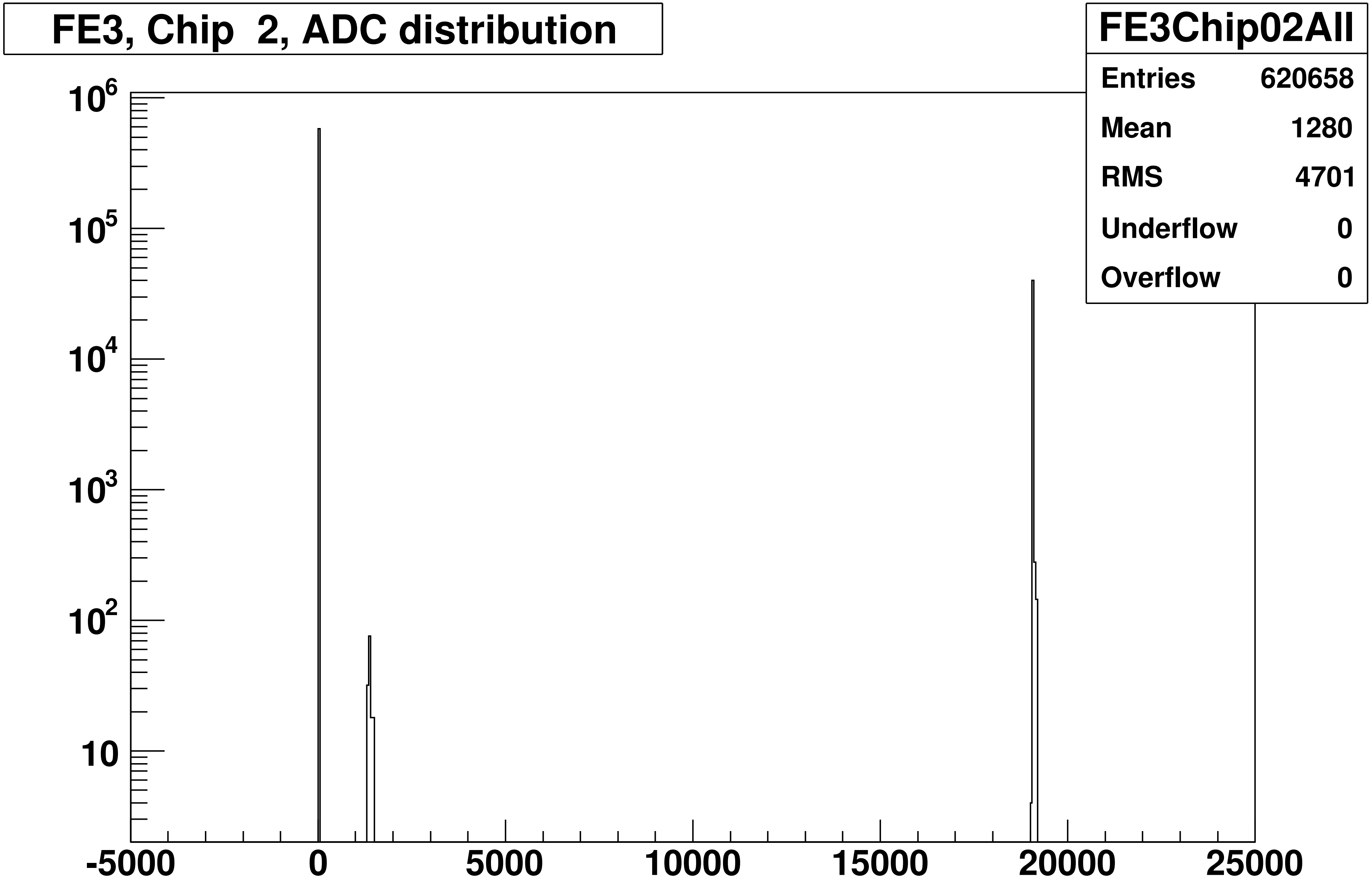
19400



FE3, Chip 2, ADC distribution
FE3Chip02All
Entries 620658
Mean 1280
RMS 4701
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
-5000
0
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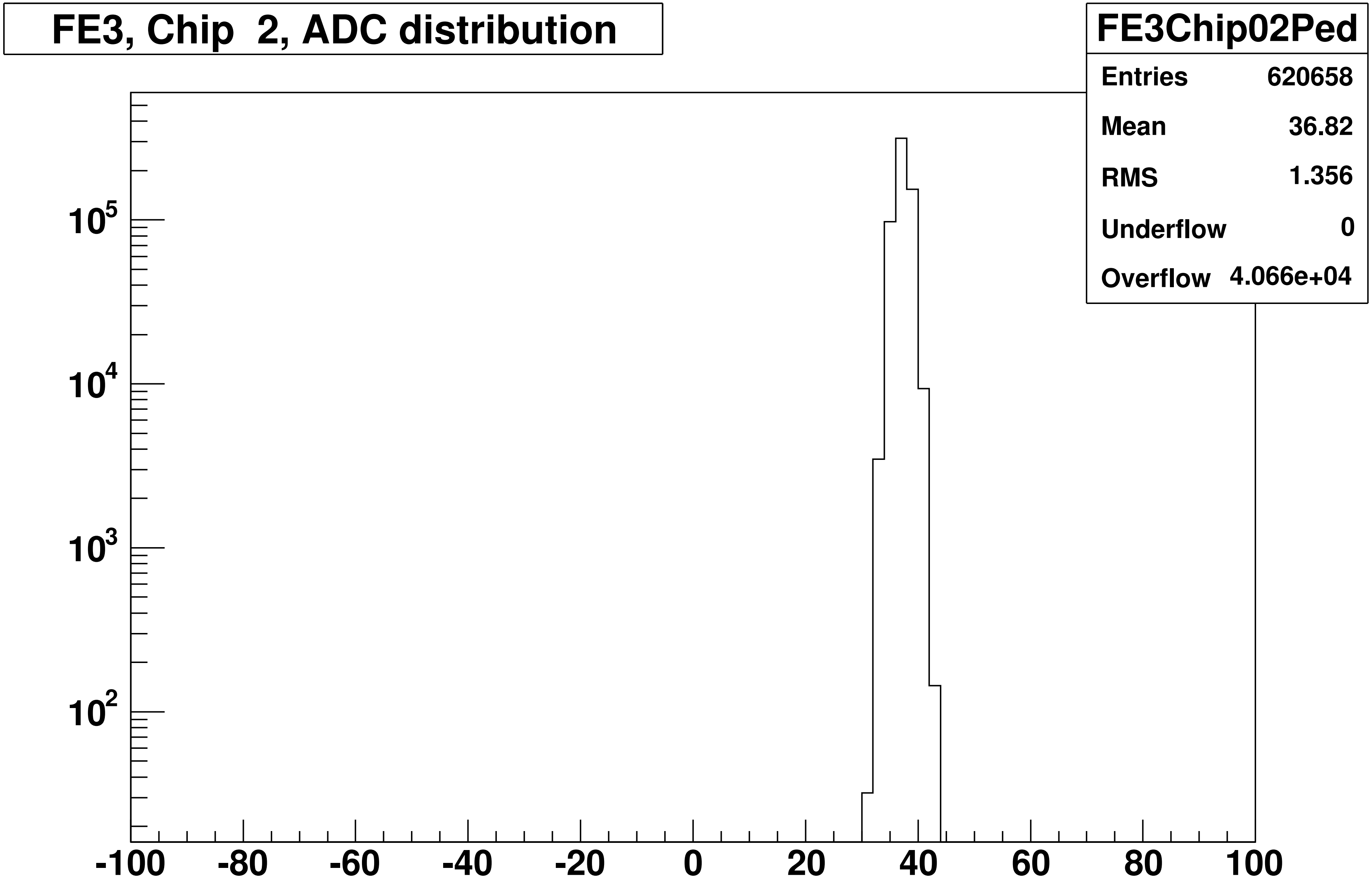
10000
15000
20000
25000


FE3, Chip 2, ADC distribution
FE3Chip02Ped
Entries 620658
Mean 36.82
RMS 1.356
Underflow 0
Overflow 4.066e+04
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10^4
10^3
10^2
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-80
-60
-40
-20
0
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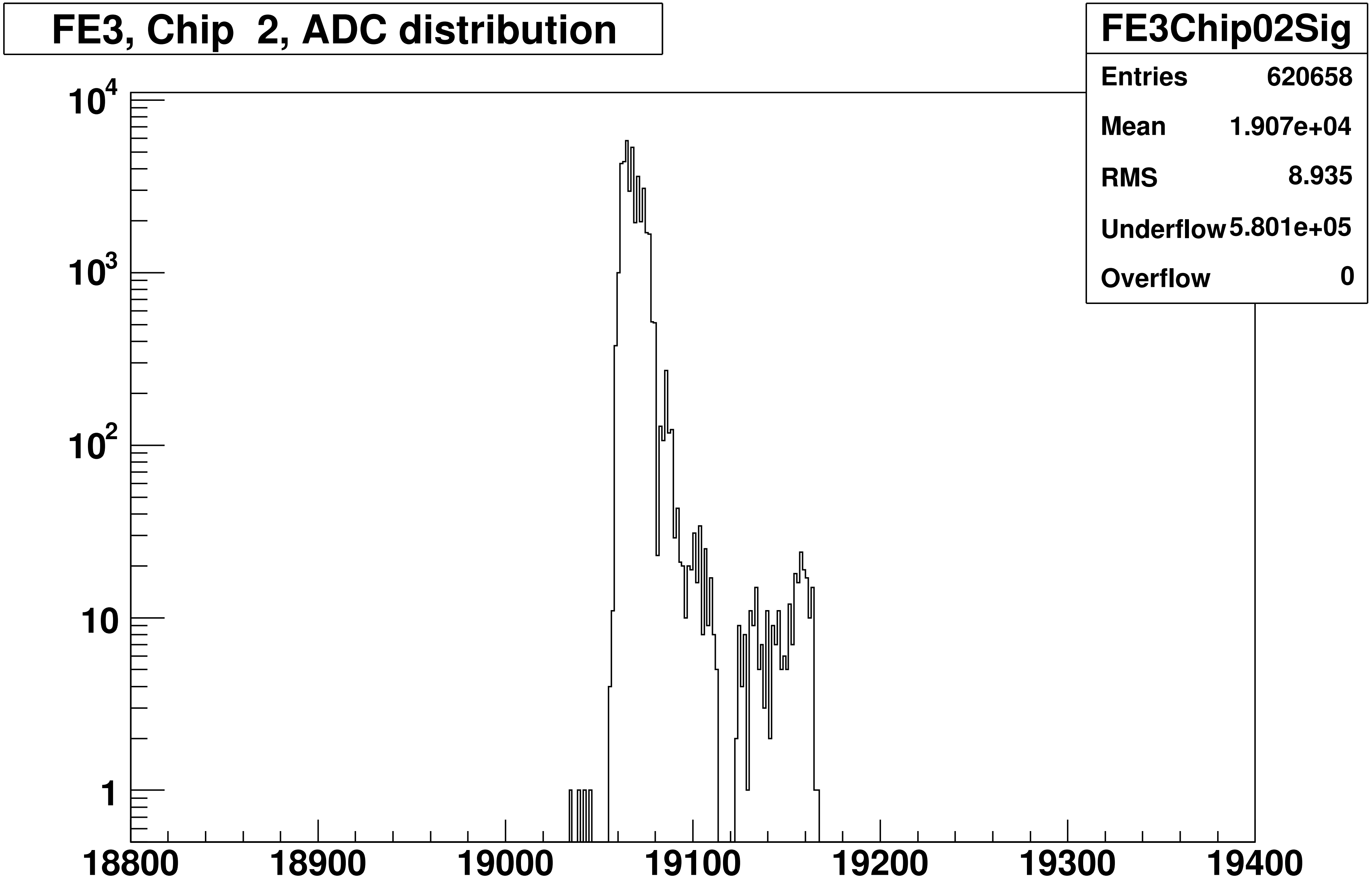
60
80
100


FE3, Chip 2, ADC distribution
FE3Chip02Sig
Entries 620658
Mean 1.907e+04
RMS 8.935
Underflow 5.801e+05
Overflow 0
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10^3
10^2
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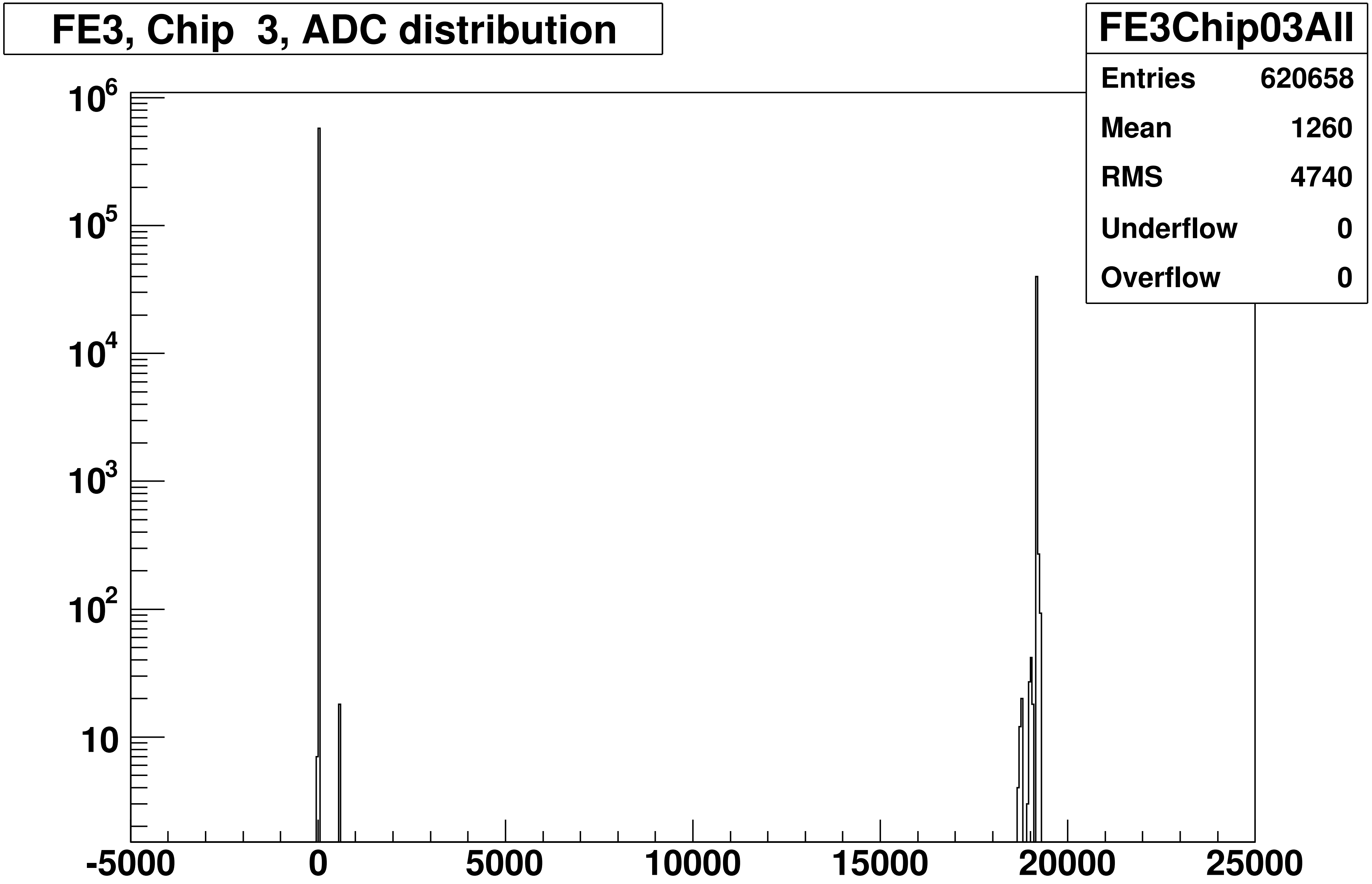
FE3, Chip 3, ADC distribution
FE3Chip03All
Entries 620658
Mean 1260
RMS 4740
Underflow 0
Overflow 0
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10^3
10^2
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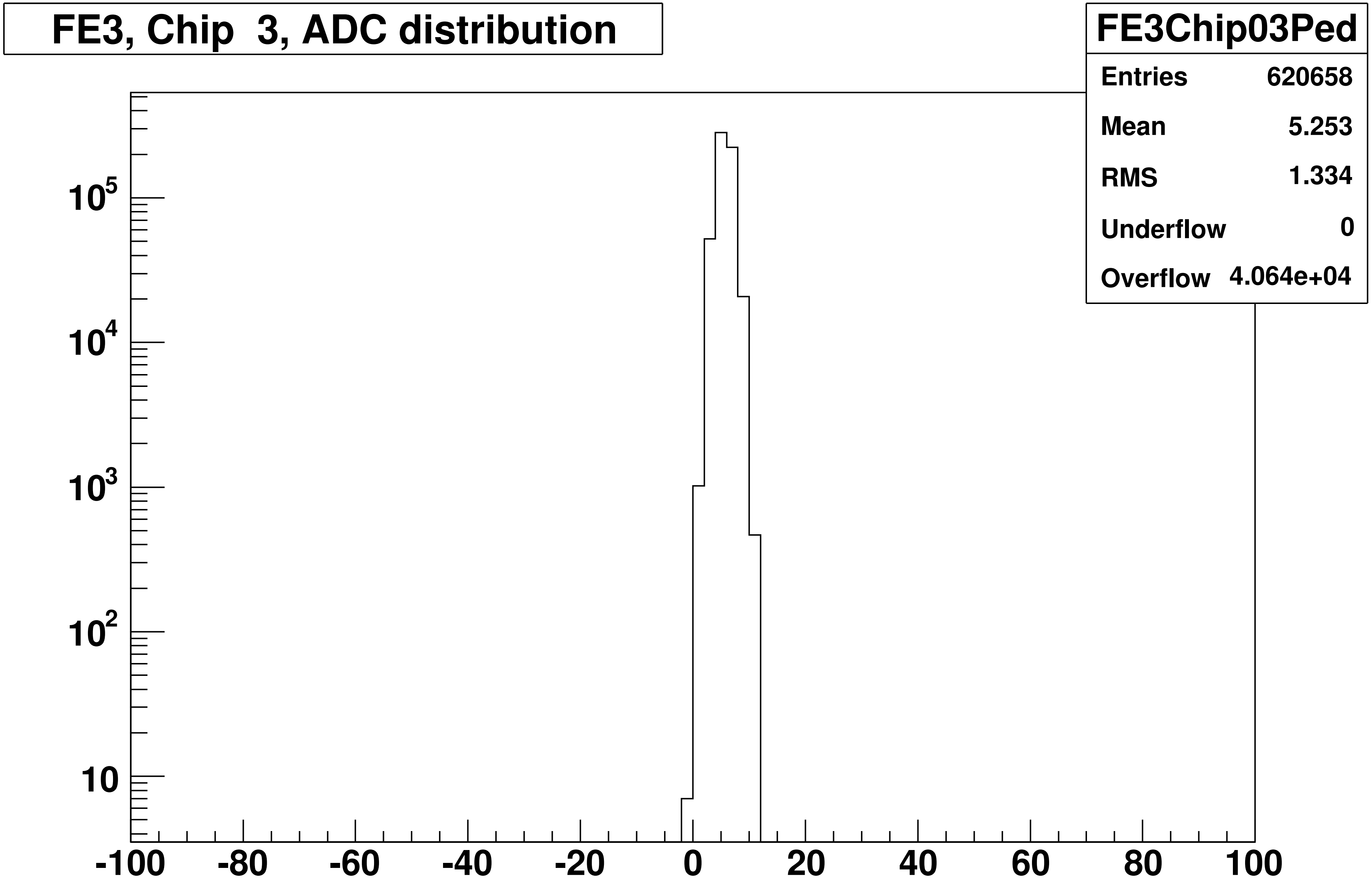
FE3, Chip 3, ADC distribution
FE3Chip03Ped
Entries 620658
Mean 5.253
RMS 1.334
Underflow 0
Overflow 4.064e+04
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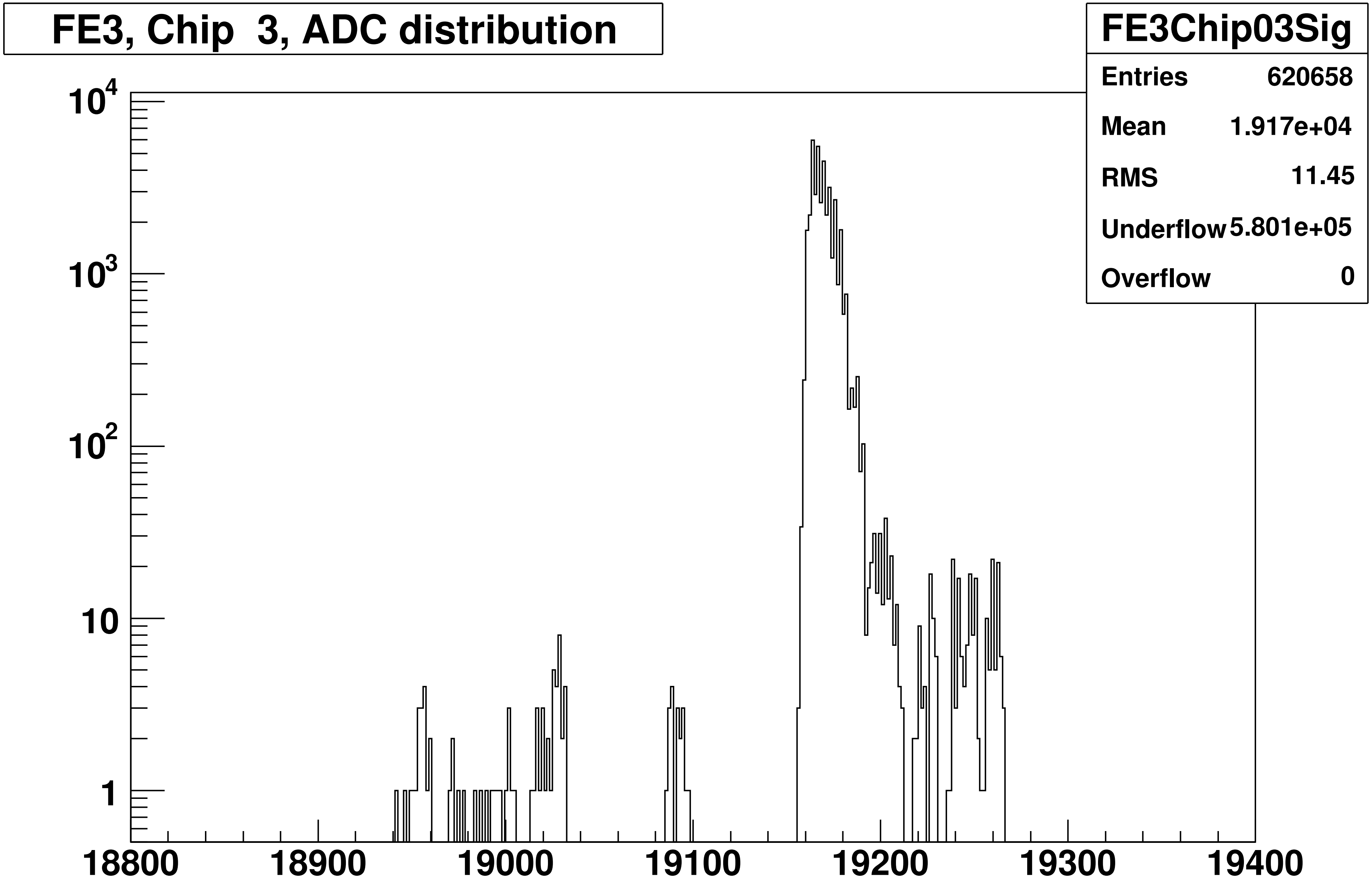
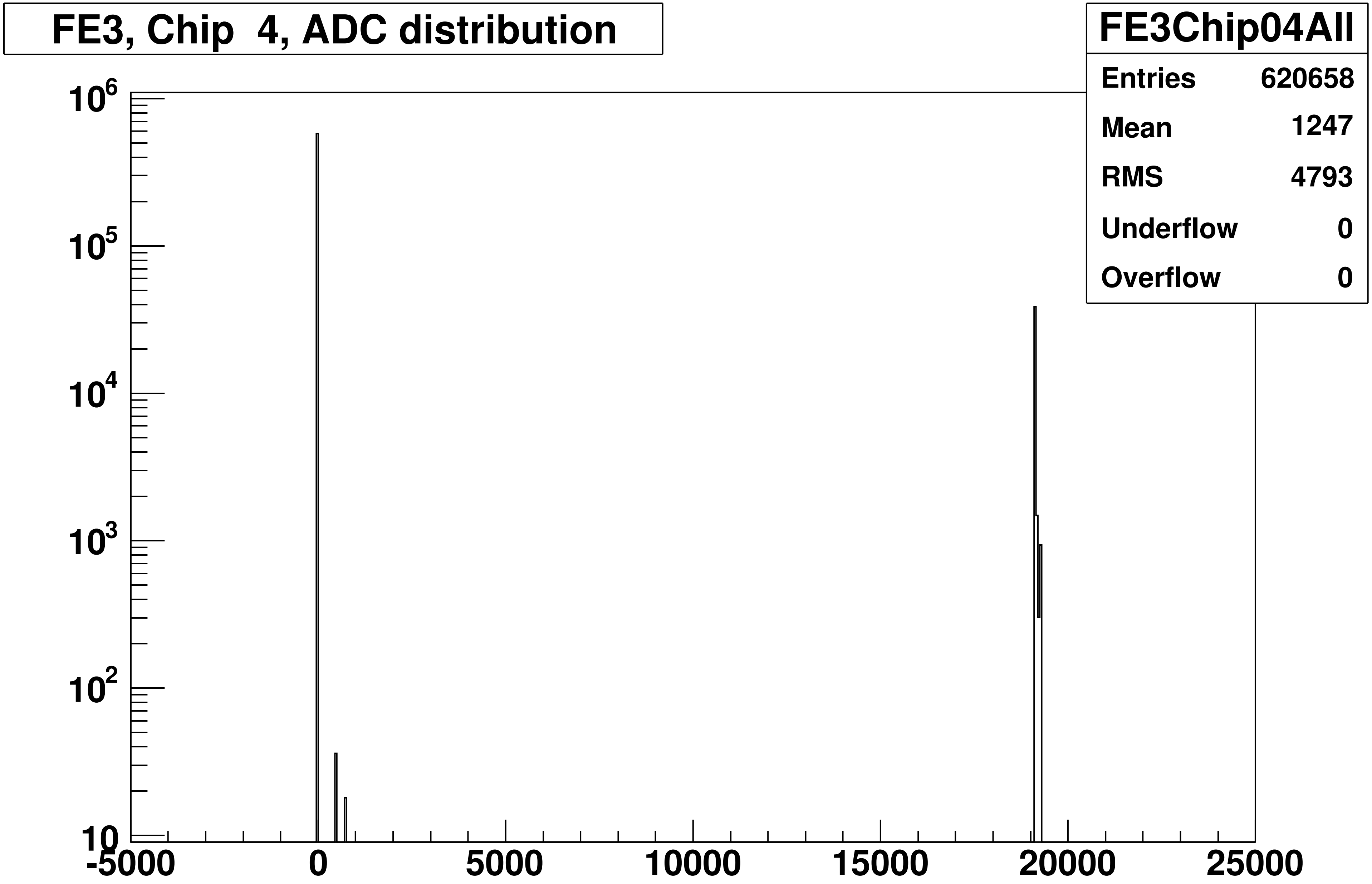
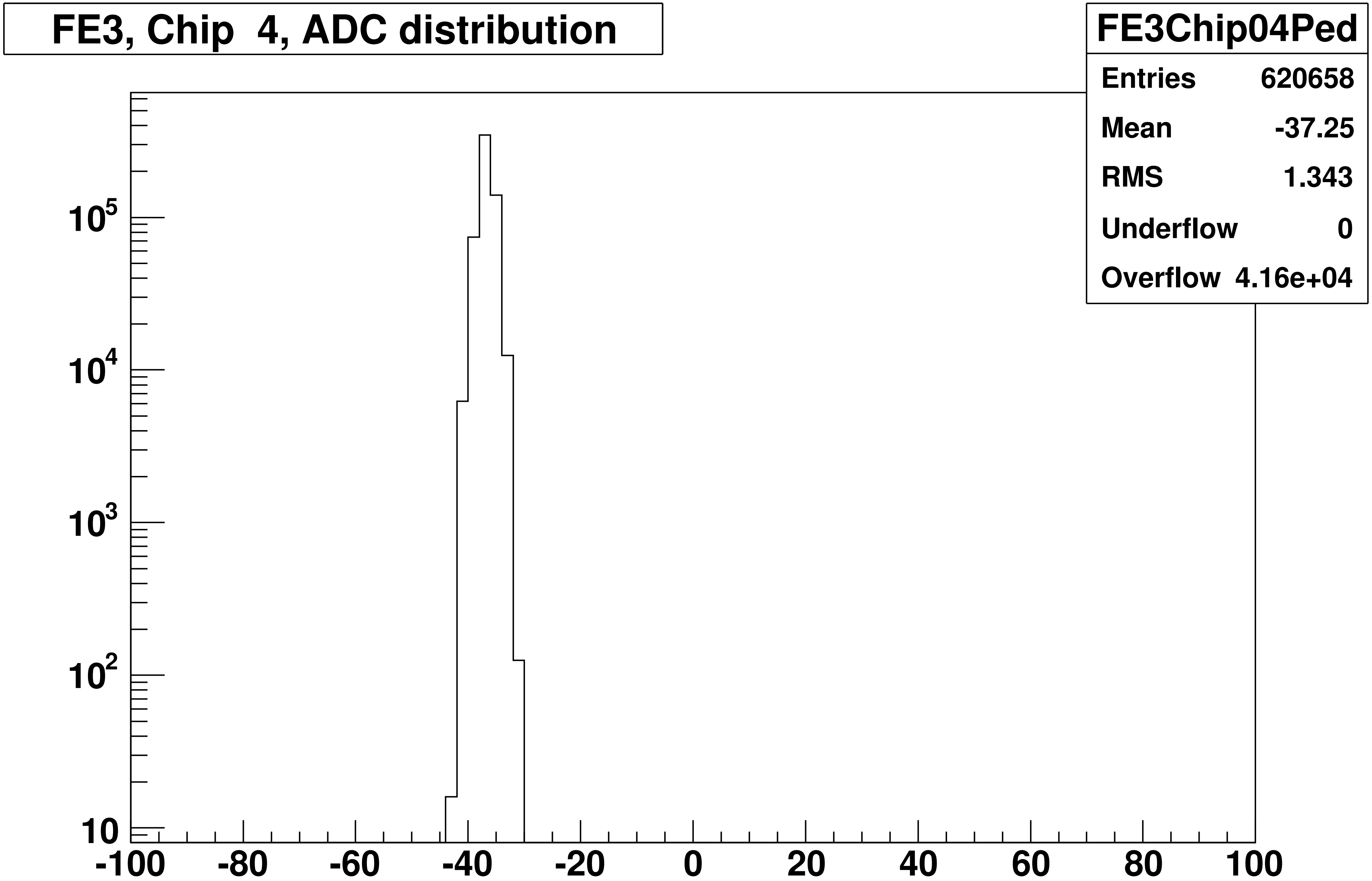
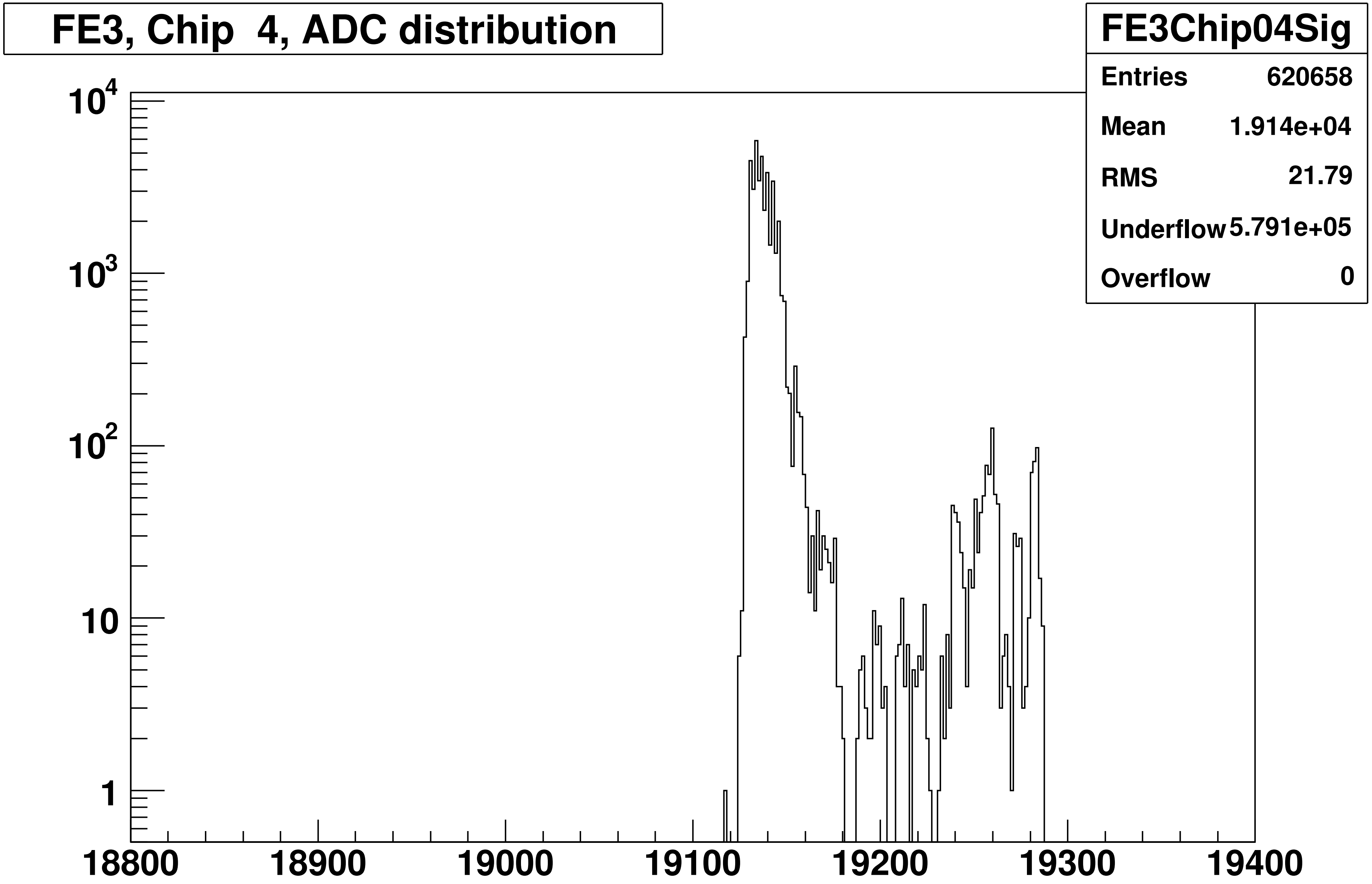


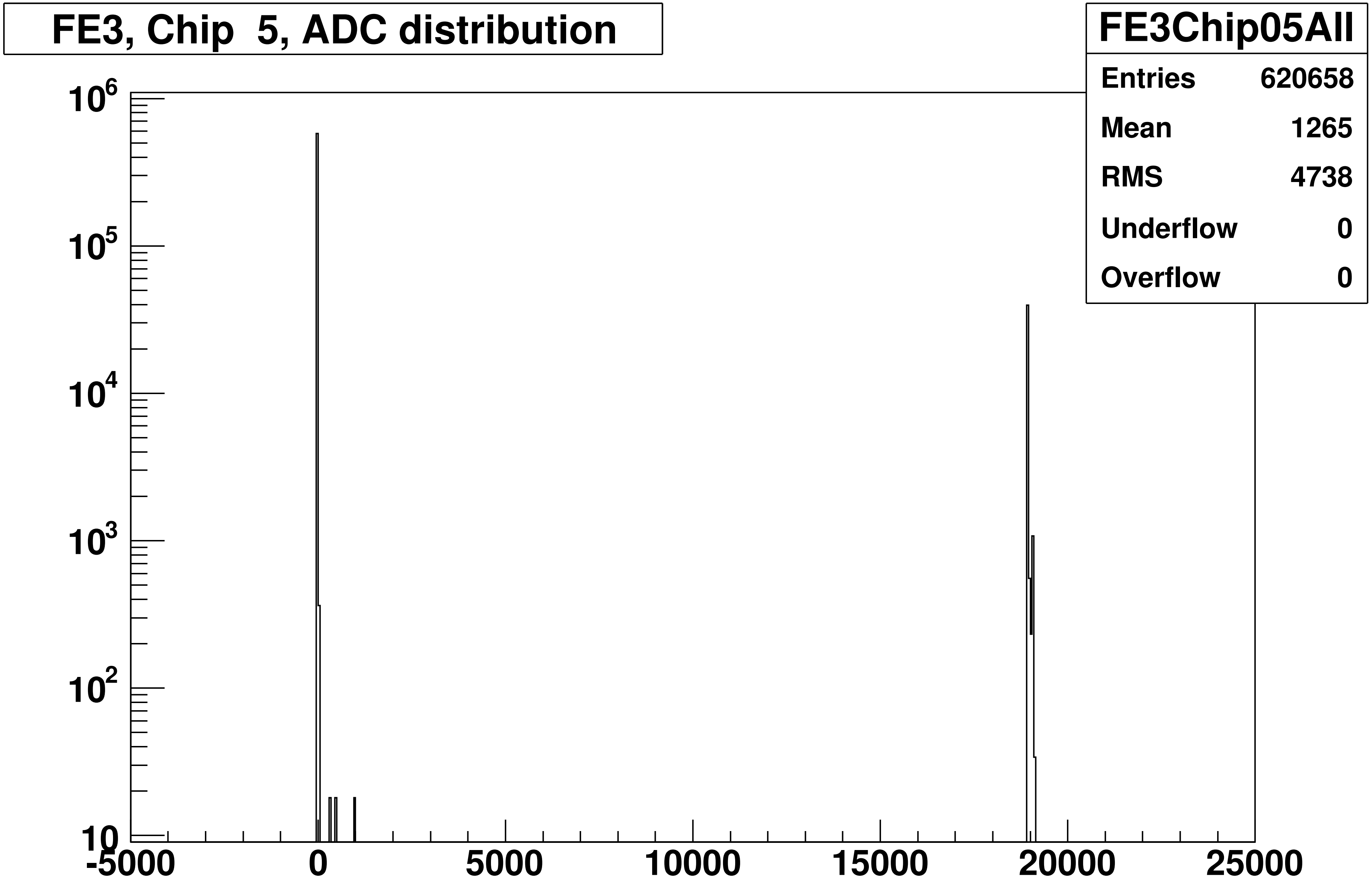
FE3, Chip 3, ADC distribution
FE3Chip03Sig
Entries 620658
Mean 1.917e+04
RMS 11.45
Underflow 5.801e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 4, ADC distribution
FE3Chip04All
Entries 620658
Mean 1247
RMS 4793
Underflow 0
Overflow 0
10
10^2
10^3
10^4
10^5
10^6
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 4, ADC distribution
FE3Chip04Ped
Entries 620658
Mean -37.25
RMS 1.343
Underflow 0
Overflow 4.16e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 4, ADC distribution
FE3Chip04Sig
Entries 620658
Mean 1.914e+04
RMS 21.79
Underflow 5.791e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 5, ADC distribution
FE3Chip05All
Entries 620658
Mean 1265
RMS 4738
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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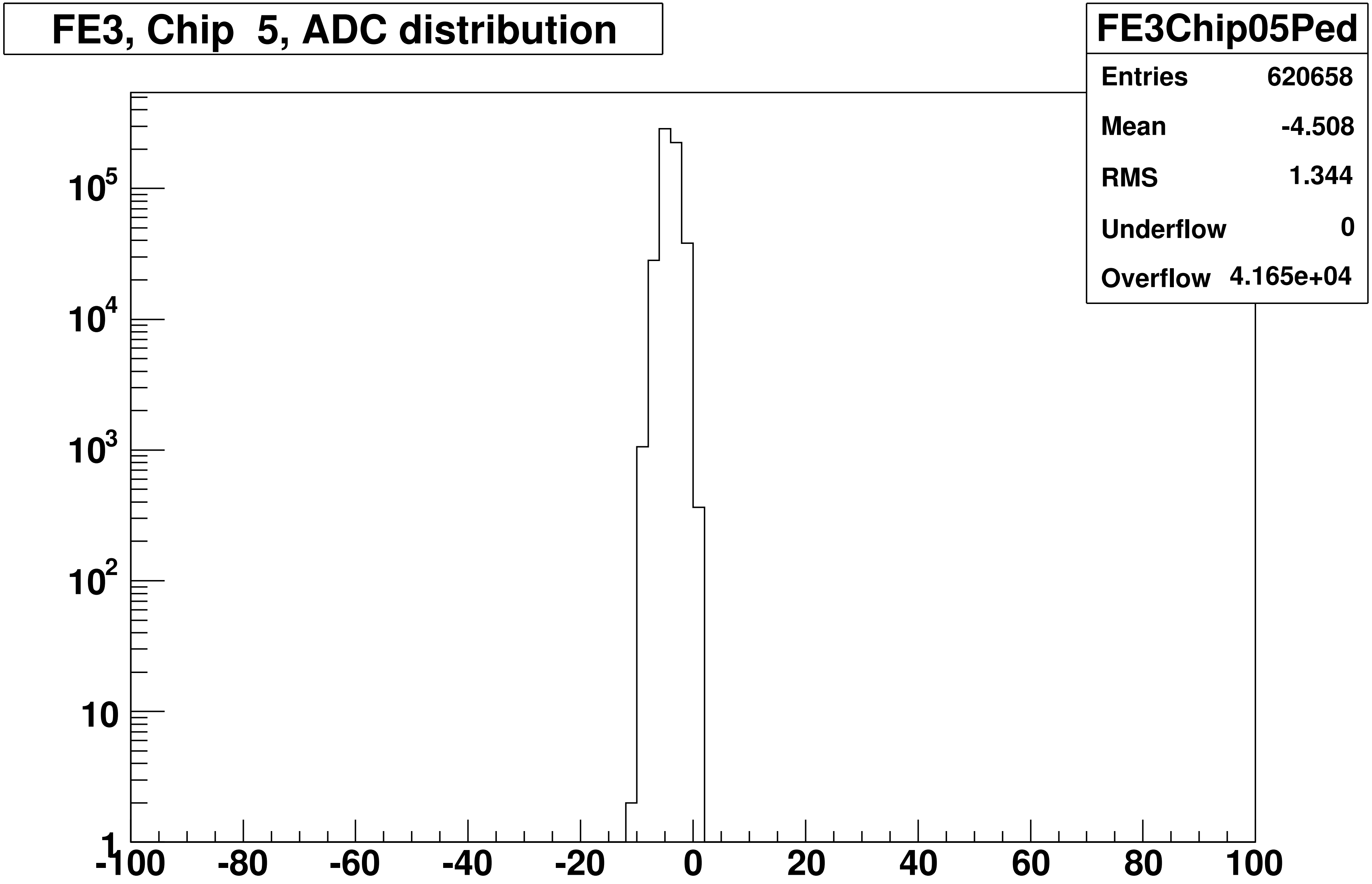
FE3, Chip 5, ADC distribution
FE3Chip05Ped
Entries 620658
Mean -4.508
RMS 1.344
Underflow 0
Overflow 4.165e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
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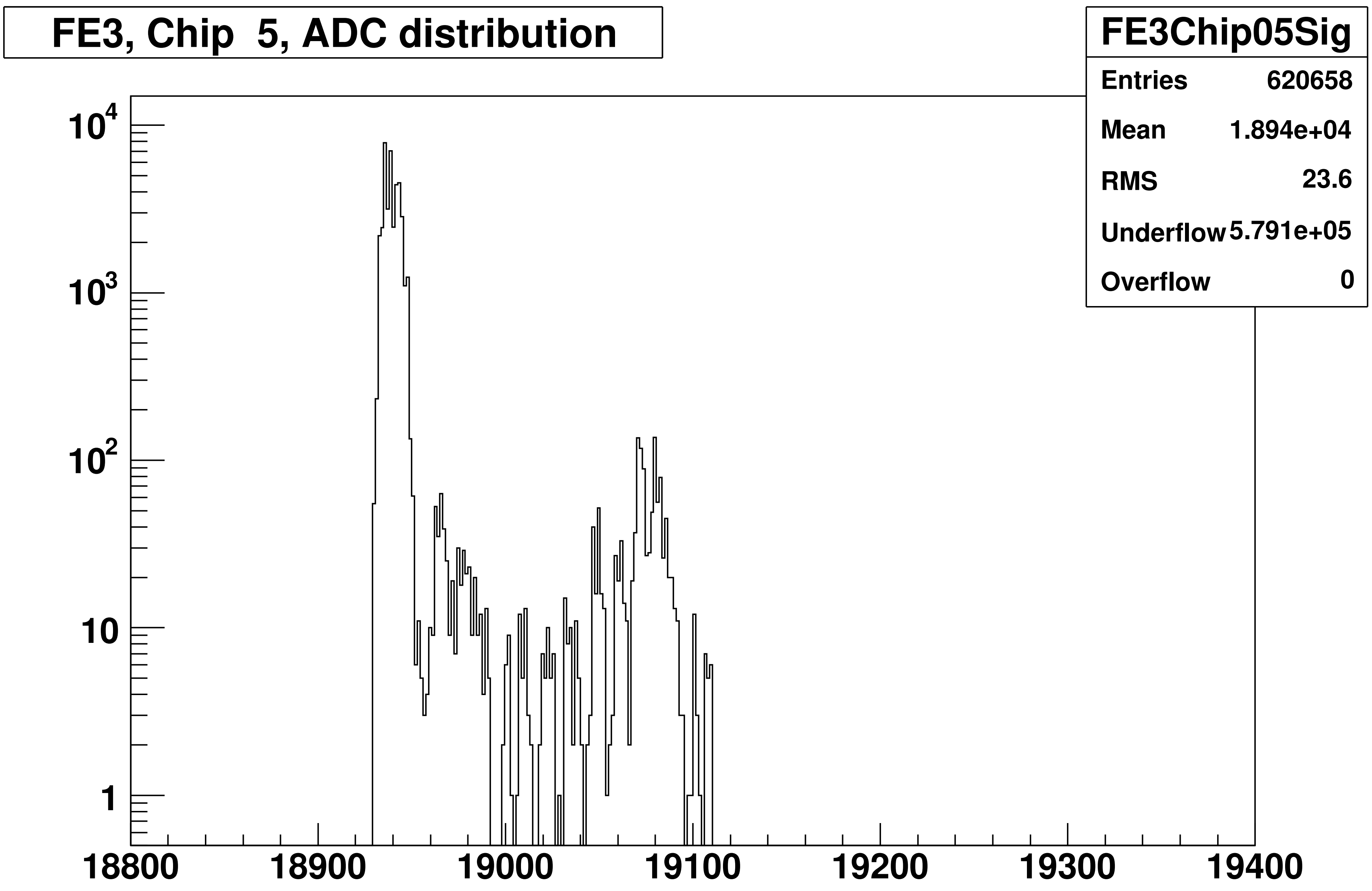
FE3, Chip 5, ADC distribution
FE3Chip05Sig
Entries 620658
Mean 1.894e+04
RMS 23.6
Underflow 5.791e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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19300
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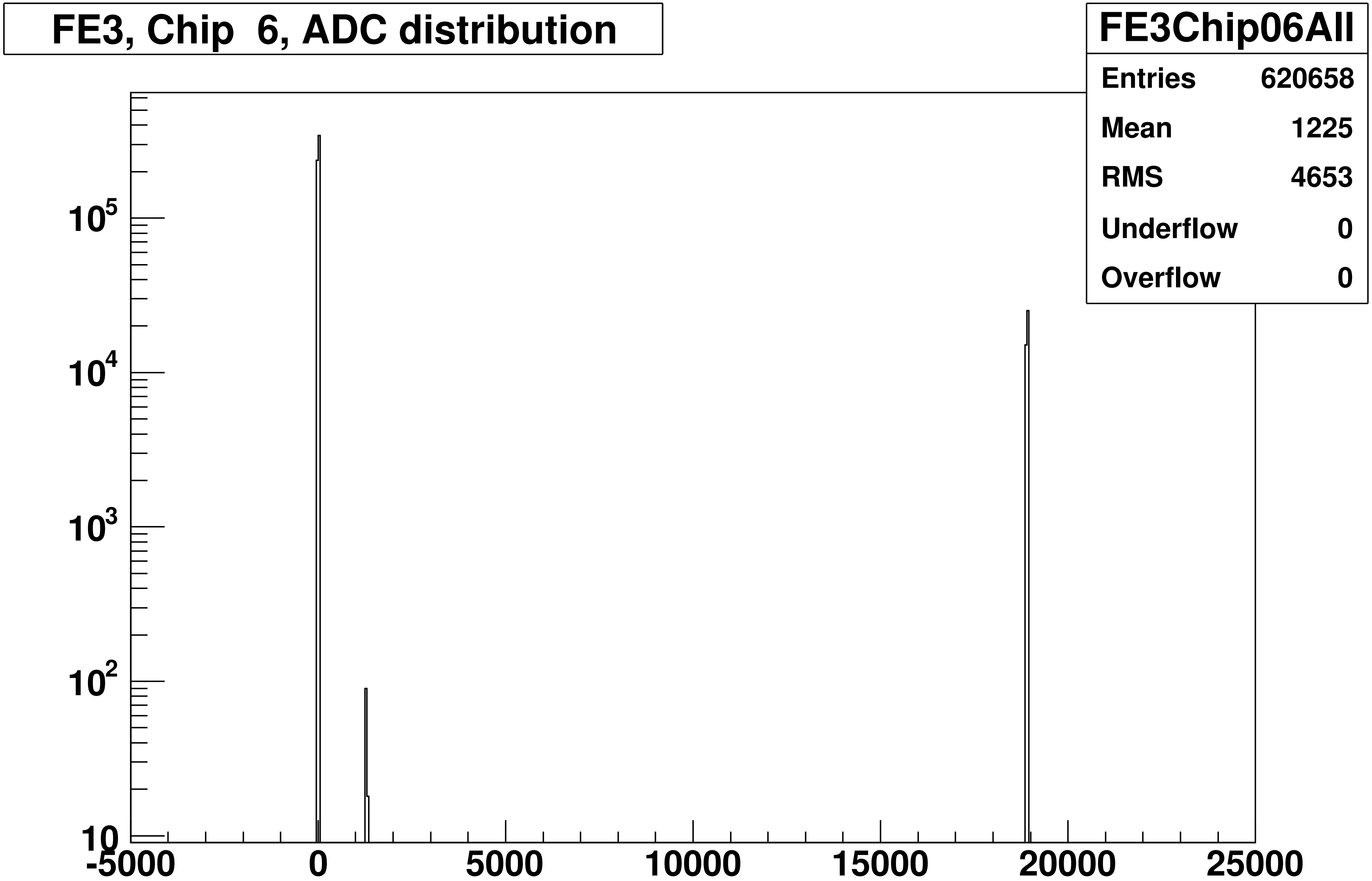
FE3, Chip 6, ADC distribution
FE3Chip06All
Entries 620658
Mean 1225
RMS 4653
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
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0
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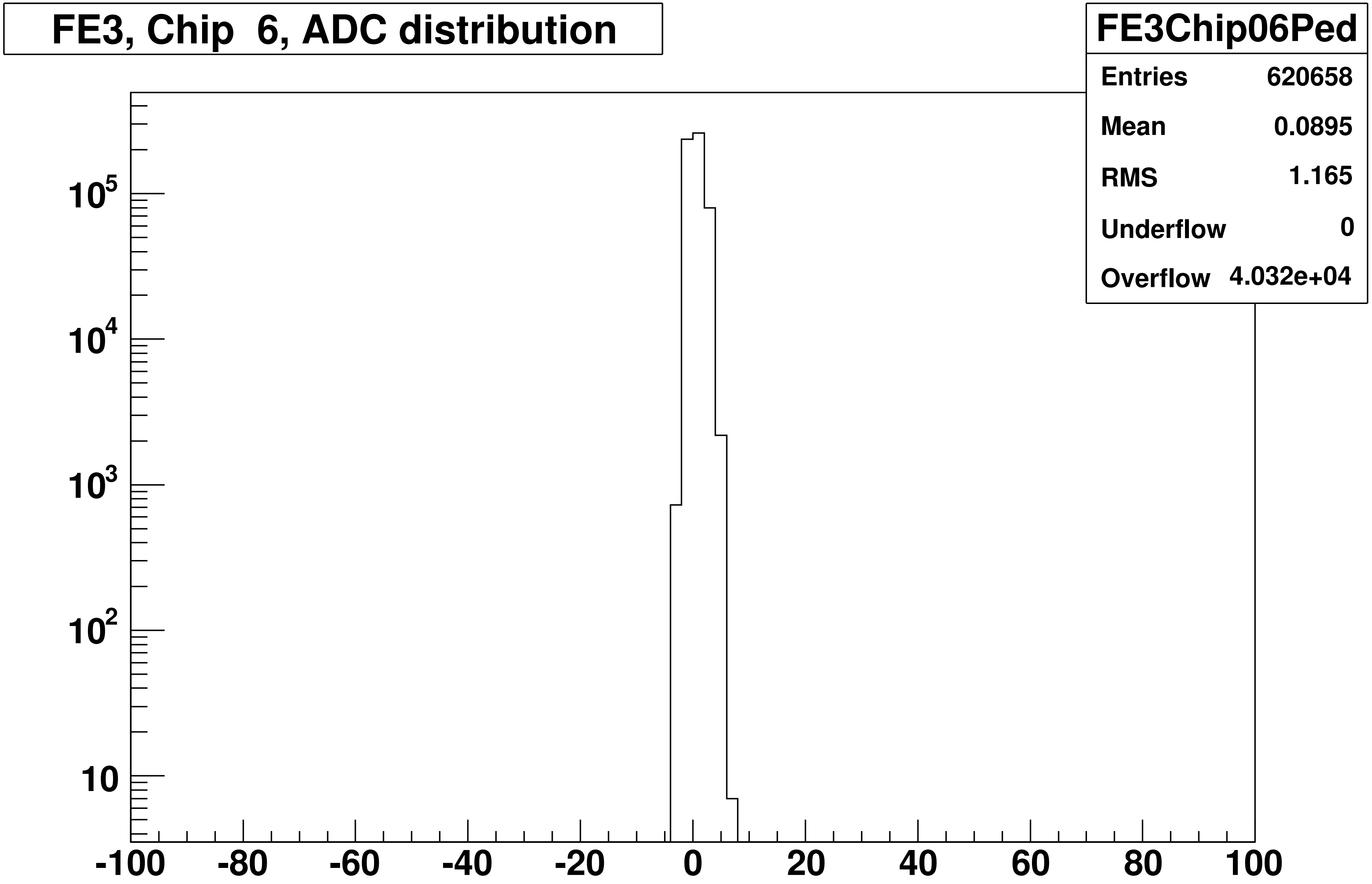
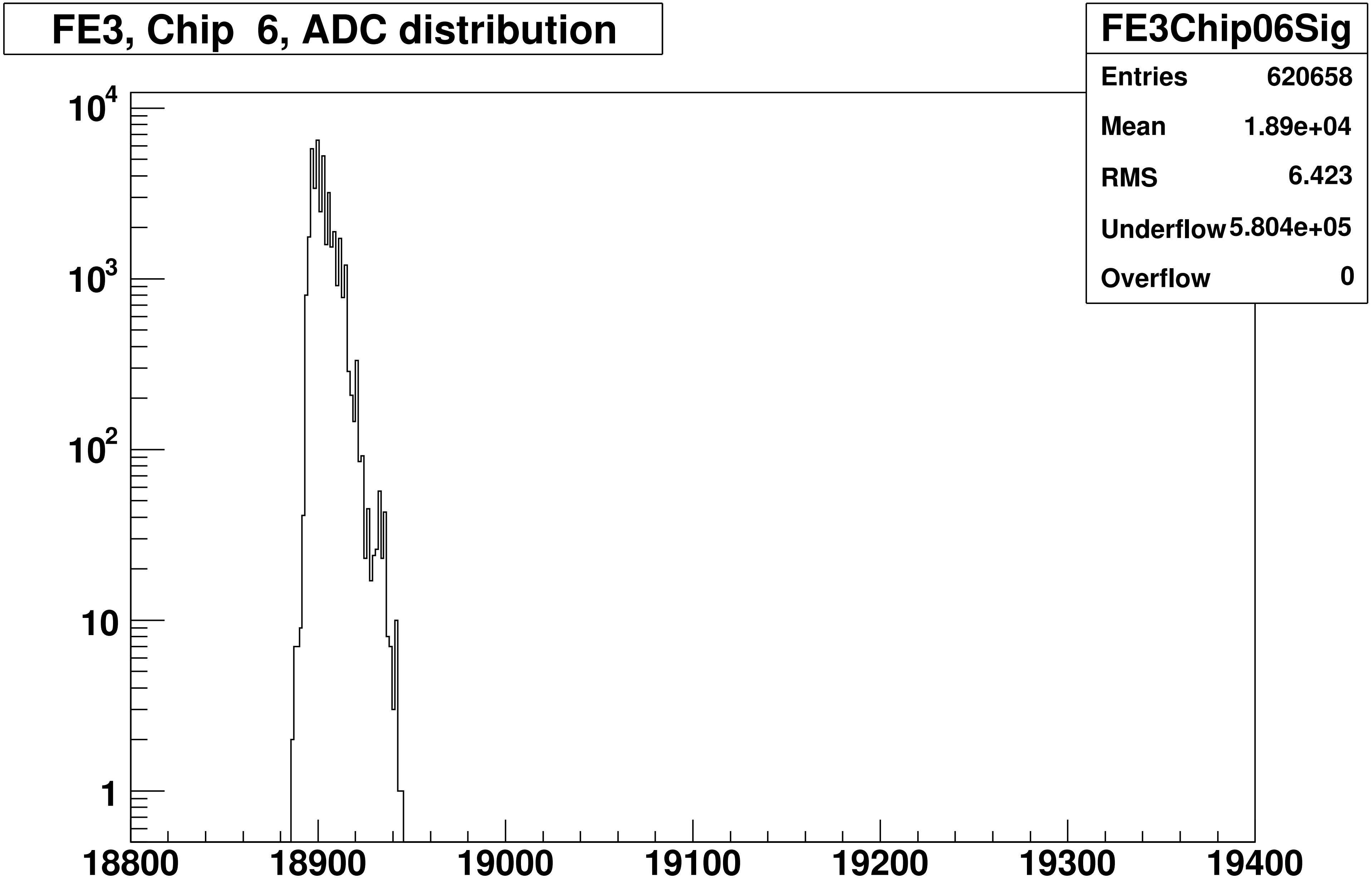
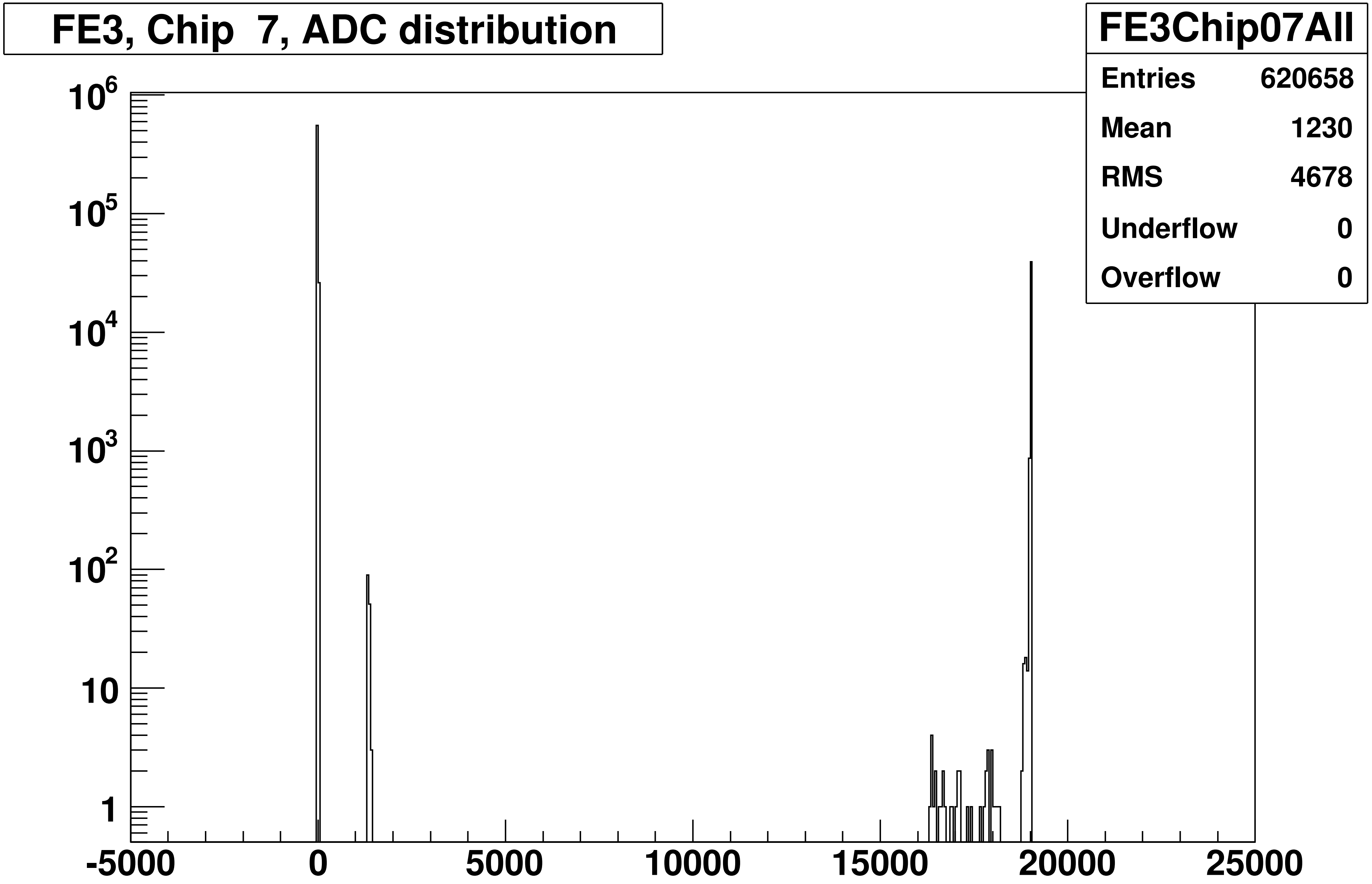
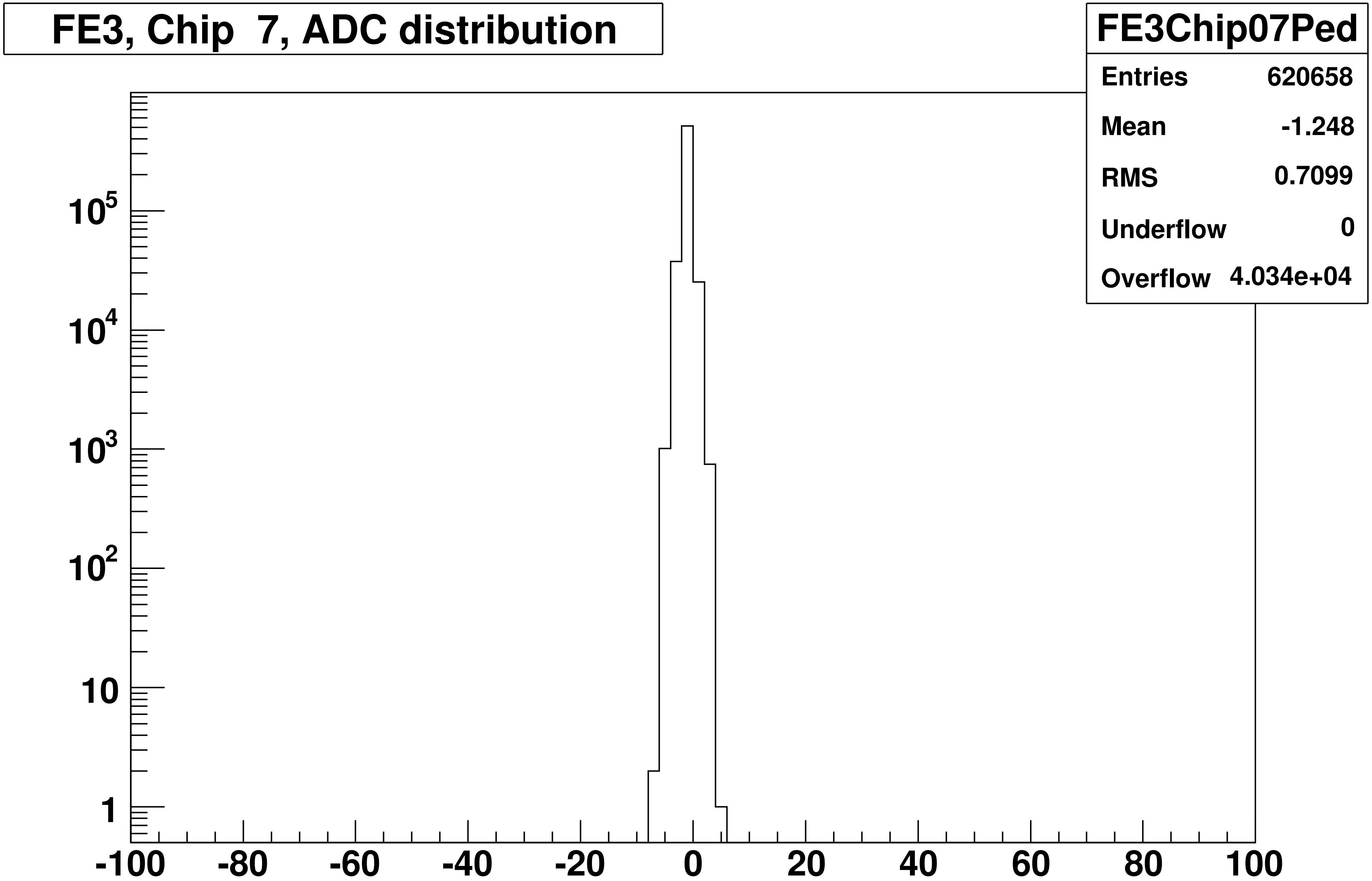
10000
15000
20000
25000

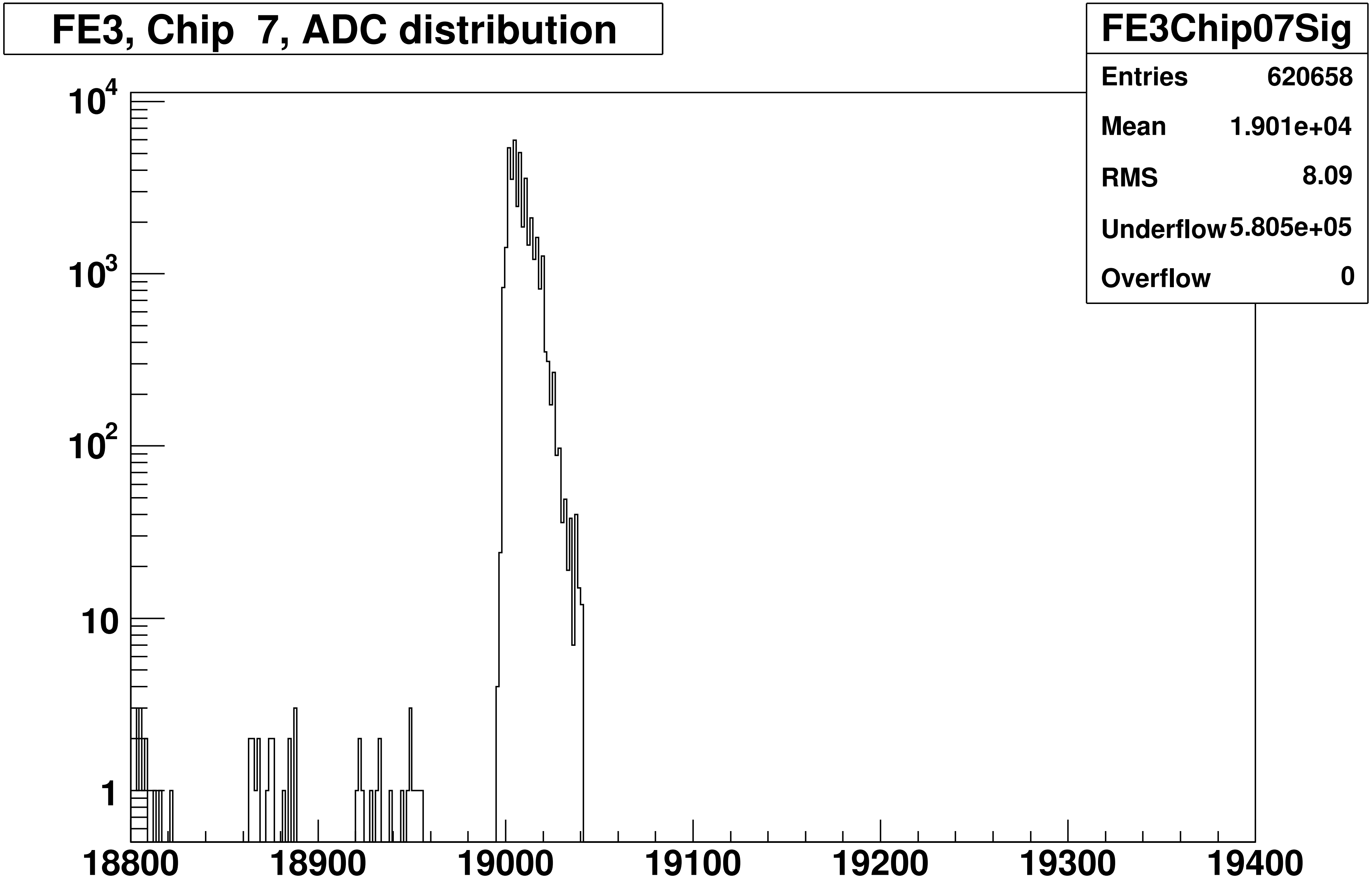

FE3, Chip 6, ADC distribution
FE3Chip06Ped
Entries 620658
Mean 0.0895
RMS 1.165
Underflow 0
Overflow 4.032e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

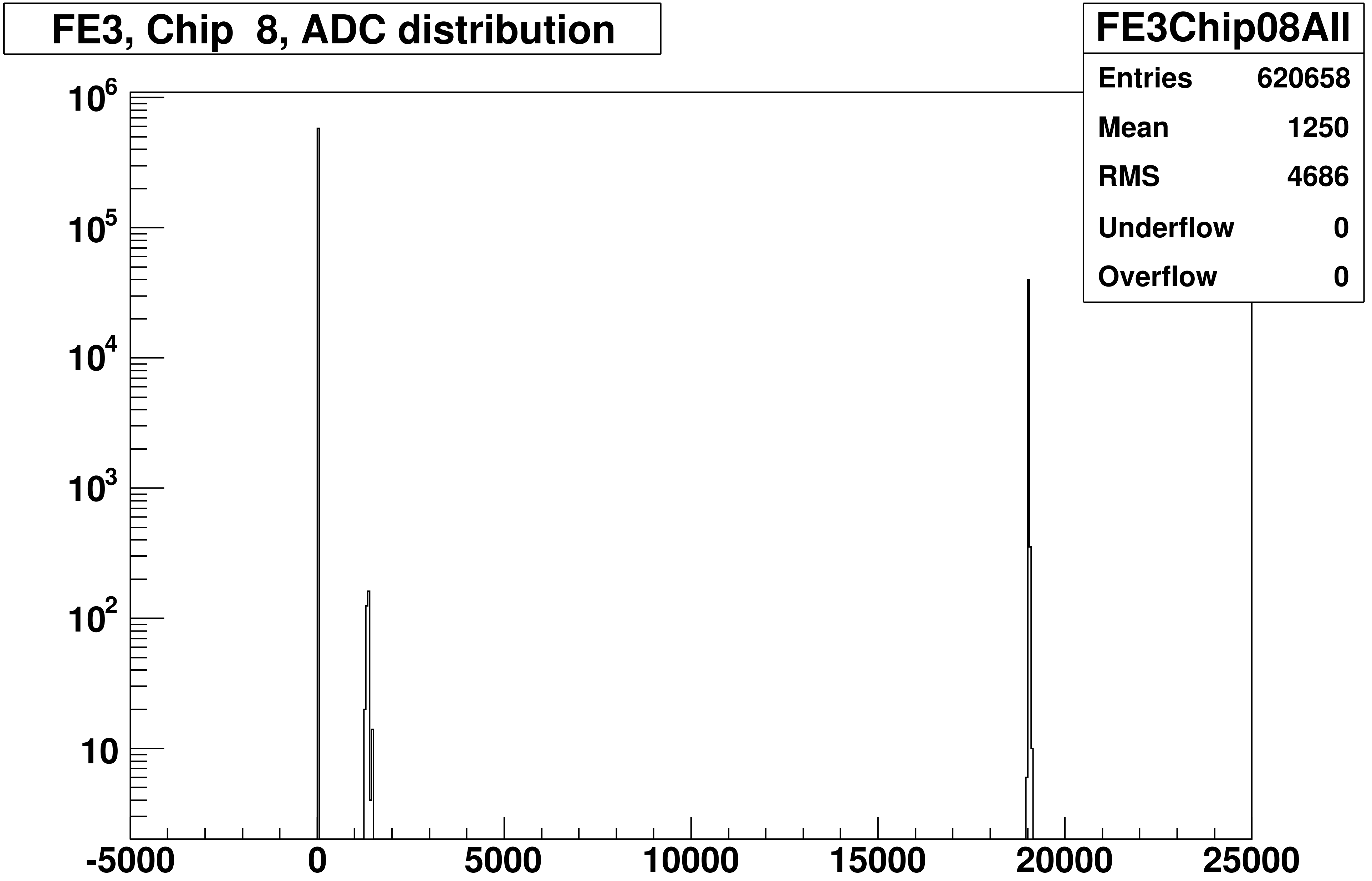

FE3, Chip 6, ADC distribution
FE3Chip06Sig
Entries 620658
Mean 1.89e+04
RMS 6.423
Underflow 5.804e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

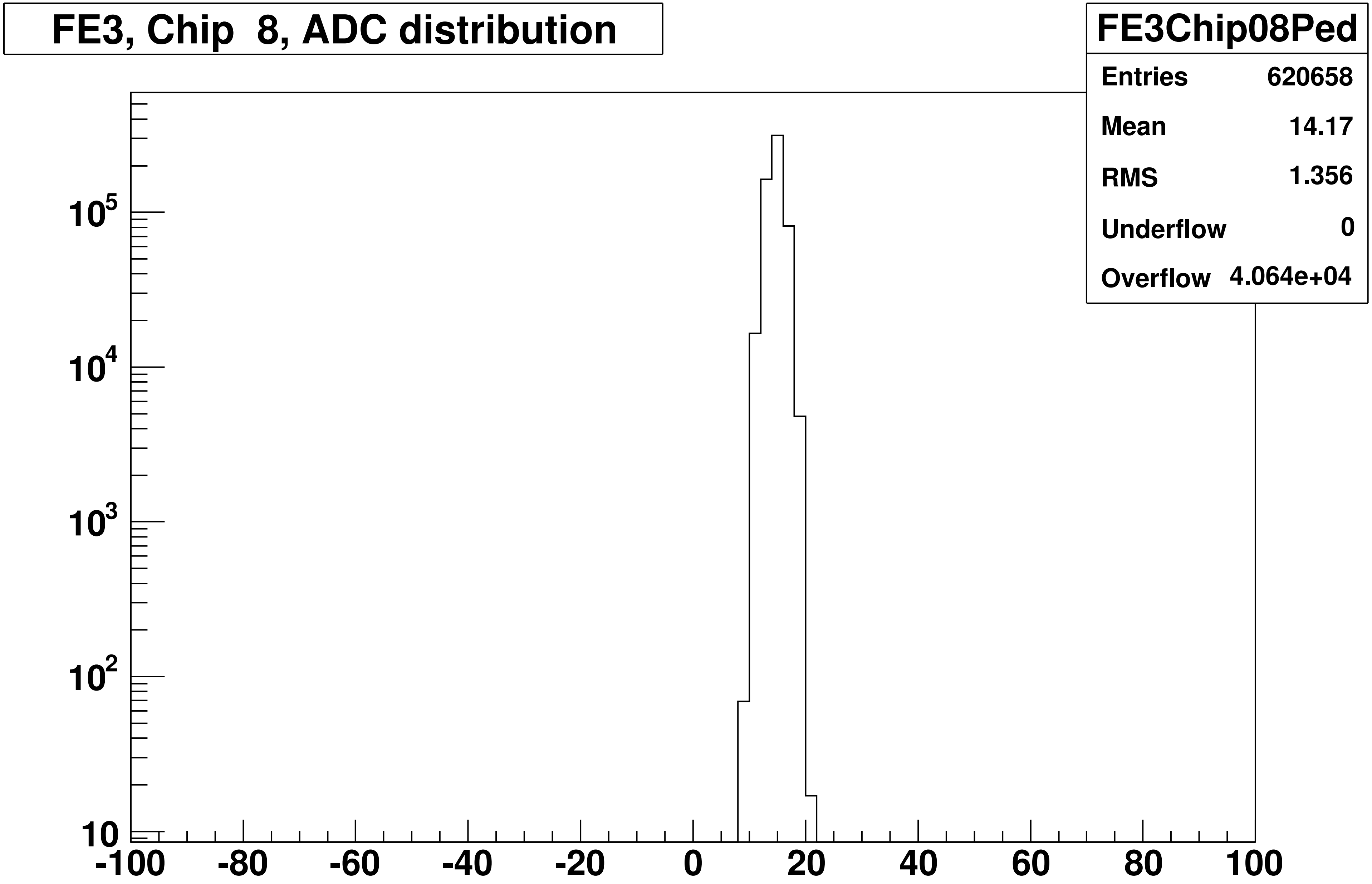

FE3, Chip 7, ADC distribution
FE3Chip07All
Entries 620658
Mean 1230
RMS 4678
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



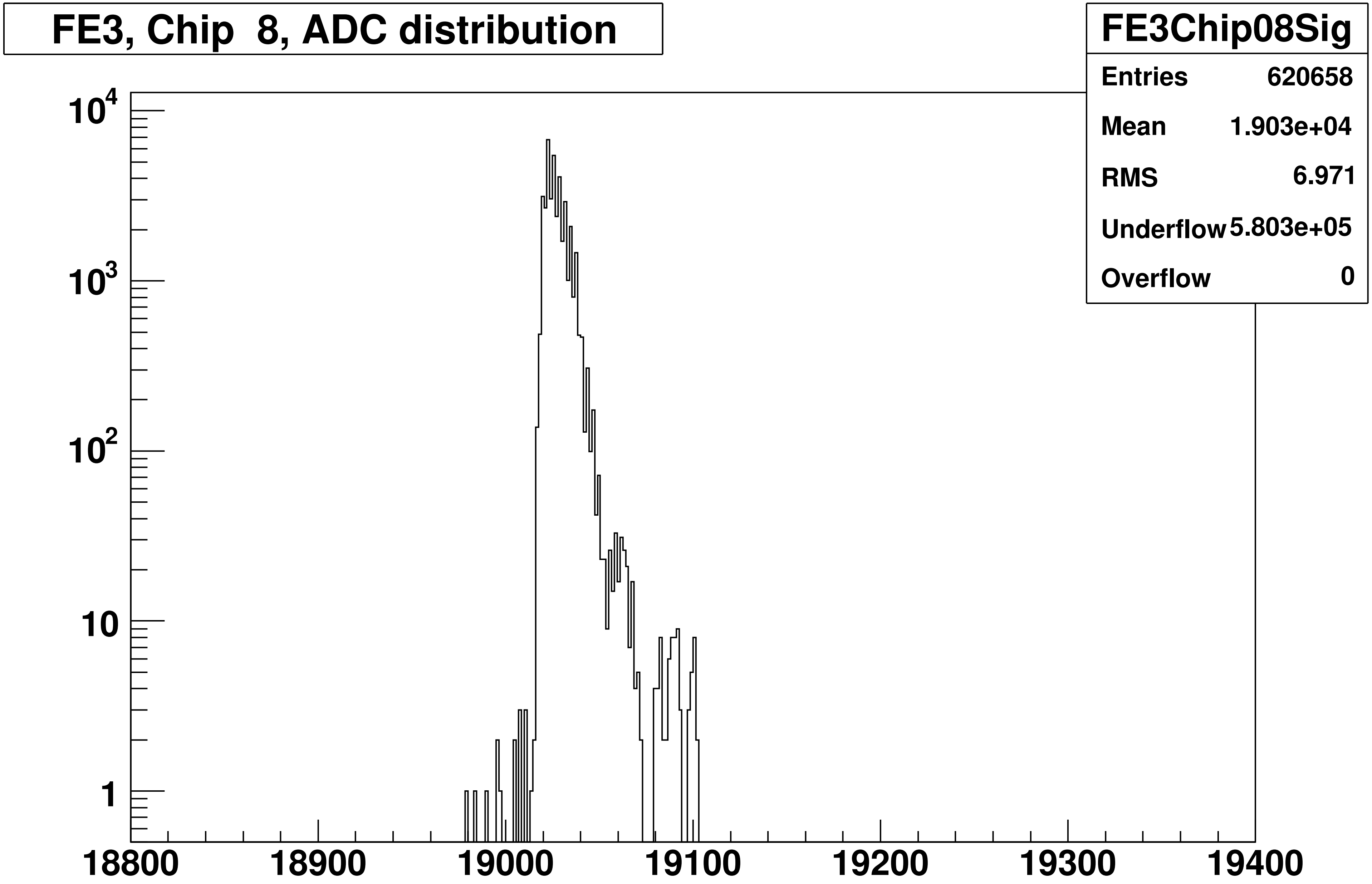
FE3, Chip 7, ADC distribution
FE3Chip07Ped
Entries 620658
Mean -1.248
RMS 0.7099
Underflow 0
Overflow 4.034e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

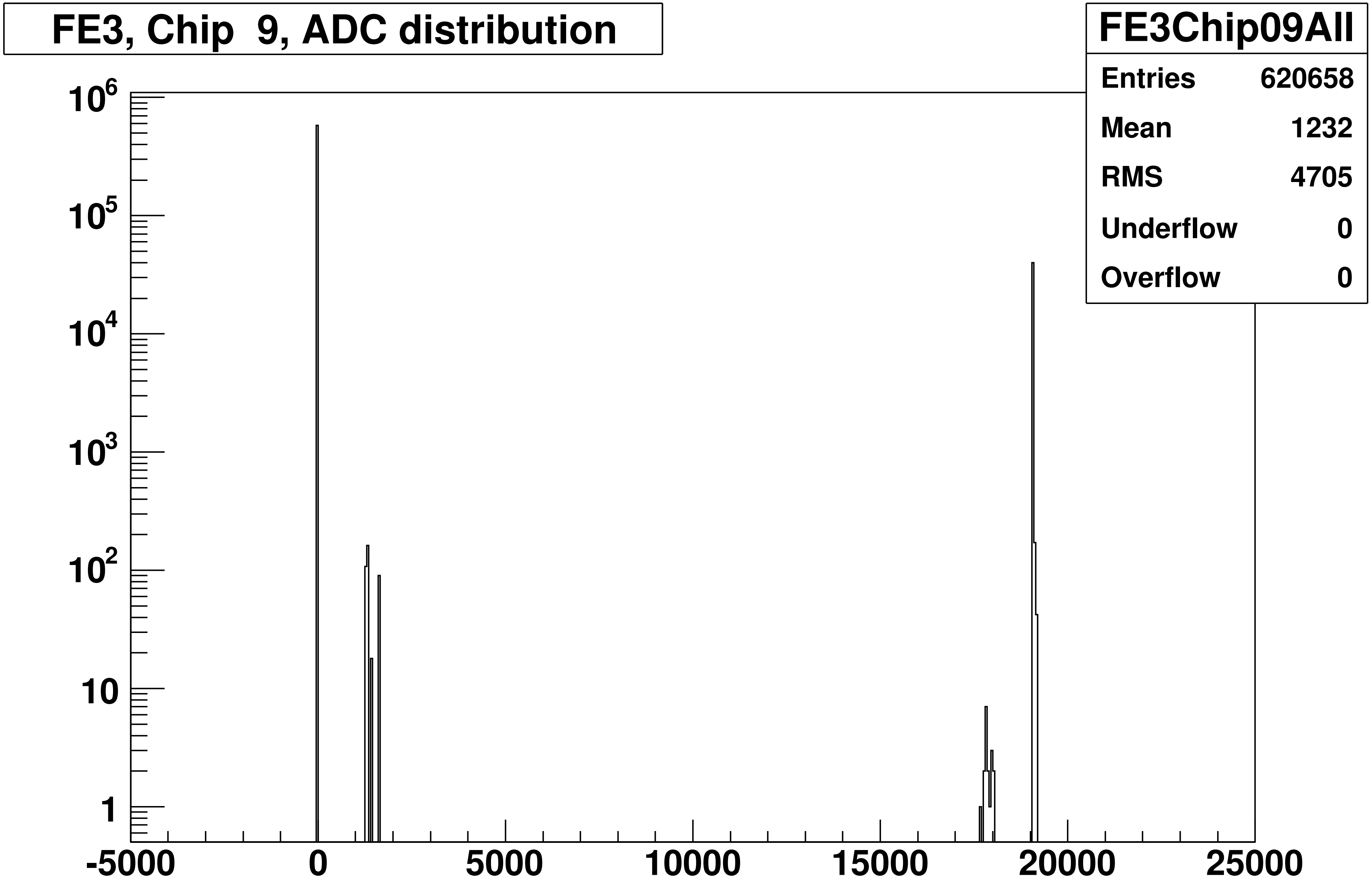

FE3, Chip 7, ADC distribution
FE3Chip07Sig
Entries 620658
Mean 1.901e+04
RMS 8.09
Underflow 5.805e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

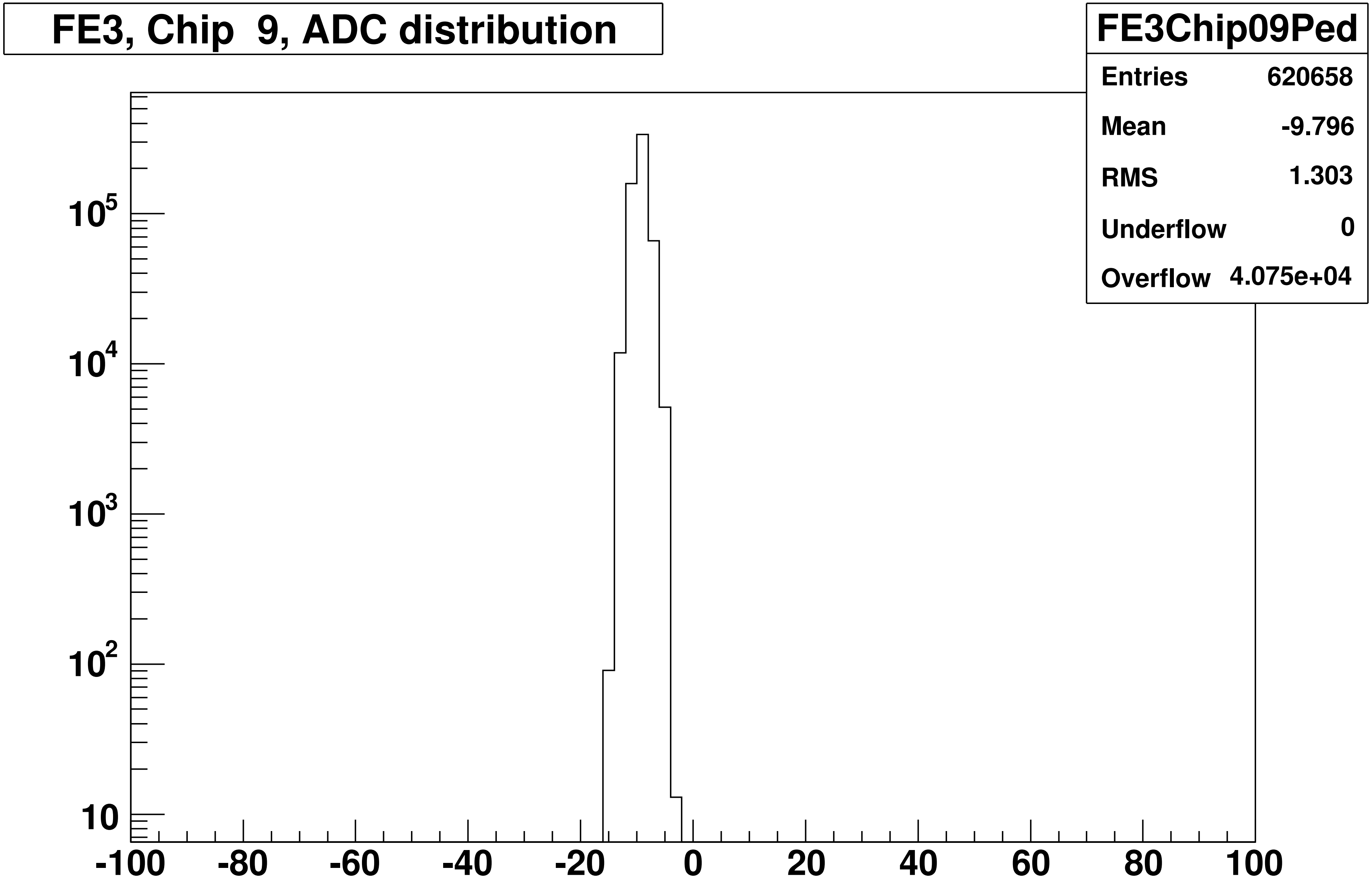

FE3, Chip 8, ADC distribution
FE3Chip08All
Entries 620658
Mean 1250
RMS 4686
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

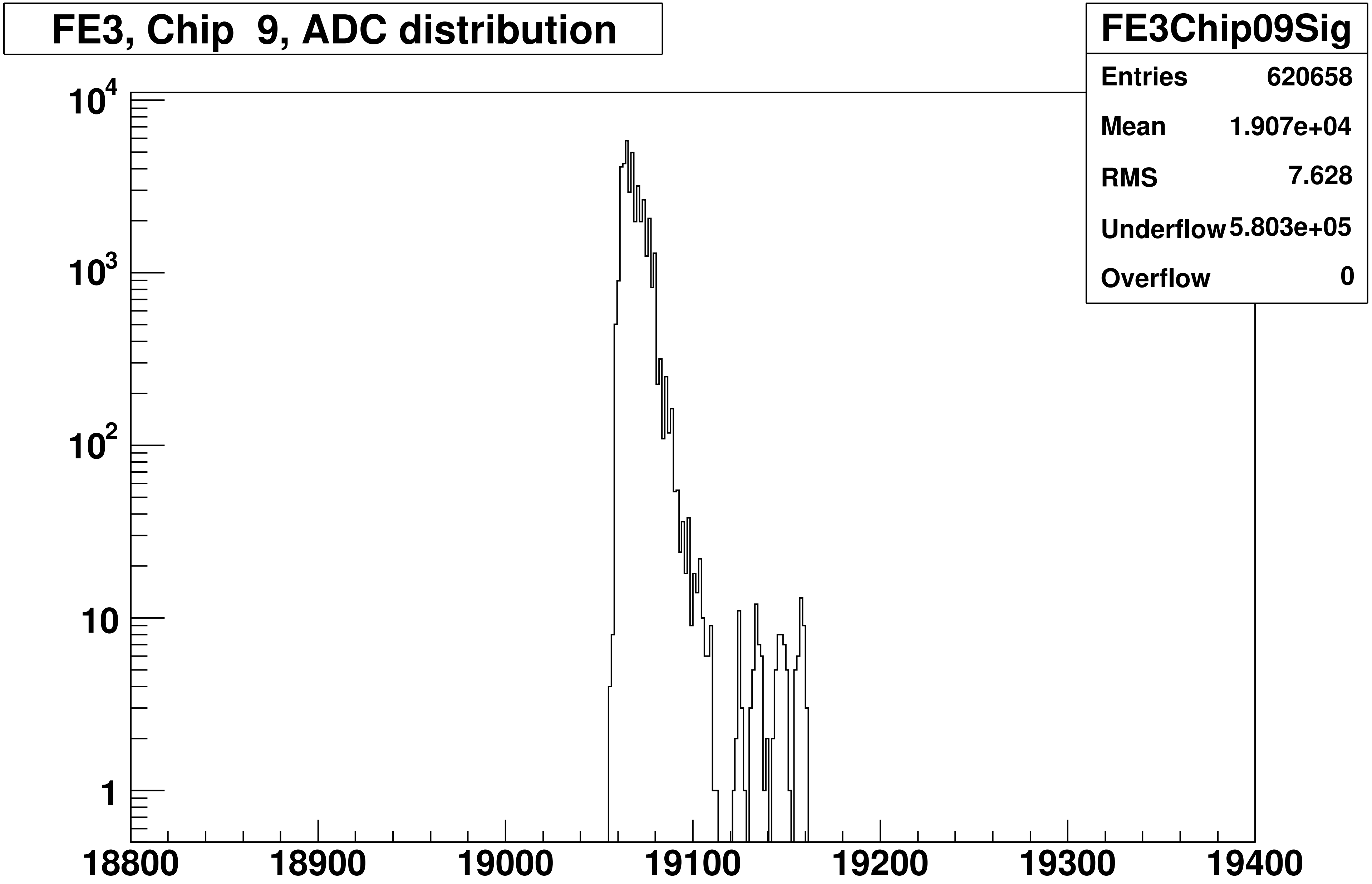


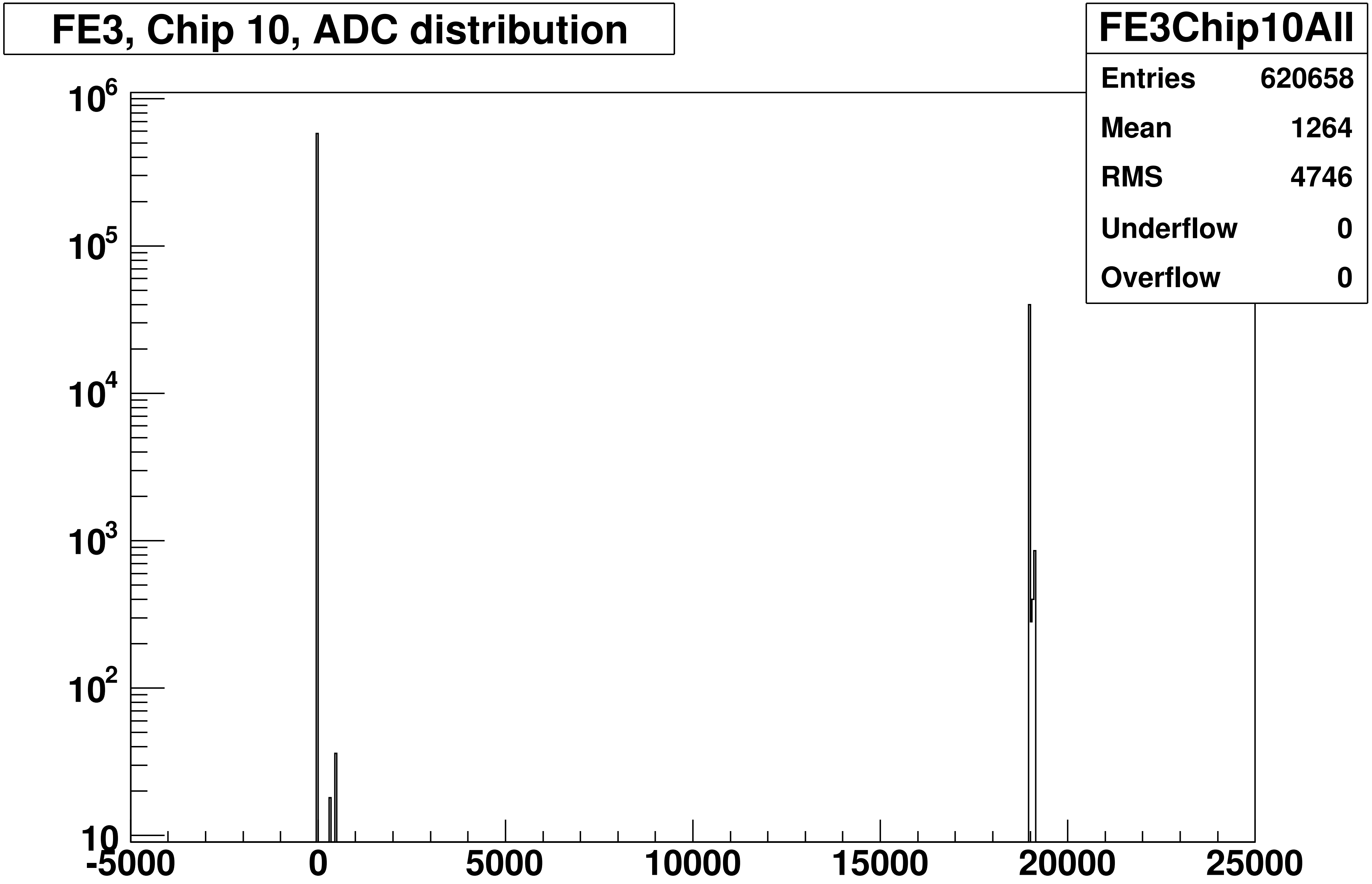
FE3, Chip 8, ADC distribution
FE3Chip08Ped
Entries 620658
Mean 14.17
RMS 1.356
Underflow 0
Overflow 4.064e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE3, Chip 8, ADC distribution
FE3Chip08Sig
Entries 620658
Mean 1.903e+04
RMS 6.971
Underflow 5.803e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

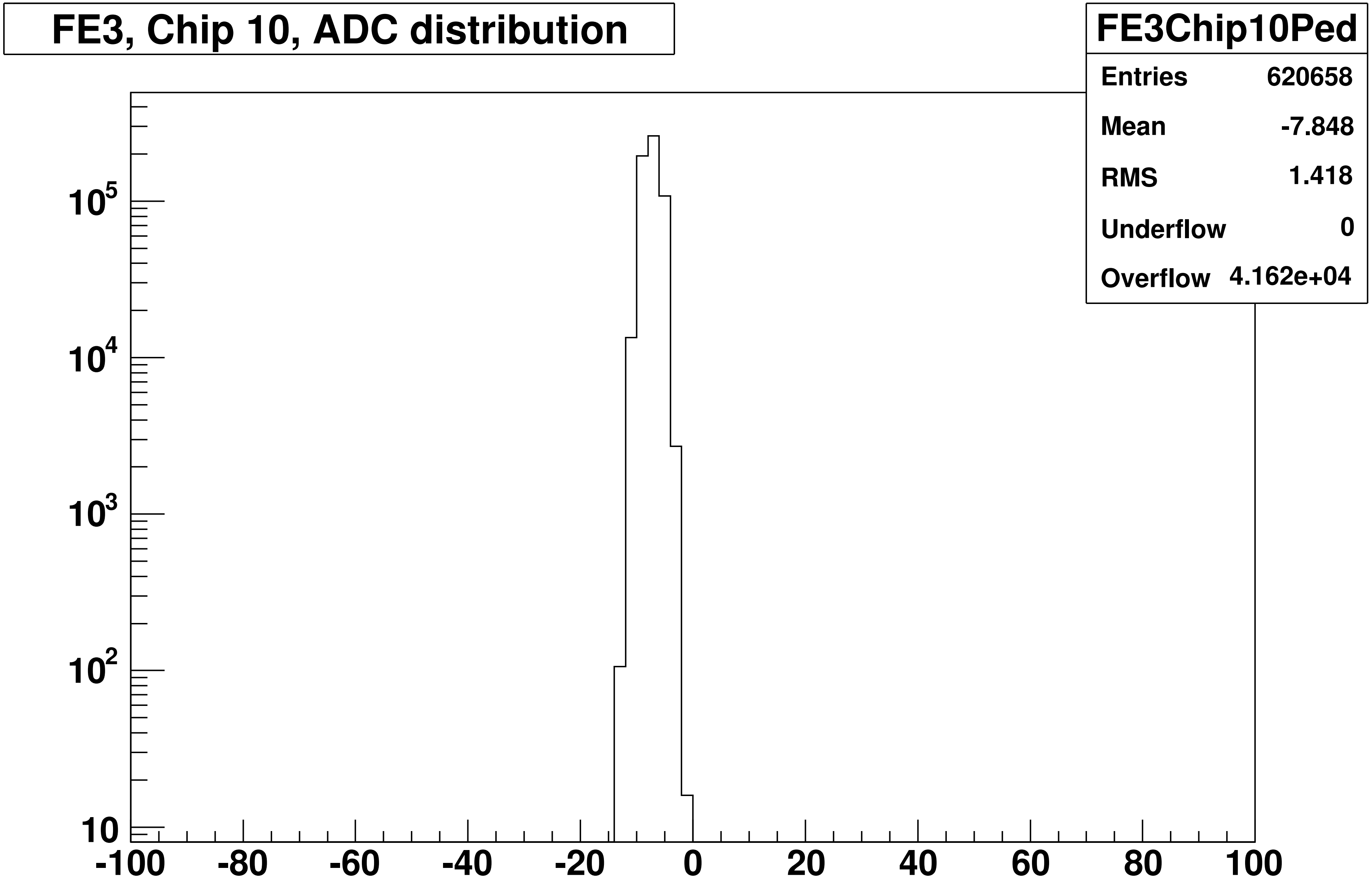

FE3, Chip 9, ADC distribution
FE3Chip09All
Entries 620658
Mean 1232
RMS 4705
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

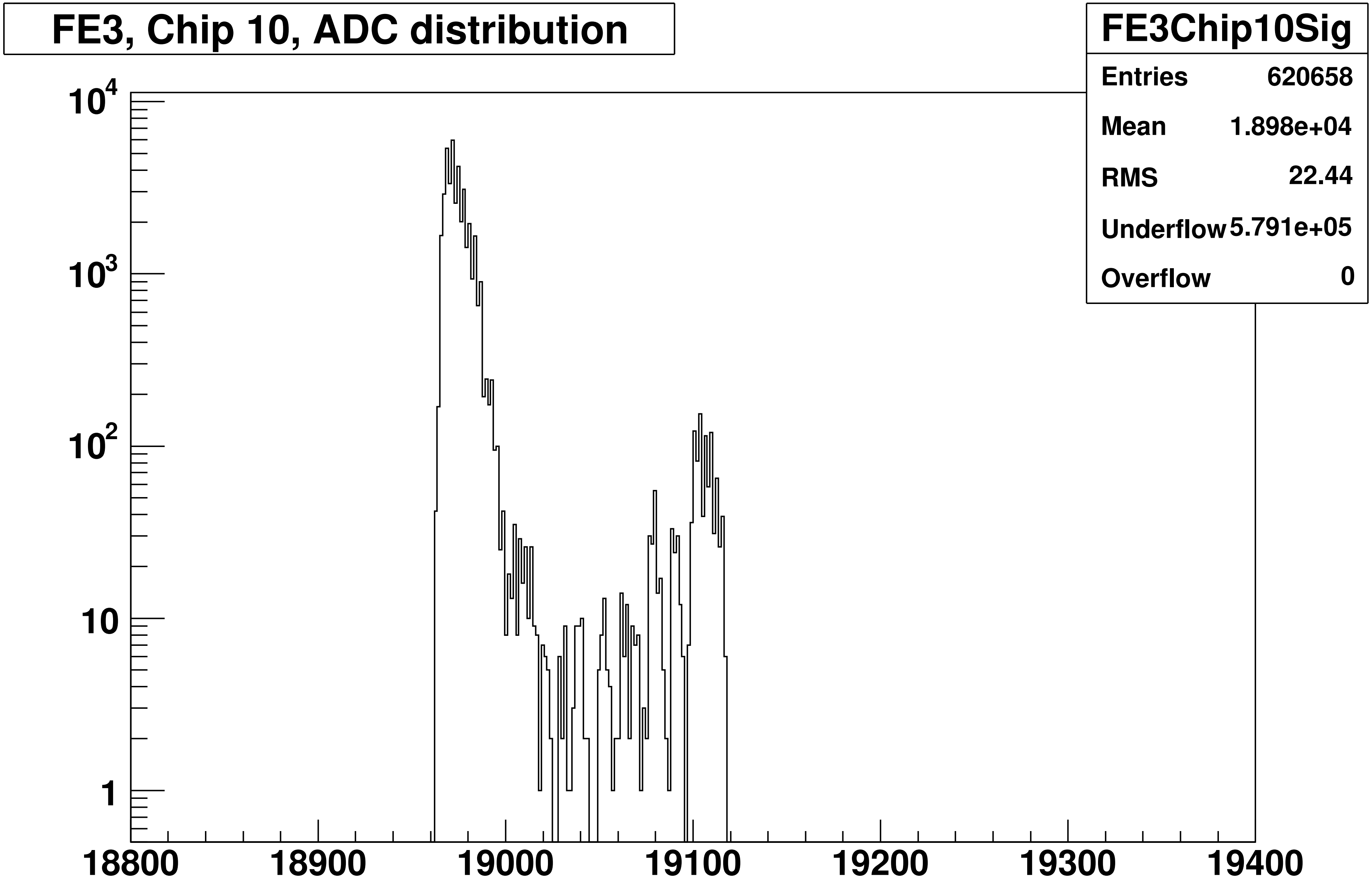

FE3, Chip 9, ADC distribution
FE3Chip09Ped
Entries 620658
Mean -9.796
RMS 1.303
Underflow 0
Overflow 4.075e+04
10
10^2
10^3
10^4
10^5
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE3, Chip 9, ADC distribution
FE3Chip09Sig
Entries 620658
Mean 1.907e+04
RMS 7.628
Underflow 5.803e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE3, Chip 10, ADC distribution
FE3Chip10All
Entries 620658
Mean 1264
RMS 4746
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 10, ADC distribution
FE3Chip10Ped
Entries 620658
Mean -7.848
RMS 1.418
Underflow 0
Overflow 4.162e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE3, Chip 10, ADC distribution
FE3Chip10Sig
Entries 620658
Mean 1.898e+04
RMS 22.44
Underflow 5.791e+05
Overflow 0
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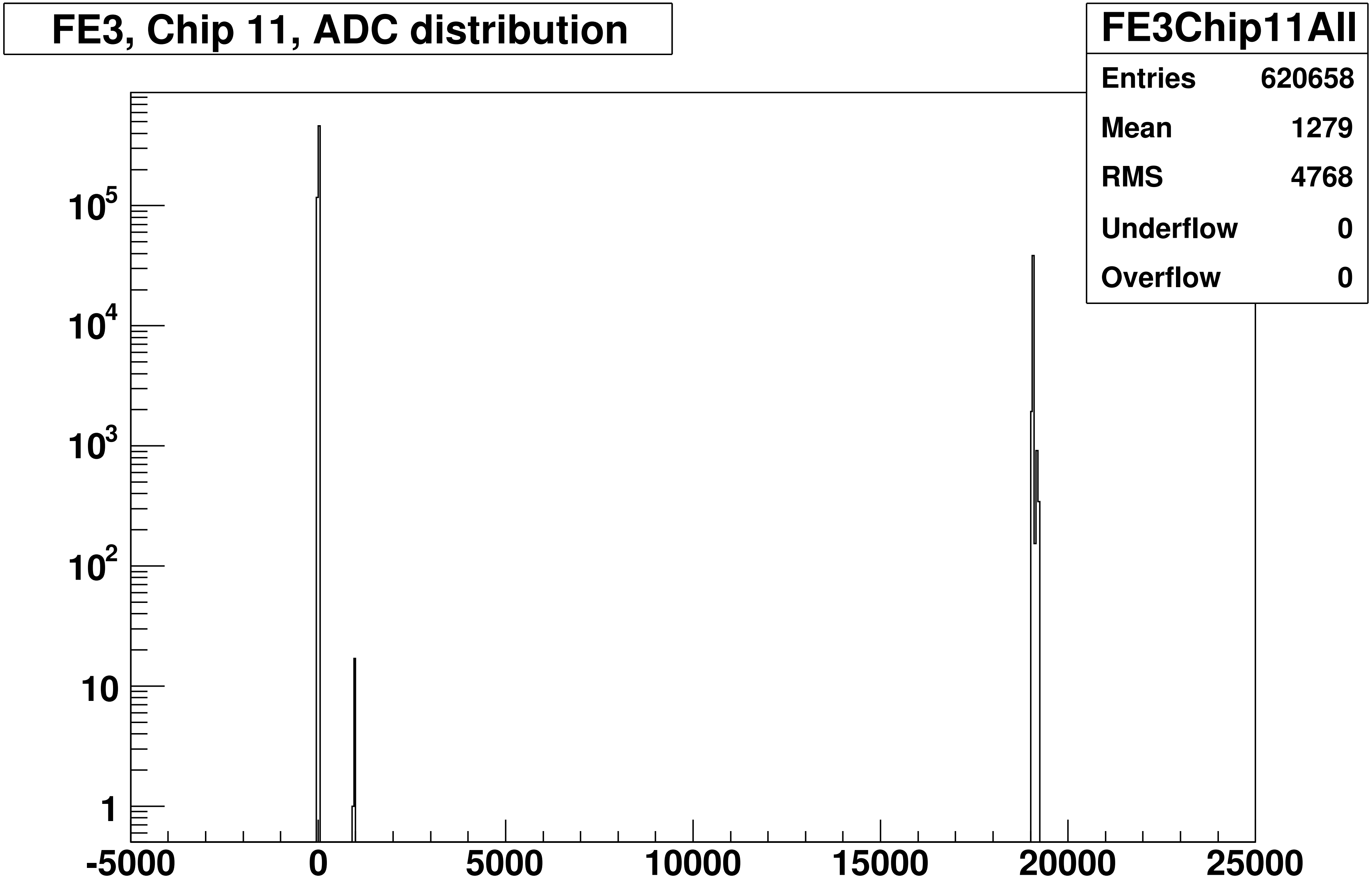
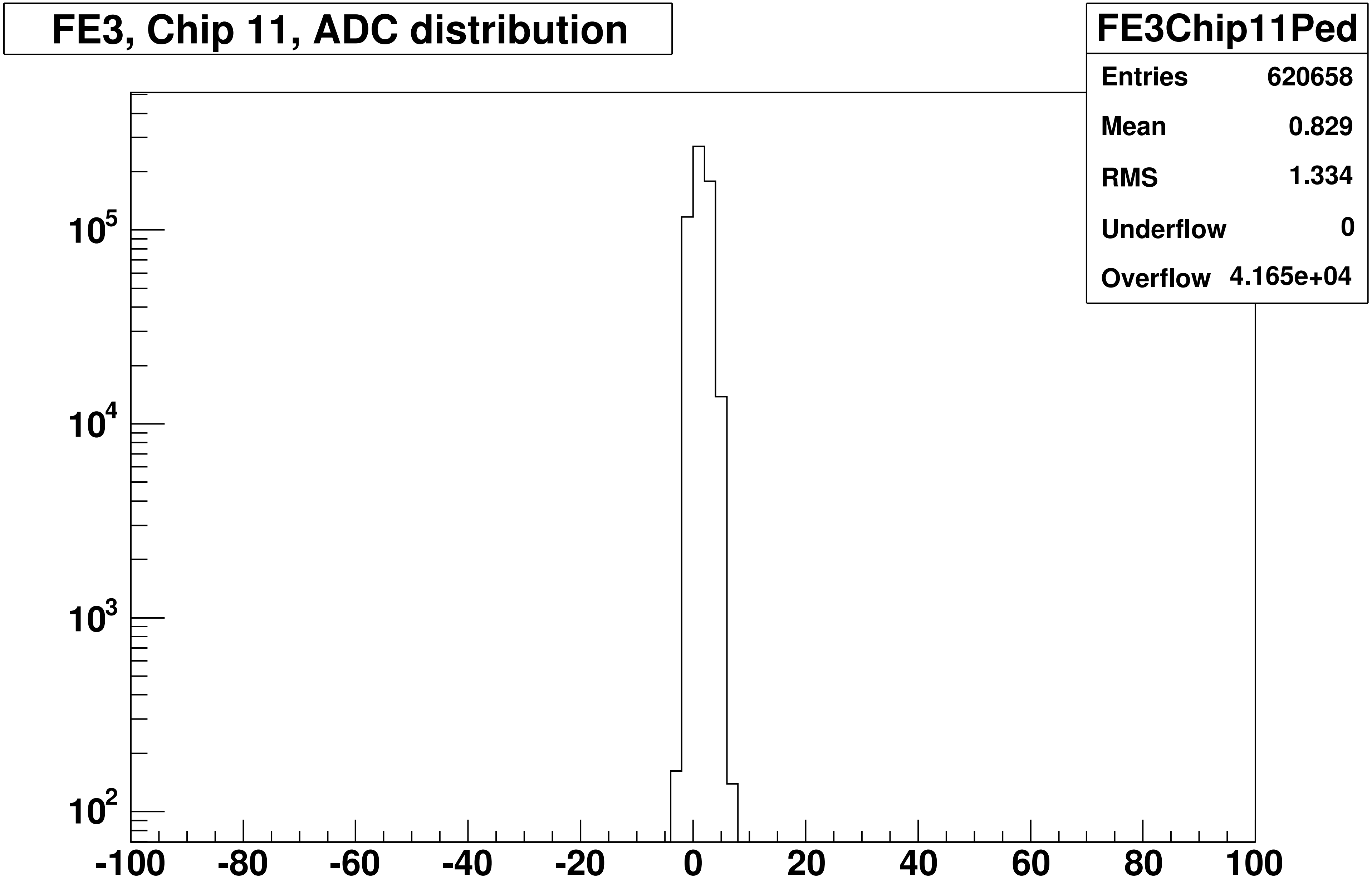
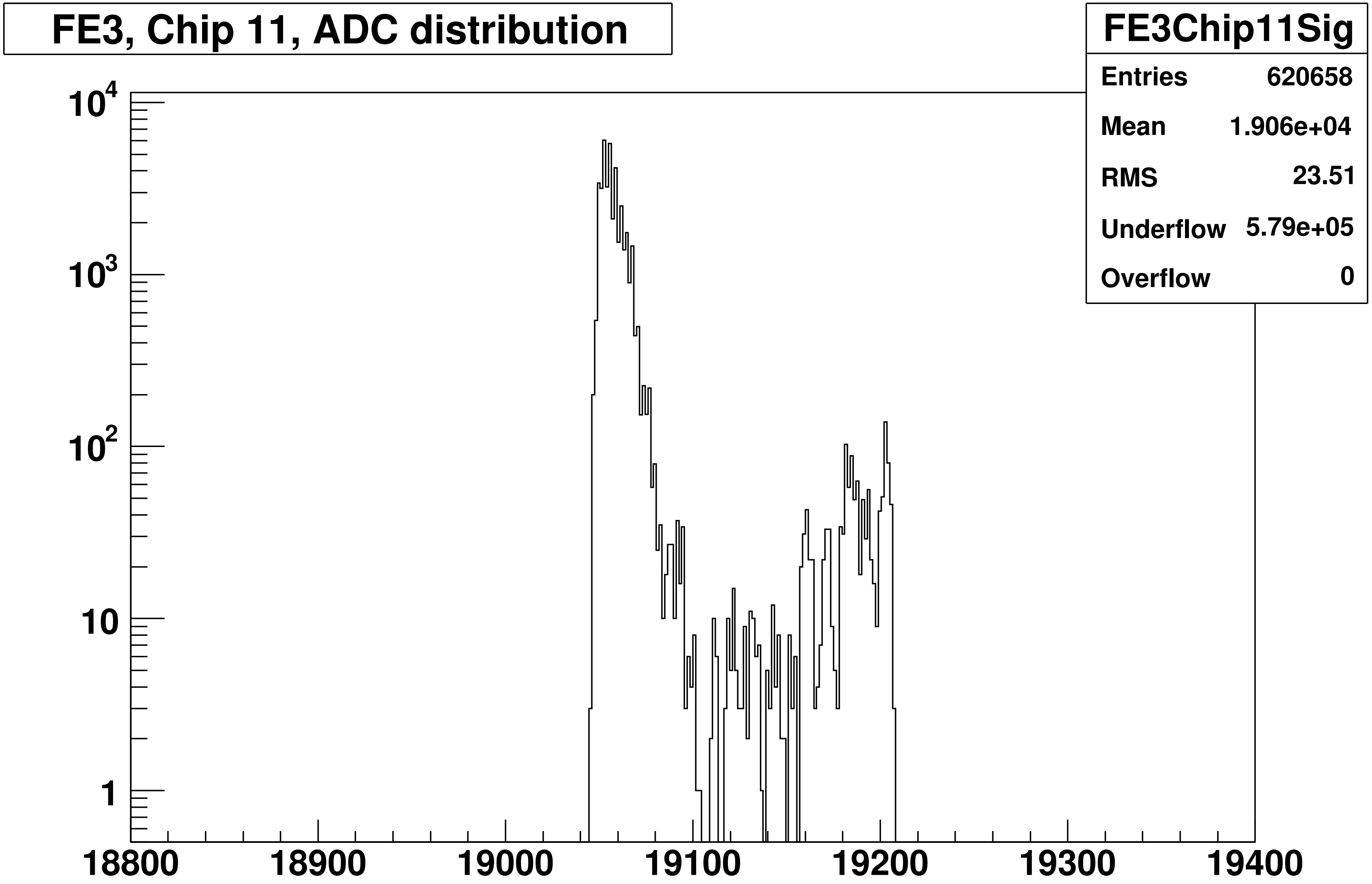
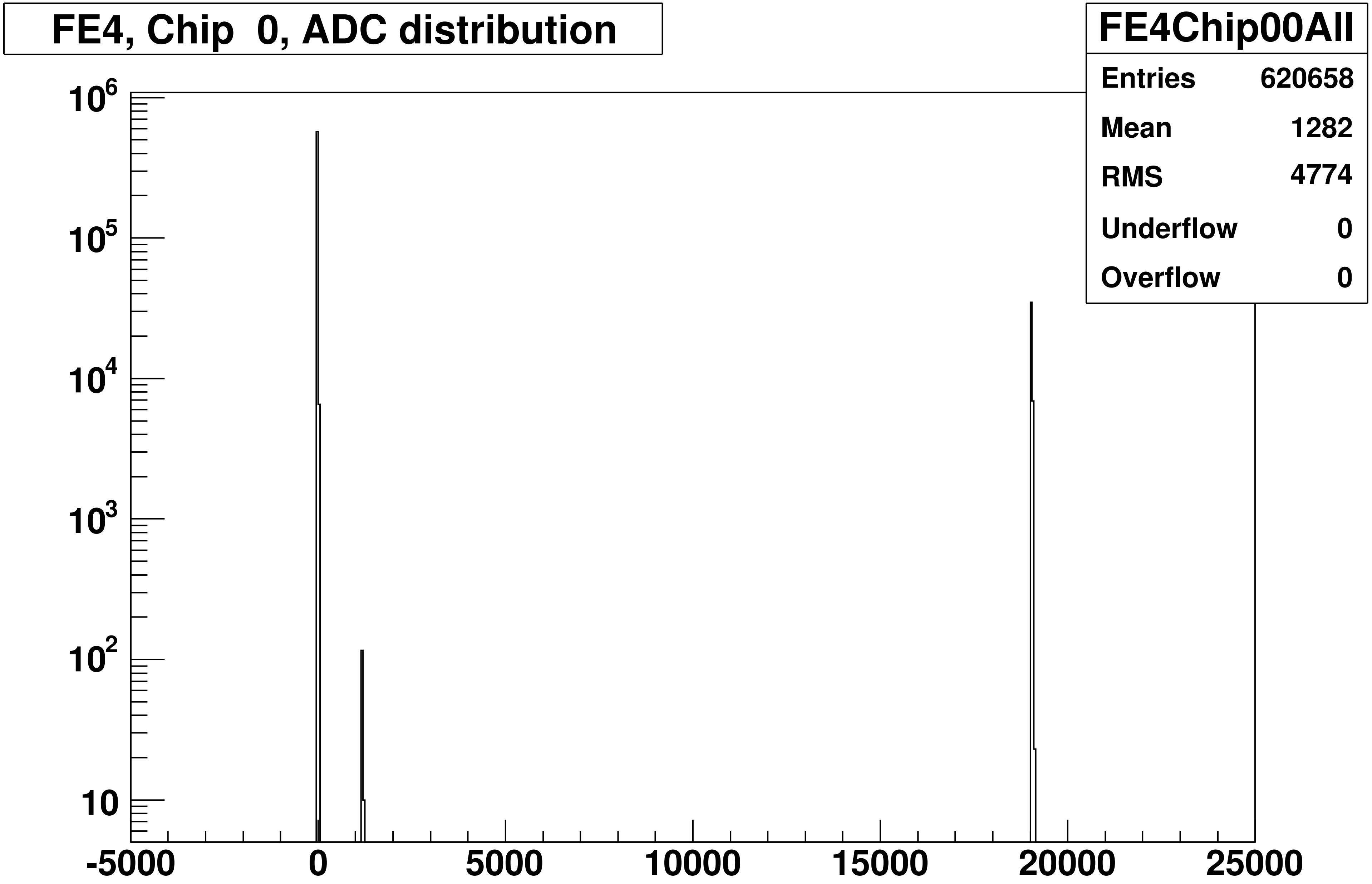
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE3, Chip 11, ADC distribution
FE3Chip11All
Entries 620658
Mean 1279
RMS 4768
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE3, Chip 11, ADC distribution
FE3Chip11Ped
Entries 620658
Mean 0.829
RMS 1.334
Underflow 0
Overflow 4.165e+04
10^5
10^4
10^3
10^2
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE3, Chip 11, ADC distribution
FE3Chip11Sig
Entries 620658
Mean 1.906e+04
RMS 23.51
Underflow 5.79e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 0, ADC distribution
FE4Chip00All
Entries 620658
Mean 1282
RMS 4774
Underflow 0
Overflow 0
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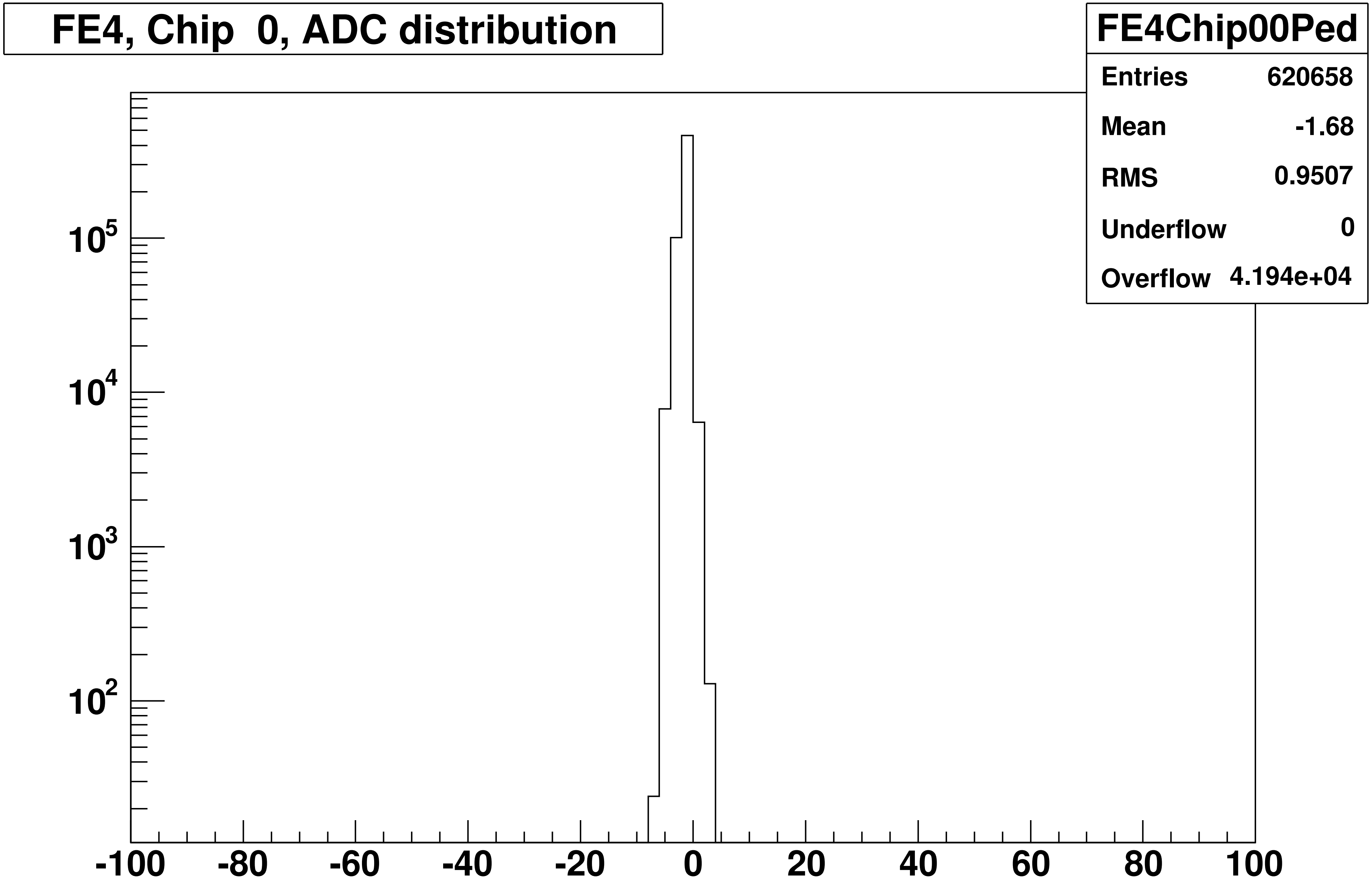
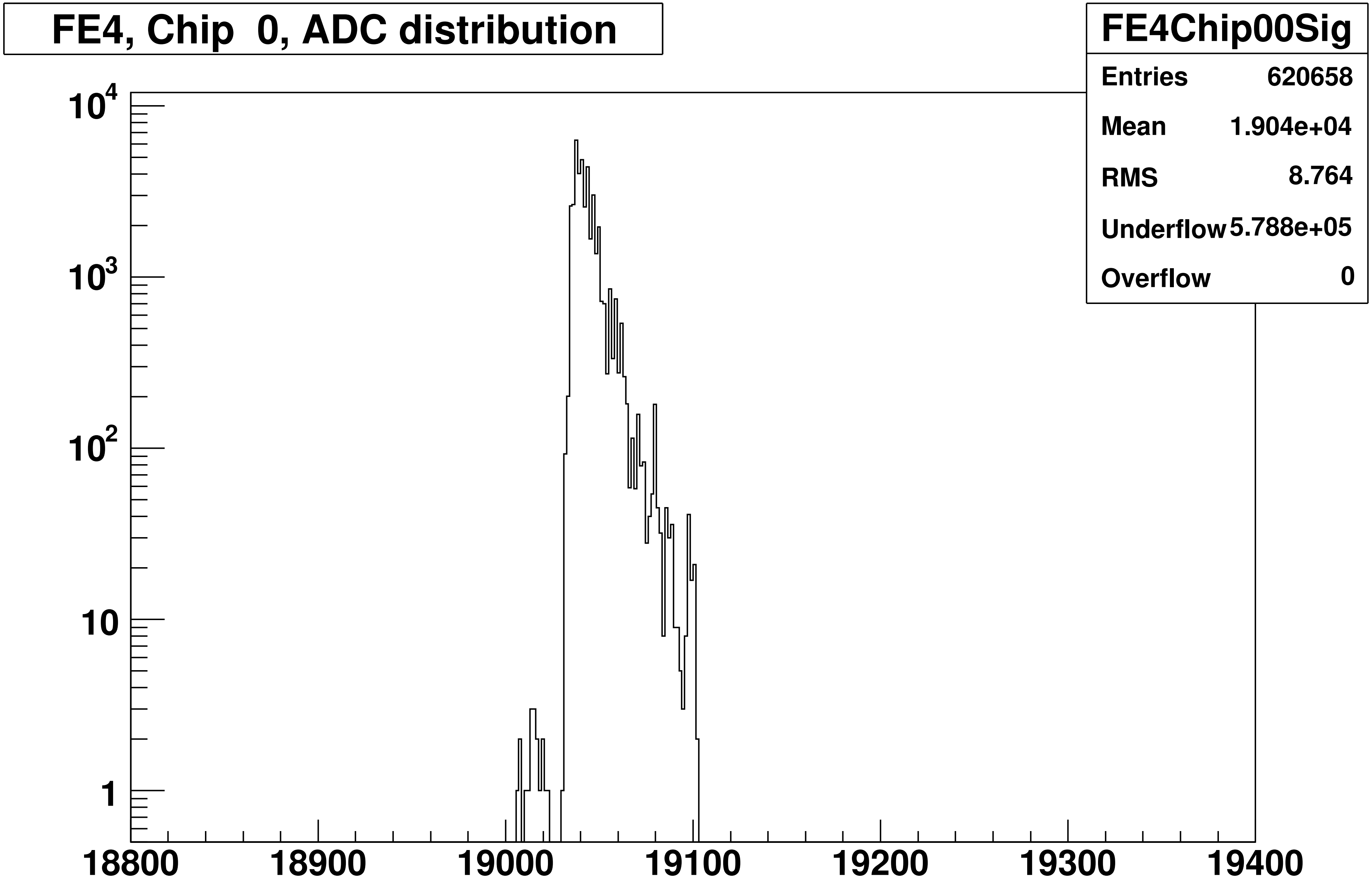
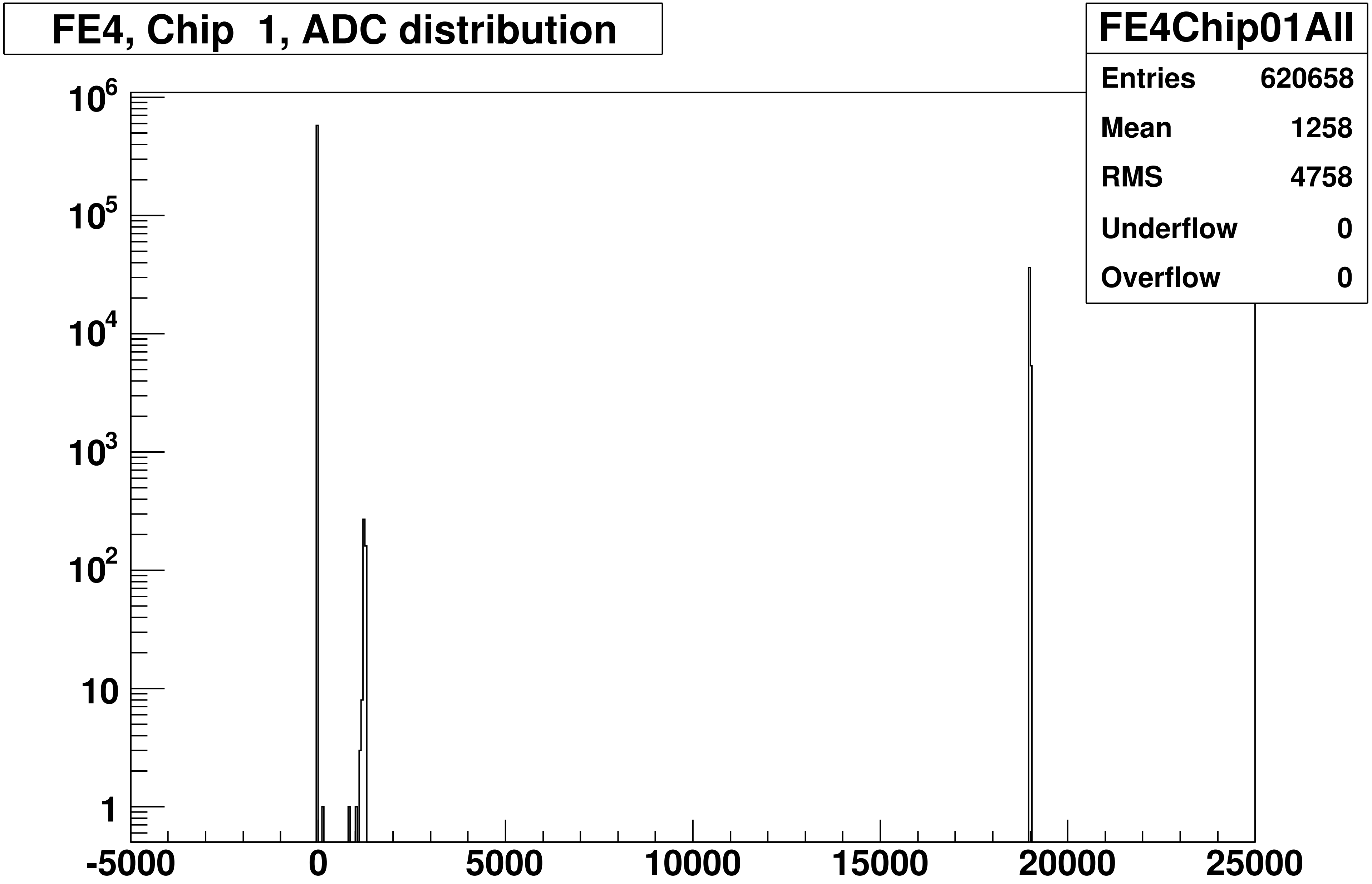
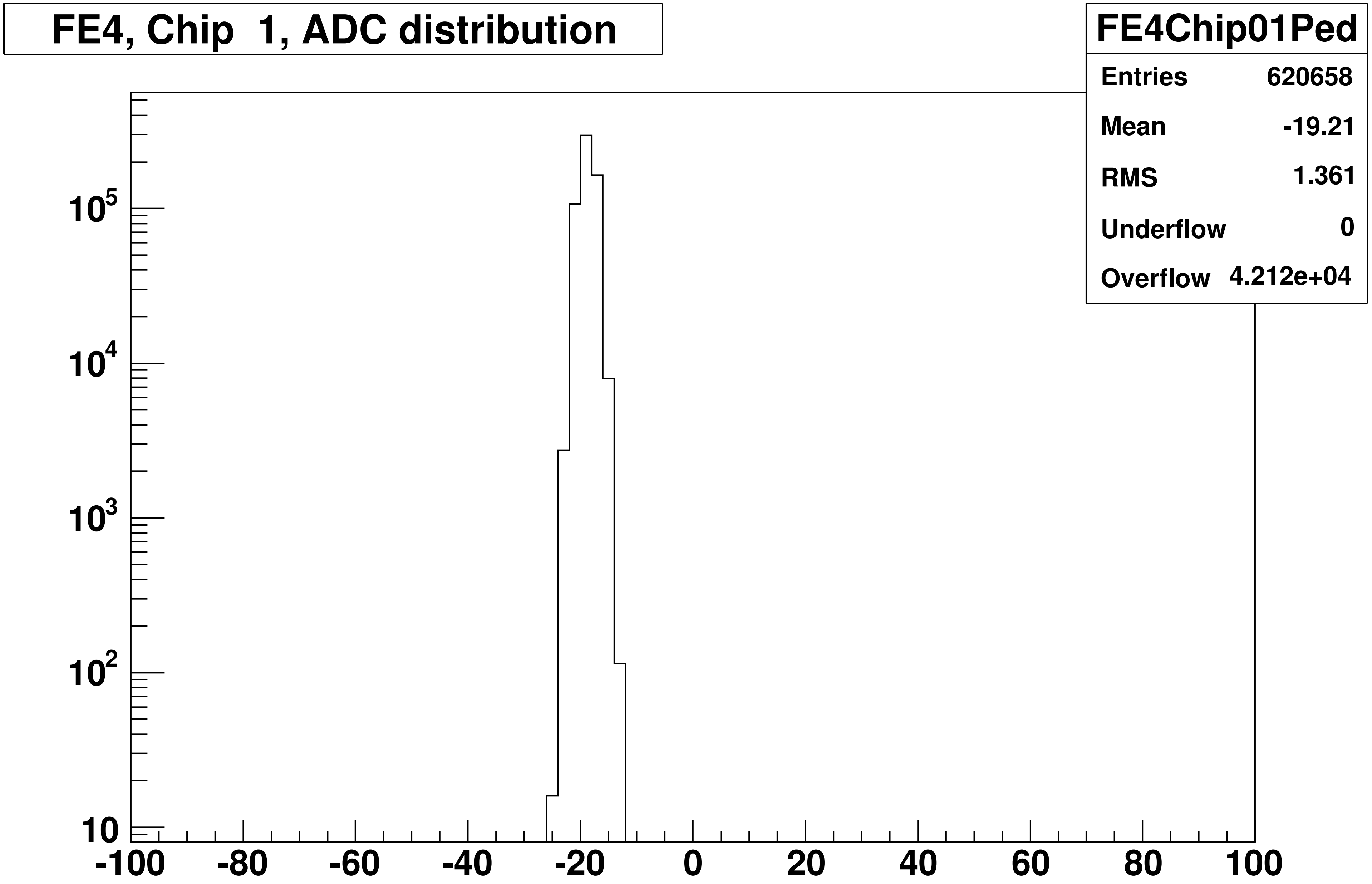
10^5
10^4
10^3
10^2
10
-5000
0
5000
10000
15000
20000
25000

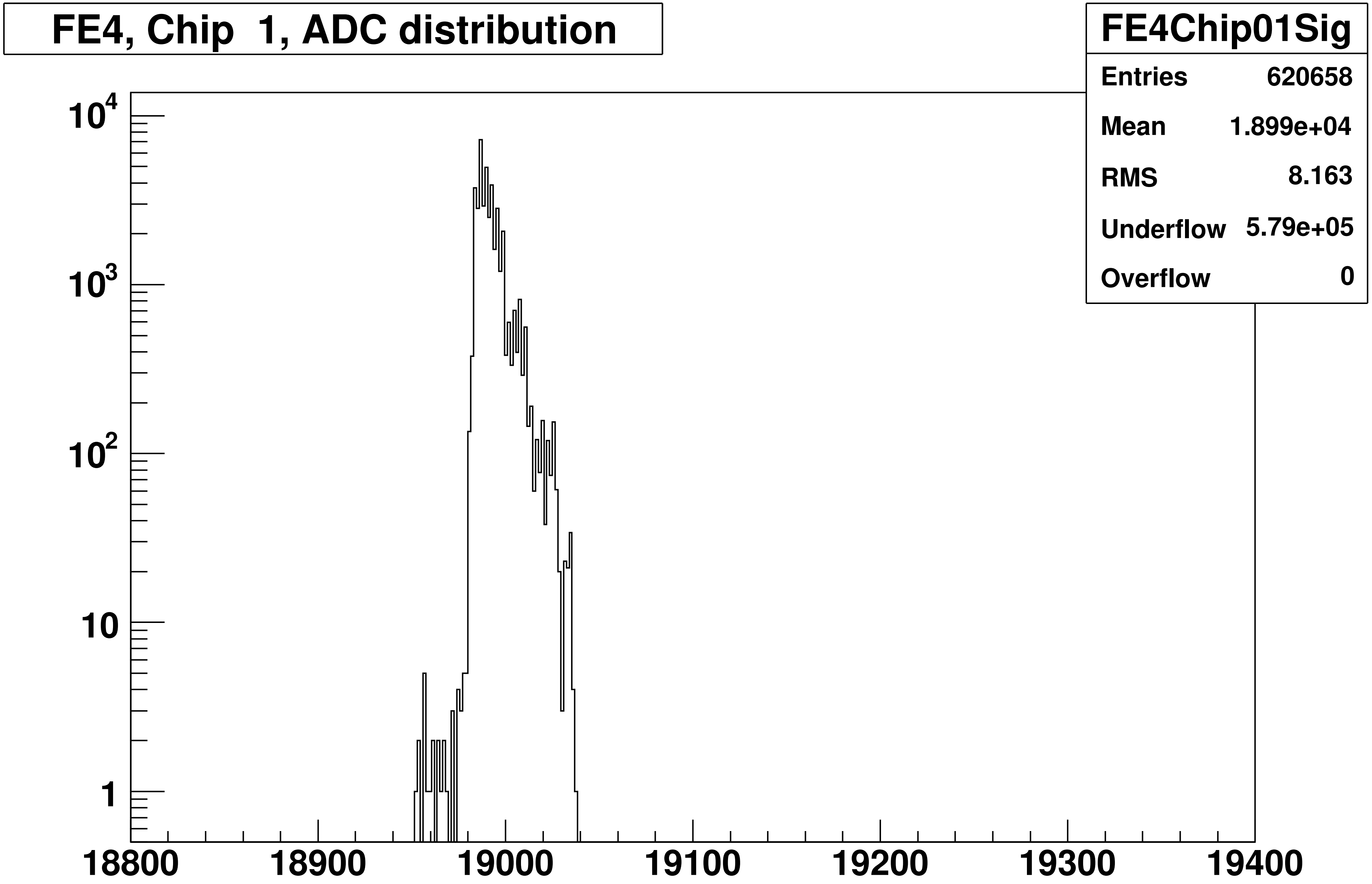

FE4, Chip 0, ADC distribution
FE4Chip00Ped
Entries 620658
Mean -1.68
RMS 0.9507
Underflow 0
Overflow 4.194e+04
10^5
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100

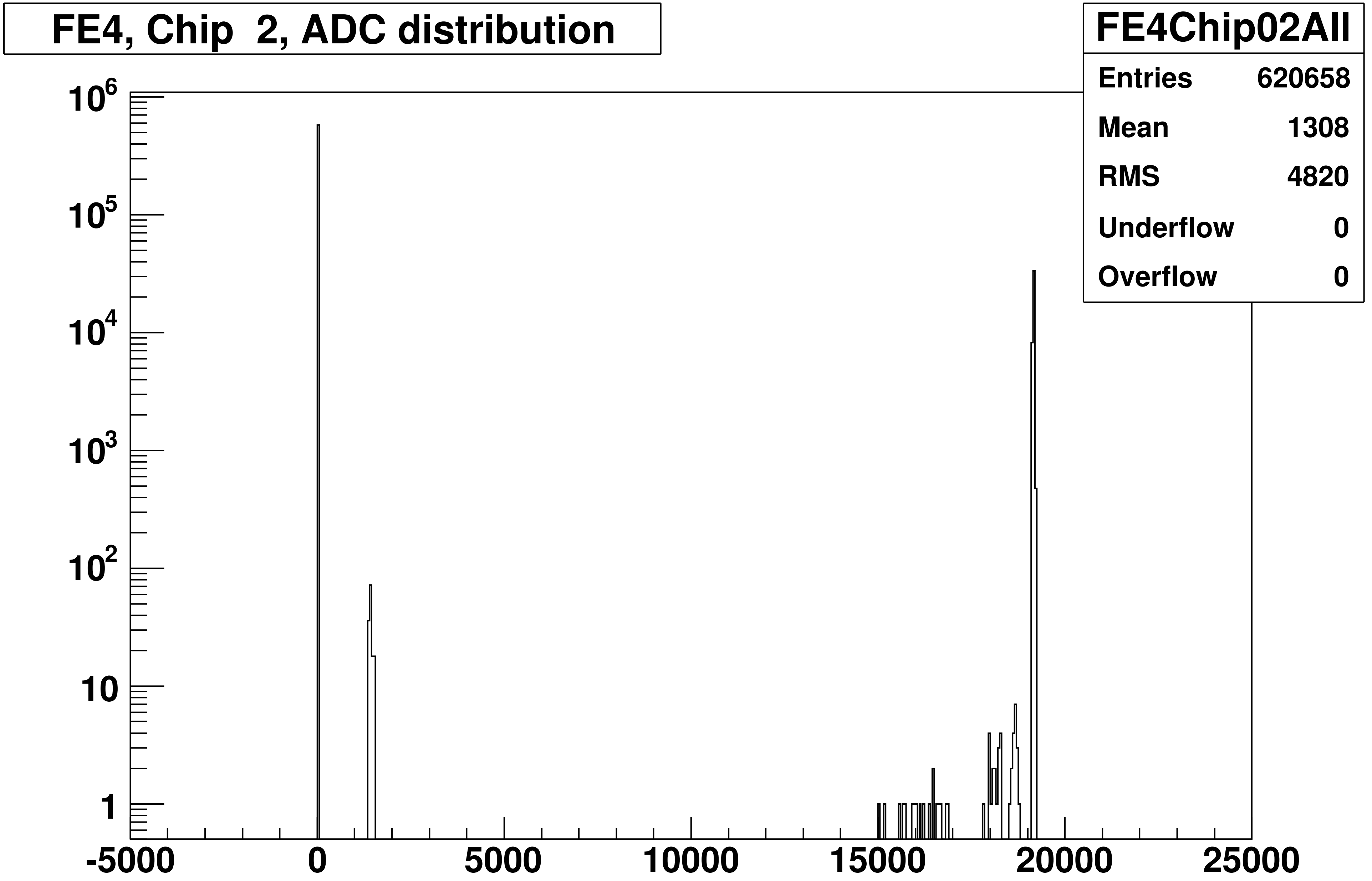

FE4, Chip 0, ADC distribution
FE4Chip00Sig
Entries 620658
Mean 1.904e+04
RMS 8.764
Underflow 5.788e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



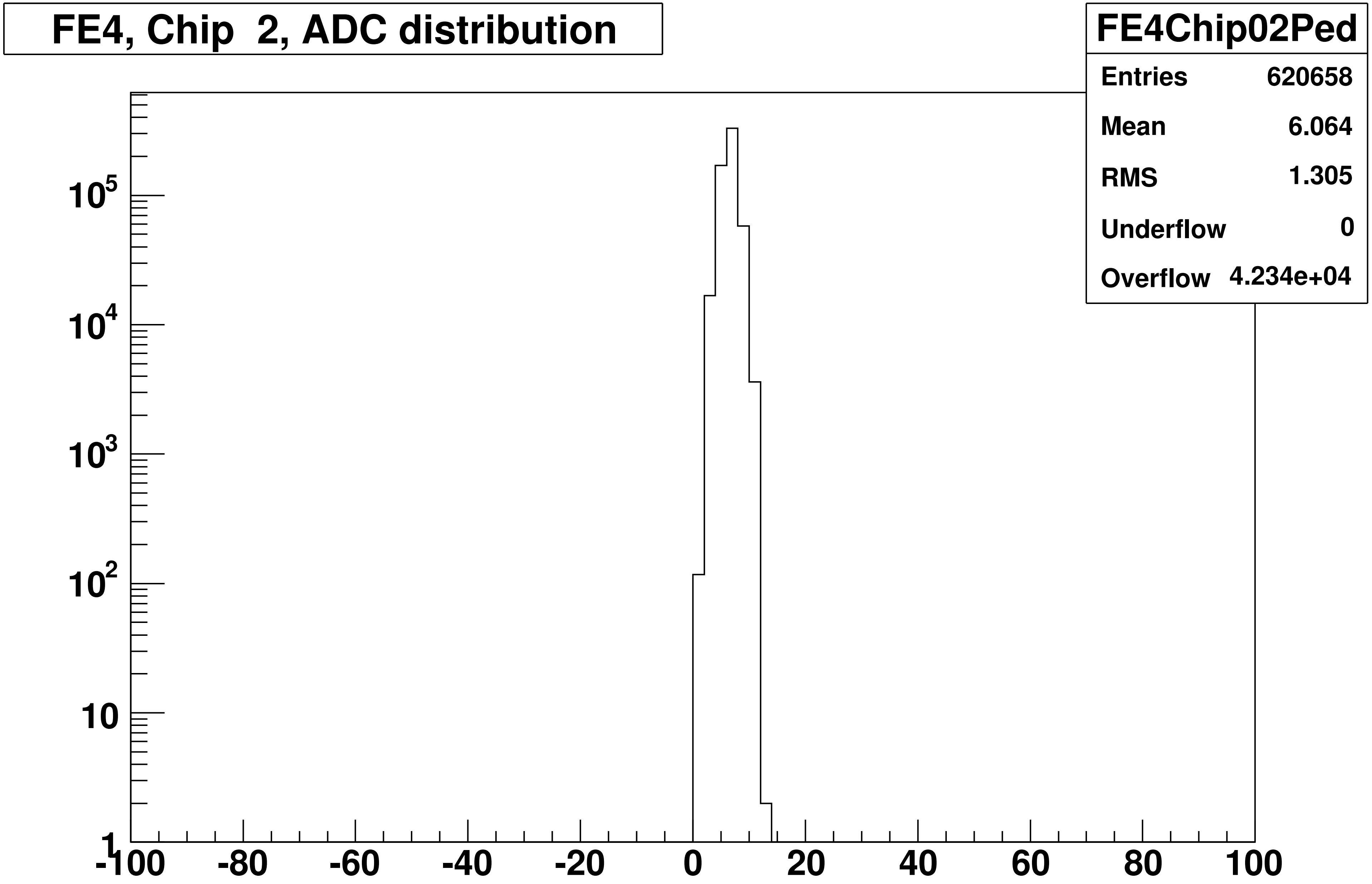
FE4, Chip 1, ADC distribution
FE4Chip01All
Entries 620658
Mean 1258
RMS 4758
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 1, ADC distribution
FE4Chip01Ped
Entries 620658
Mean -19.21
RMS 1.361
Underflow 0
Overflow 4.212e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

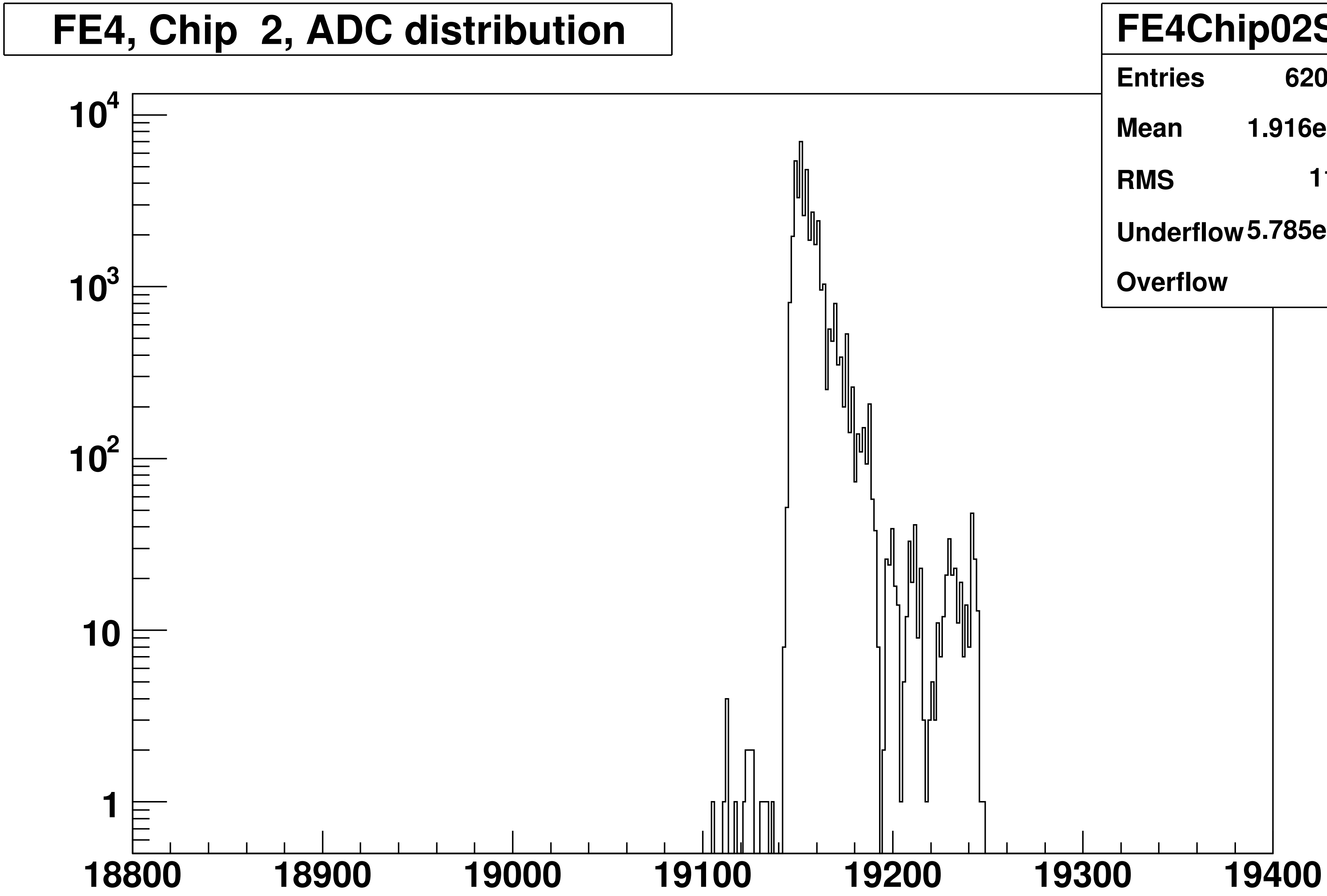

FE4, Chip 1, ADC distribution
FE4Chip01Sig
Entries 620658
Mean 1.899e+04
RMS 8.163
Underflow 5.79e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

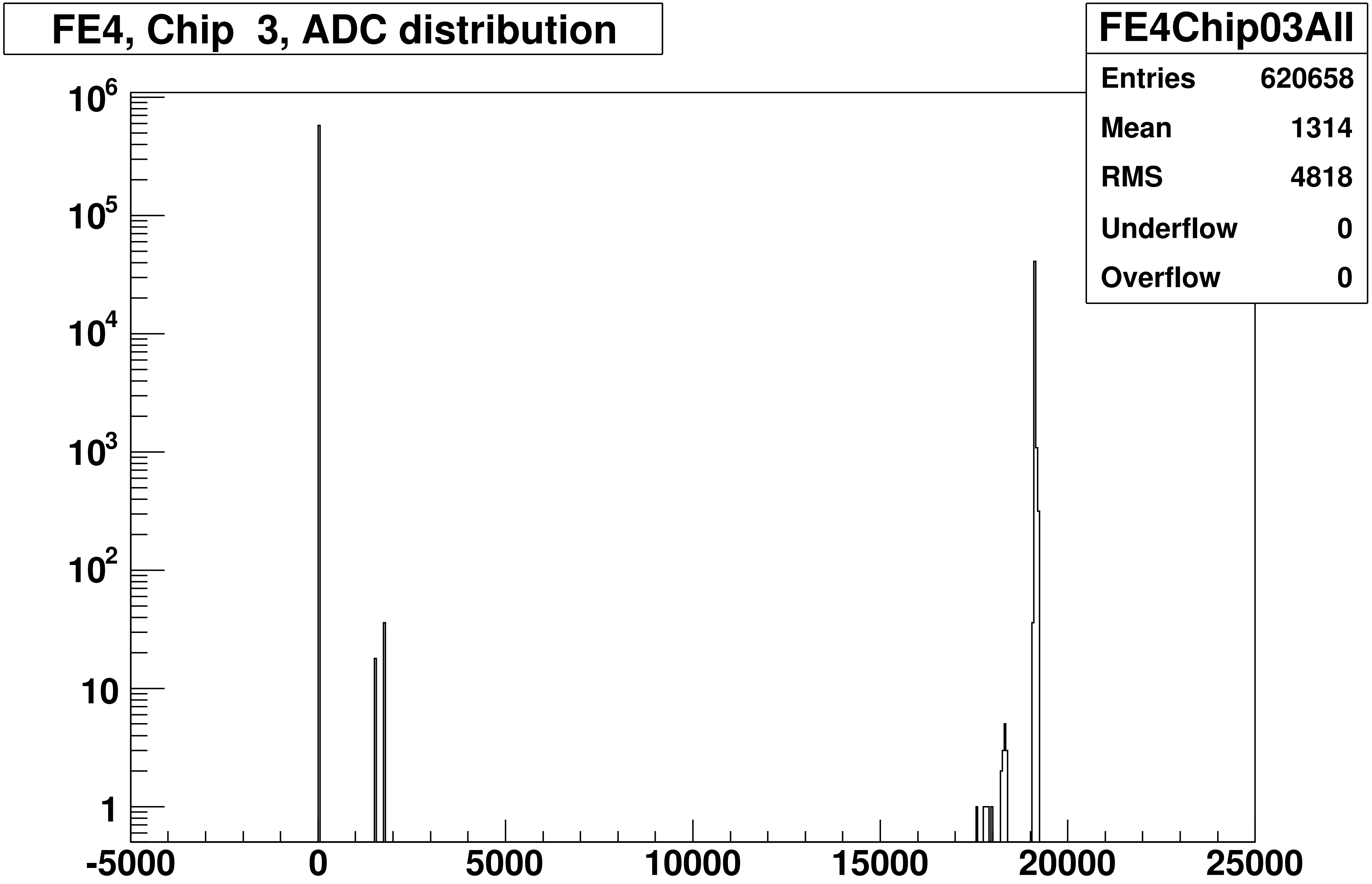


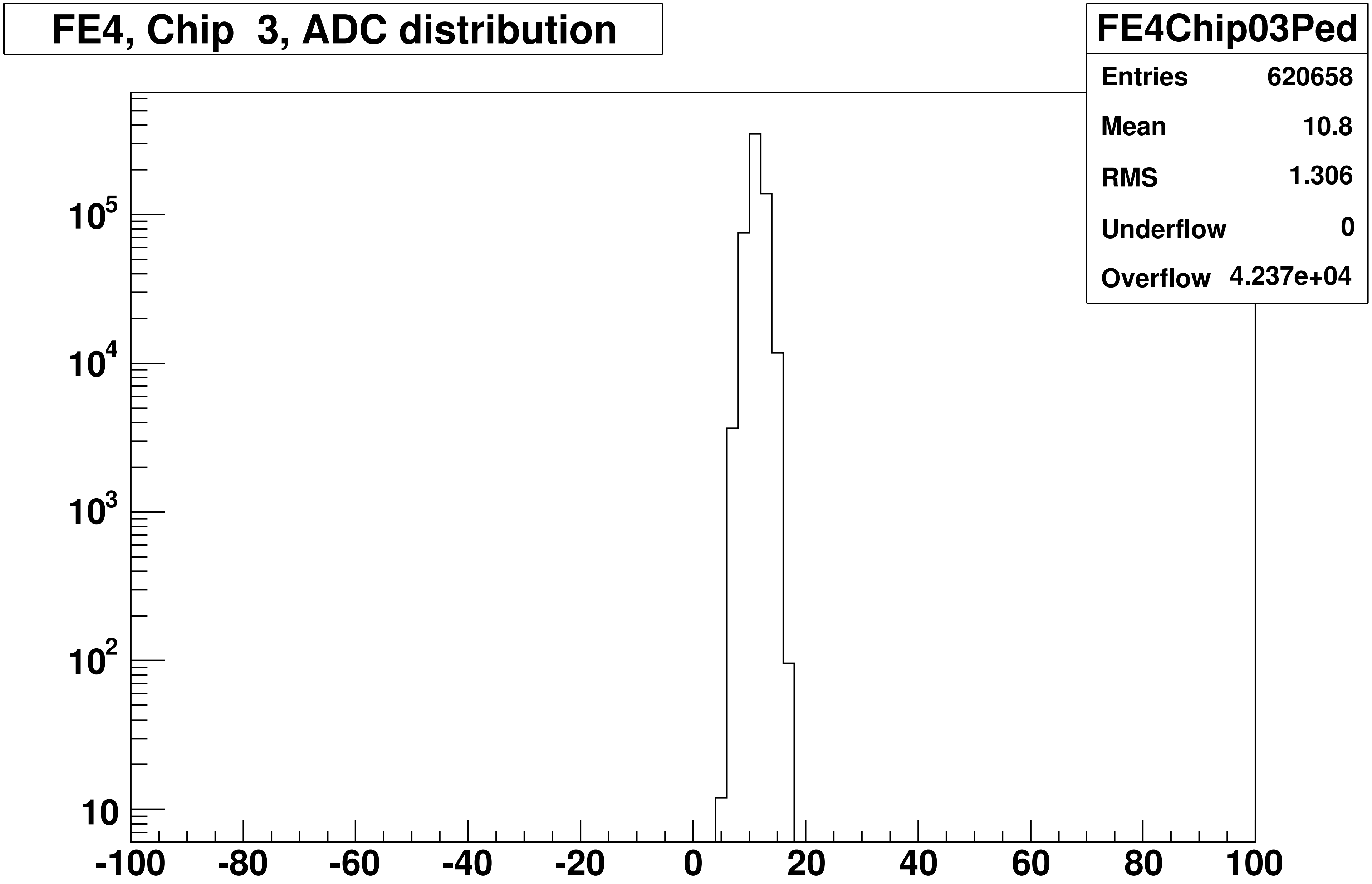
FE4, Chip 2, ADC distribution
FE4Chip02All
Entries 620658
Mean 1308
RMS 4820
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



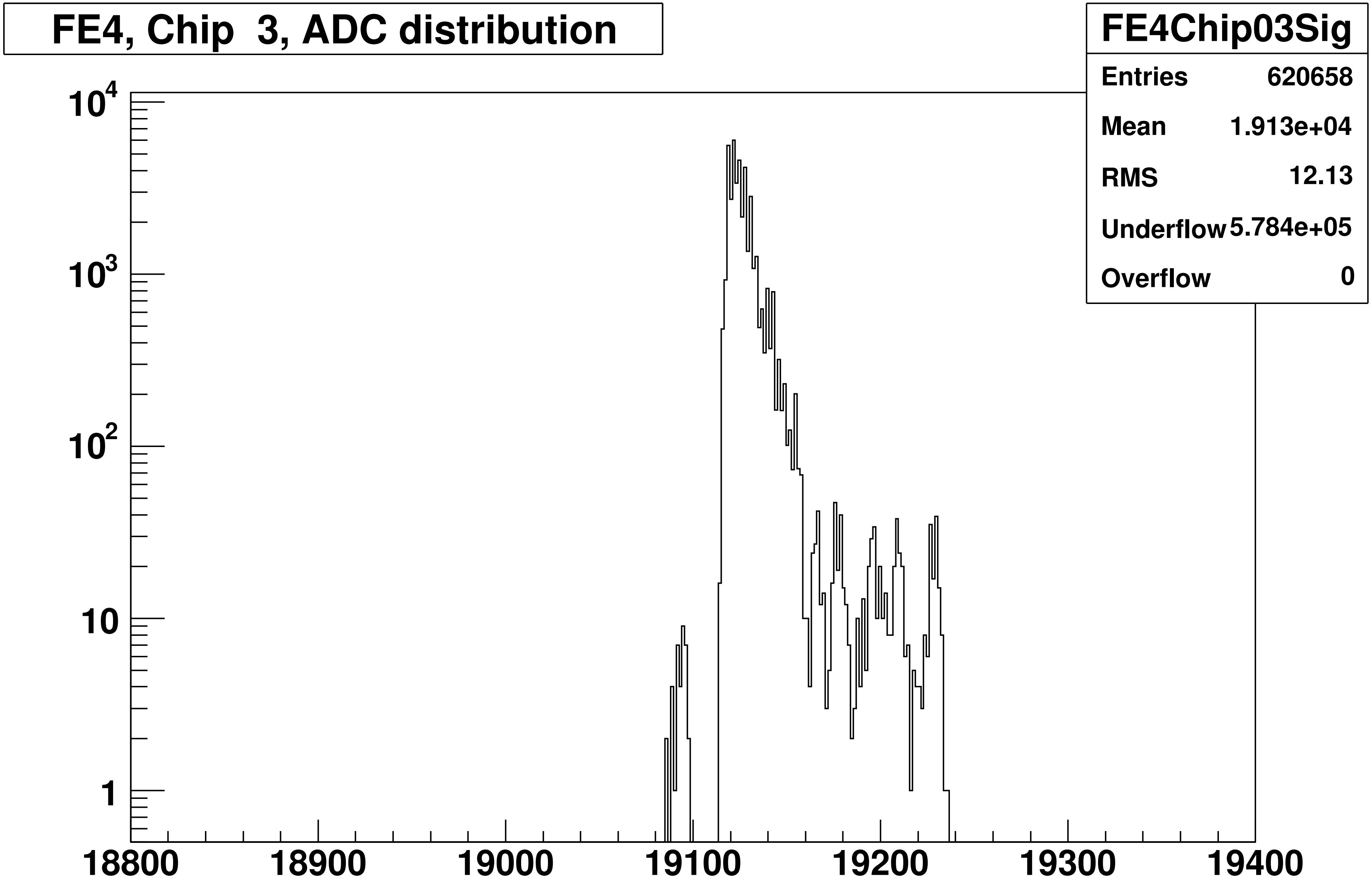
FE4, Chip 2, ADC distribution
FE4Chip02Ped
Entries 620658
Mean 6.064
RMS 1.305
Underflow 0
Overflow 4.234e+04
10^5
10^4
10^3
10^2
10
1
-100
-80
-60
-40
-20
0
20
40
60
80
100

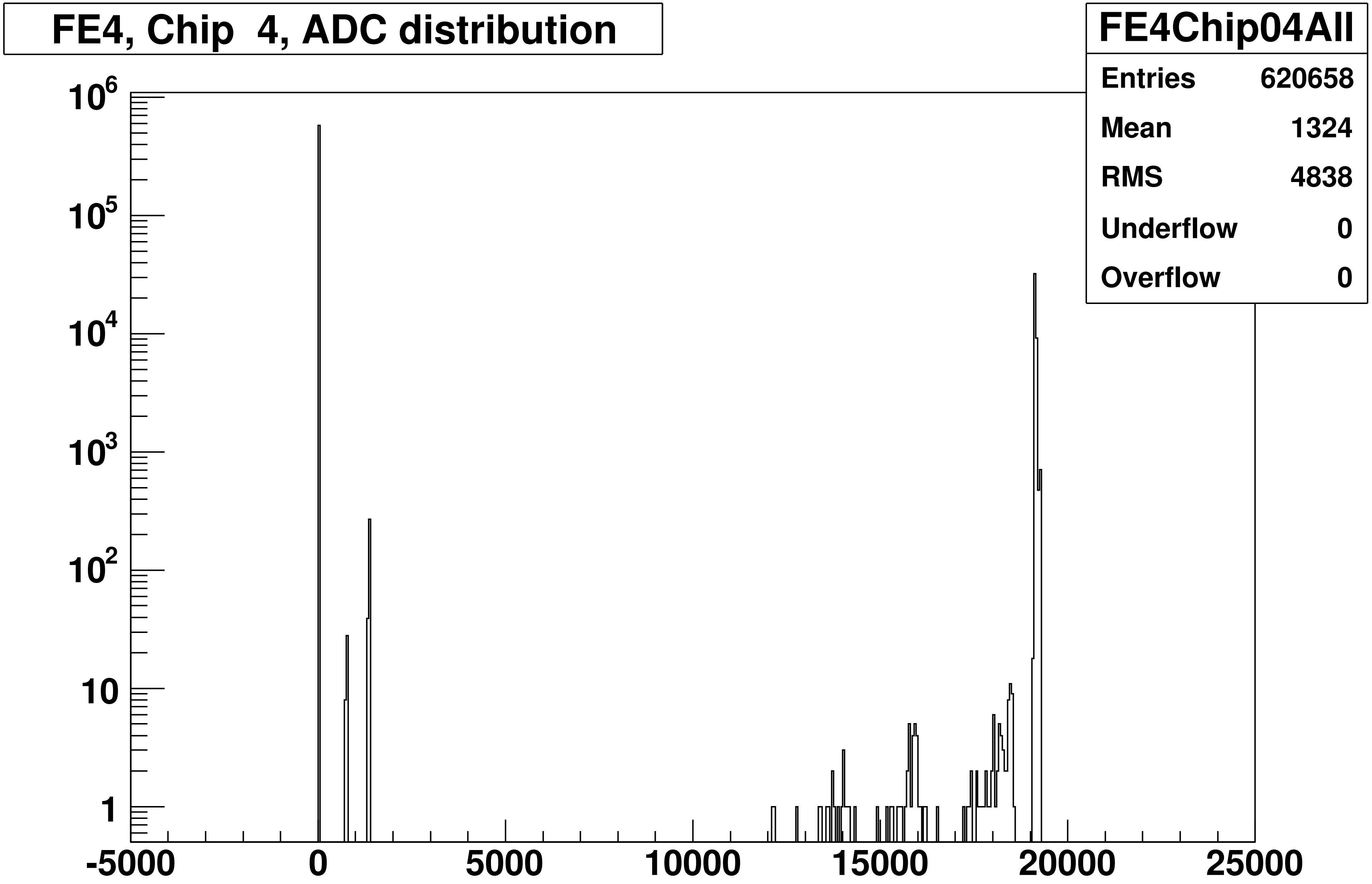

FE4, Chip 2, ADC distribution
FE4Chip02Sig
Entries 620658
Mean 1.916e+04
RMS 11.08
Underflow 5.785e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 3, ADC distribution
FE4Chip03All
Entries 620658
Mean 1314
RMS 4818
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 3, ADC distribution
FE4Chip03Ped
Entries 620658
Mean 10.8
RMS 1.306
Underflow 0
Overflow 4.237e+04
10^5
10^4
10^3
10^2
10
-100
-80
-60
-40
-20
0
20
40
60
80
100


FE4, Chip 3, ADC distribution
FE4Chip03Sig
Entries 620658
Mean 1.913e+04
RMS 12.13
Underflow 5.784e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400



FE4, Chip 4, ADC distribution
FE4Chip04All
Entries 620658
Mean 1324
RMS 4838
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
-5000
0
5000
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15000
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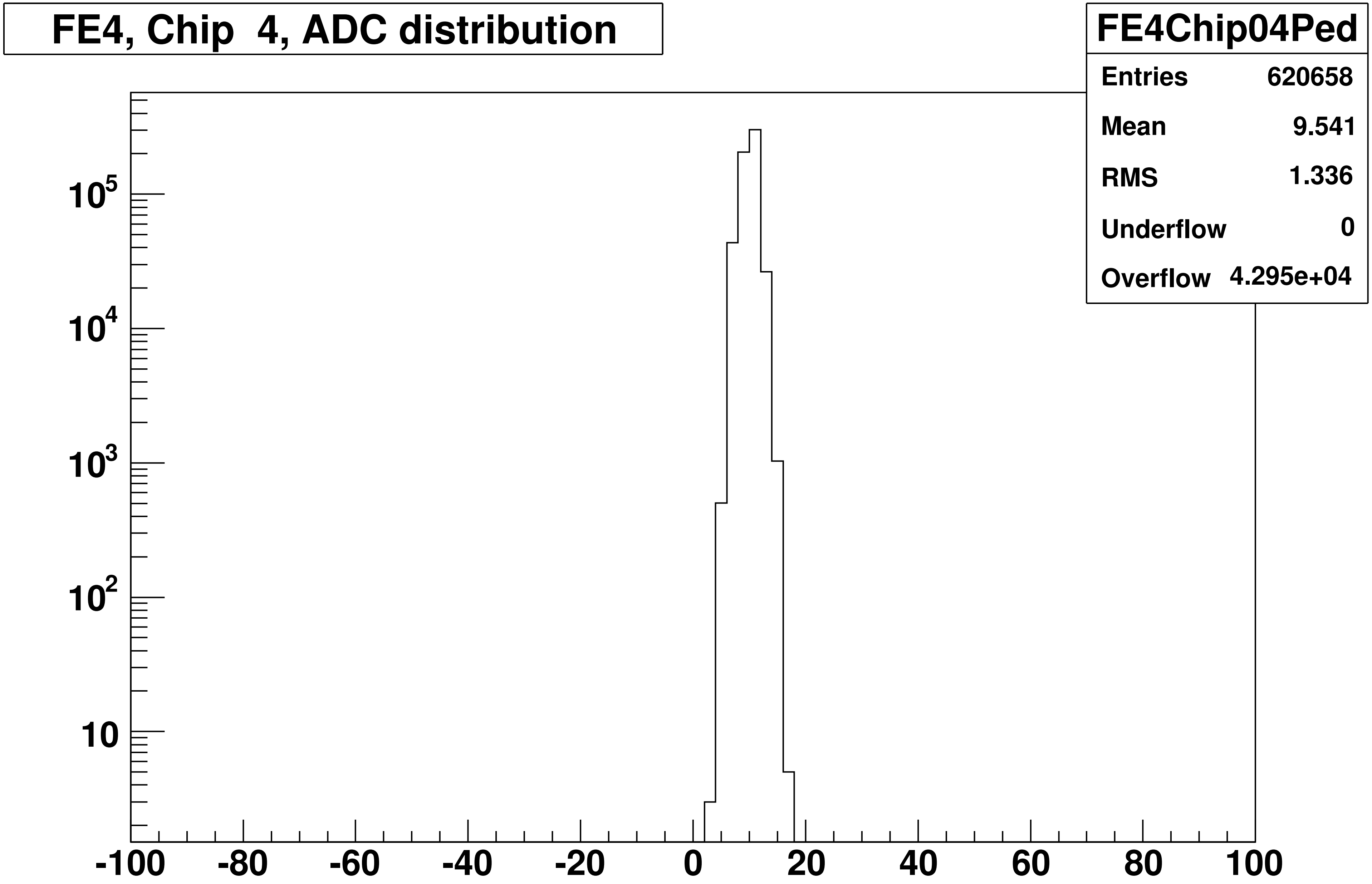
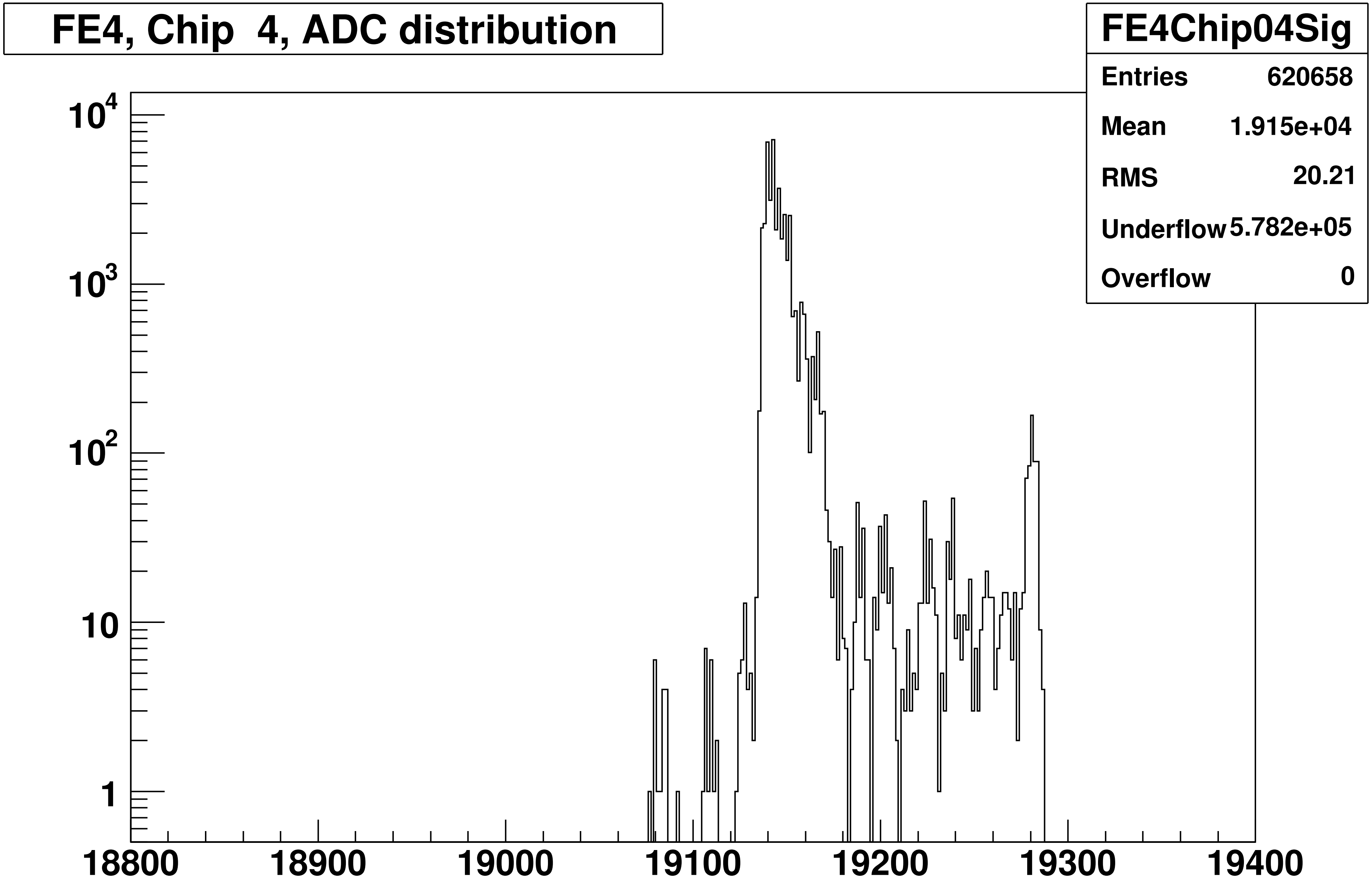

FE4, Chip 4, ADC distribution
FE4Chip04Ped
Entries 620658
Mean 9.541
RMS 1.336
Underflow 0
Overflow 4.295e+04
10
10^2
10^3
10^4
10^5
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 4, ADC distribution
FE4Chip04Sig
Entries 620658
Mean 1.915e+04
RMS 20.21
Underflow 5.782e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
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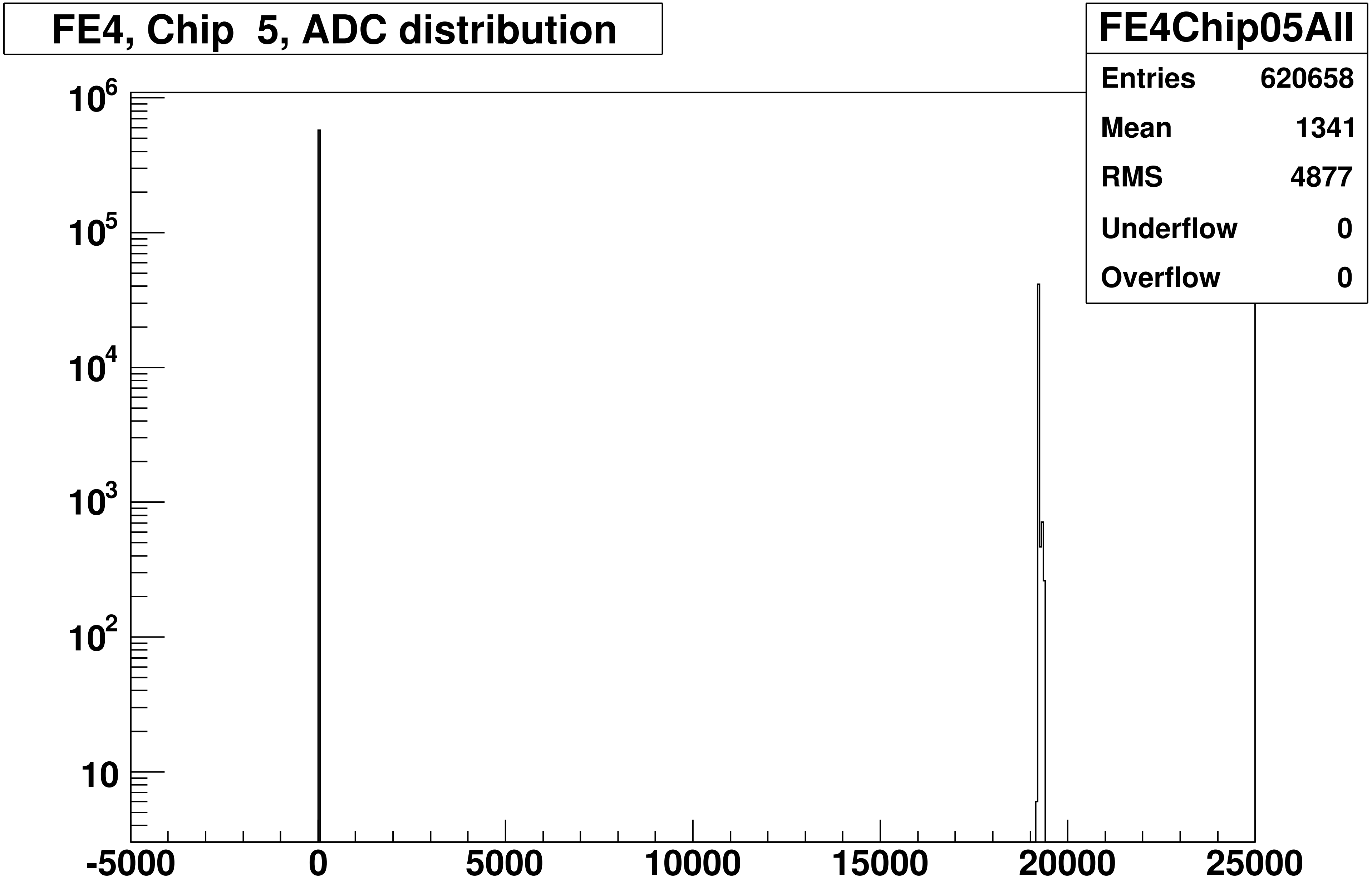
19400



FE4, Chip 5, ADC distribution
FE4Chip05All
Entries 620658
Mean 1341
RMS 4877
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
-5000
0
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10000
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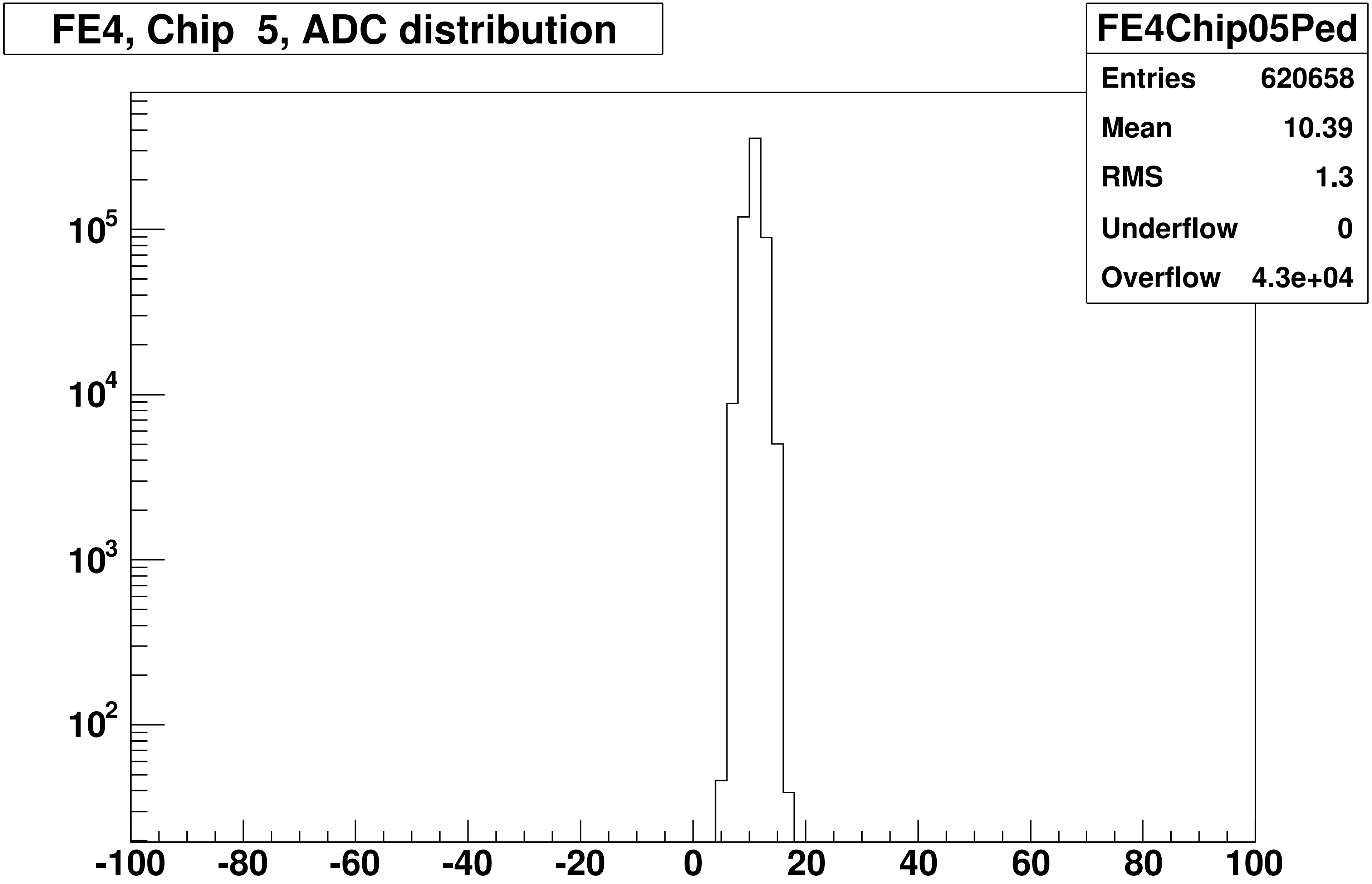
25000


FE4, Chip 5, ADC distribution
FE4Chip05Ped
Entries 620658
Mean 10.39
RMS 1.3
Underflow 0
Overflow 4.3e+04
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10^3
10^2
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-60
-40
-20
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80
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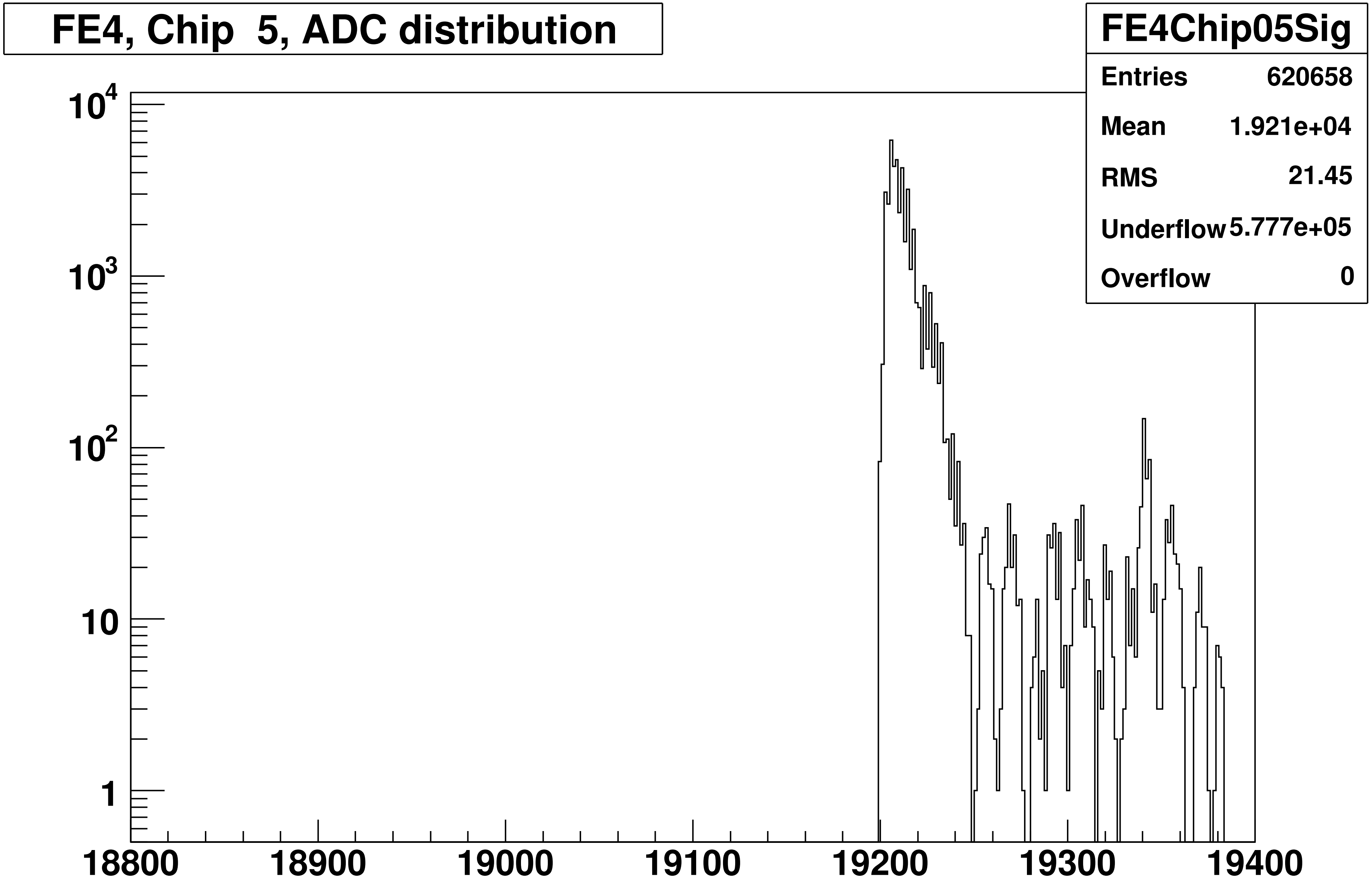
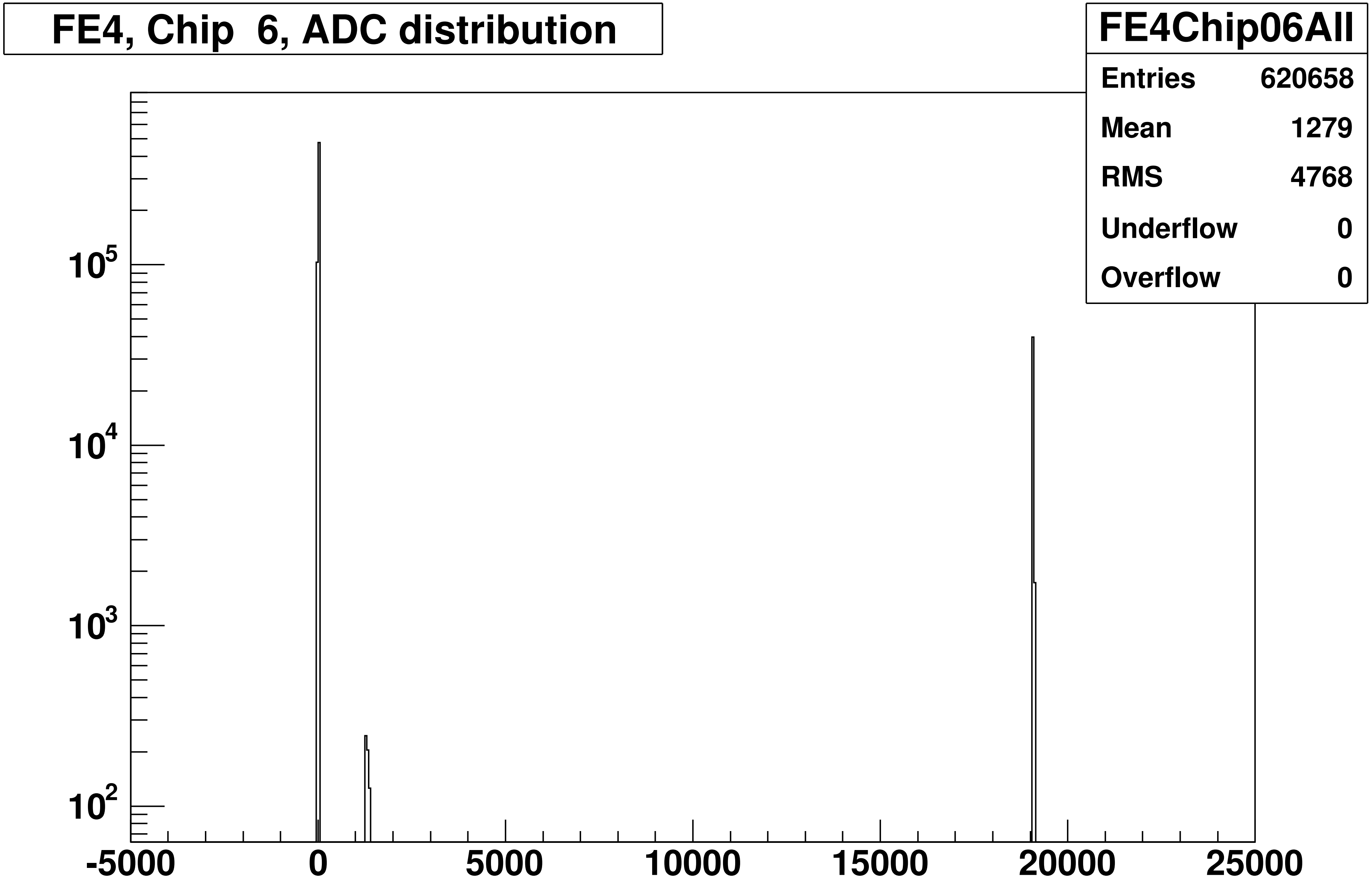

FE4, Chip 5, ADC distribution
FE4Chip05Sig
Entries 620658
Mean 1.921e+04
RMS 21.45
Underflow 5.777e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE4, Chip 6, ADC distribution
FE4Chip06All
Entries 620658
Mean 1279
RMS 4768
Underflow 0
Overflow 0
10^2
10^3
10^4
10^5
-5000
0
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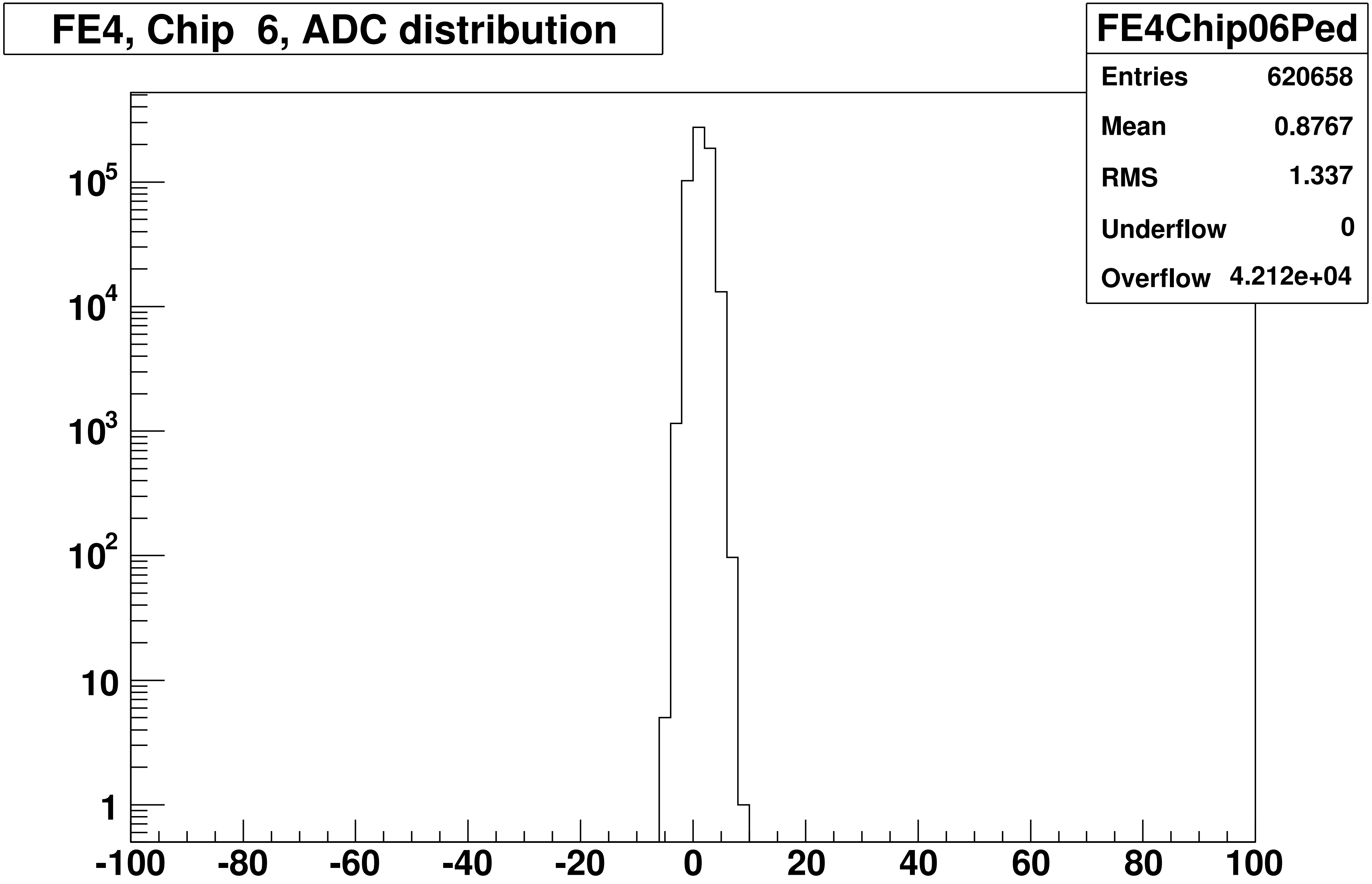
10000
15000
20000
25000


FE4, Chip 6, ADC distribution
FE4Chip06Ped
Entries 620658
Mean 0.8767
RMS 1.337
Underflow 0
Overflow 4.212e+04
10^5
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10^2
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1
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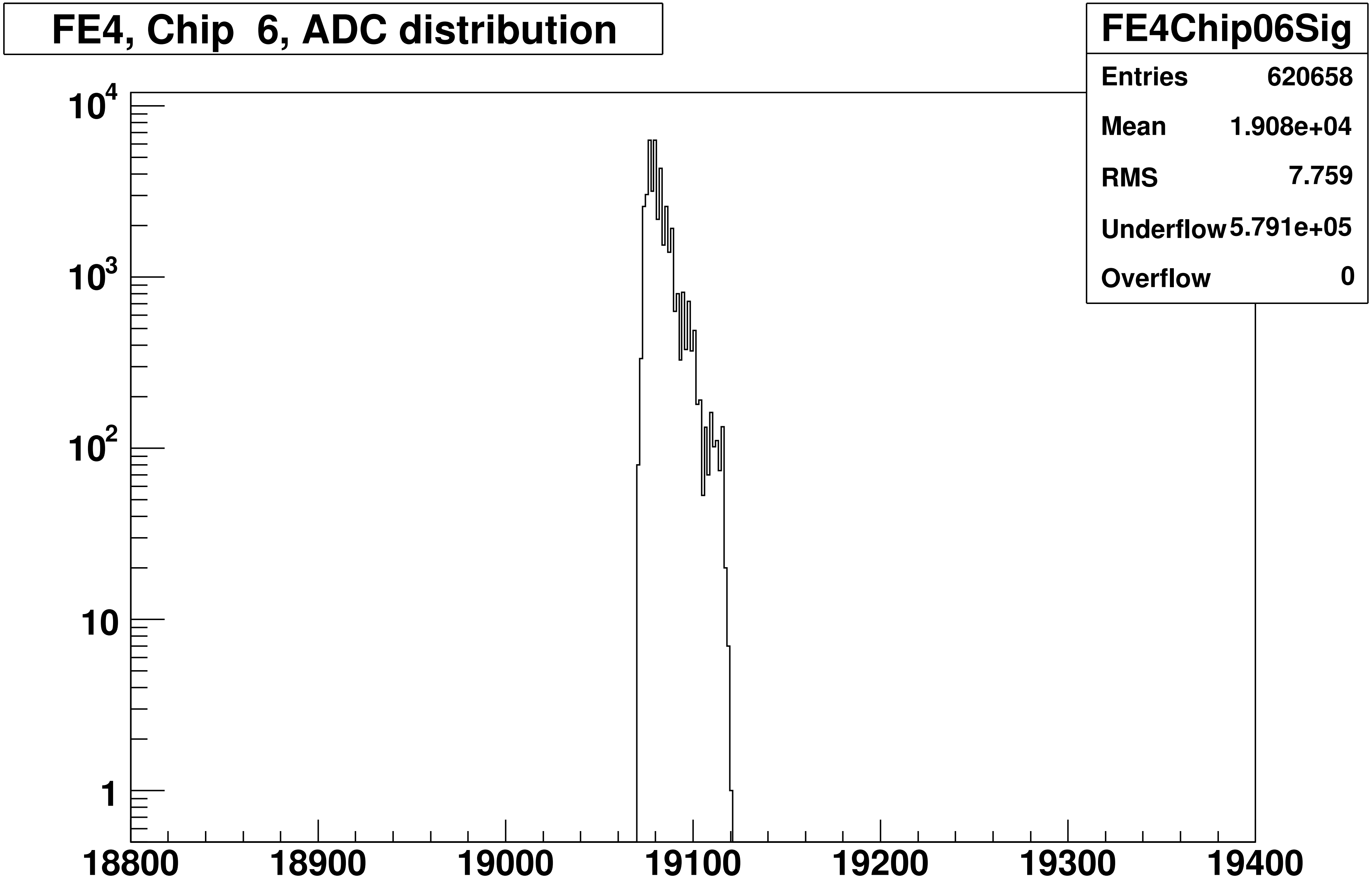
FE4, Chip 6, ADC distribution
FE4Chip06Sig
Entries 620658
Mean 1.908e+04
RMS 7.759
Underflow 5.791e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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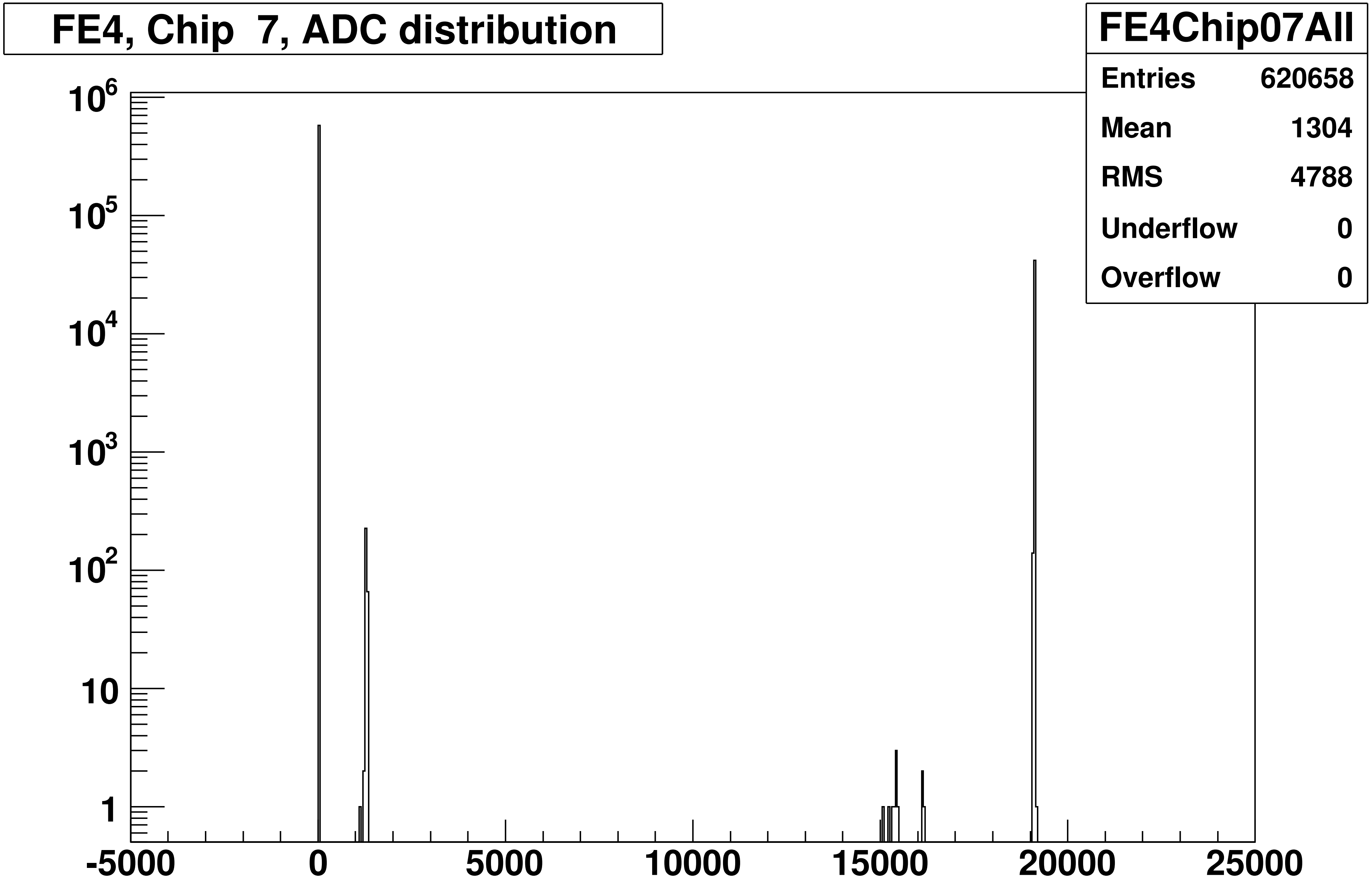
19400


FE4, Chip 7, ADC distribution
FE4Chip07All
Entries 620658
Mean 1304
RMS 4788
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
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0
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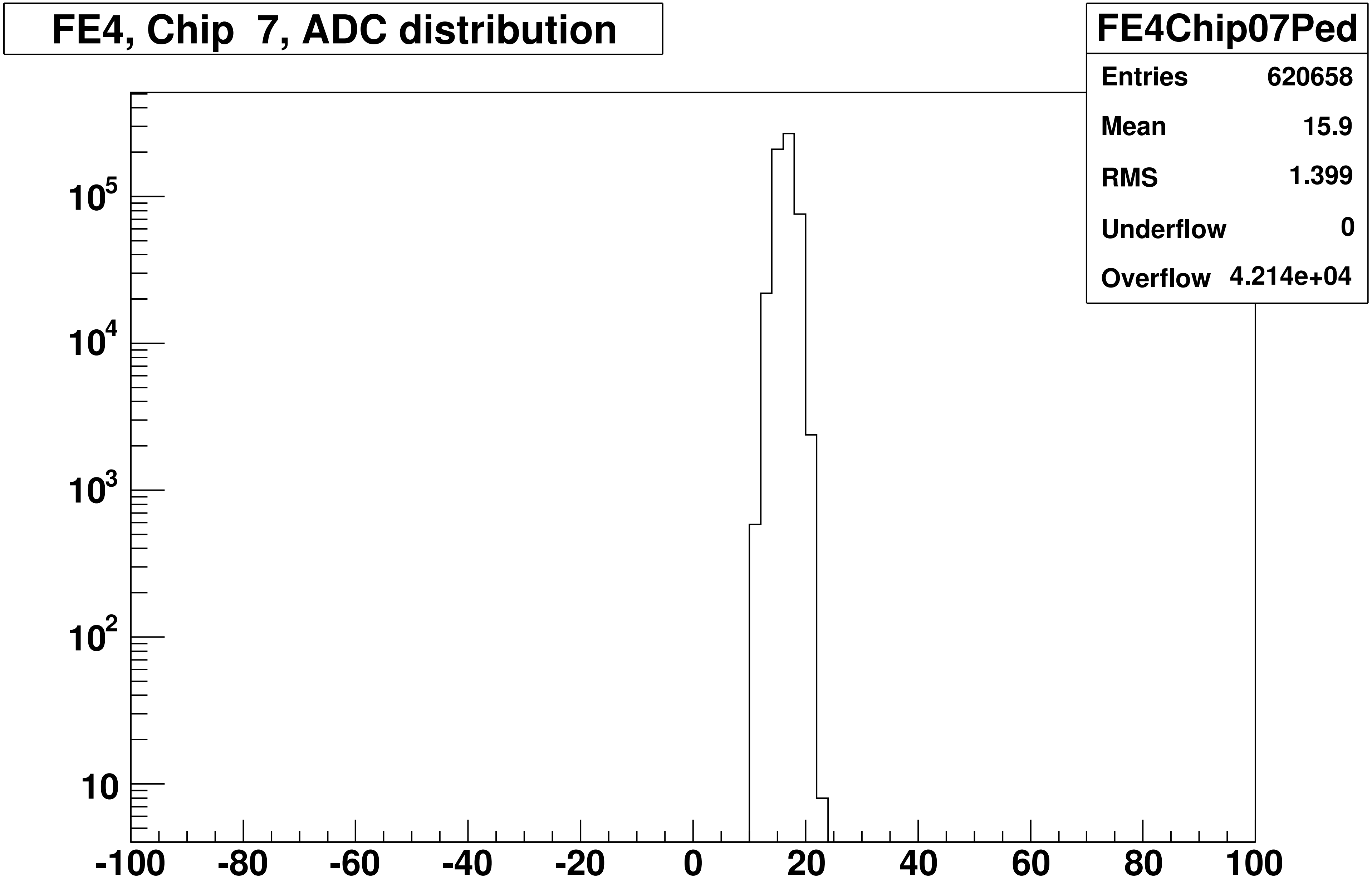
20000
25000



FE4, Chip 7, ADC distribution
FE4Chip07Ped
Entries 620658
Mean 15.9
RMS 1.399
Underflow 0
Overflow 4.214e+04
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10^4
10^3
10^2
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-100
-80
-60
-40
-20
0
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80
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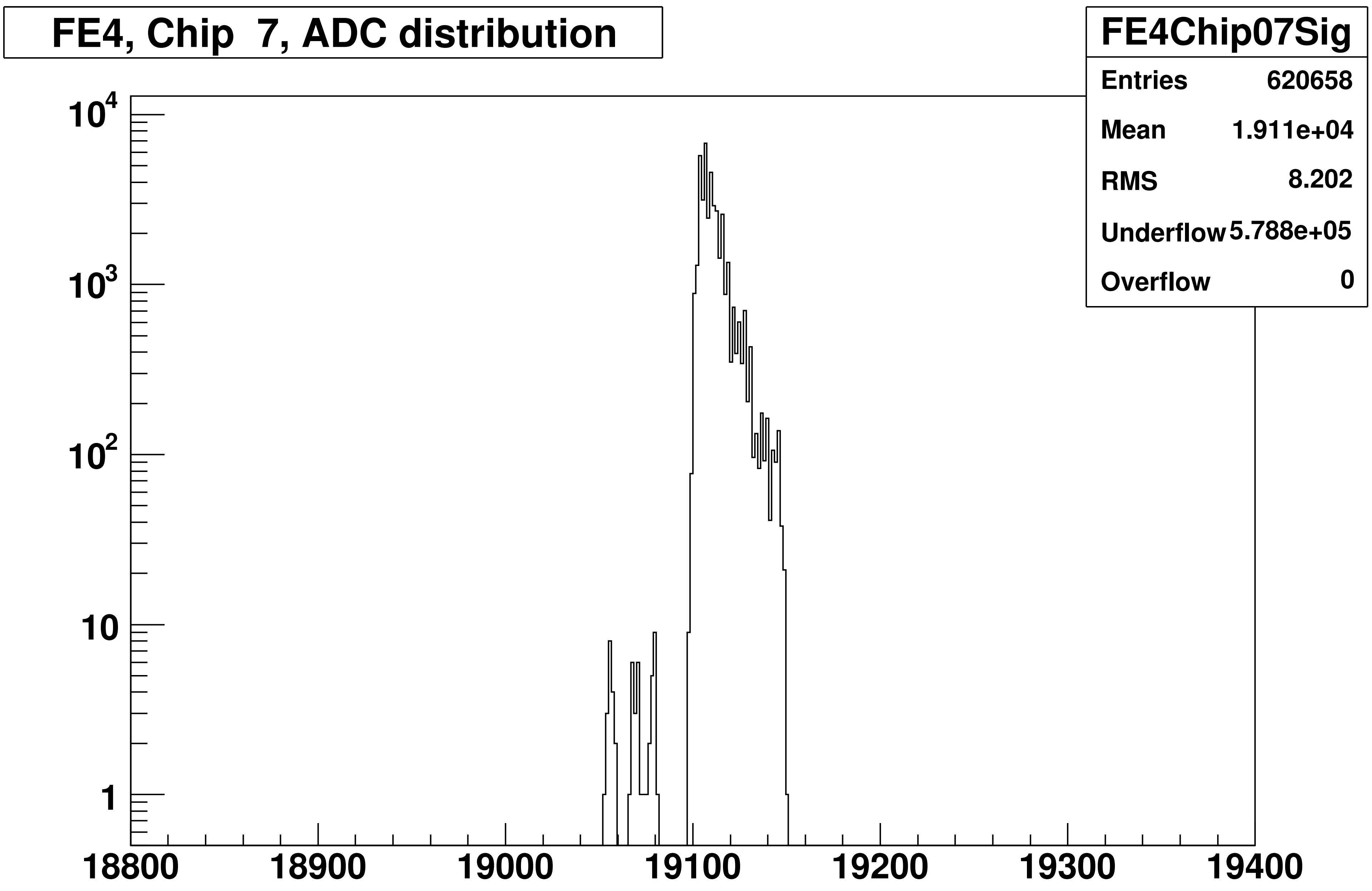
FE4, Chip 7, ADC distribution
FE4Chip07Sig
Entries 620658
Mean 1.911e+04
RMS 8.202
Underflow 5.788e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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19200
19300
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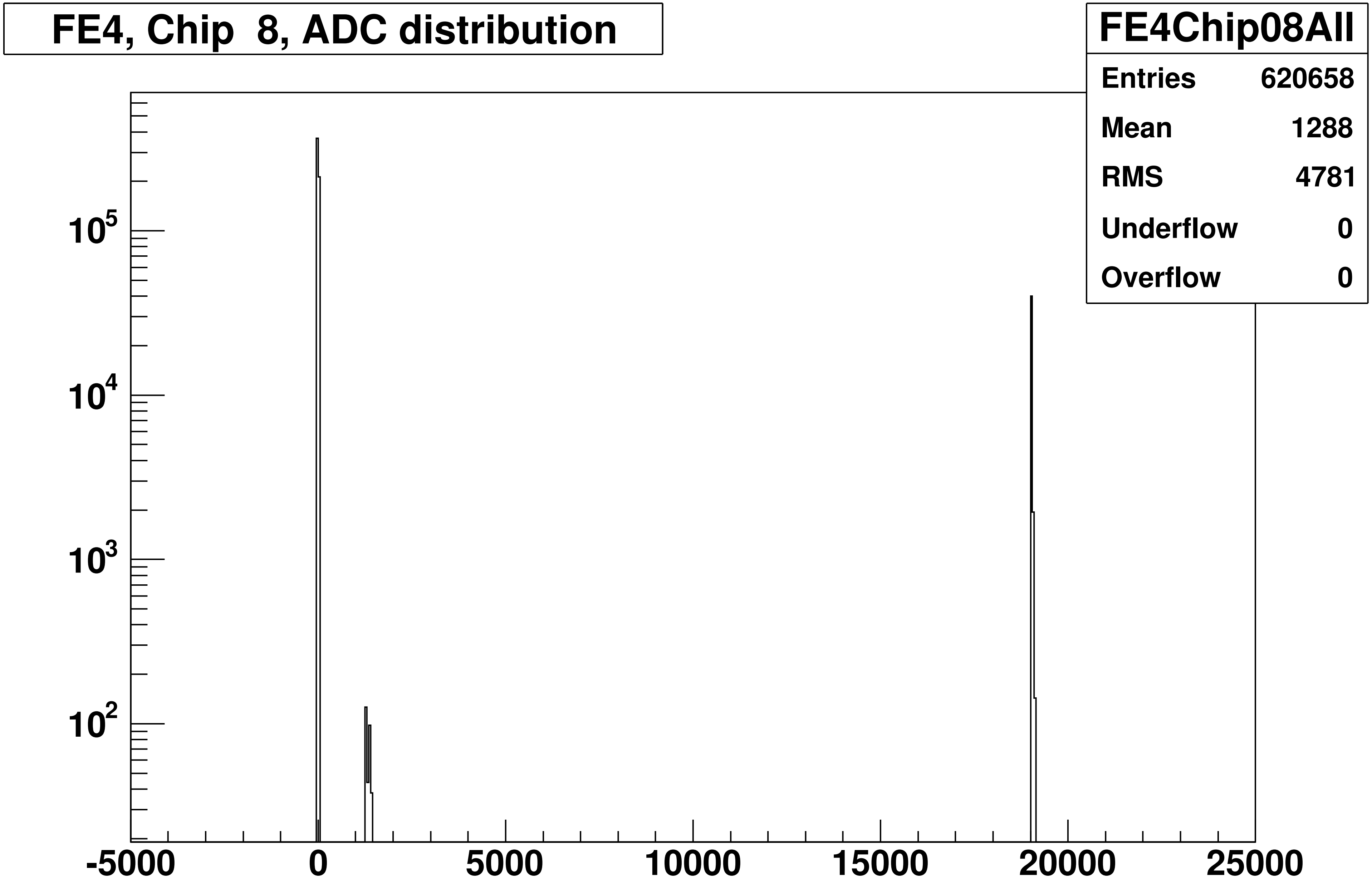
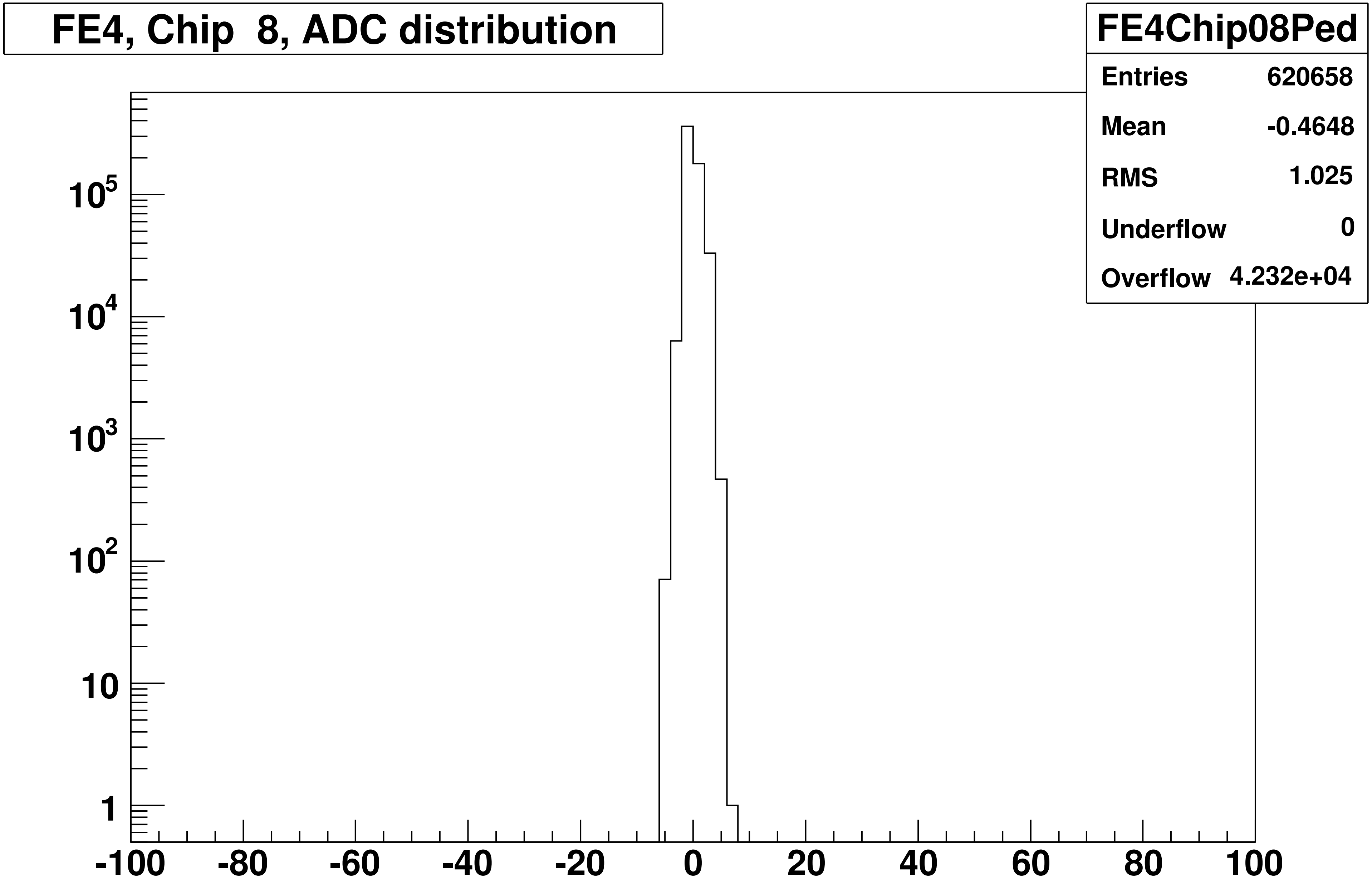

FE4, Chip 8, ADC distribution
FE4Chip08All
Entries 620658
Mean 1288
RMS 4781
Underflow 0
Overflow 0
10^5
10^4
10^3
10^2
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 8, ADC distribution
FE4Chip08Ped
Entries 620658
Mean -0.4648
RMS 1.025
Underflow 0
Overflow 4.232e+04
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10^4
10^3
10^2
10
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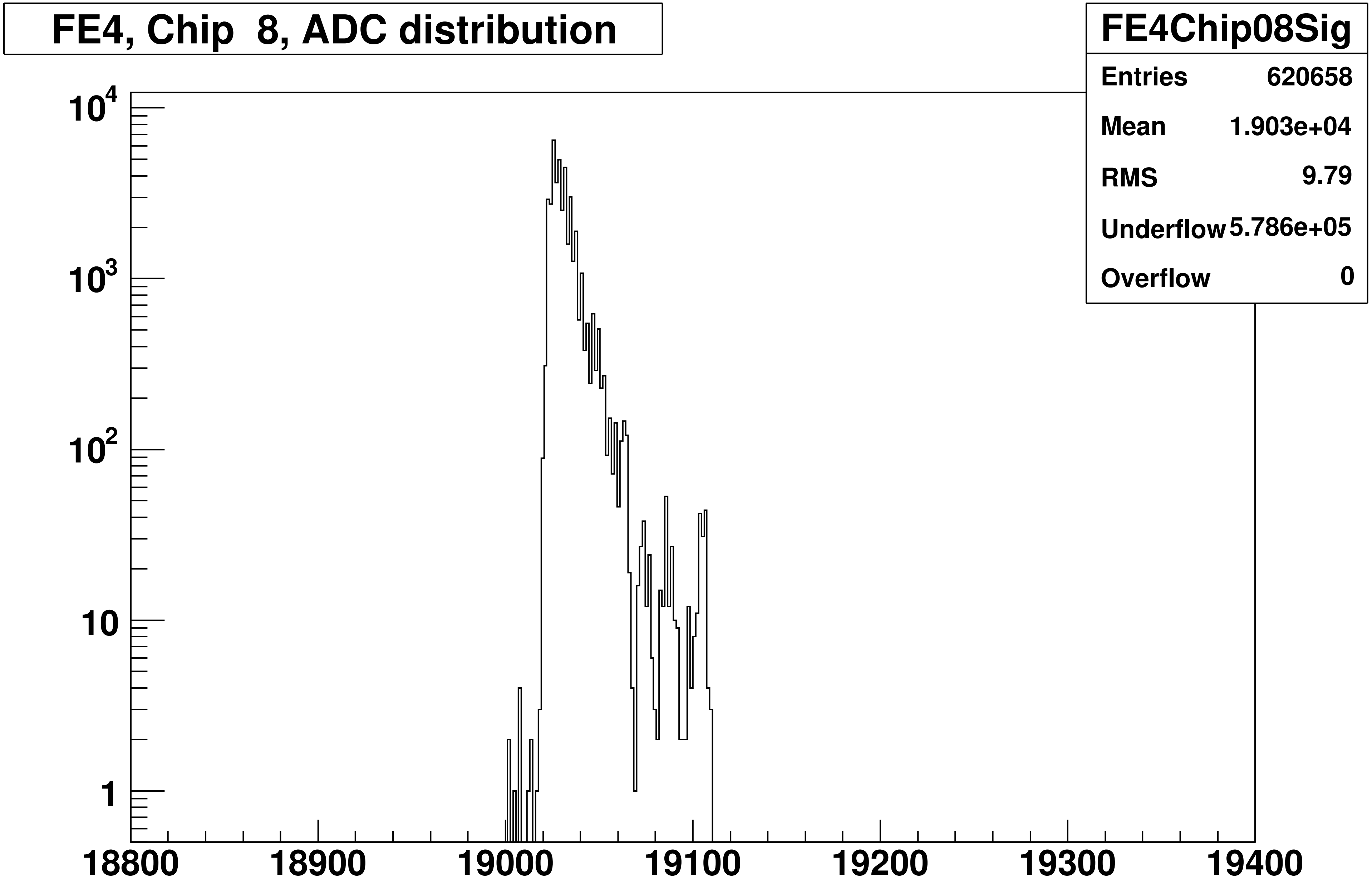
FE4, Chip 8, ADC distribution
FE4Chip08Sig
Entries 620658
Mean 1.903e+04
RMS 9.79
Underflow 5.786e+05
Overflow 0
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10^3
10^2
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18900
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19300
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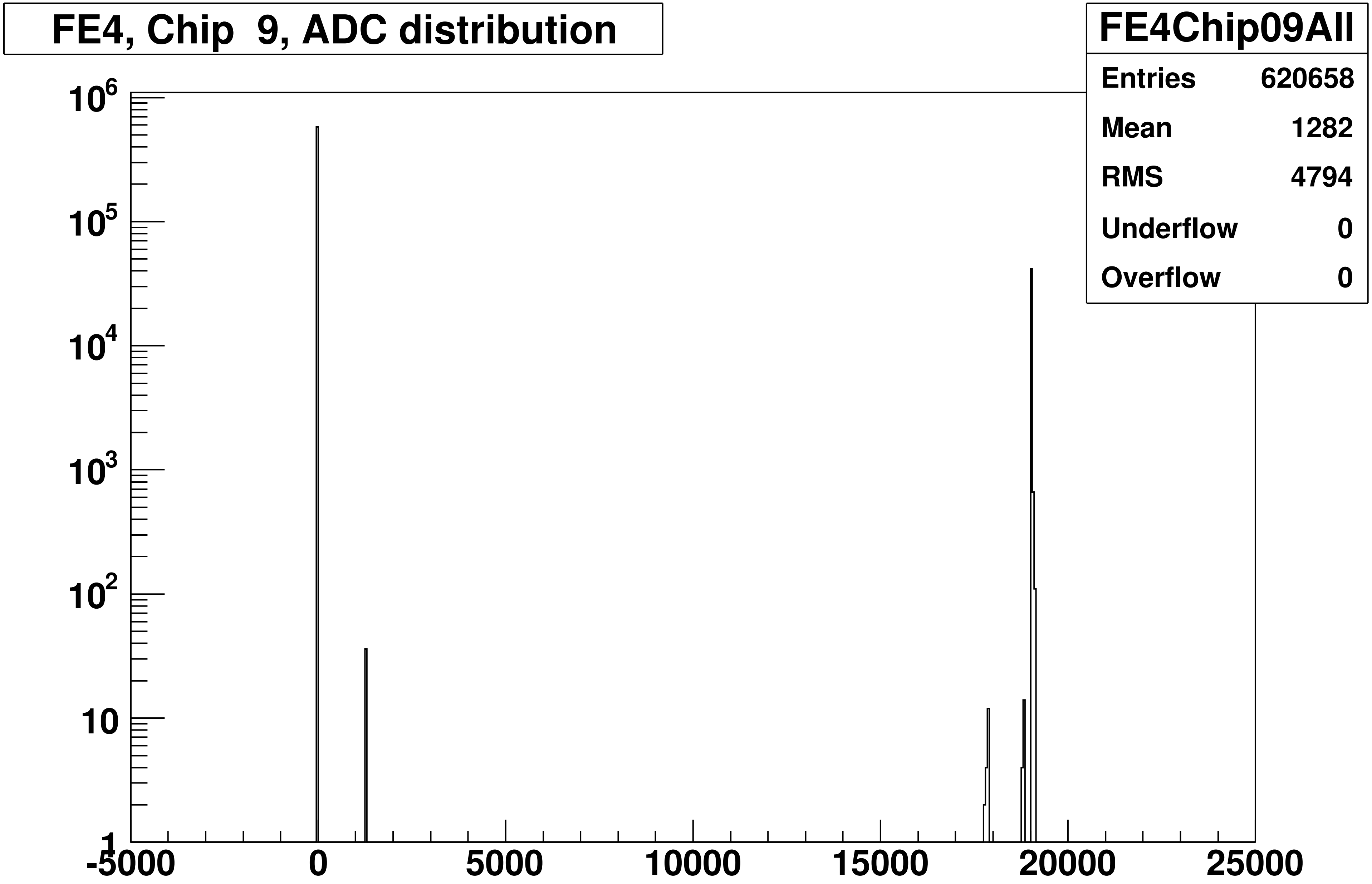

FE4, Chip 9, ADC distribution
FE4Chip09All
Entries 620658
Mean 1282
RMS 4794
Underflow 0
Overflow 0
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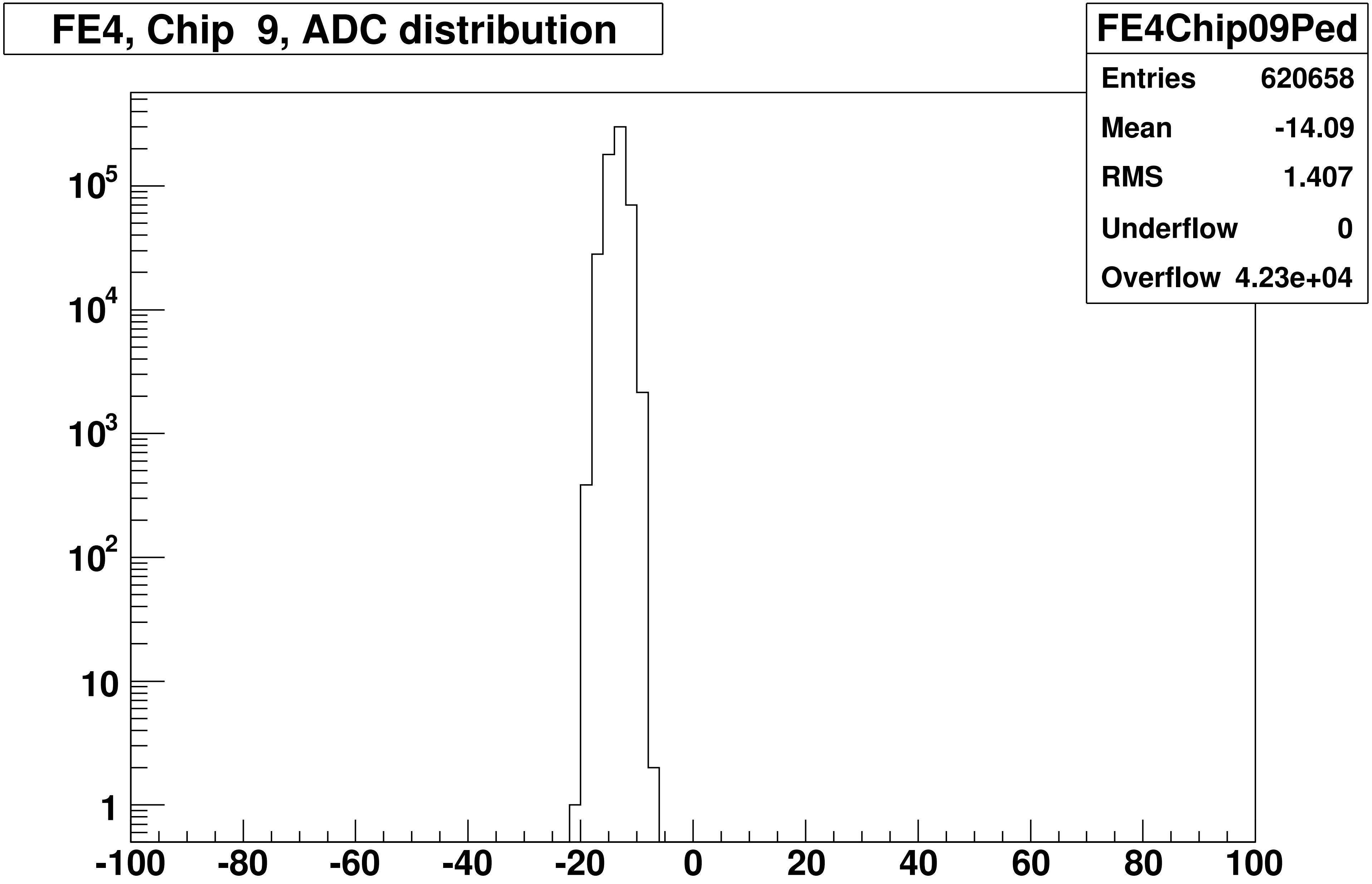
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE4, Chip 9, ADC distribution
FE4Chip09Ped
Entries 620658
Mean -14.09
RMS 1.407
Underflow 0
Overflow 4.23e+04
10^5
10^4
10^3
10^2
10
1
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-20
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40
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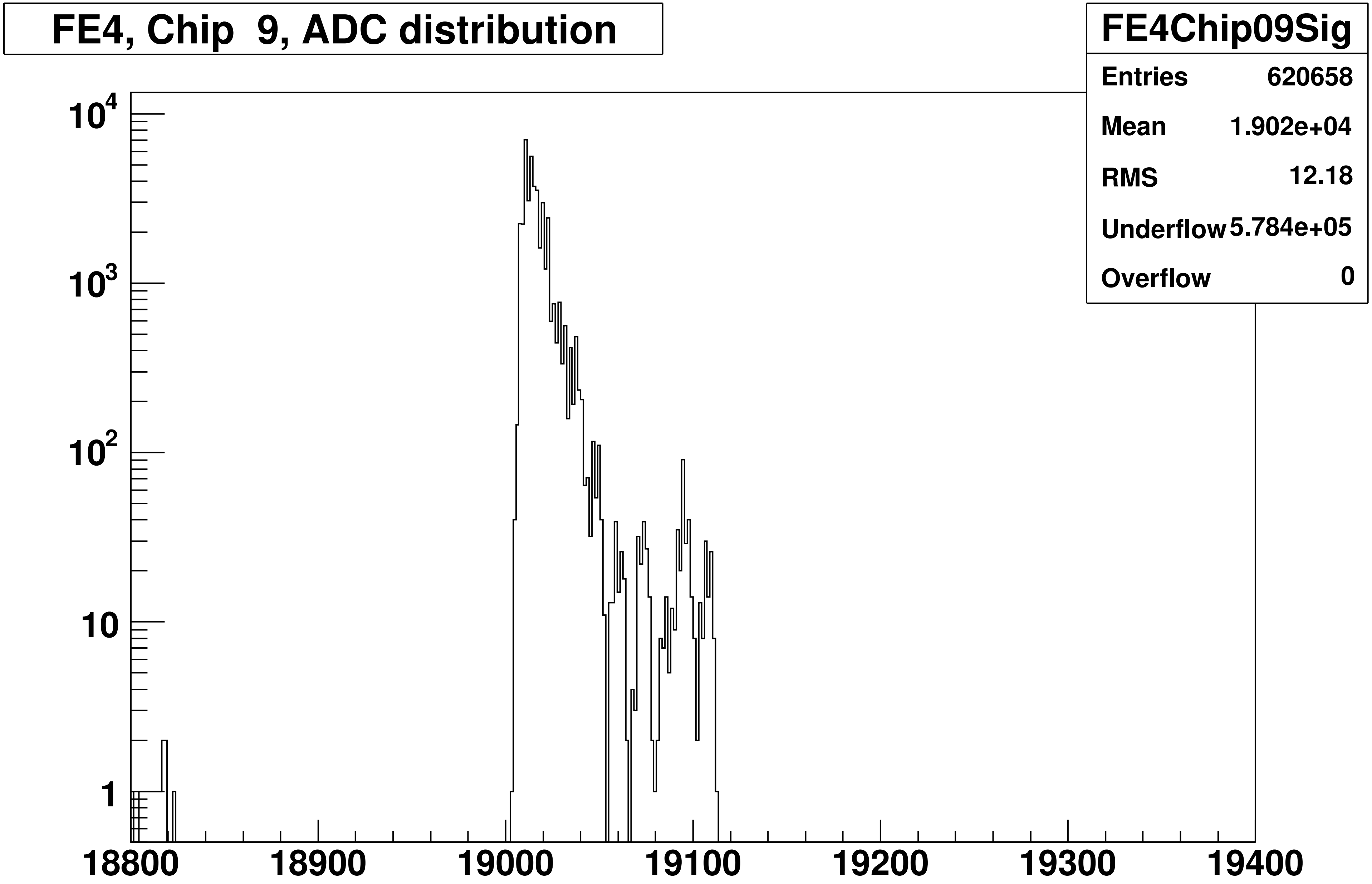
80
100


FE4, Chip 9, ADC distribution
FE4Chip09Sig
Entries 620658
Mean 1.902e+04
RMS 12.18
Underflow 5.784e+05
Overflow 0
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10^3
10^2
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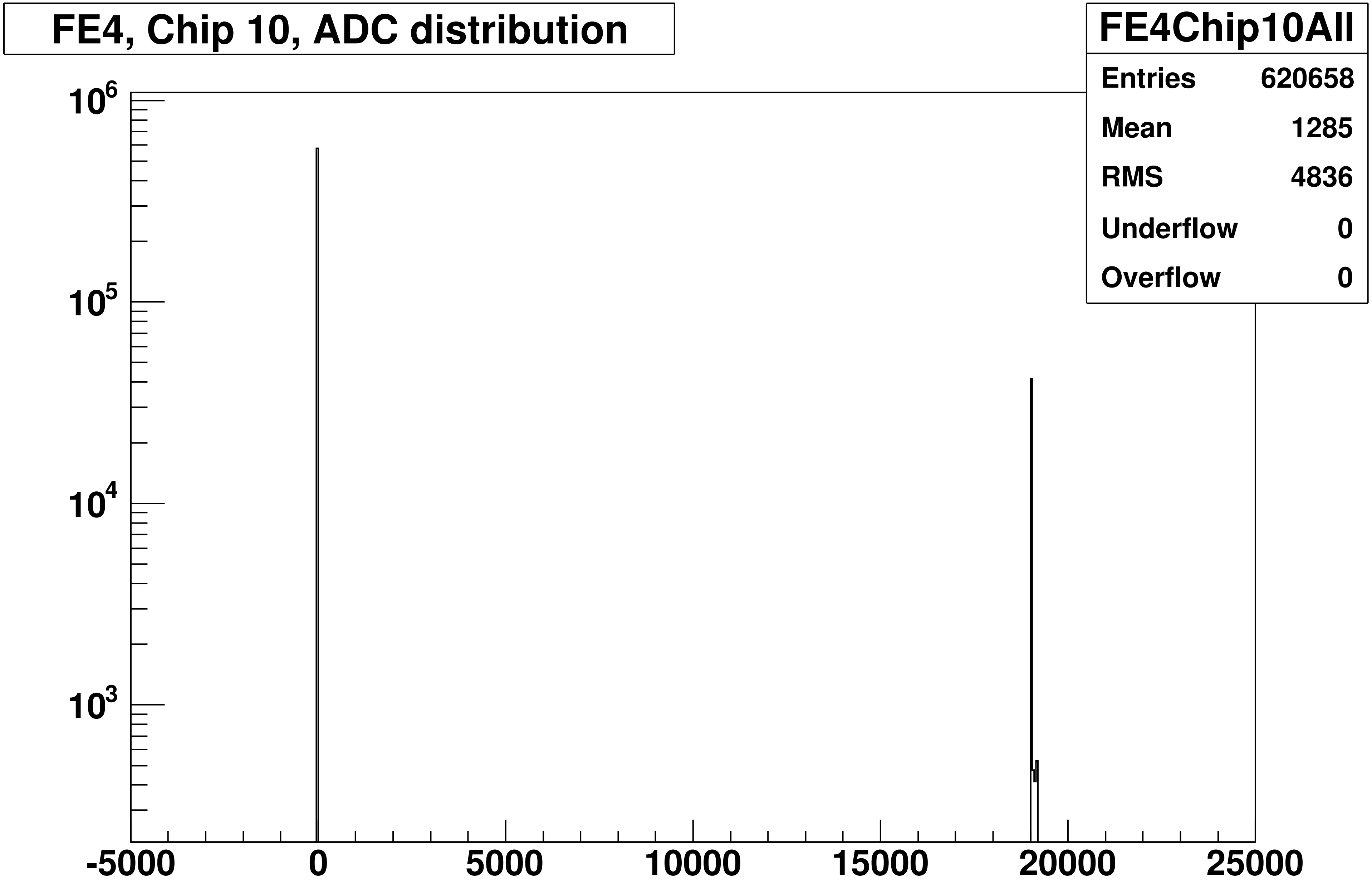

FE4, Chip 10, ADC distribution
FE4Chip10All
Entries 620658
Mean 1285
RMS 4836
Underflow 0
Overflow 0
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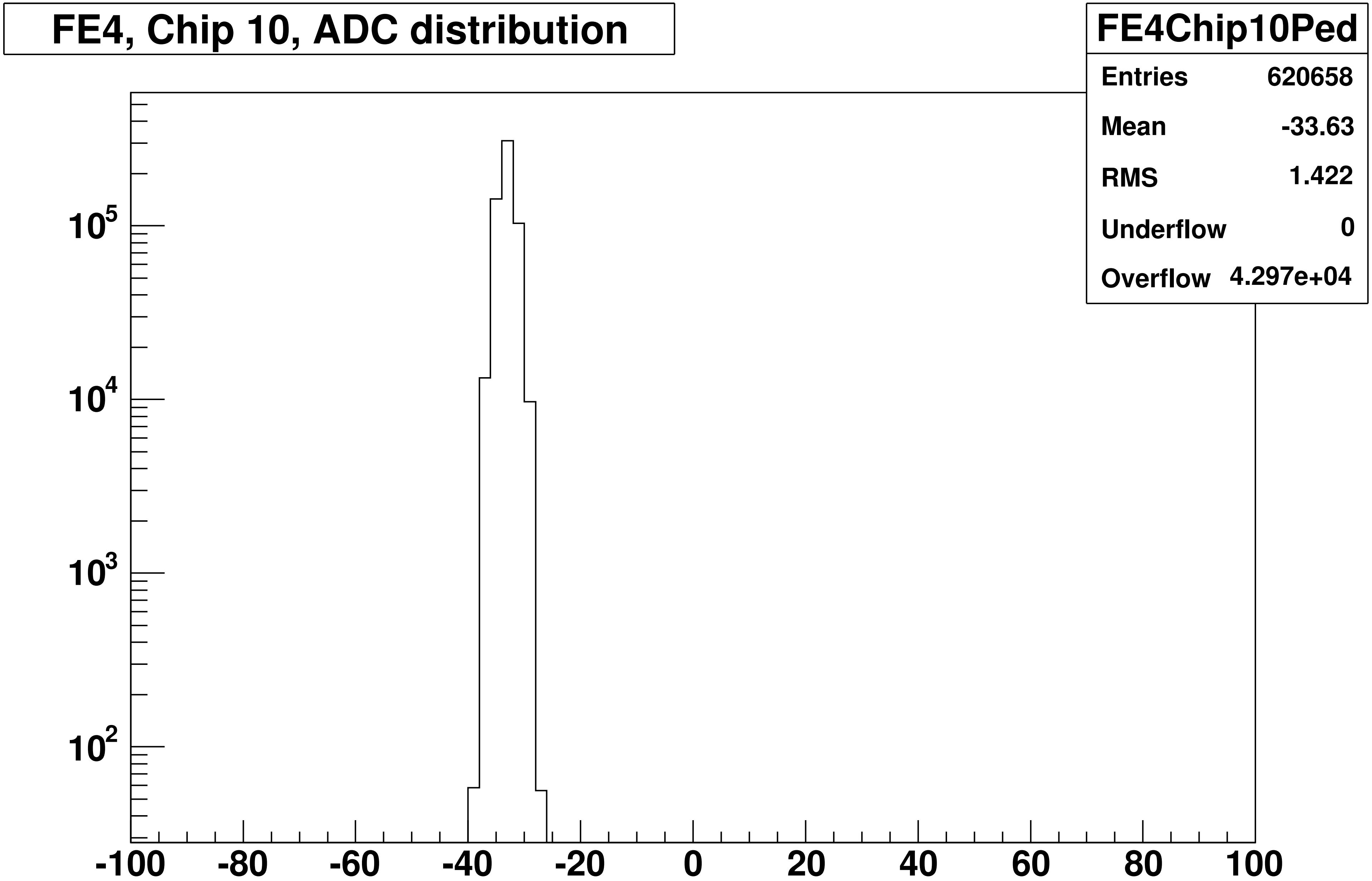
10^5
10^4
10^3
-5000
0
5000
10000
15000
20000
25000


FE4, Chip 10, ADC distribution
FE4Chip10Ped
Entries 620658
Mean -33.63
RMS 1.422
Underflow 0
Overflow 4.297e+04
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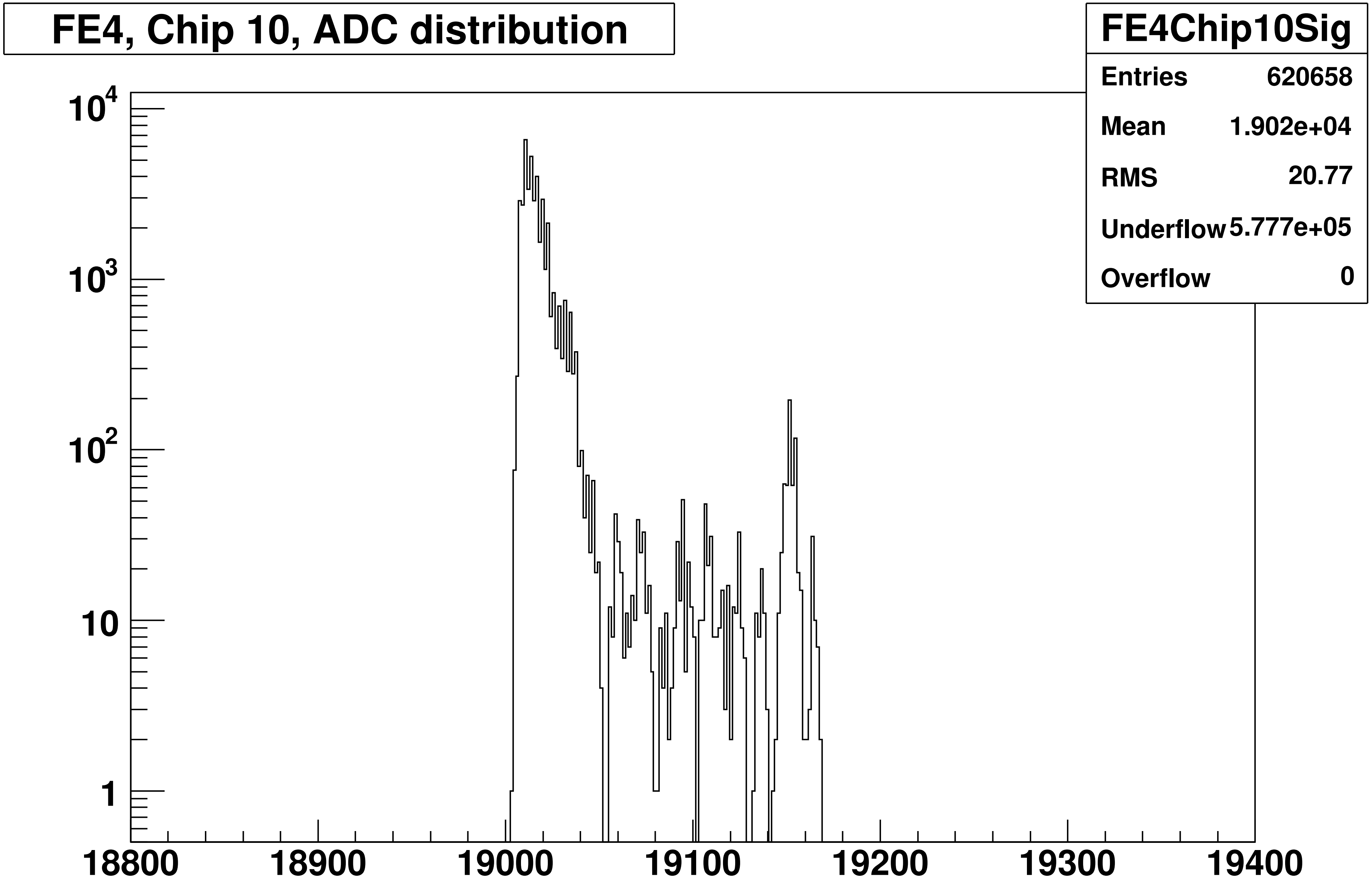
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE4, Chip 10, ADC distribution
FE4Chip10Sig
Entries 620658
Mean 1.902e+04
RMS 20.77
Underflow 5.777e+05
Overflow 0
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10^3
10^2
10
1
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18900
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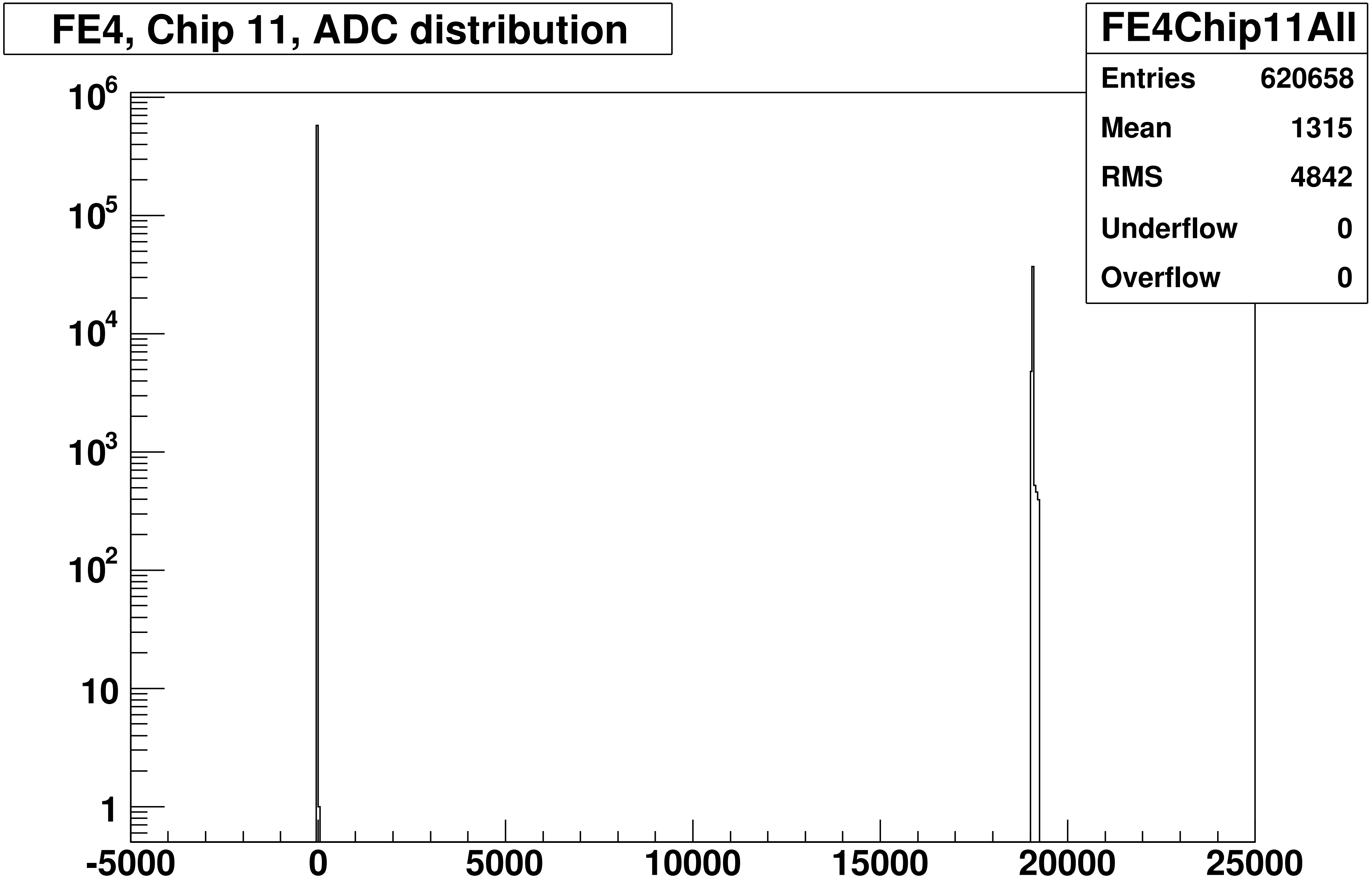
19100
19200
19300
19400


FE4, Chip 11, ADC distribution
FE4Chip11All
Entries 620658
Mean 1315
RMS 4842
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
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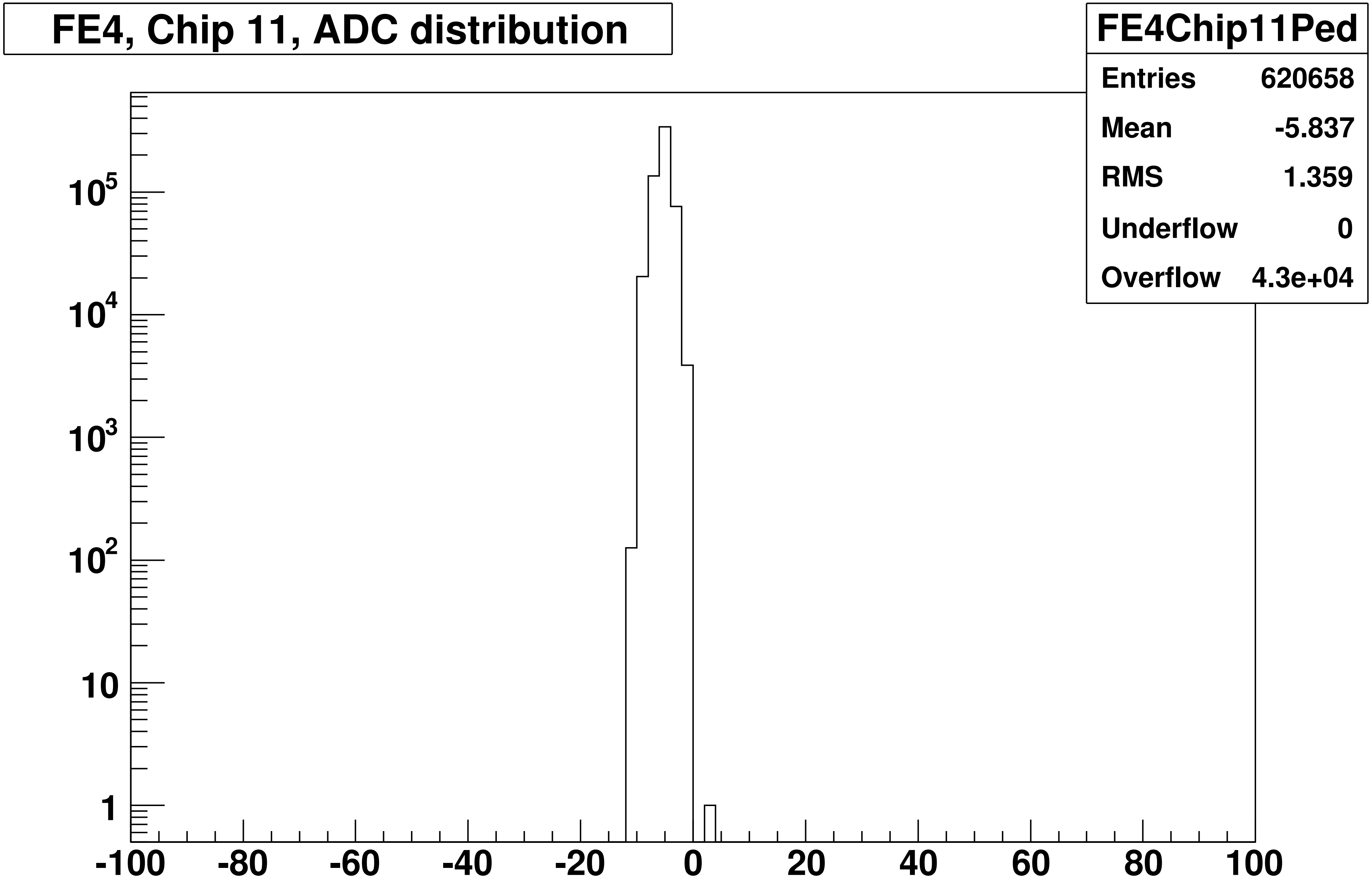
10000
15000
20000
25000


FE4, Chip 11, ADC distribution
FE4Chip11Ped
Entries 620658
Mean -5.837
RMS 1.359
Underflow 0
Overflow 4.3e+04
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10^3
10^2
10
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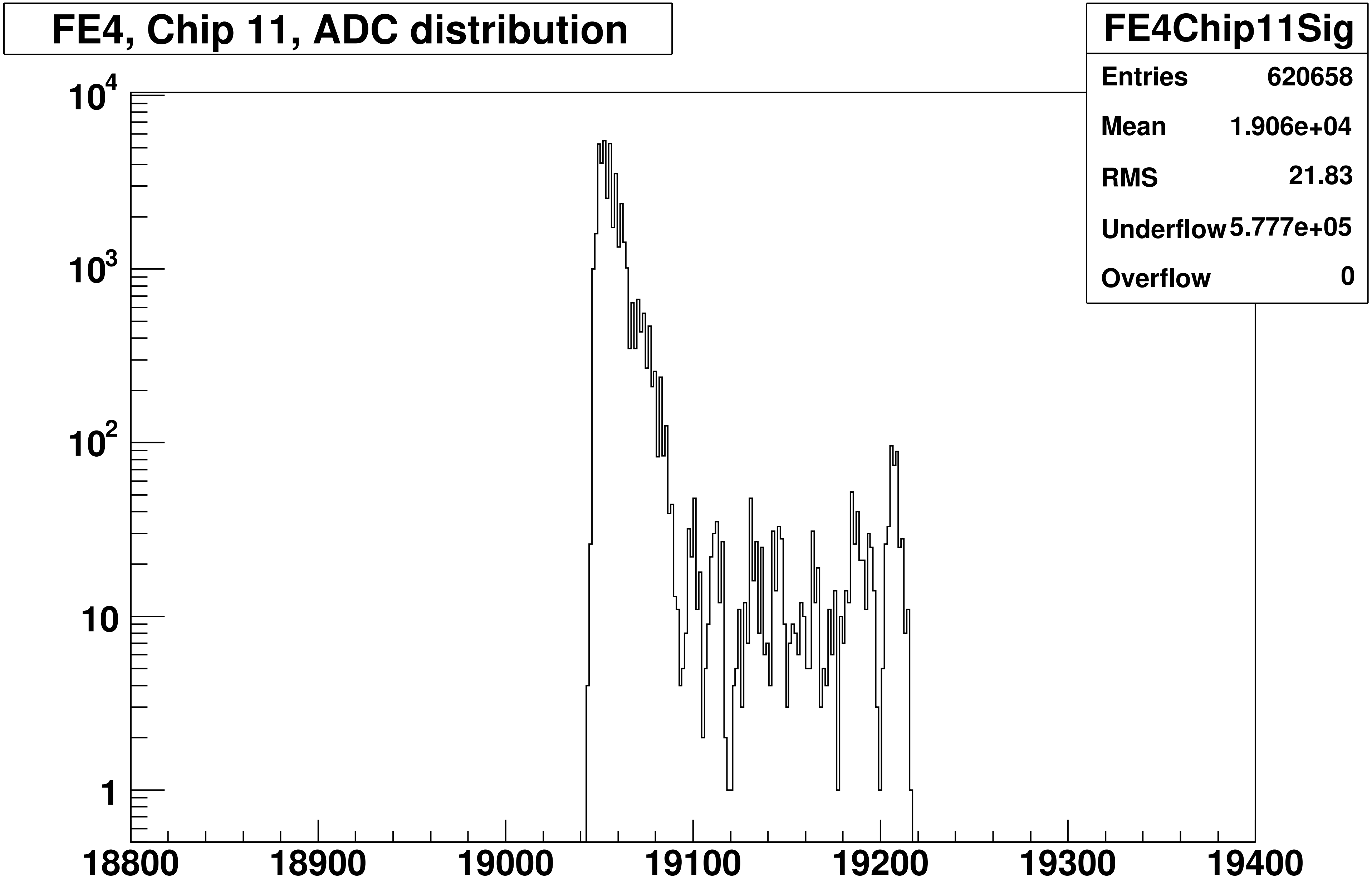
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE4, Chip 11, ADC distribution
FE4Chip11Sig
Entries 620658
Mean 1.906e+04
RMS 21.83
Underflow 5.777e+05
Overflow 0
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10^3
10^2
10
1
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18900
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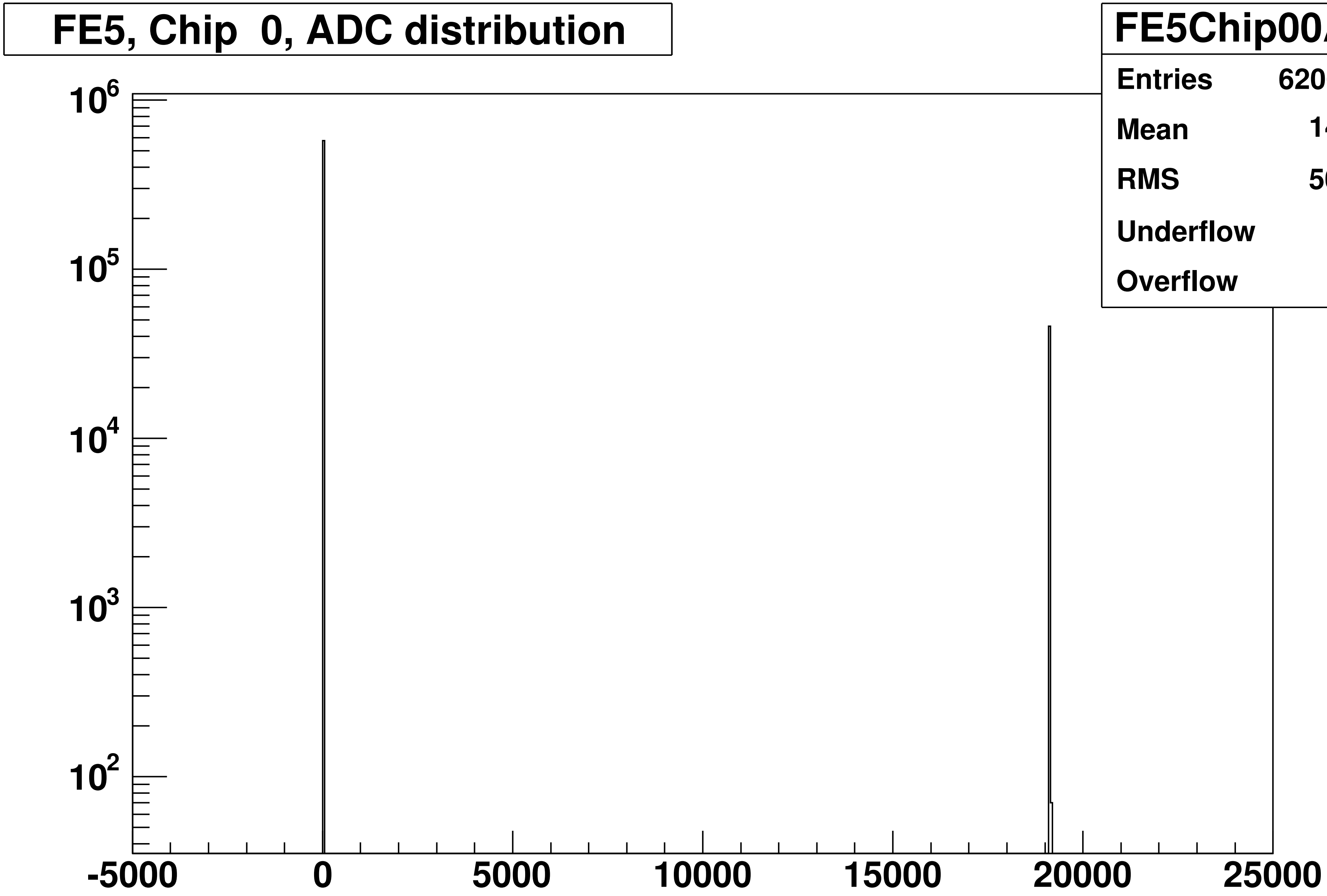
FE5, Chip 0, ADC distribution
FE5Chip00All
Entries 620658
Mean 1444
RMS 5010
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
-5000
0
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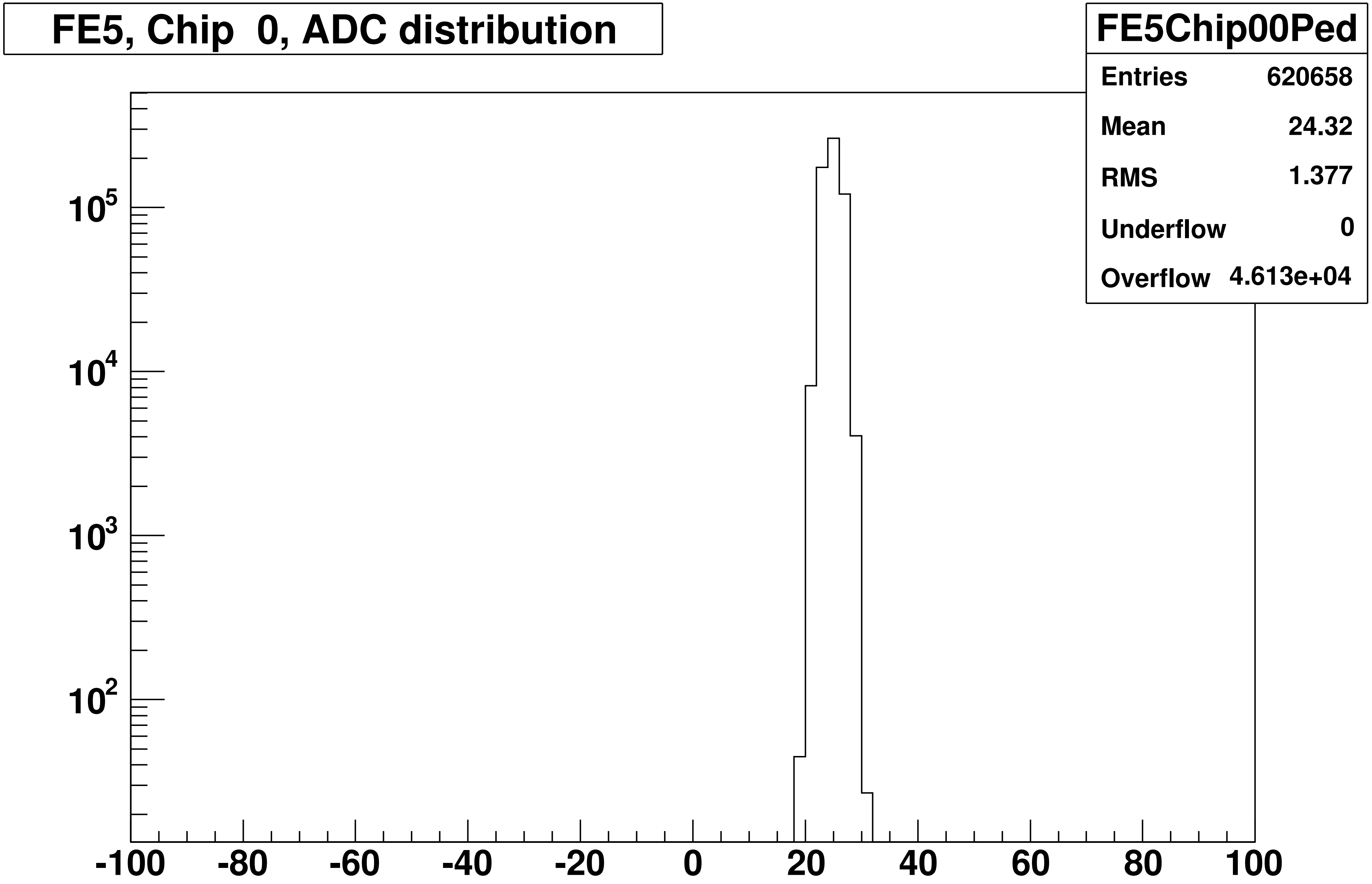
10000
15000
20000
25000


FE5, Chip 0, ADC distribution
FE5Chip00Ped
Entries 620658
Mean 24.32
RMS 1.377
Underflow 0
Overflow 4.613e+04
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10^4
10^3
10^2
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-80
-60
-40
-20
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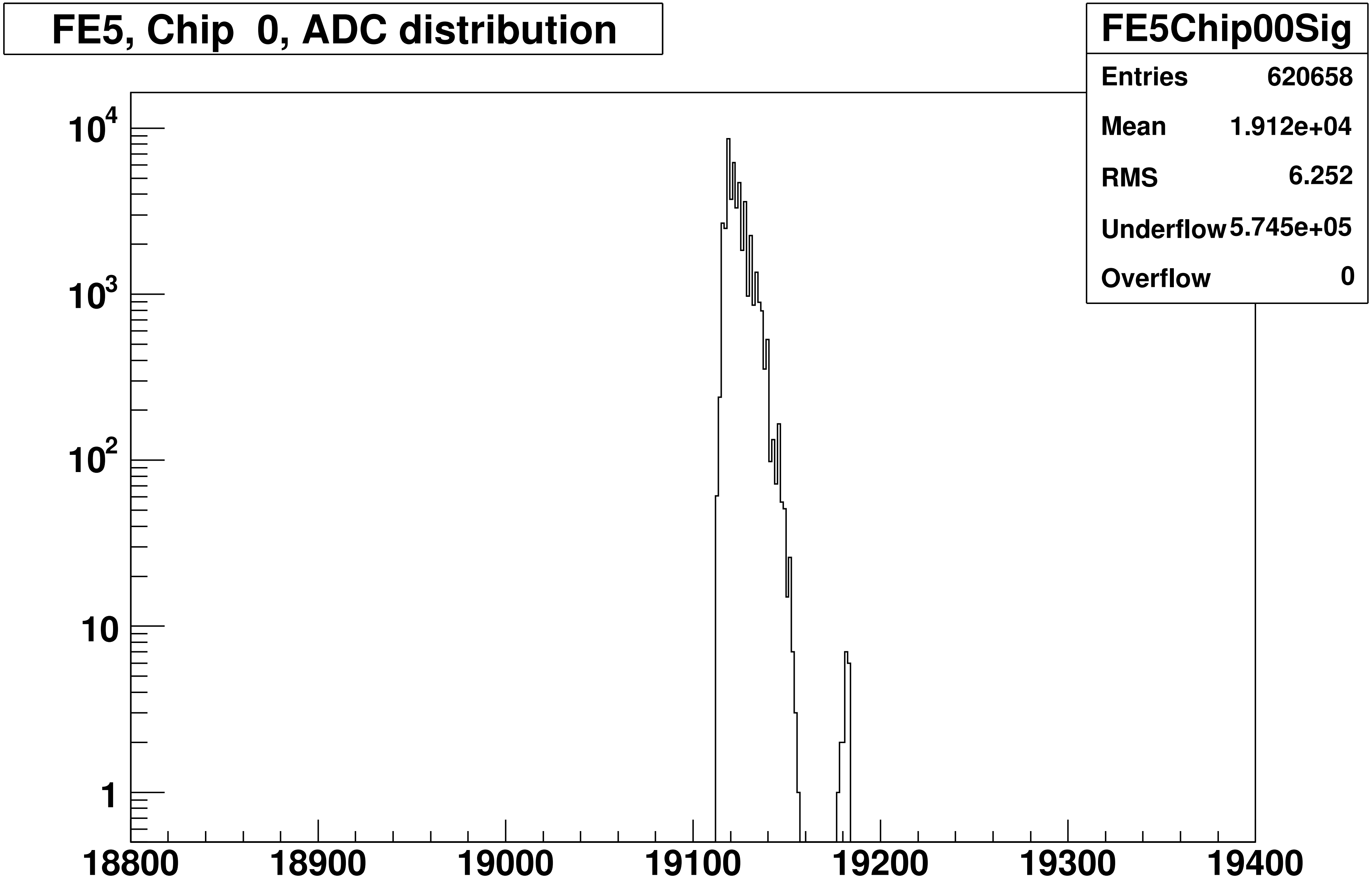
FE5, Chip 0, ADC distribution
FE5Chip00Sig
Entries 620658
Mean 1.912e+04
RMS 6.252
Underflow 5.745e+05
Overflow 0
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10^3
10^2
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19200
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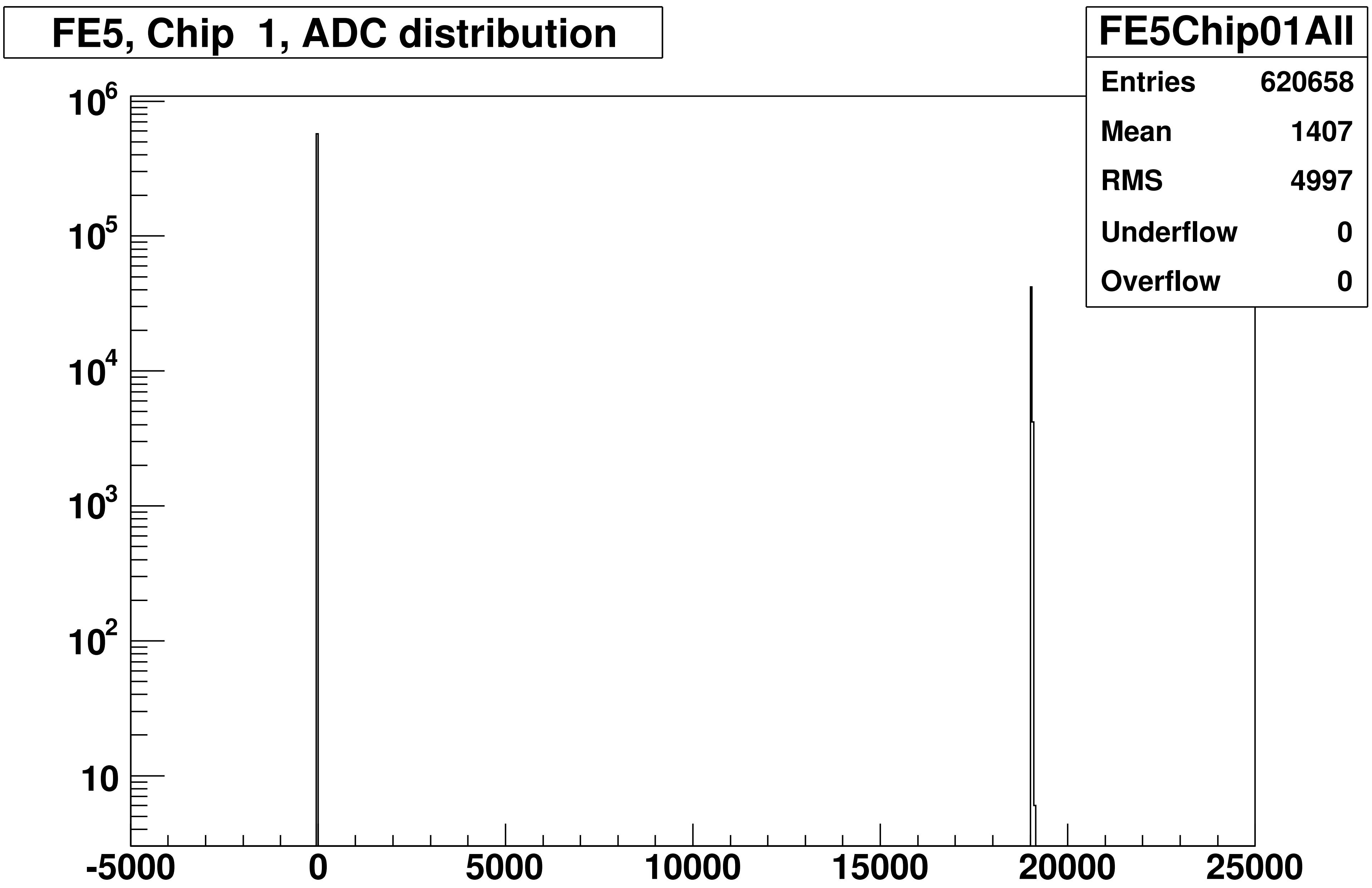
FE5, Chip 1, ADC distribution
FE5Chip01All
Entries 620658
Mean 1407
RMS 4997
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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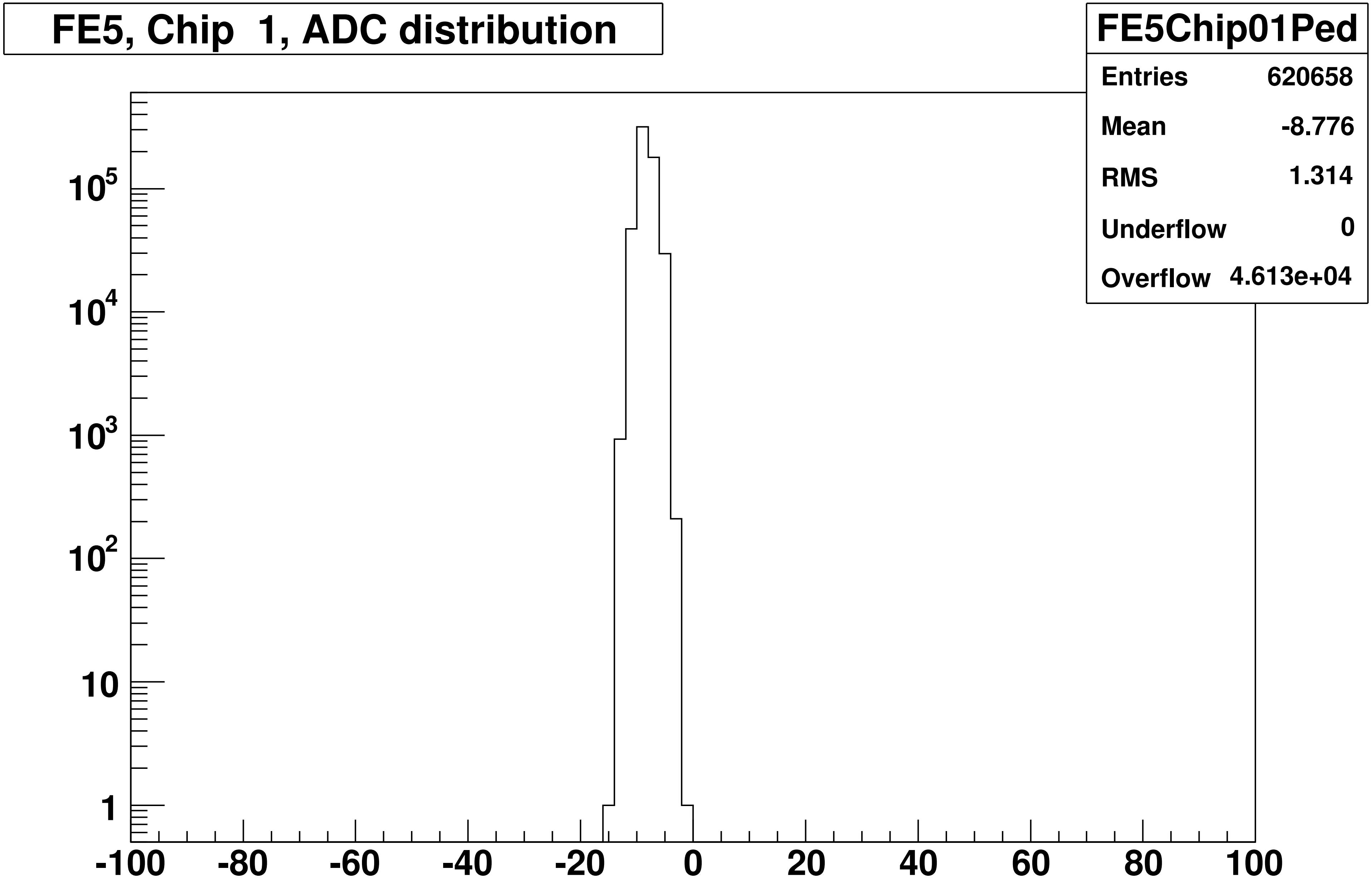
FE5, Chip 1, ADC distribution
FE5Chip01Ped
Entries 620658
Mean -8.776
RMS 1.314
Underflow 0
Overflow 4.613e+04
10^5
10^4
10^3
10^2
10
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-20
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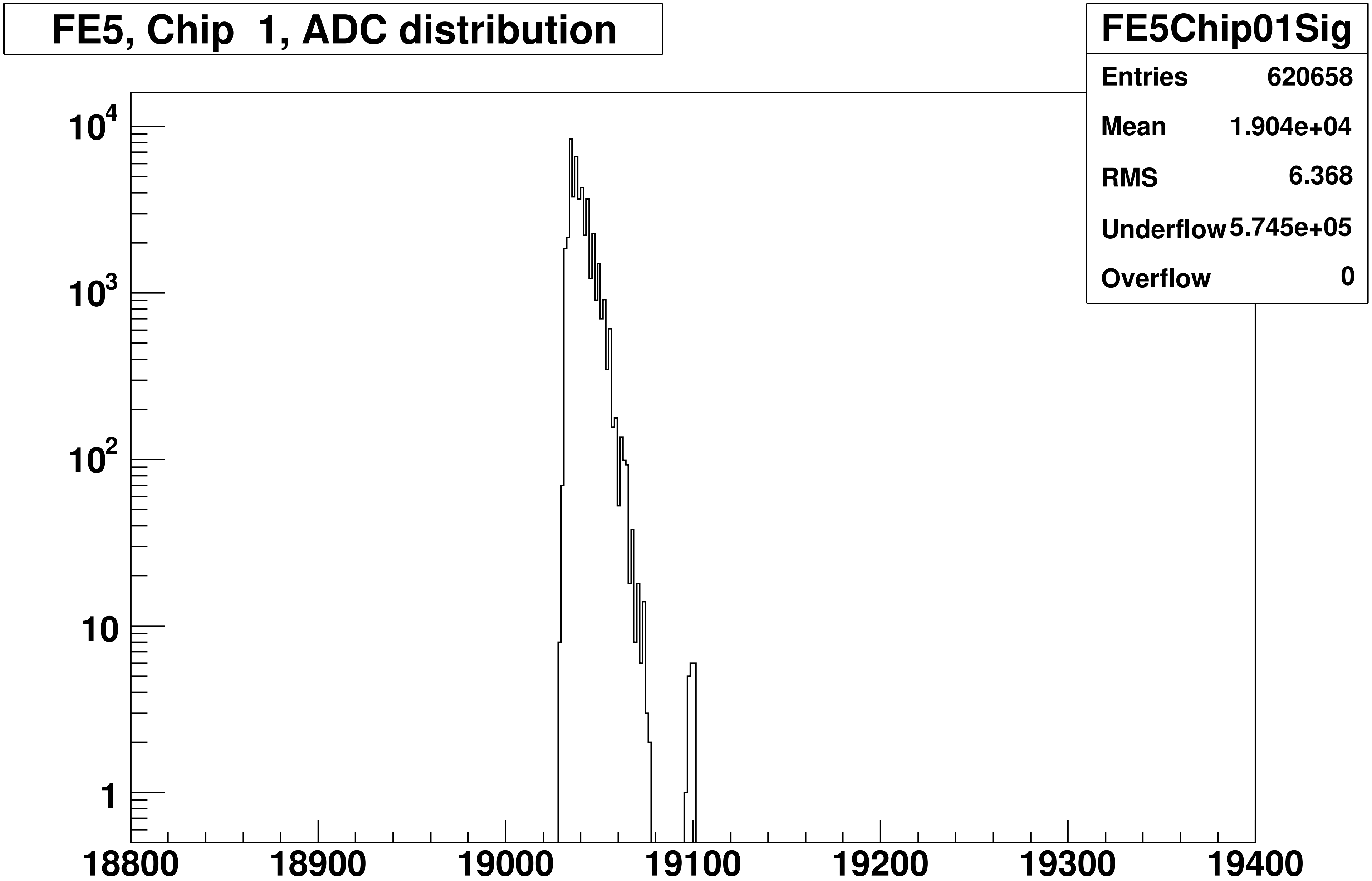
FE5, Chip 1, ADC distribution
FE5Chip01Sig
Entries 620658
Mean 1.904e+04
RMS 6.368
Underflow 5.745e+05
Overflow 0
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10^3
10^2
10
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18900
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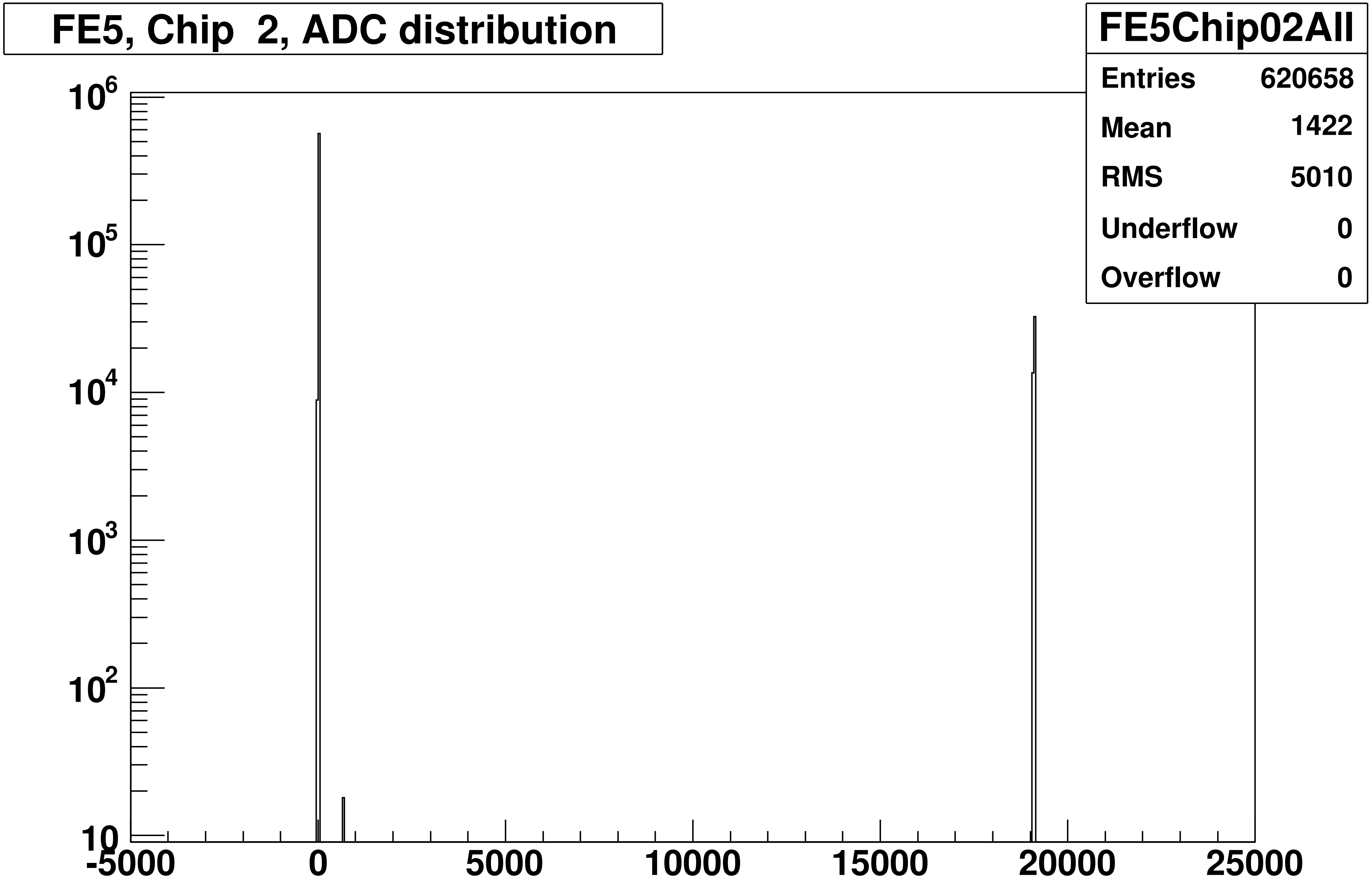
19400



FE5, Chip 2, ADC distribution
FE5Chip02All
Entries 620658
Mean 1422
RMS 5010
Underflow 0
Overflow 0
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10^2
10^3
10^4
10^5
10^6
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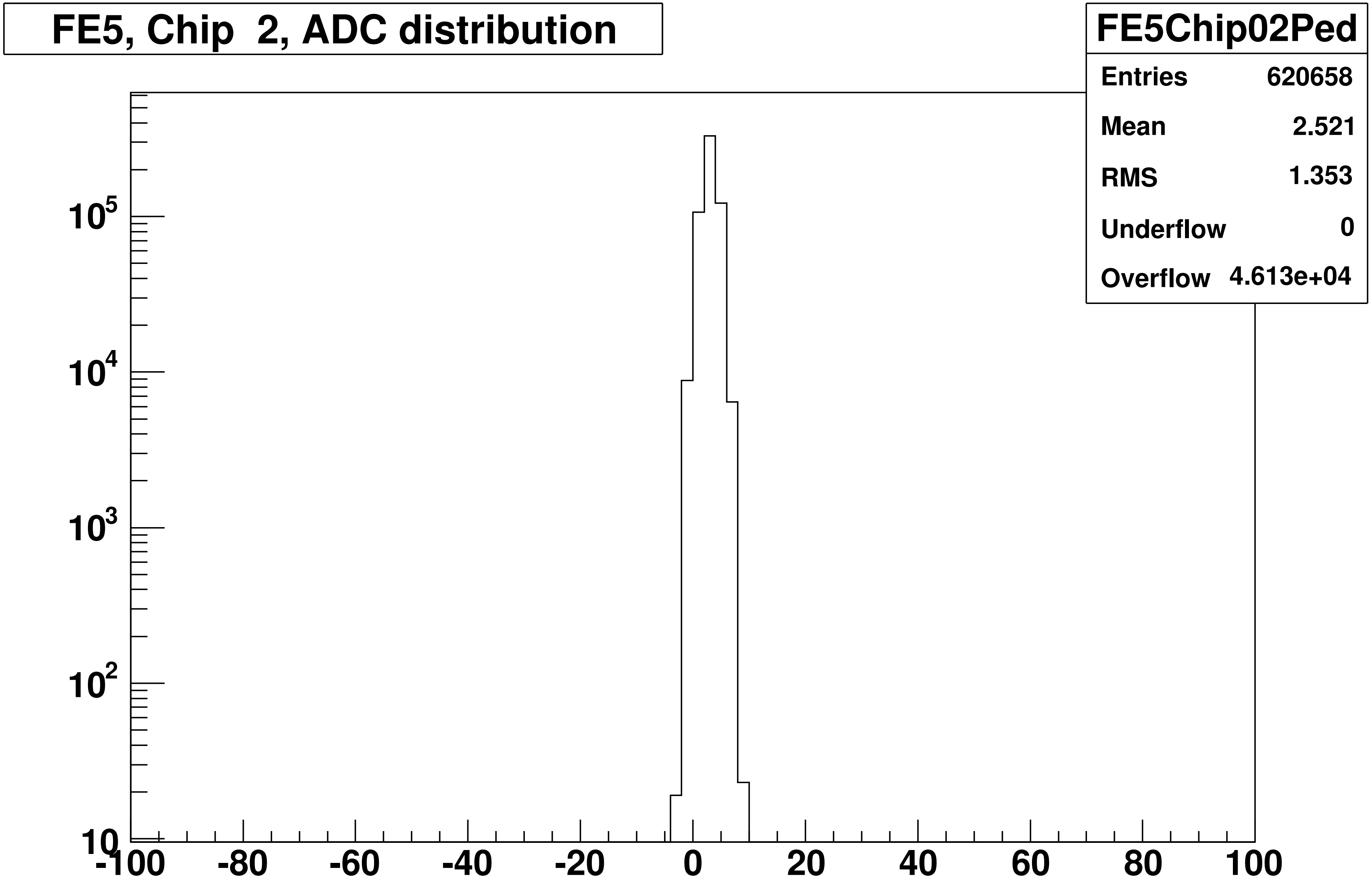
5000
10000
15000
20000
25000


FE5, Chip 2, ADC distribution
FE5Chip02Ped
Entries 620658
Mean 2.521
RMS 1.353
Underflow 0
Overflow 4.613e+04
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10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
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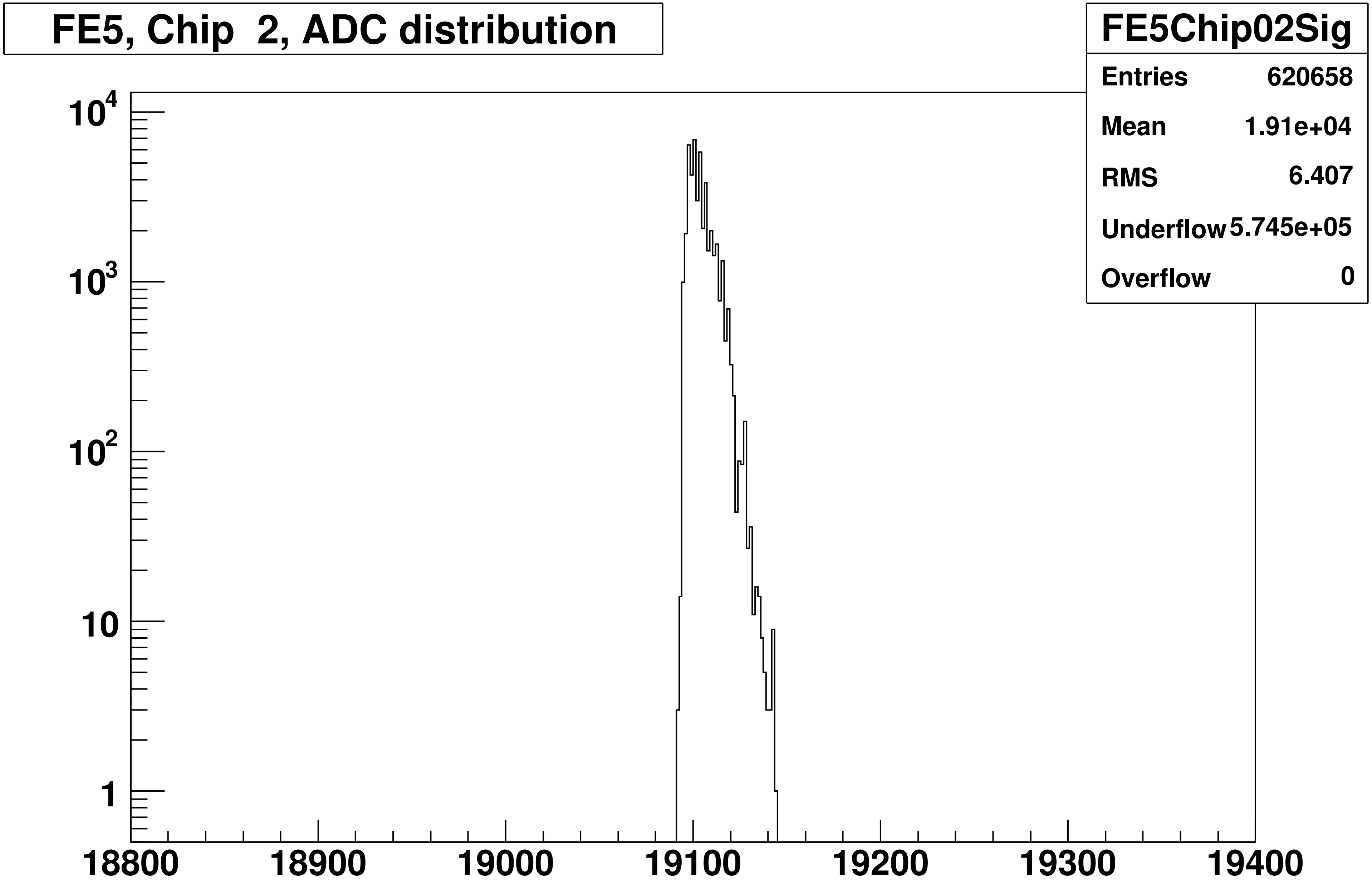
80
100


FE5, Chip 2, ADC distribution
FE5Chip02Sig
Entries 620658
Mean 1.91e+04
RMS 6.407
Underflow 5.745e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
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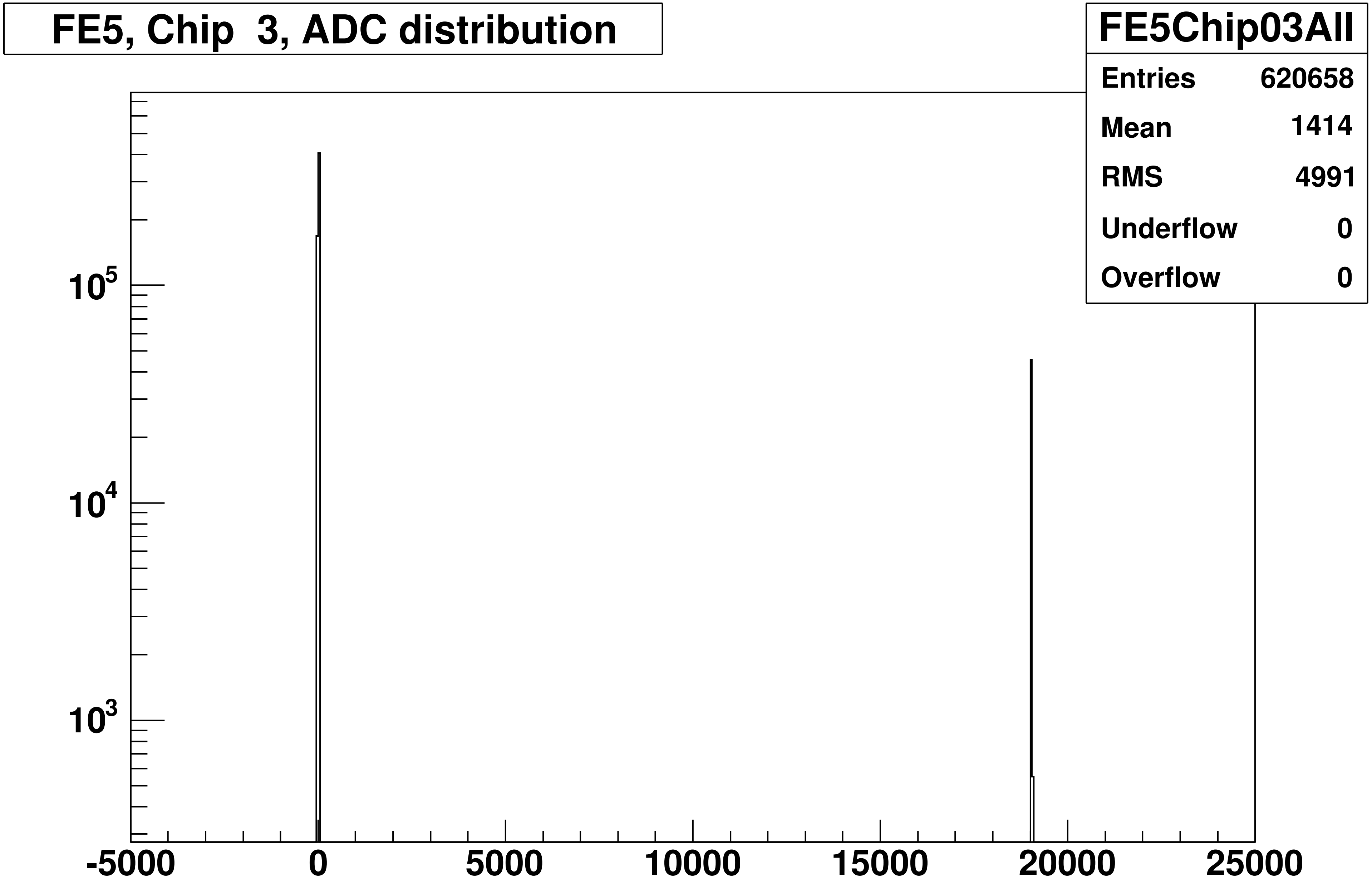
19200
19300
19400


FE5, Chip 3, ADC distribution
FE5Chip03All
Entries 620658
Mean 1414
RMS 4991
Underflow 0
Overflow 0
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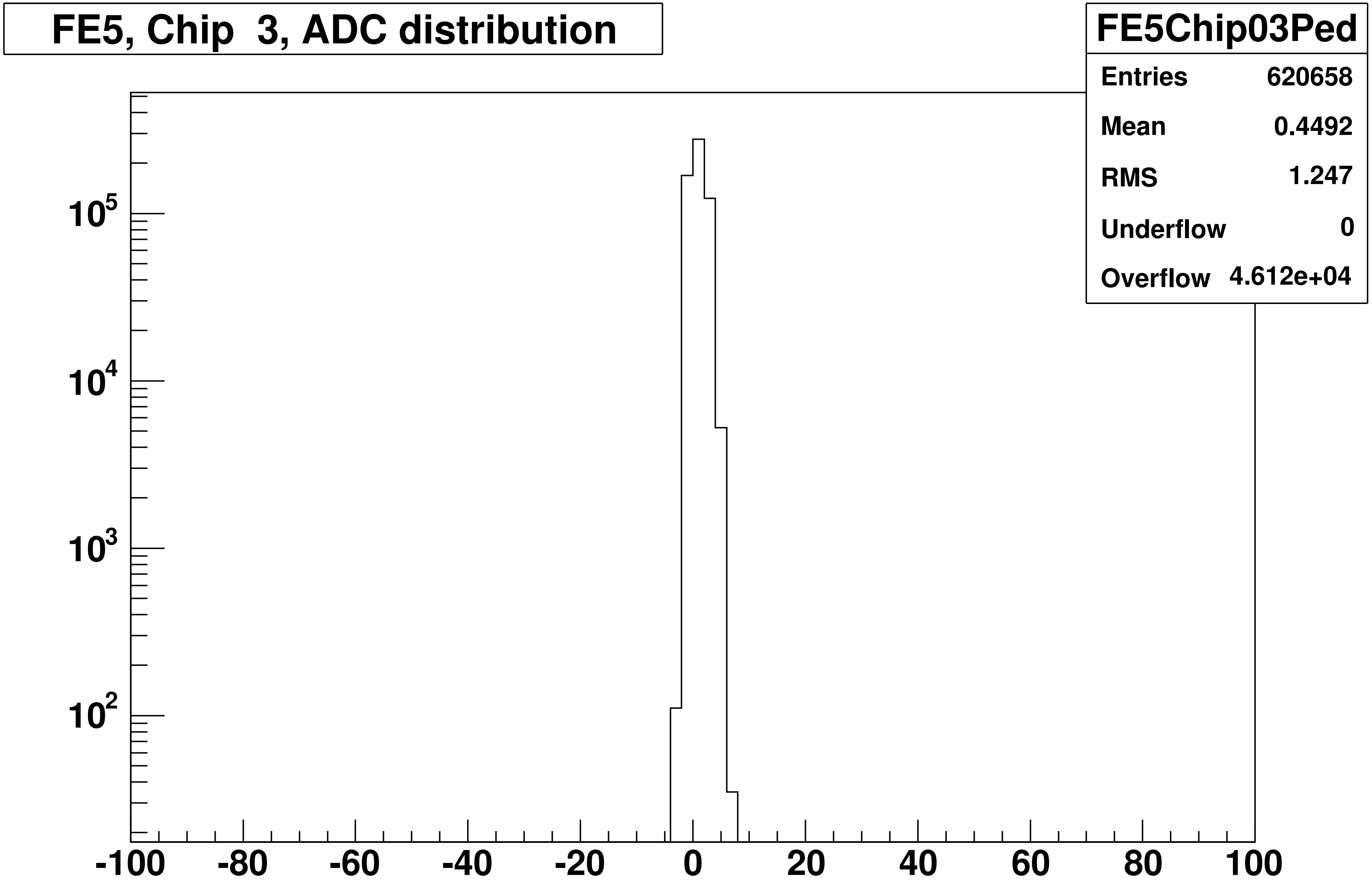
10^4
10^3
-5000
0
5000
10000
15000
20000
25000



FE5, Chip 3, ADC distribution
FE5Chip03Ped
Entries 620658
Mean 0.4492
RMS 1.247
Underflow 0
Overflow 4.612e+04
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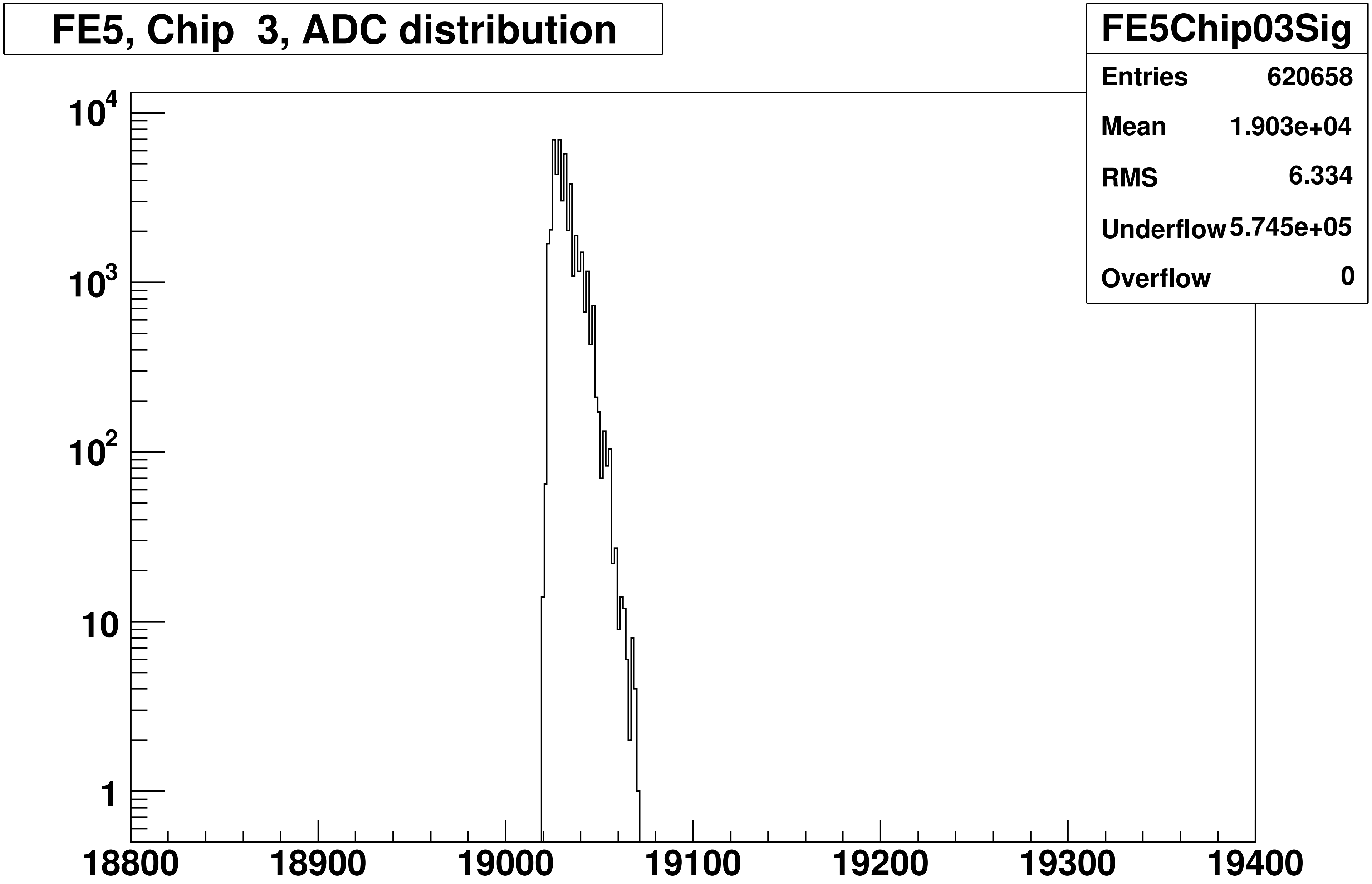
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE5, Chip 3, ADC distribution
FE5Chip03Sig
Entries 620658
Mean 1.903e+04
RMS 6.334
Underflow 5.745e+05
Overflow 0
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10^3
10^2
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18900
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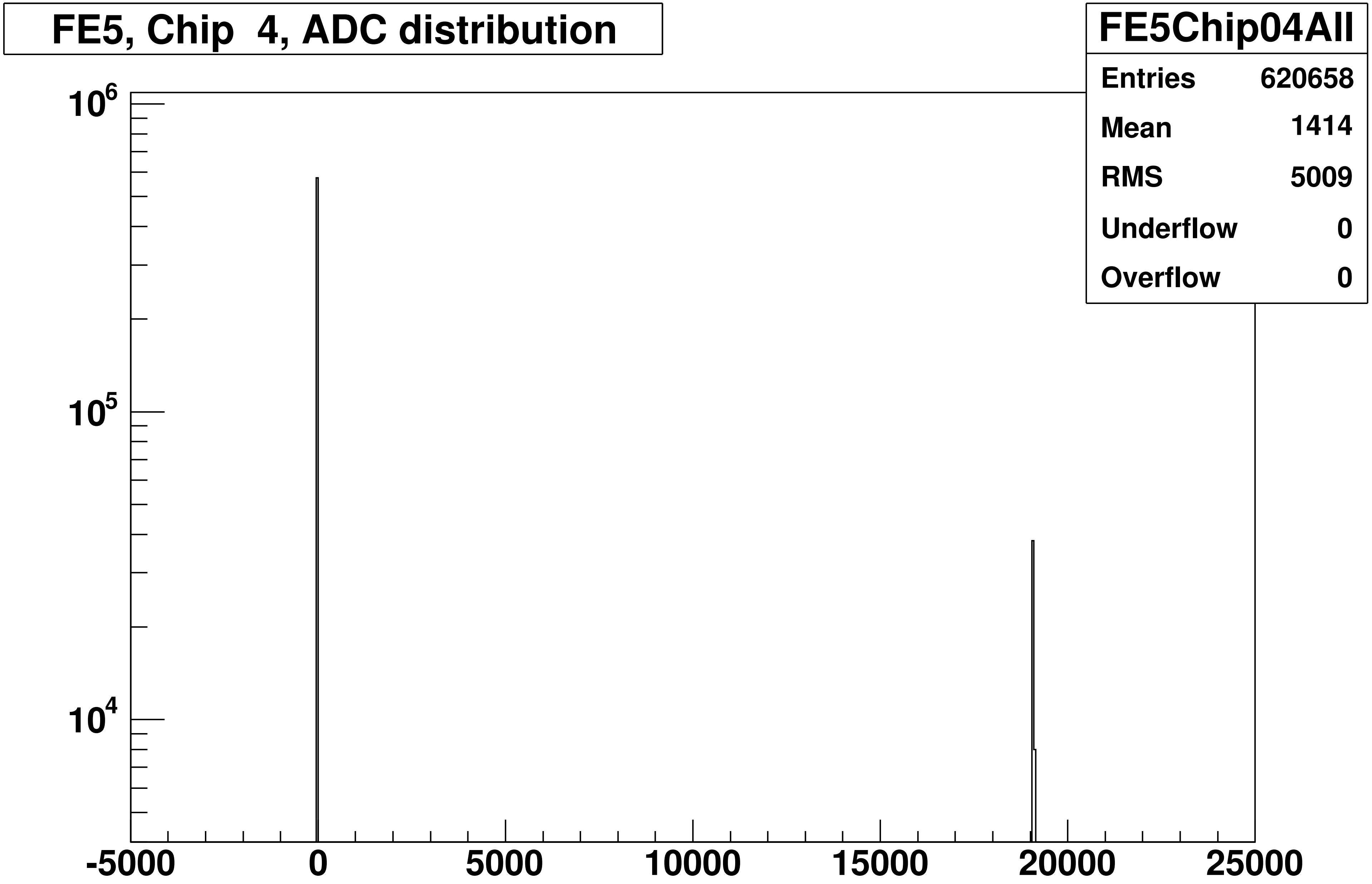
19400


FE5, Chip 4, ADC distribution
FE5Chip04All
Entries 620658
Mean 1414
RMS 5009
Underflow 0
Overflow 0
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10^5
10^4
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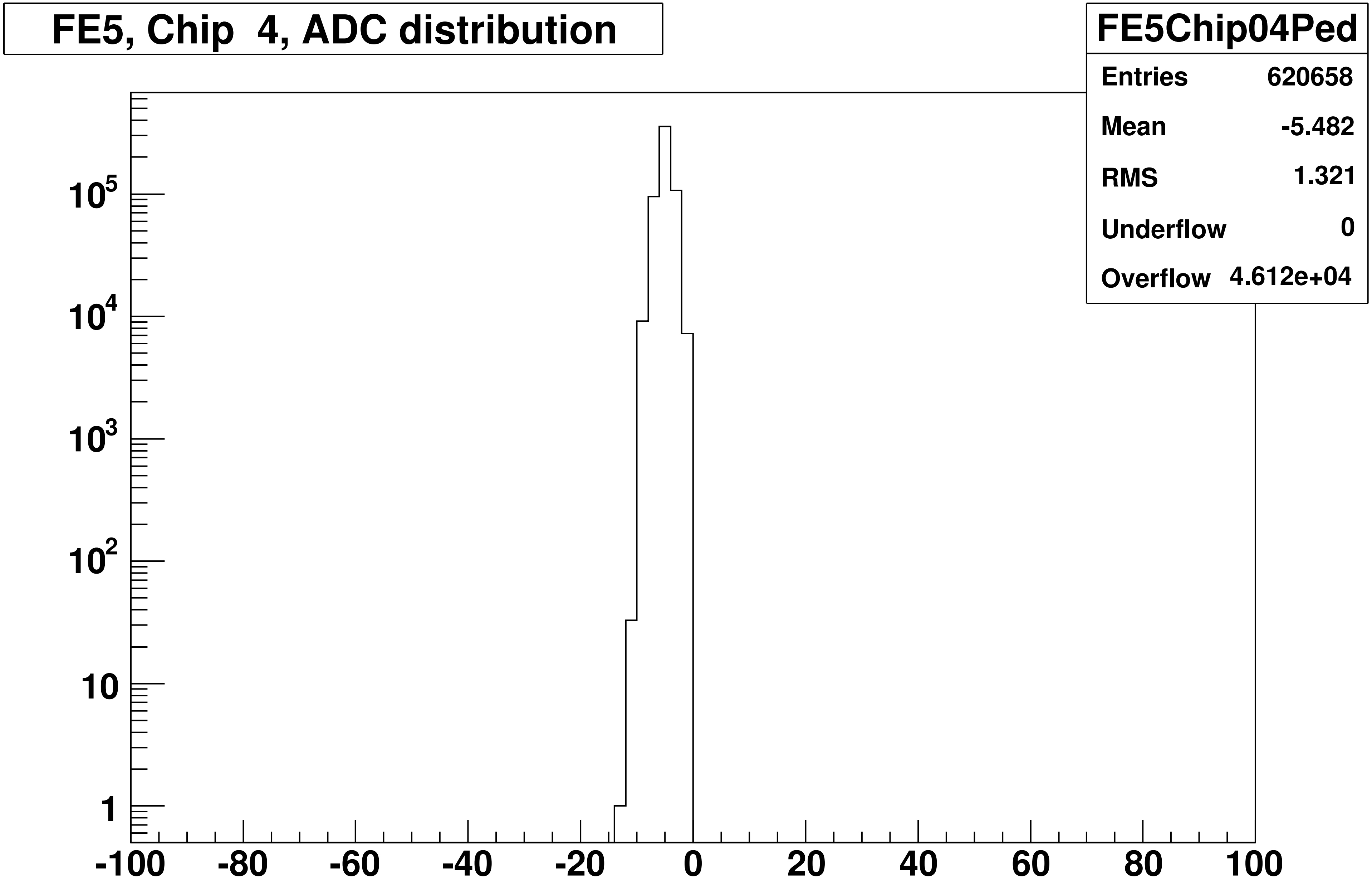
20000
25000


FE5, Chip 4, ADC distribution
FE5Chip04Ped
Entries 620658
Mean -5.482
RMS 1.321
Underflow 0
Overflow 4.612e+04
10^5
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10^3
10^2
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80
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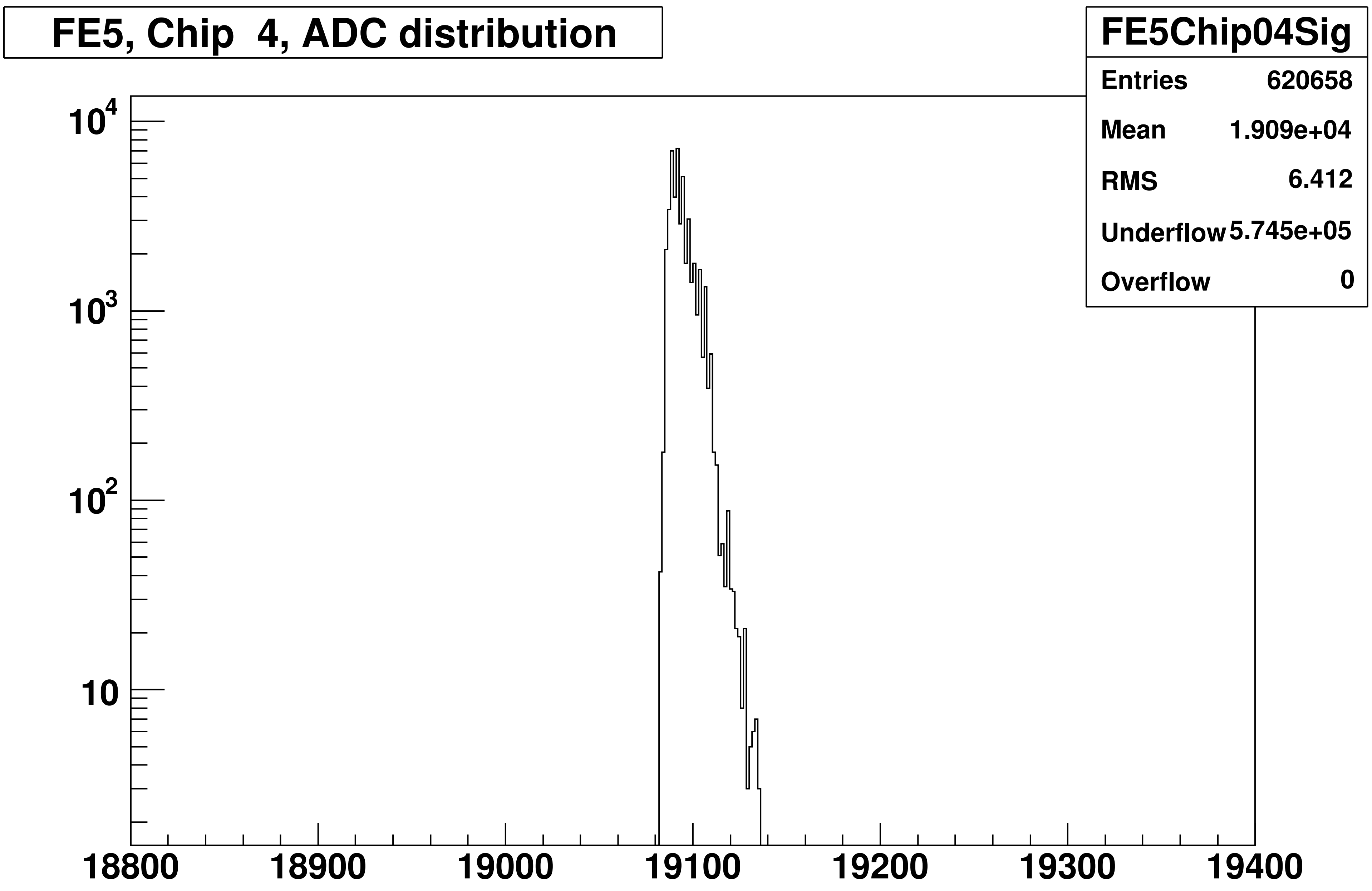

FE5, Chip 4, ADC distribution
FE5Chip04Sig
Entries 620658
Mean 1.909e+04
RMS 6.412
Underflow 5.745e+05
Overflow 0
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10^3
10^2
10
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18900
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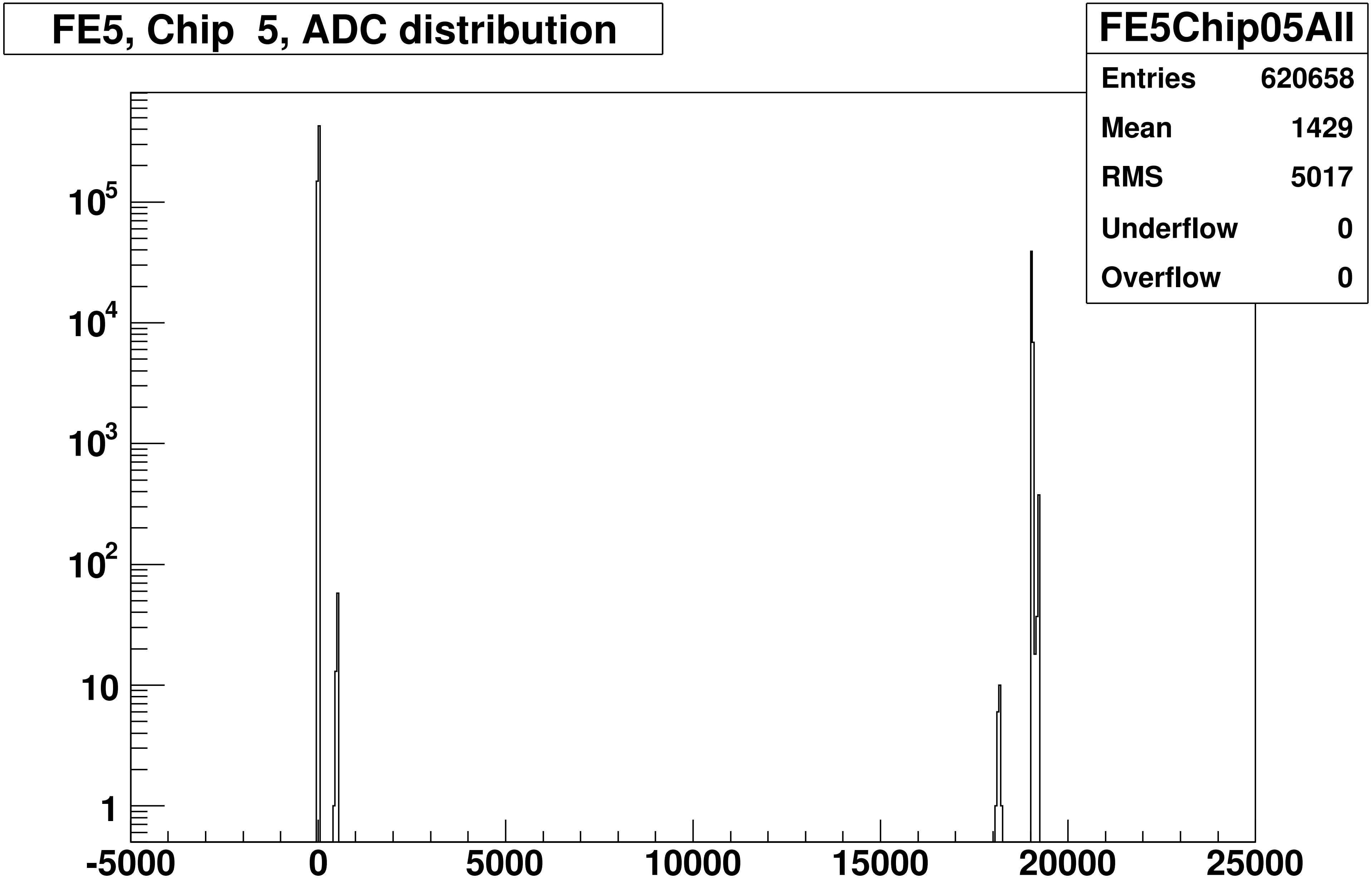
19300
19400



FE5, Chip 5, ADC distribution
FE5Chip05All
Entries 620658
Mean 1429
RMS 5017
Underflow 0
Overflow 0
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10^4
10^3
10^2
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0
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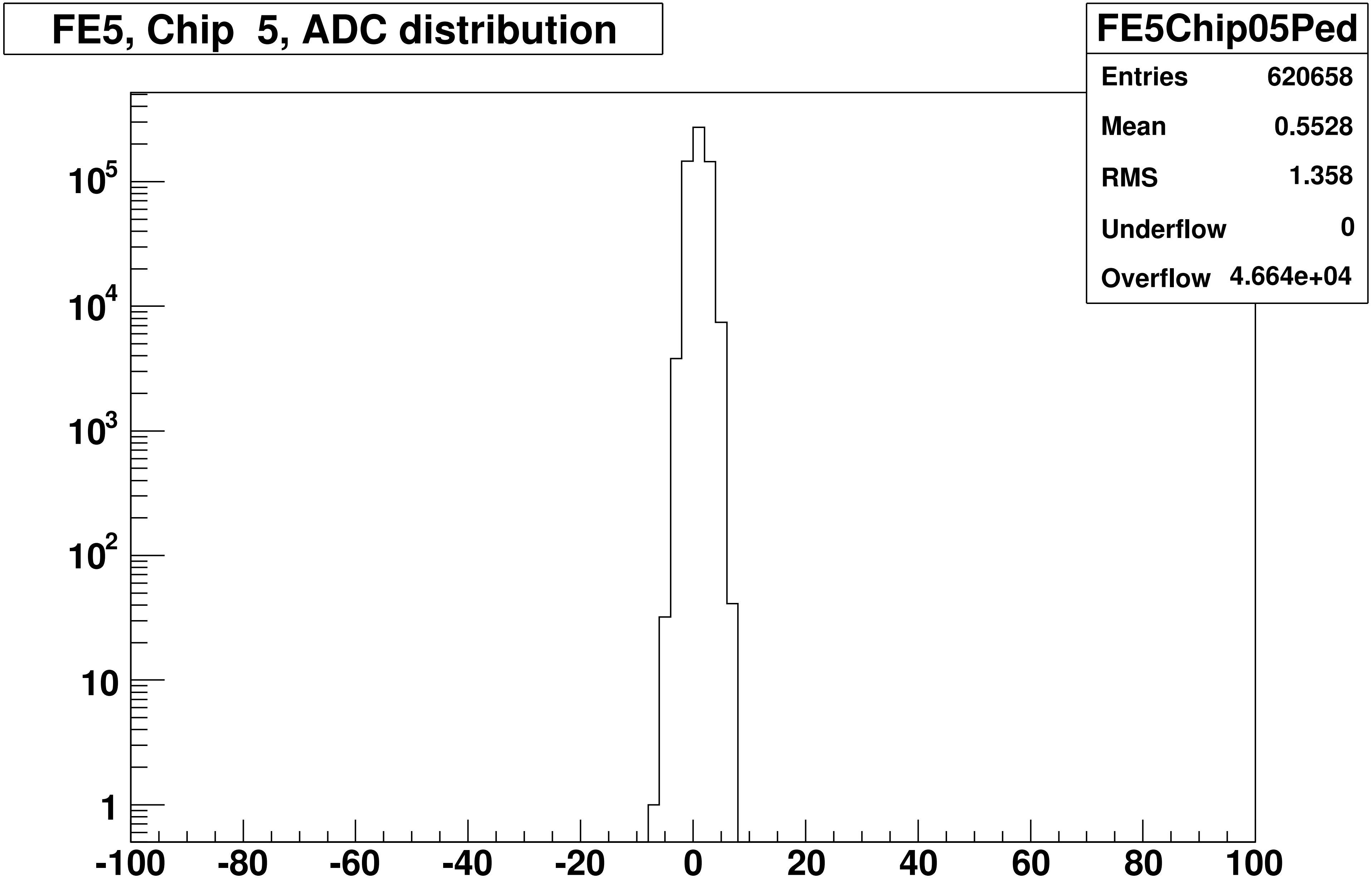
15000
20000
25000



FE5, Chip 5, ADC distribution
FE5Chip05Ped
Entries 620658
Mean 0.5528
RMS 1.358
Underflow 0
Overflow 4.664e+04
10^5
10^4
10^3
10^2
10
1
-100
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-40
-20
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80
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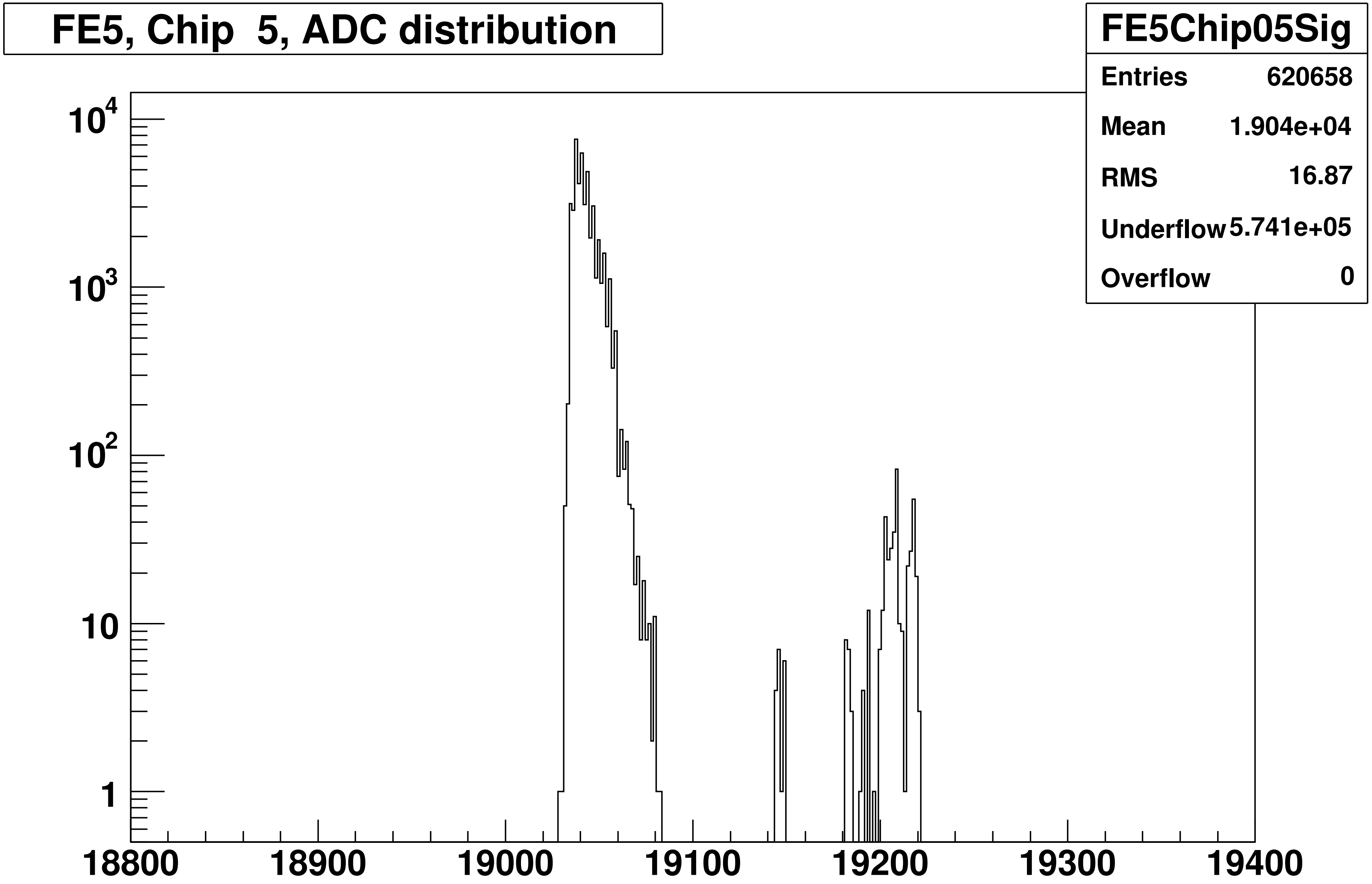
FE5, Chip 5, ADC distribution
FE5Chip05Sig
Entries 620658
Mean 1.904e+04
RMS 16.87
Underflow 5.741e+05
Overflow 0
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10^3
10^2
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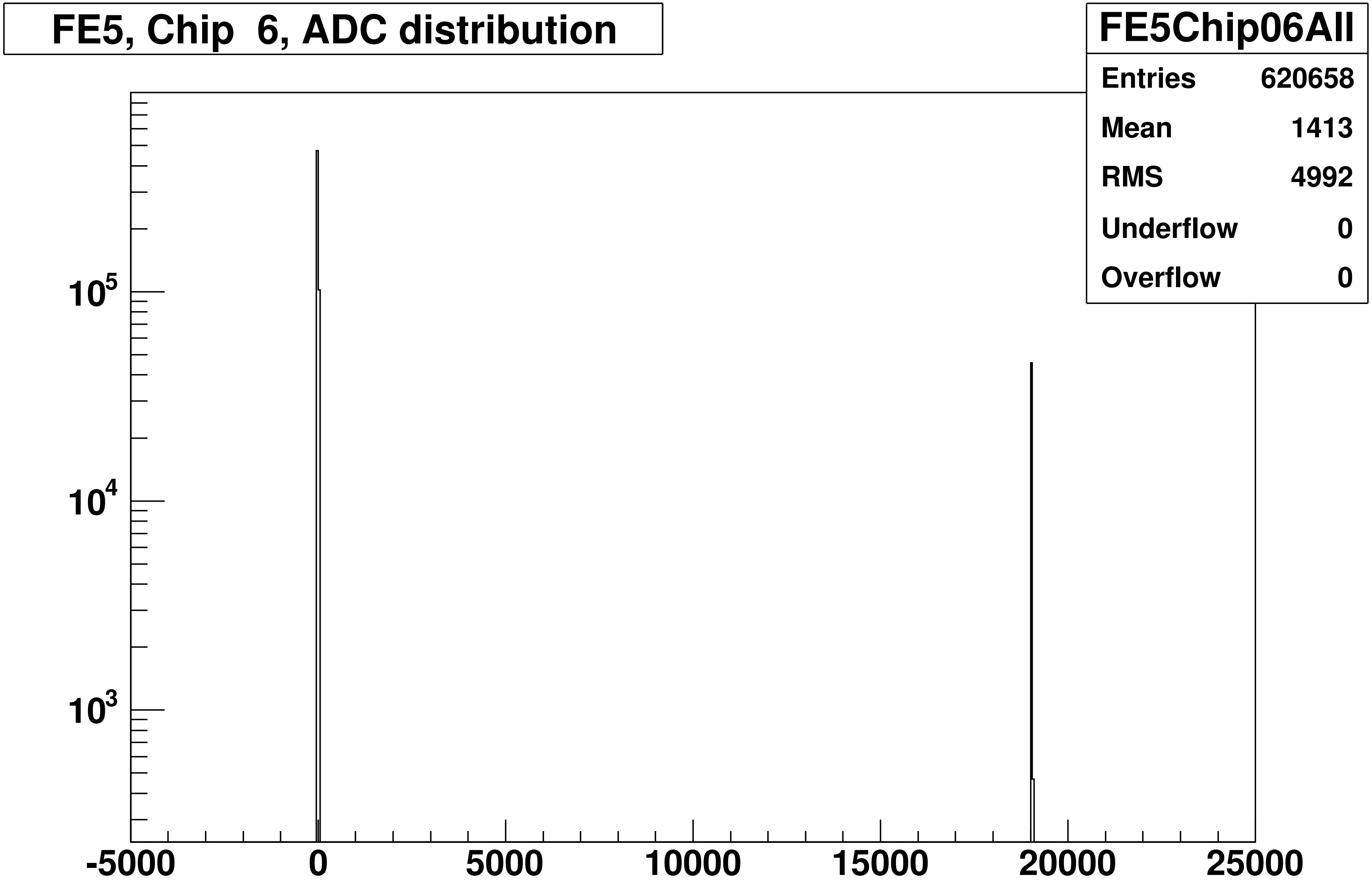
FE5, Chip 6, ADC distribution
FE5Chip06All
Entries 620658
Mean 1413
RMS 4992
Underflow 0
Overflow 0
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10^3
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0
5000
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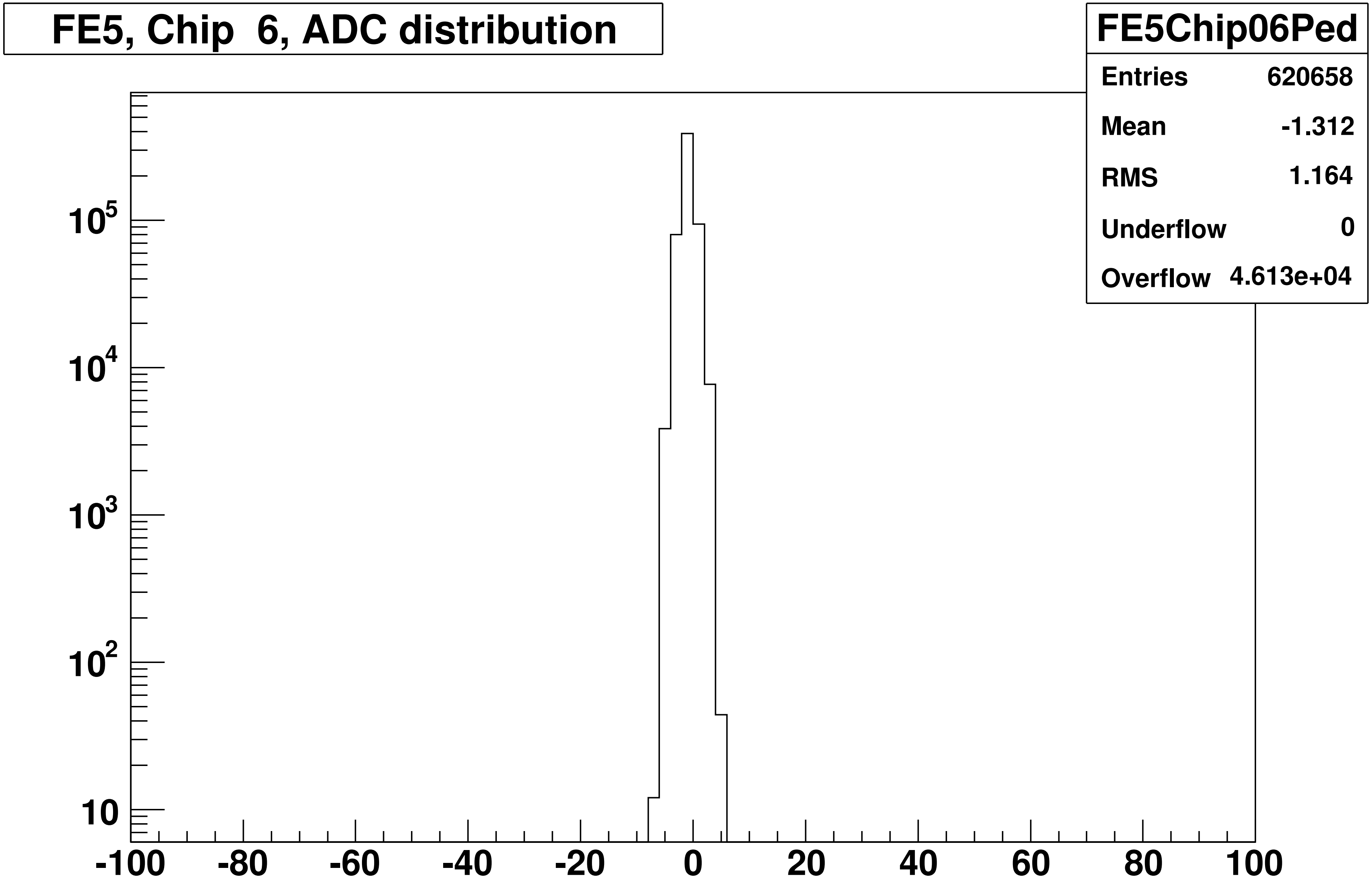
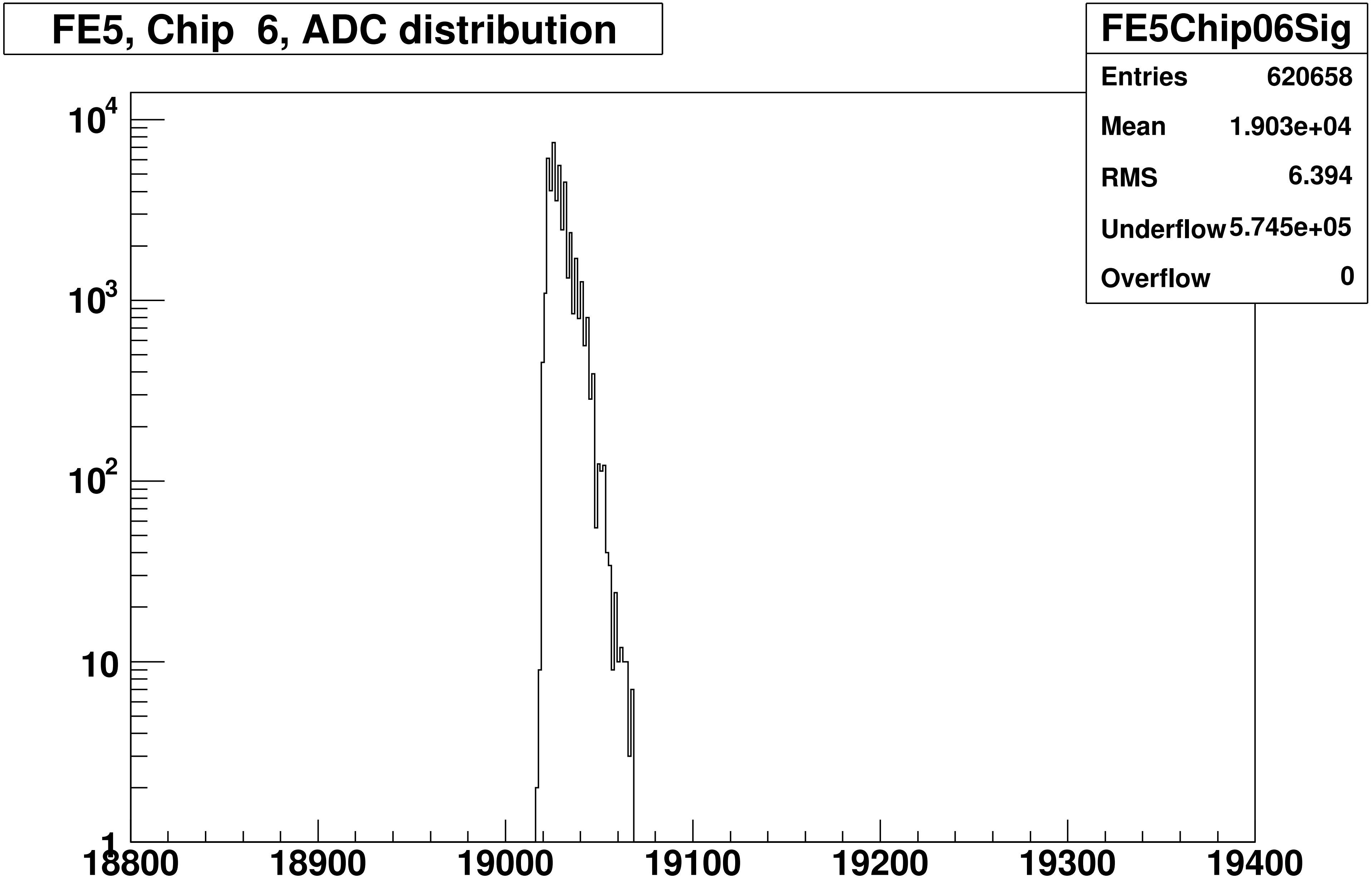
20000
25000


FE5, Chip 6, ADC distribution
FE5Chip06Ped
Entries 620658
Mean -1.312
RMS 1.164
Underflow 0
Overflow 4.613e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100



FE5, Chip 6, ADC distribution
FE5Chip06Sig
Entries 620658
Mean 1.903e+04
RMS 6.394
Underflow 5.745e+05
Overflow 0
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10^3
10^2
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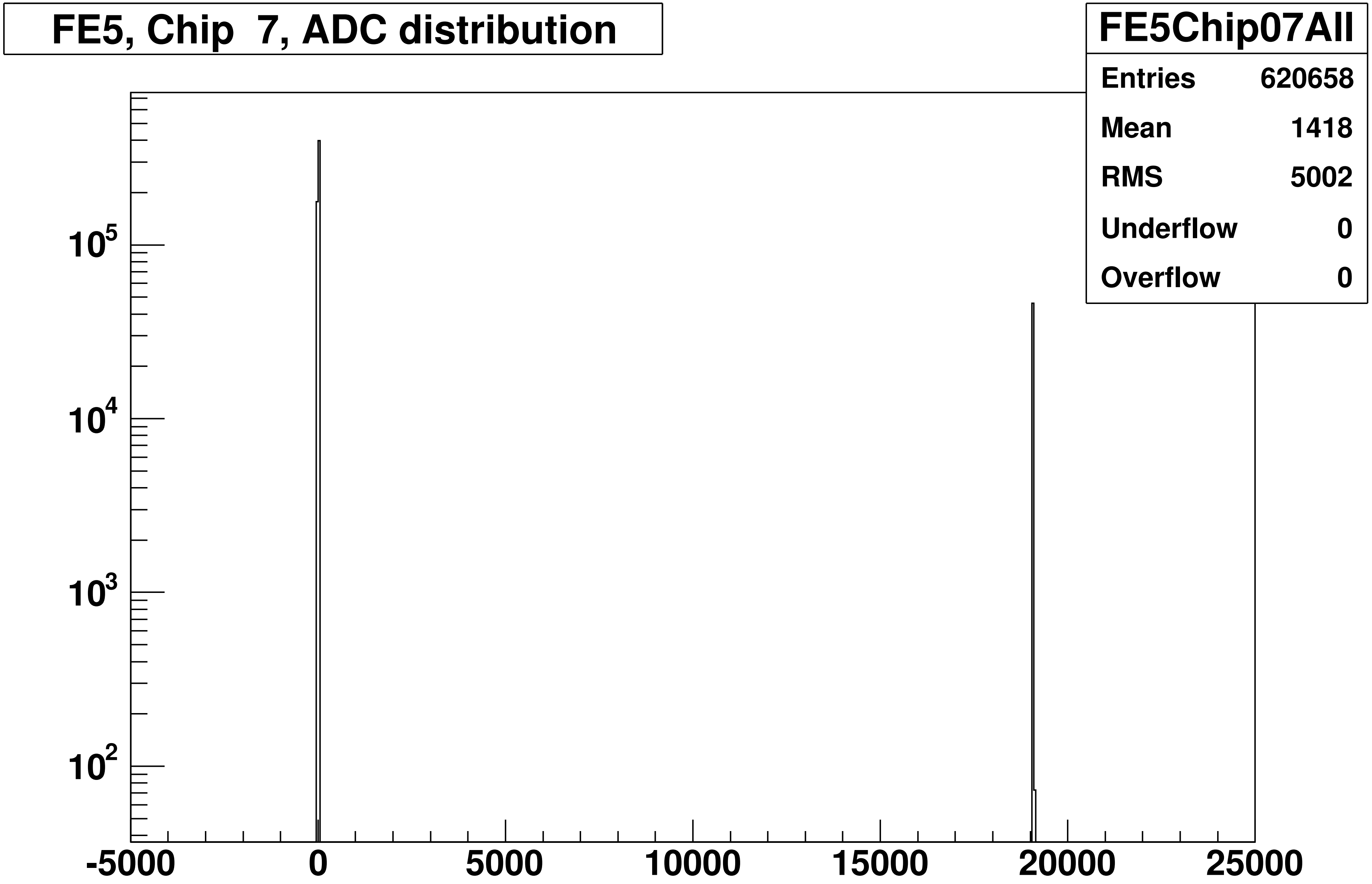
FE5, Chip 7, ADC distribution
FE5Chip07All
Entries 620658
Mean 1418
RMS 5002
Underflow 0
Overflow 0
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10^4
10^3
10^2
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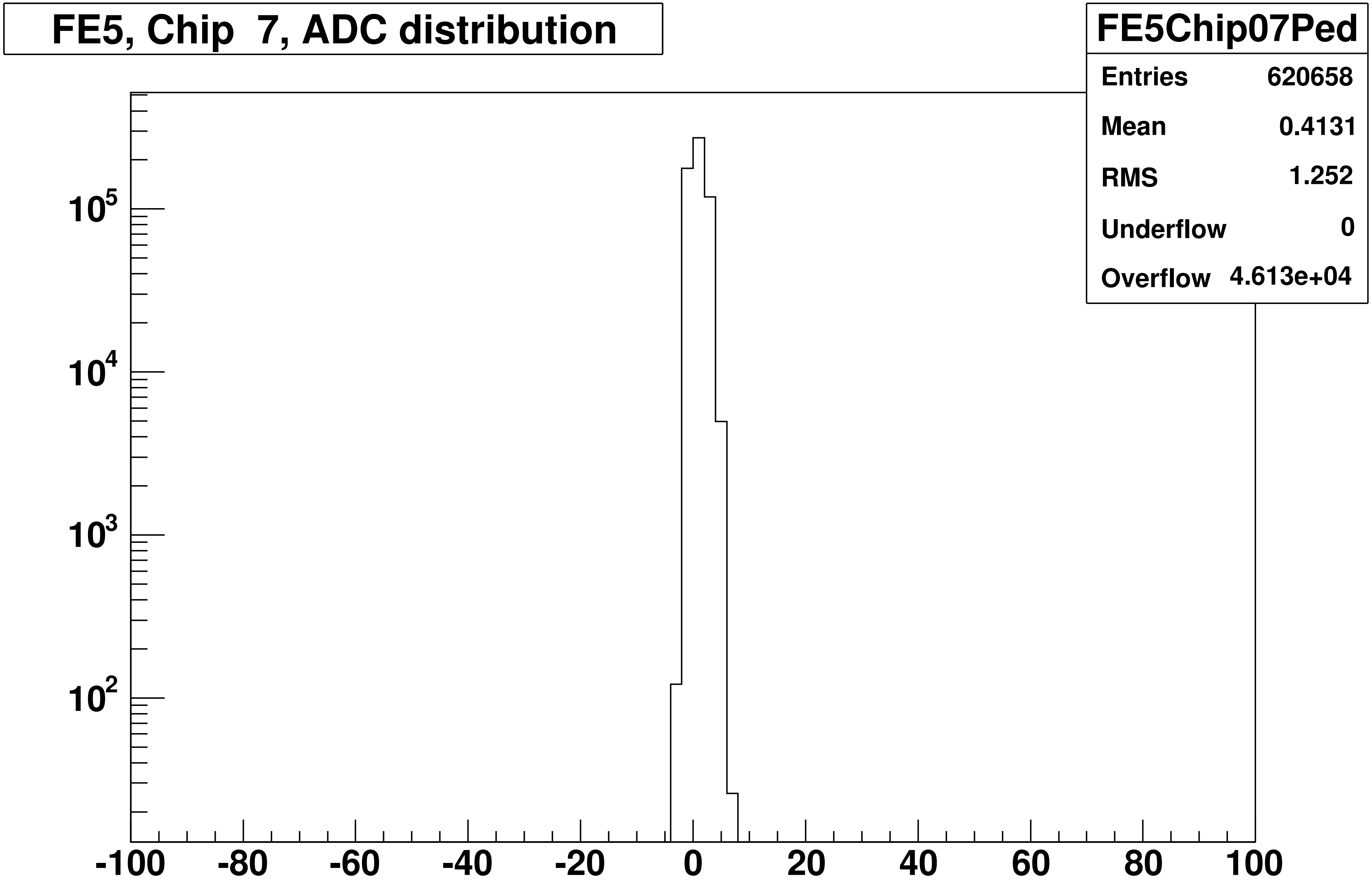
FE5, Chip 7, ADC distribution
FE5Chip07Ped
Entries 620658
Mean 0.4131
RMS 1.252
Underflow 0
Overflow 4.613e+04
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-40
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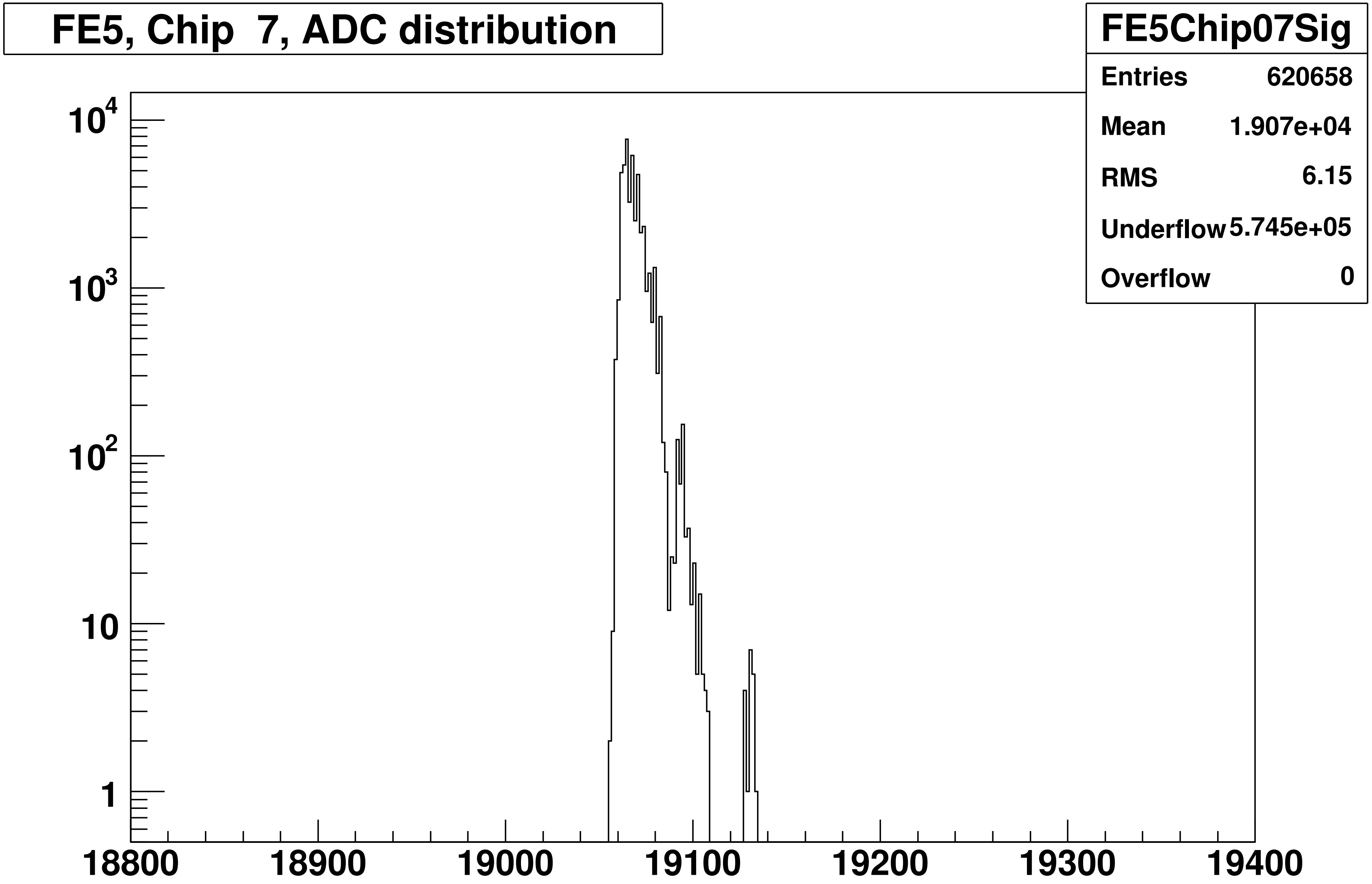

FE5, Chip 7, ADC distribution
FE5Chip07Sig
Entries 620658
Mean 1.907e+04
RMS 6.15
Underflow 5.745e+05
Overflow 0
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10^3
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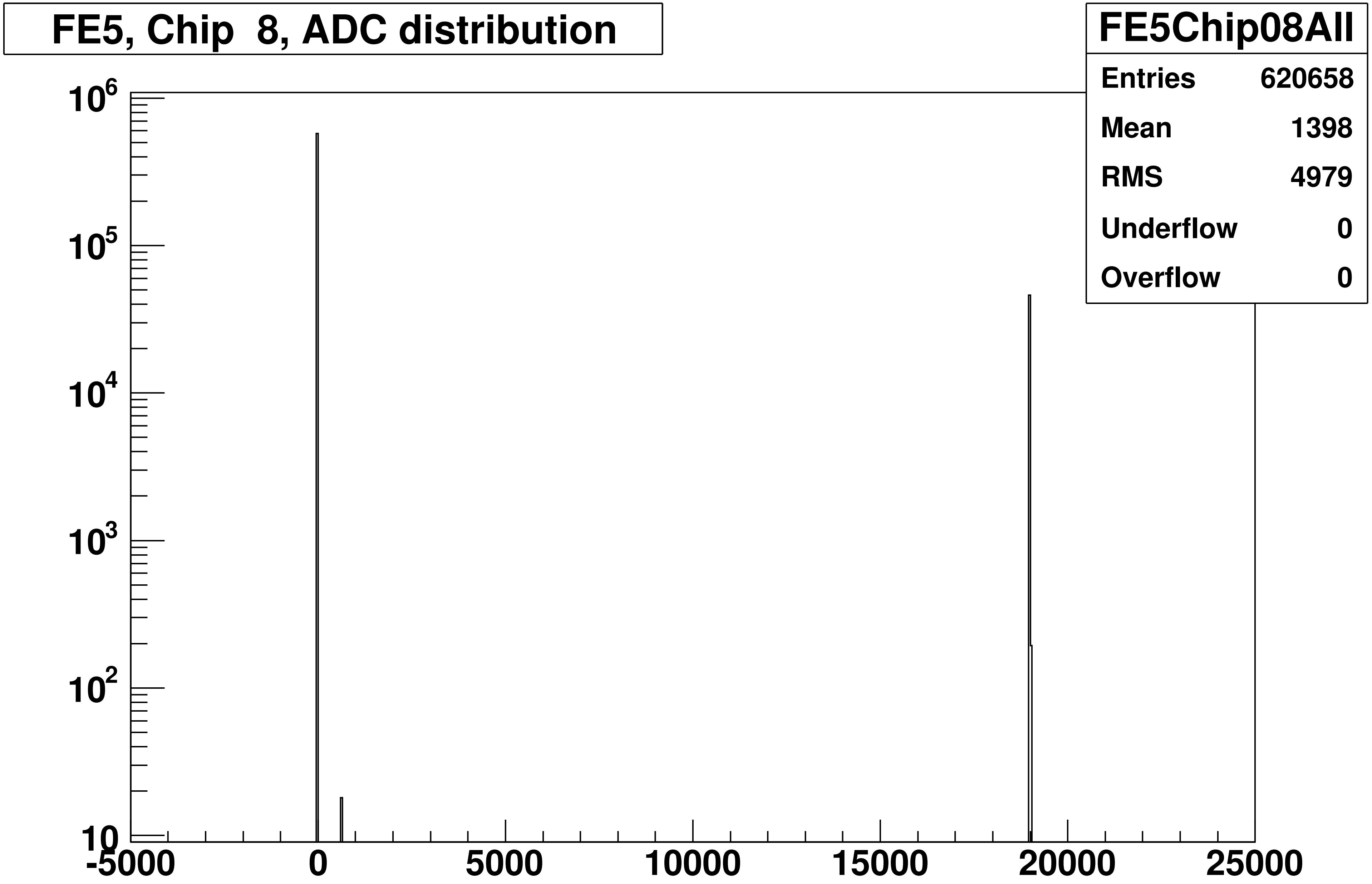
FE5, Chip 8, ADC distribution
FE5Chip08All
Entries 620658
Mean 1398
RMS 4979
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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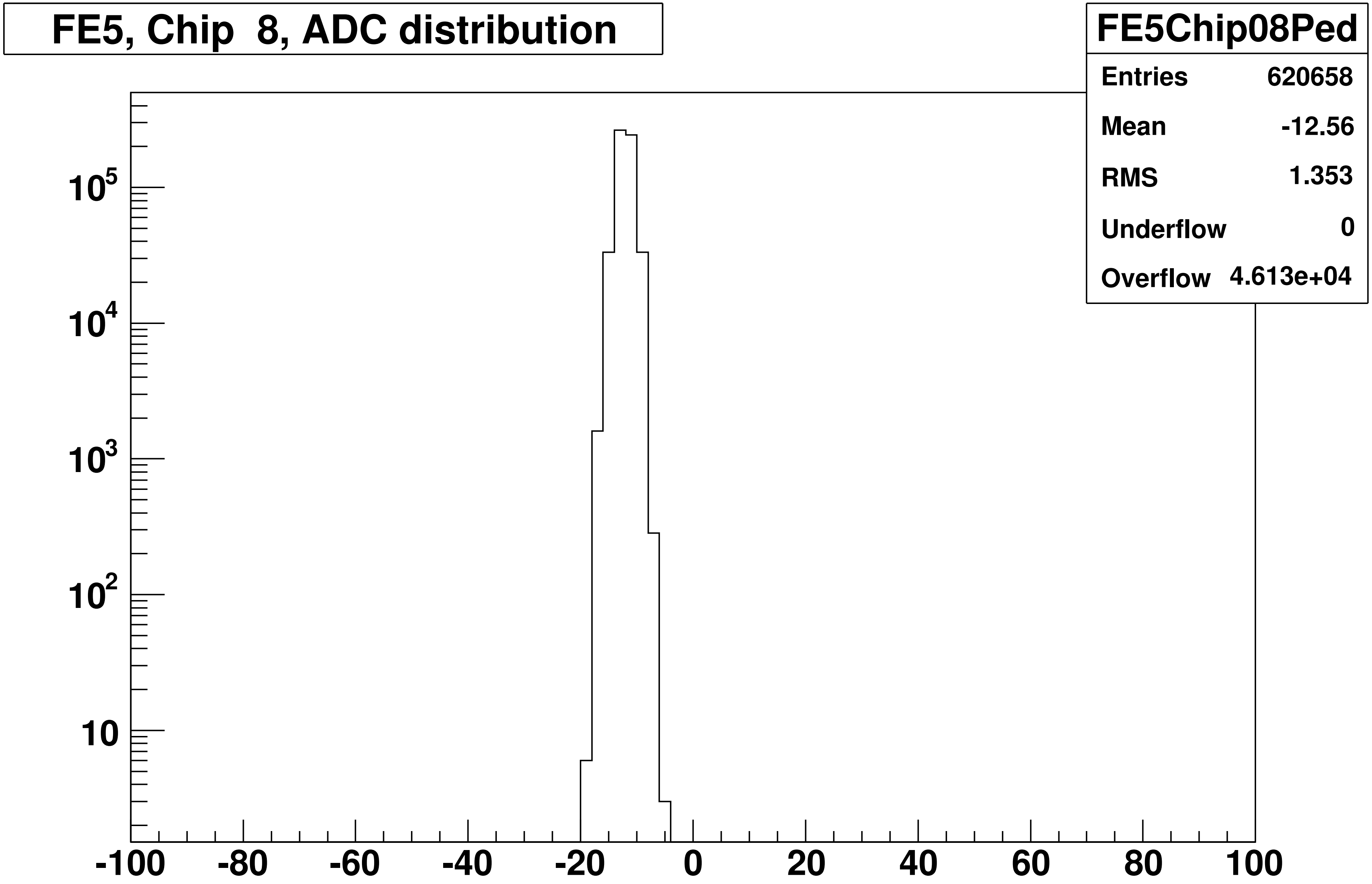
10000
15000
20000
25000


FE5, Chip 8, ADC distribution
FE5Chip08Ped
Entries 620658
Mean -12.56
RMS 1.353
Underflow 0
Overflow 4.613e+04
10^5
10^4
10^3
10^2
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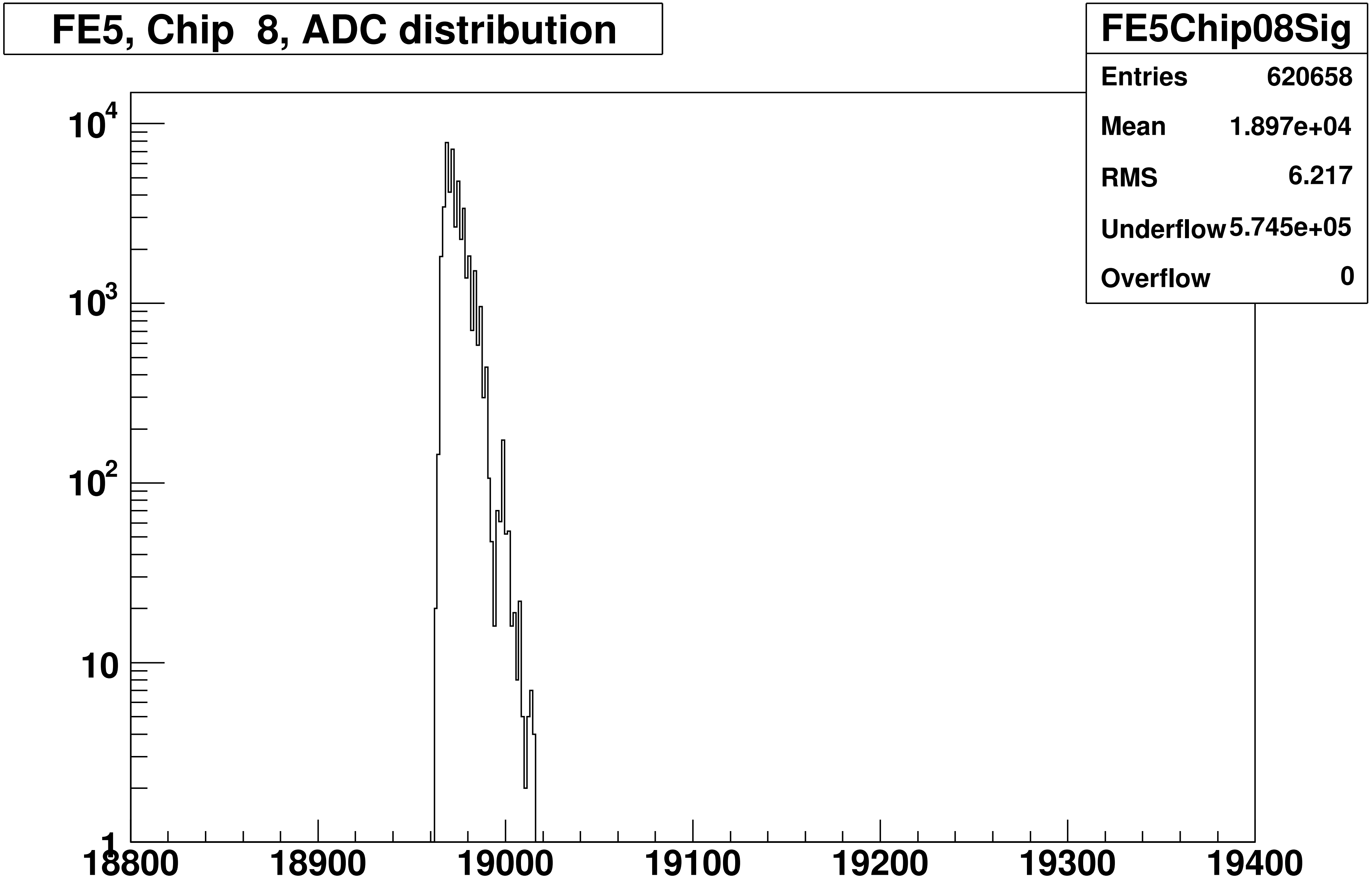
FE5, Chip 8, ADC distribution
FE5Chip08Sig
Entries 620658
Mean 1.897e+04
RMS 6.217
Underflow 5.745e+05
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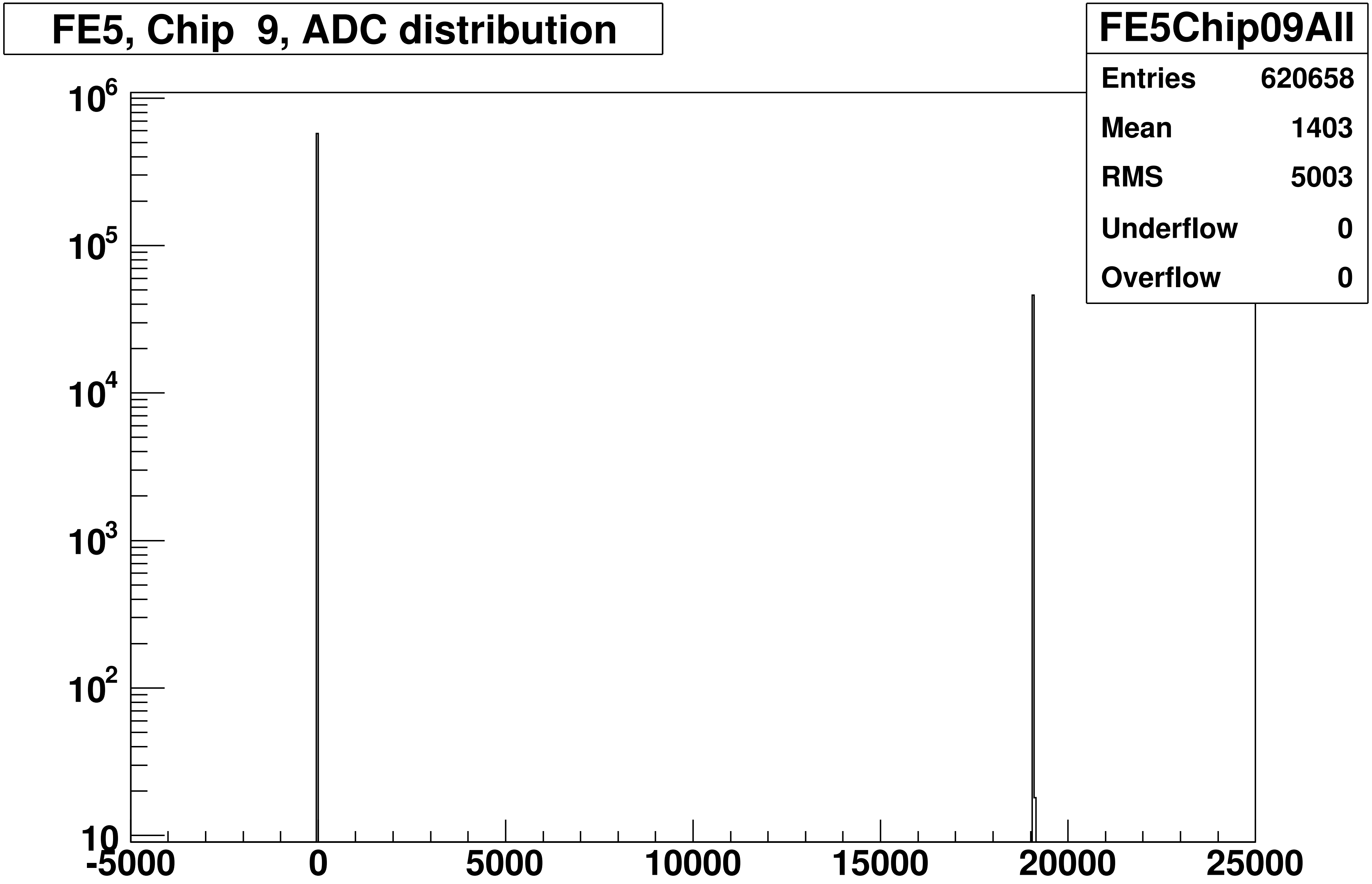
FE5, Chip 9, ADC distribution
FE5Chip09All
Entries 620658
Mean 1403
RMS 5003
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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-5000
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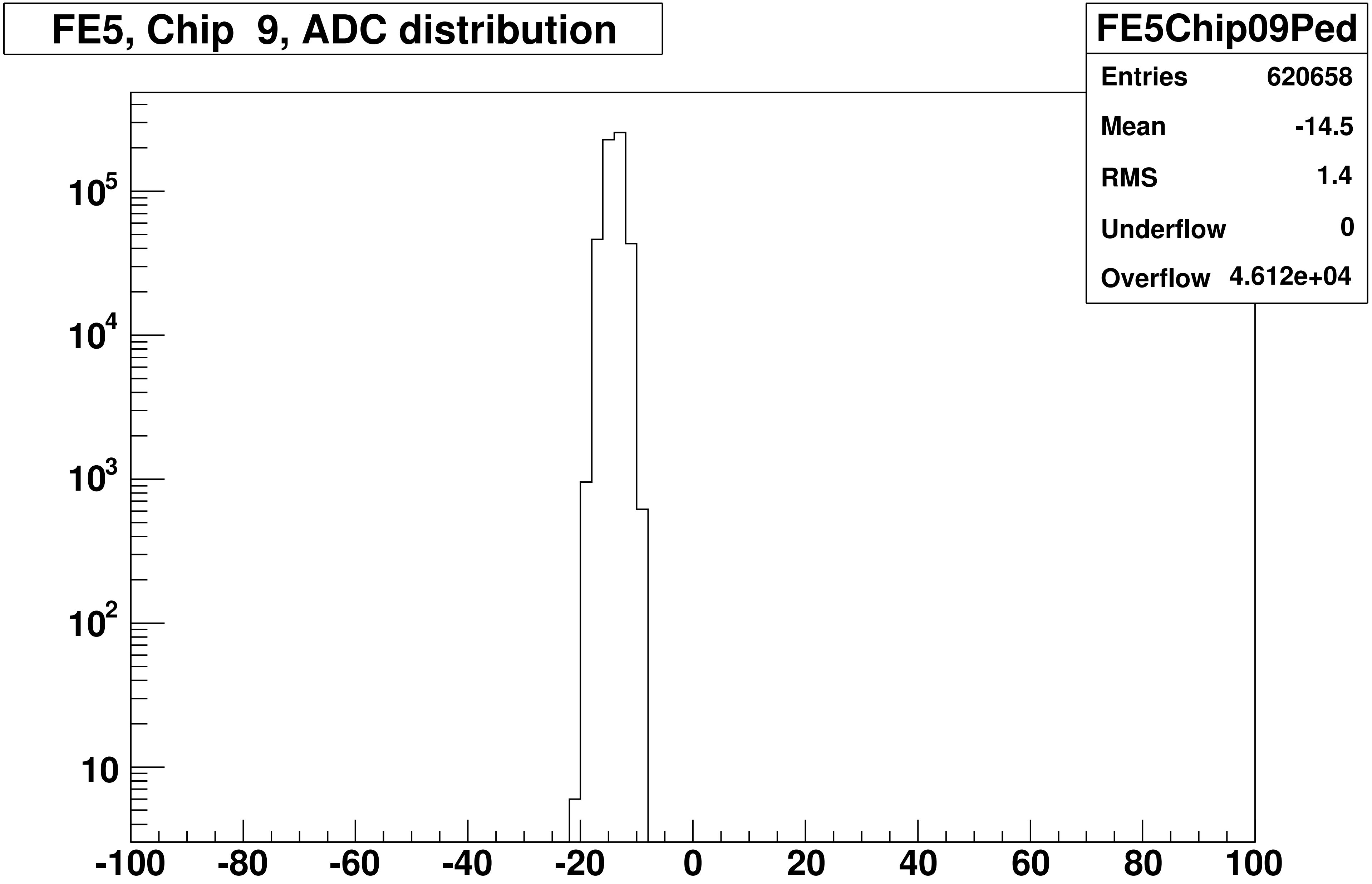
20000
25000


FE5, Chip 9, ADC distribution
FE5Chip09Ped
Entries 620658
Mean -14.5
RMS 1.4
Underflow 0
Overflow 4.612e+04
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10^4
10^3
10^2
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-20
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60
80
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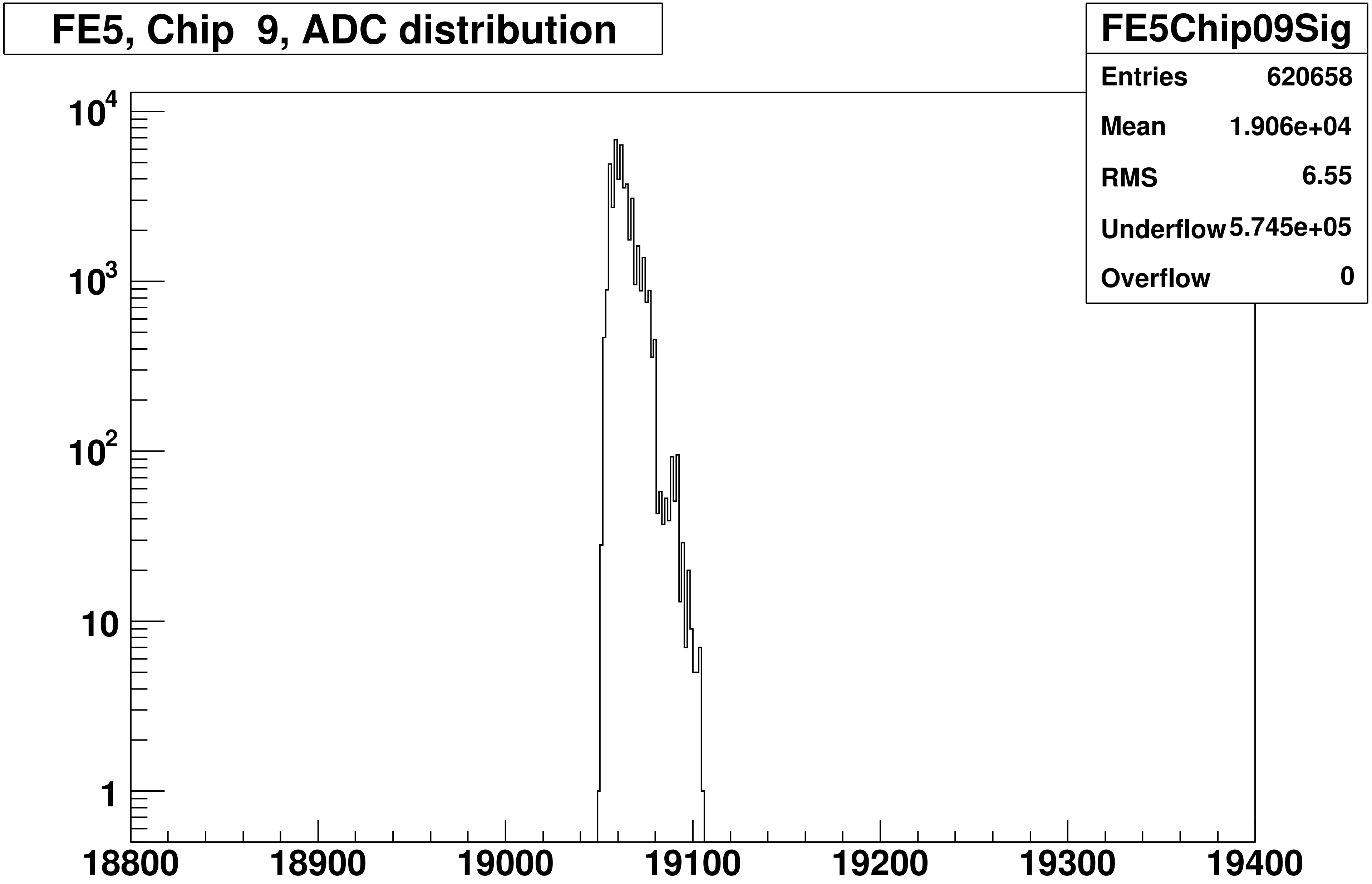
FE5, Chip 9, ADC distribution
FE5Chip09Sig
Entries 620658
Mean 1.906e+04
RMS 6.55
Underflow 5.745e+05
Overflow 0
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10^3
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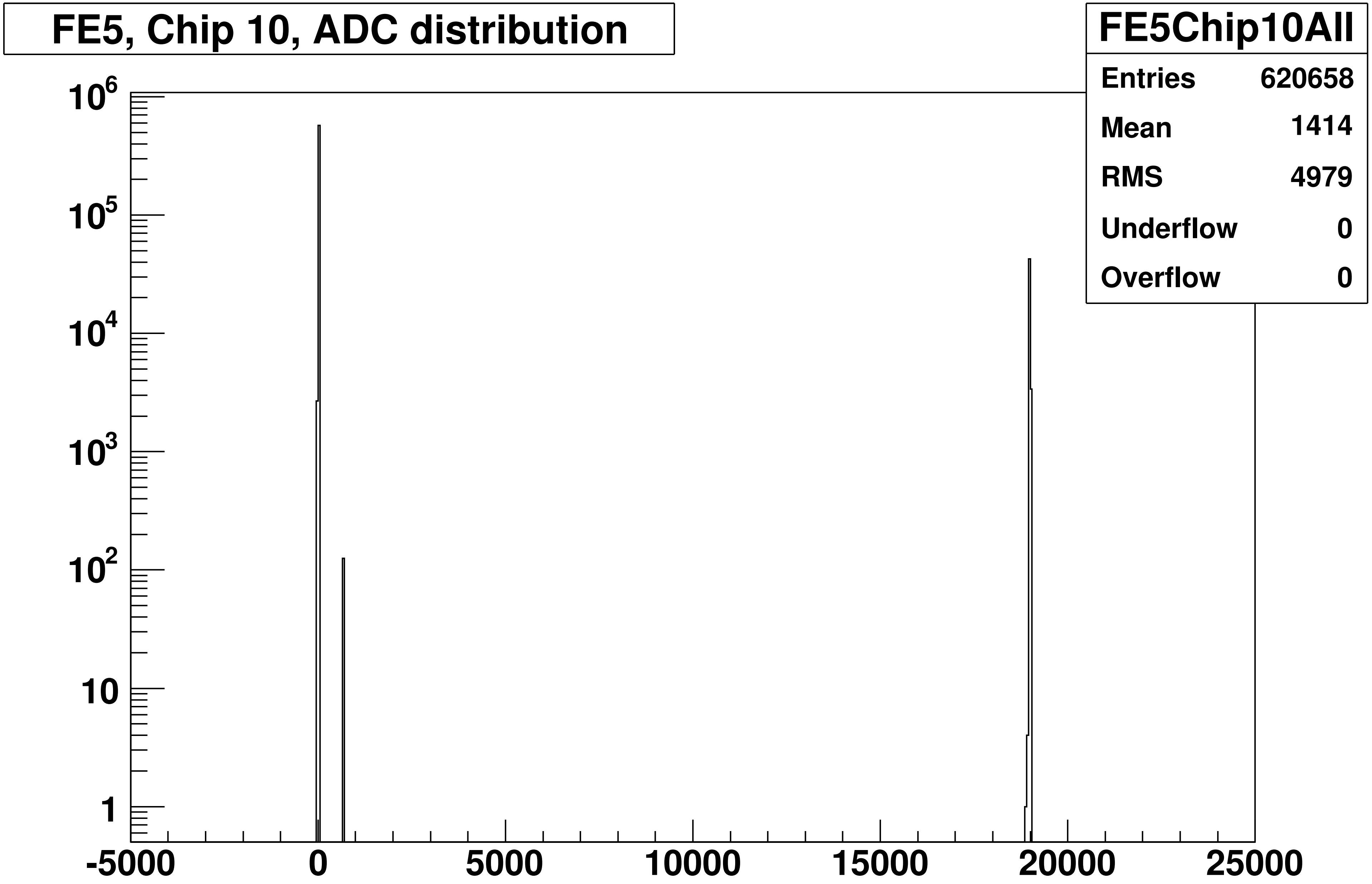
FE5, Chip 10, ADC distribution
FE5Chip10All
Entries 620658
Mean 1414
RMS 4979
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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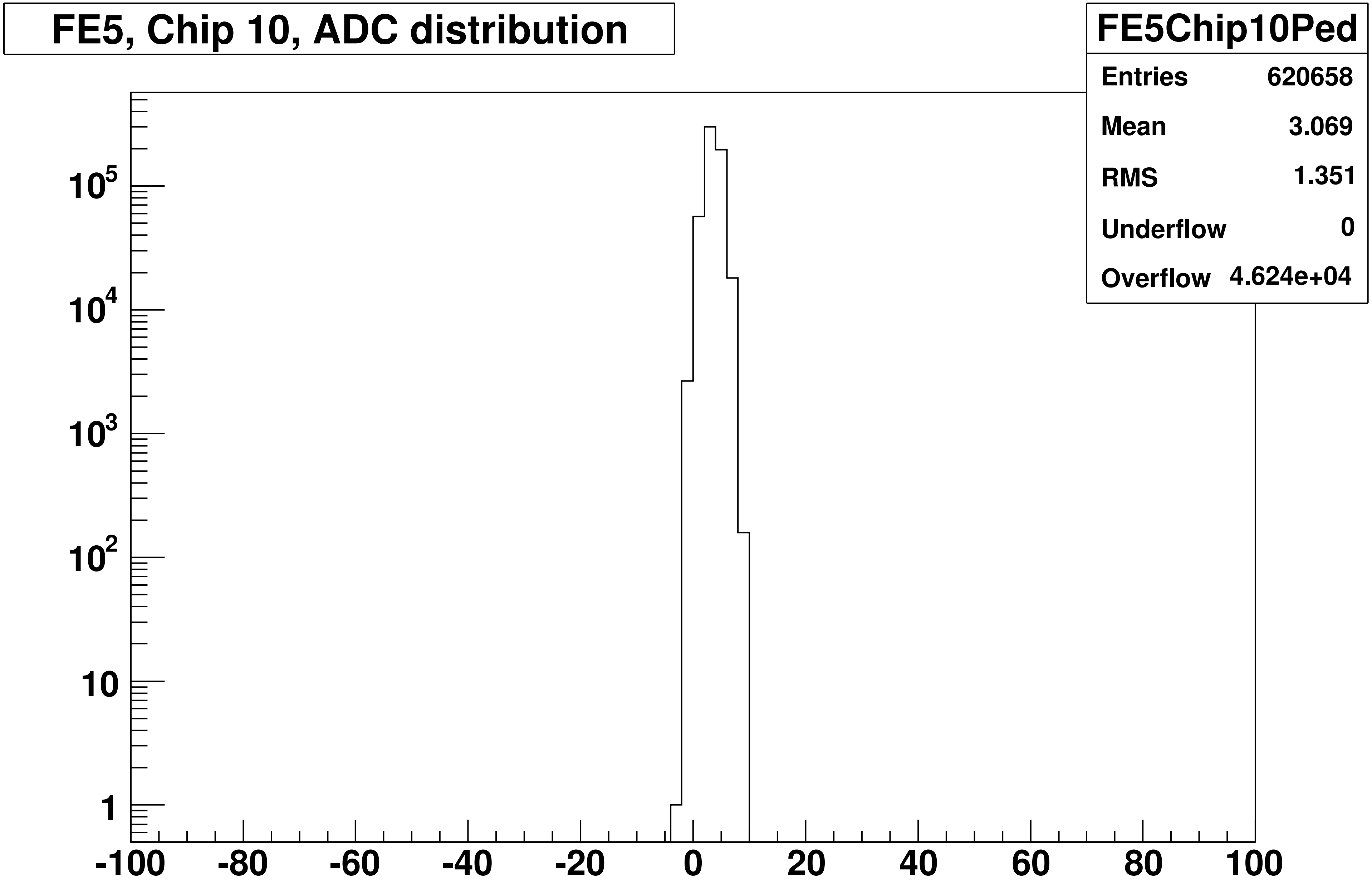
10000
15000
20000
25000


FE5, Chip 10, ADC distribution
FE5Chip10Ped
Entries 620658
Mean 3.069
RMS 1.351
Underflow 0
Overflow 4.624e+04
10^5
10^4
10^3
10^2
10
1
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-40
-20
0
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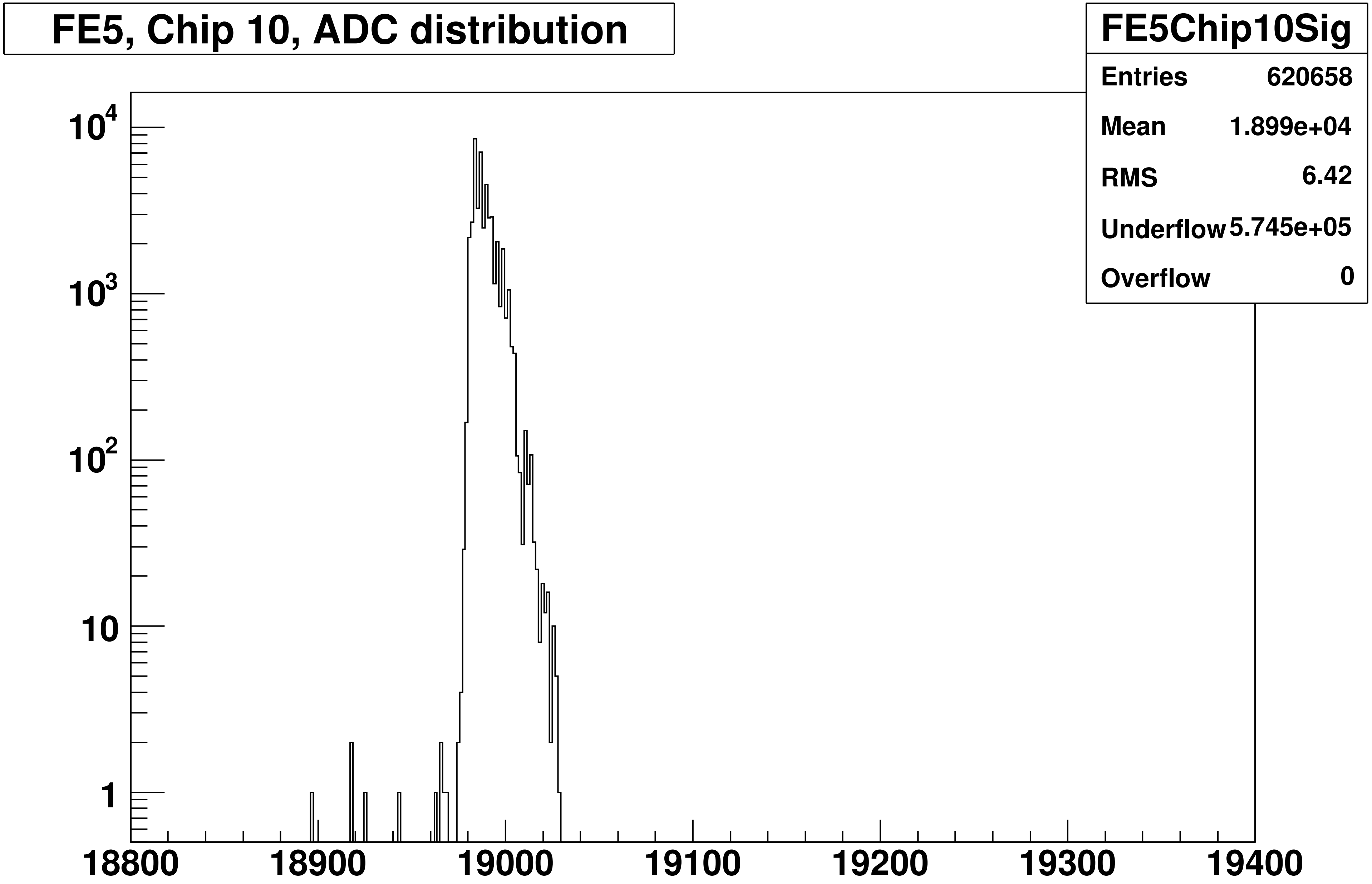
40
60
80
100



FE5, Chip 10, ADC distribution
FE5Chip10Sig
Entries 620658
Mean 1.899e+04
RMS 6.42
Underflow 5.745e+05
Overflow 0
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10^3
10^2
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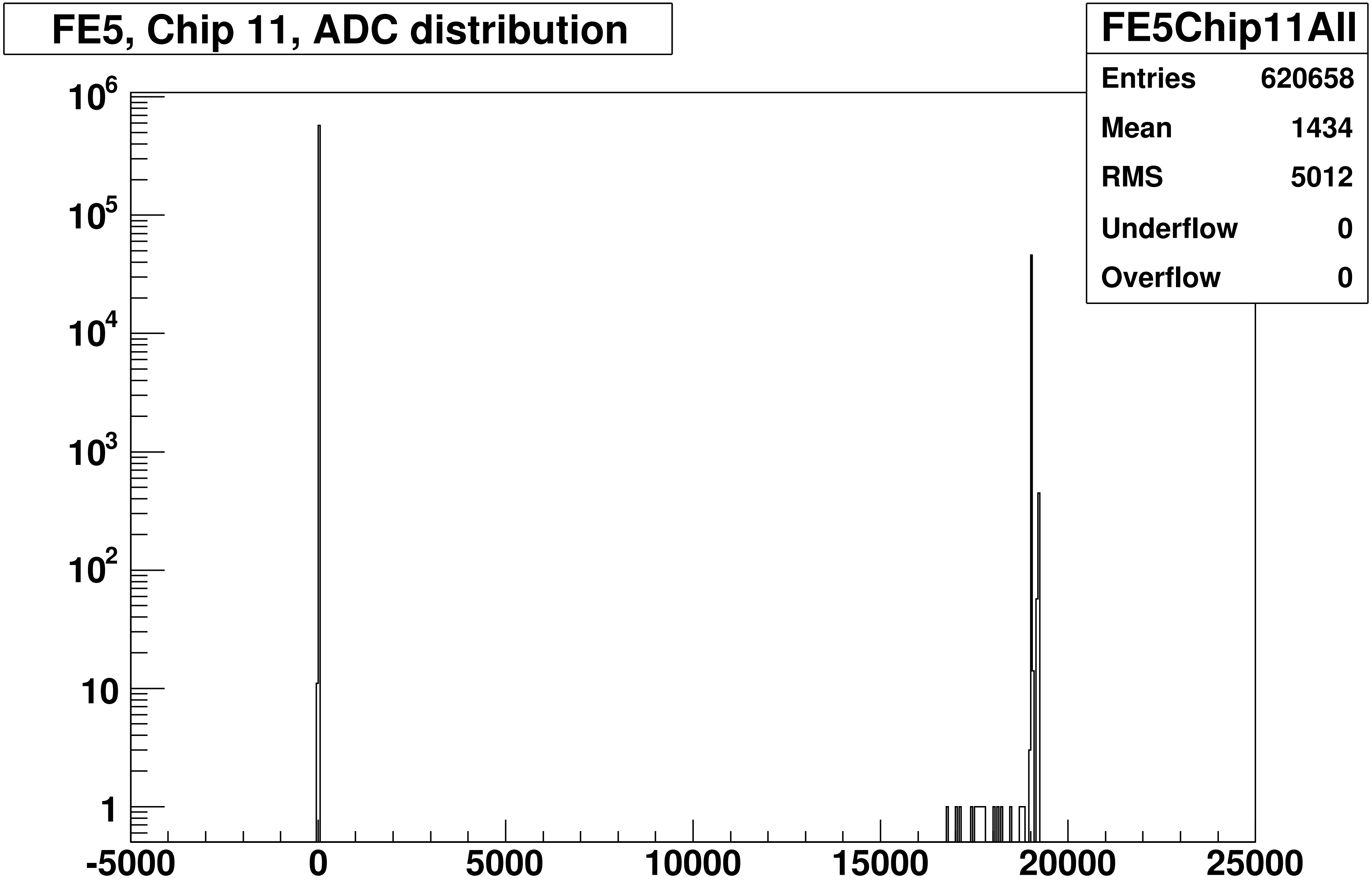

FE5, Chip 11, ADC distribution
FE5Chip11All
Entries 620658
Mean 1434
RMS 5012
Underflow 0
Overflow 0
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10^4
10^3
10^2
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20000
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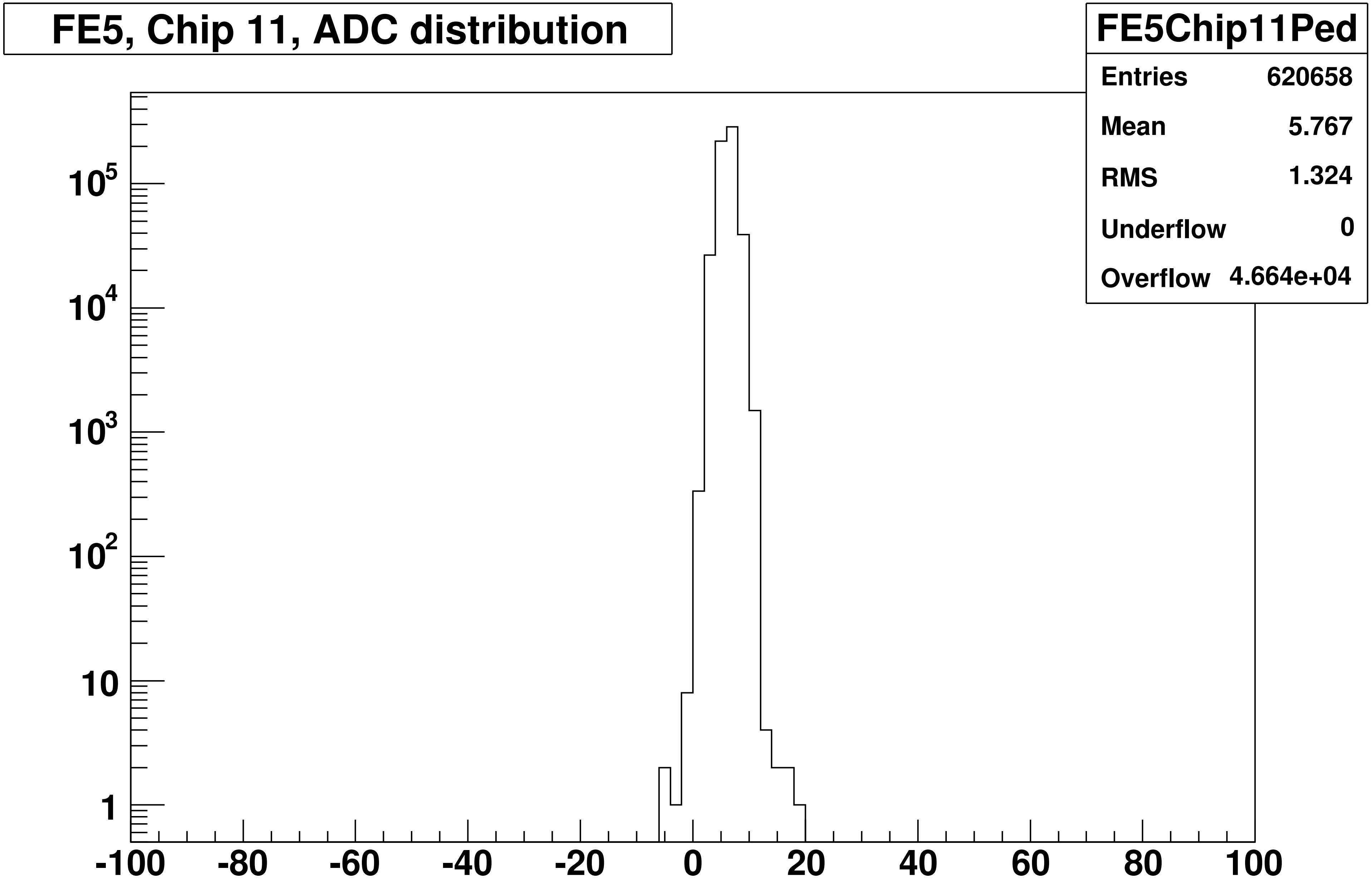
FE5, Chip 11, ADC distribution
FE5Chip11Ped
Entries 620658
Mean 5.767
RMS 1.324
Underflow 0
Overflow 4.664e+04
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10^4
10^3
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-20
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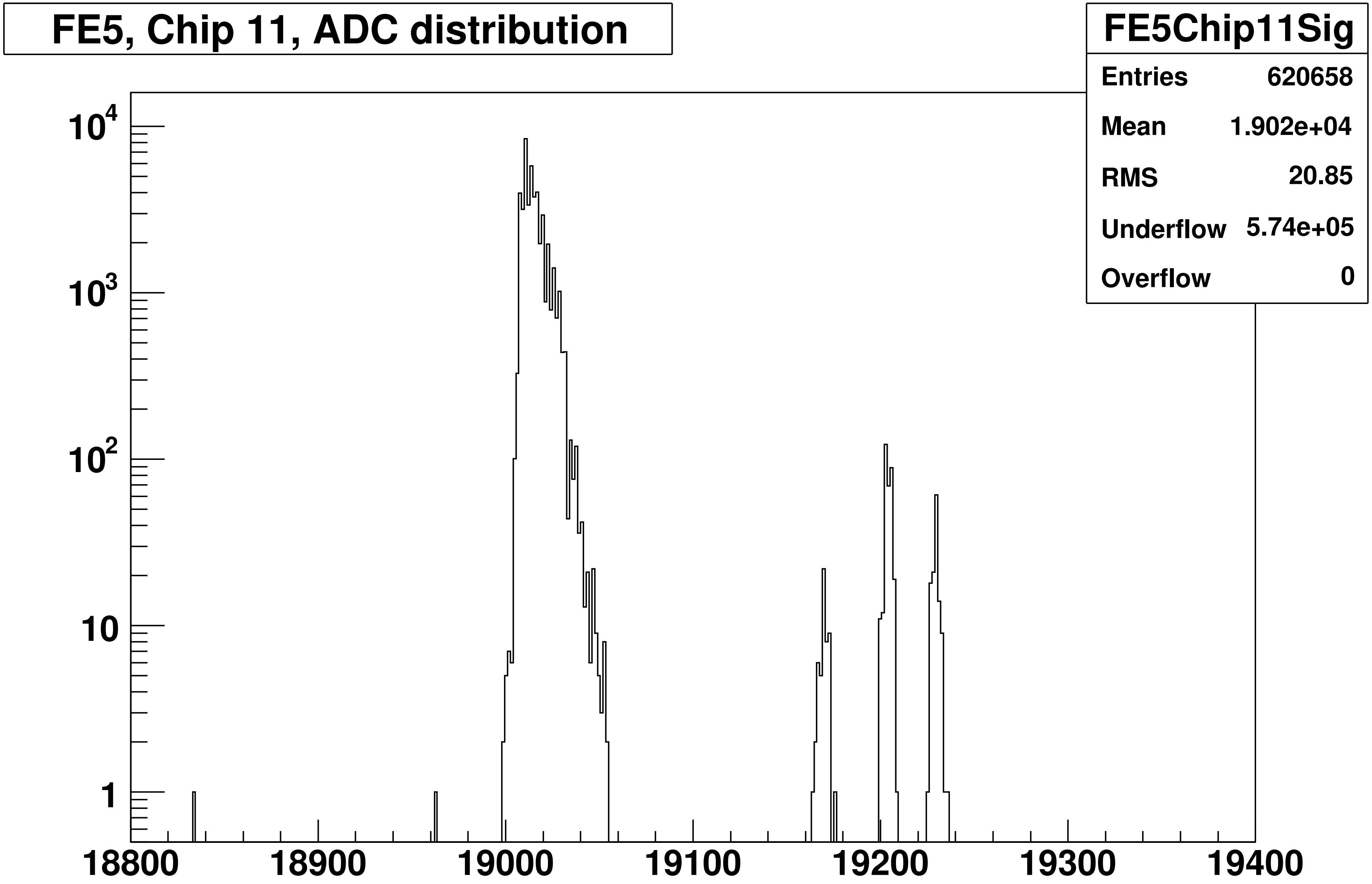

FE5, Chip 11, ADC distribution
FE5Chip11Sig
Entries 620658
Mean 1.902e+04
RMS 20.85
Underflow 5.74e+05
Overflow 0
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10^3
10^2
10
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18900
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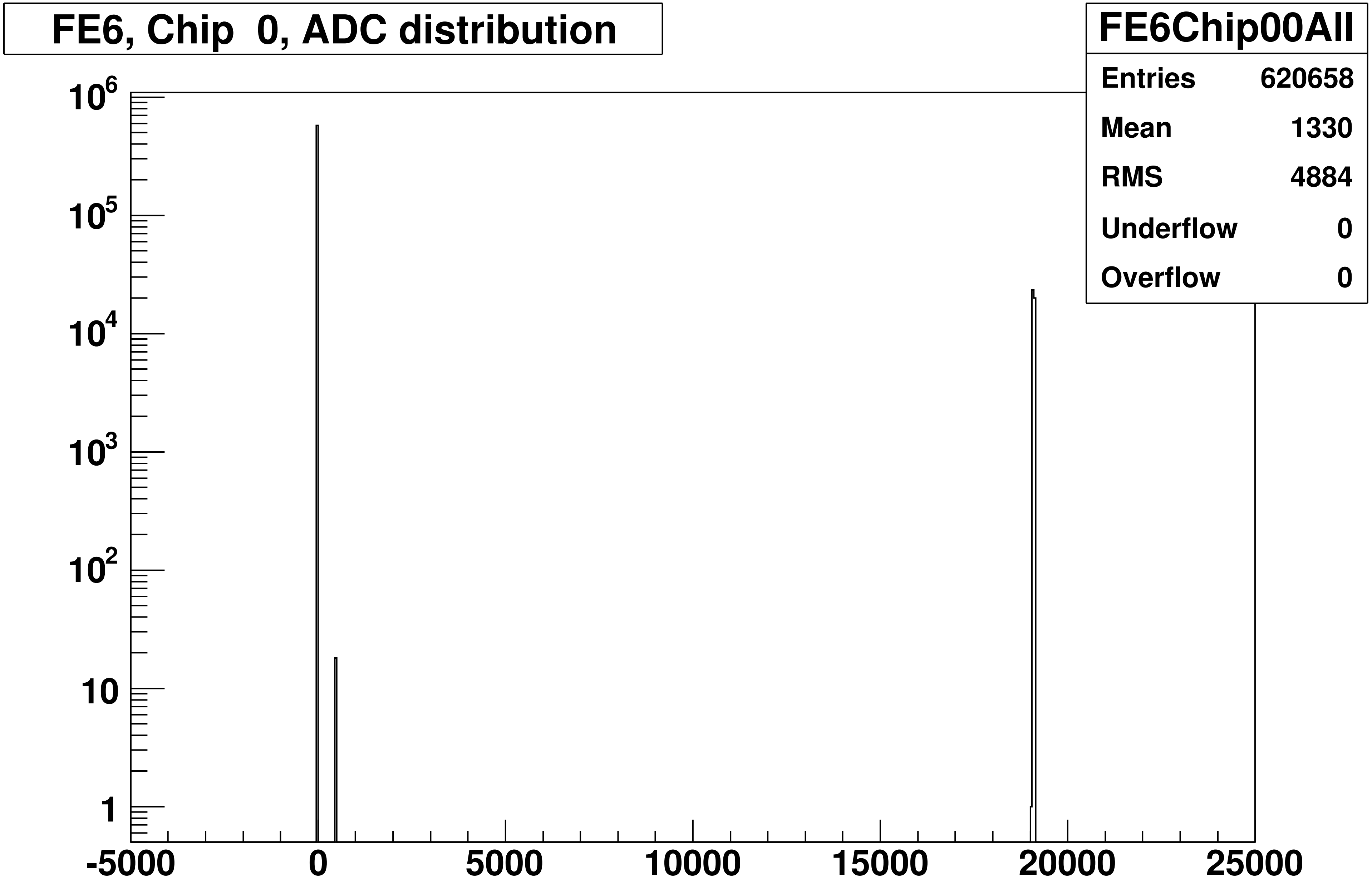
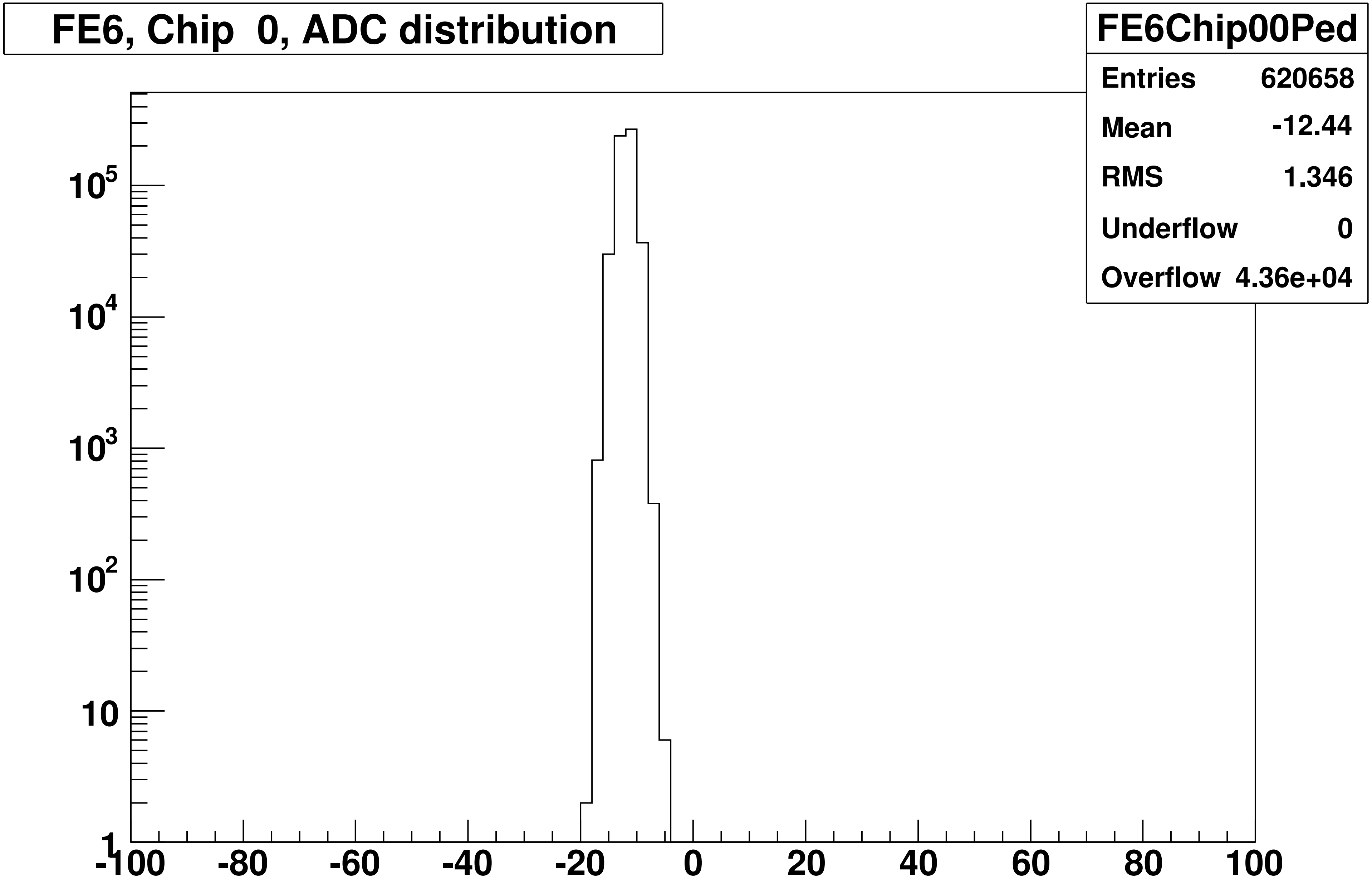
19200
19300
19400



FE6, Chip 0, ADC distribution
FE6Chip00All
Entries 620658
Mean 1330
RMS 4884
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000



FE6, Chip 0, ADC distribution
FE6Chip00Ped
Entries 620658
Mean -12.44
RMS 1.346
Underflow 0
Overflow 4.36e+04
10^5
10^4
10^3
10^2
10
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-100
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-40
-20
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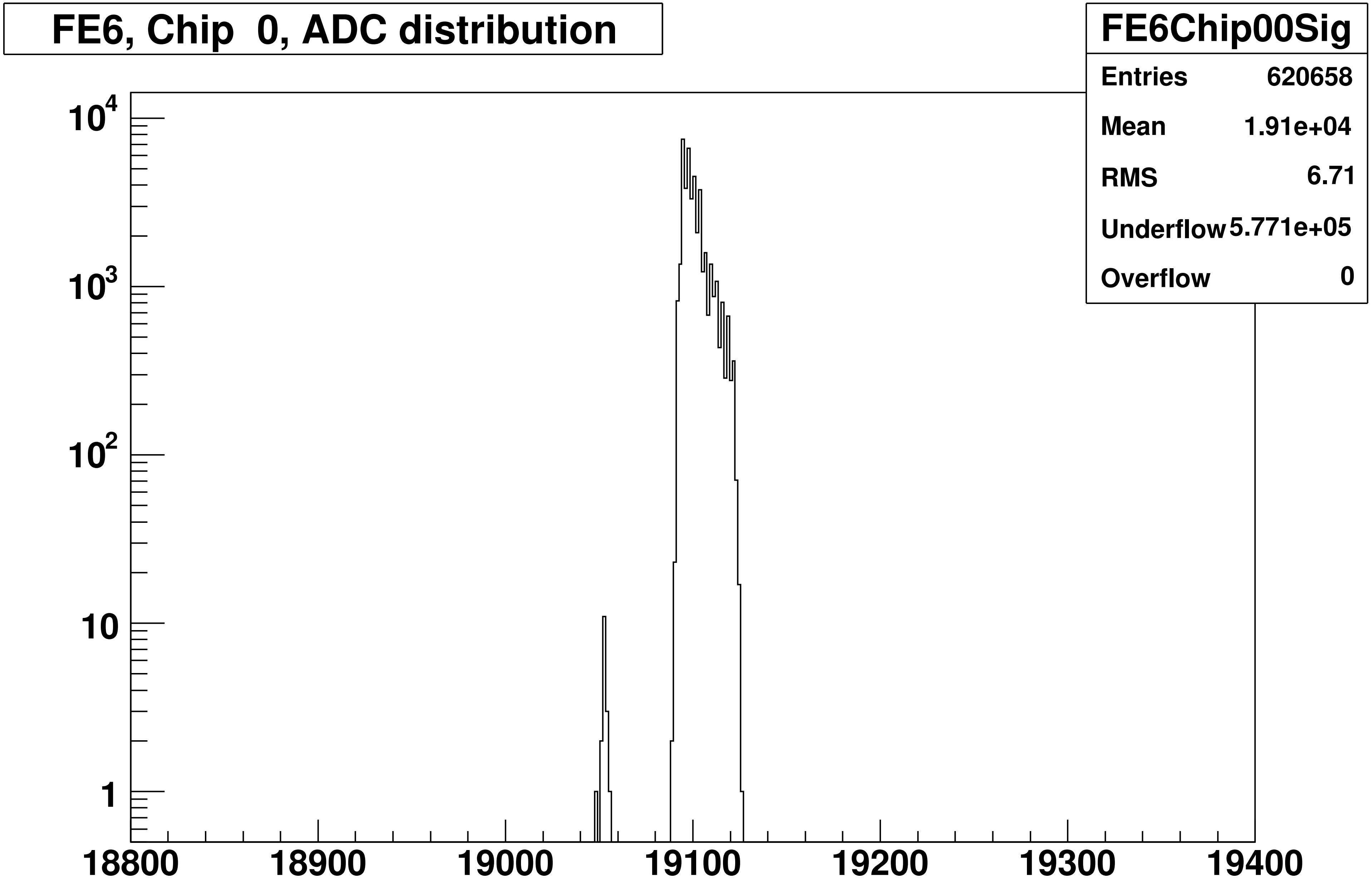
FE6, Chip 0, ADC distribution
FE6Chip00Sig
Entries 620658
Mean 1.91e+04
RMS 6.71
Underflow 5.771e+05
Overflow 0
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10^3
10^2
10
1
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18900
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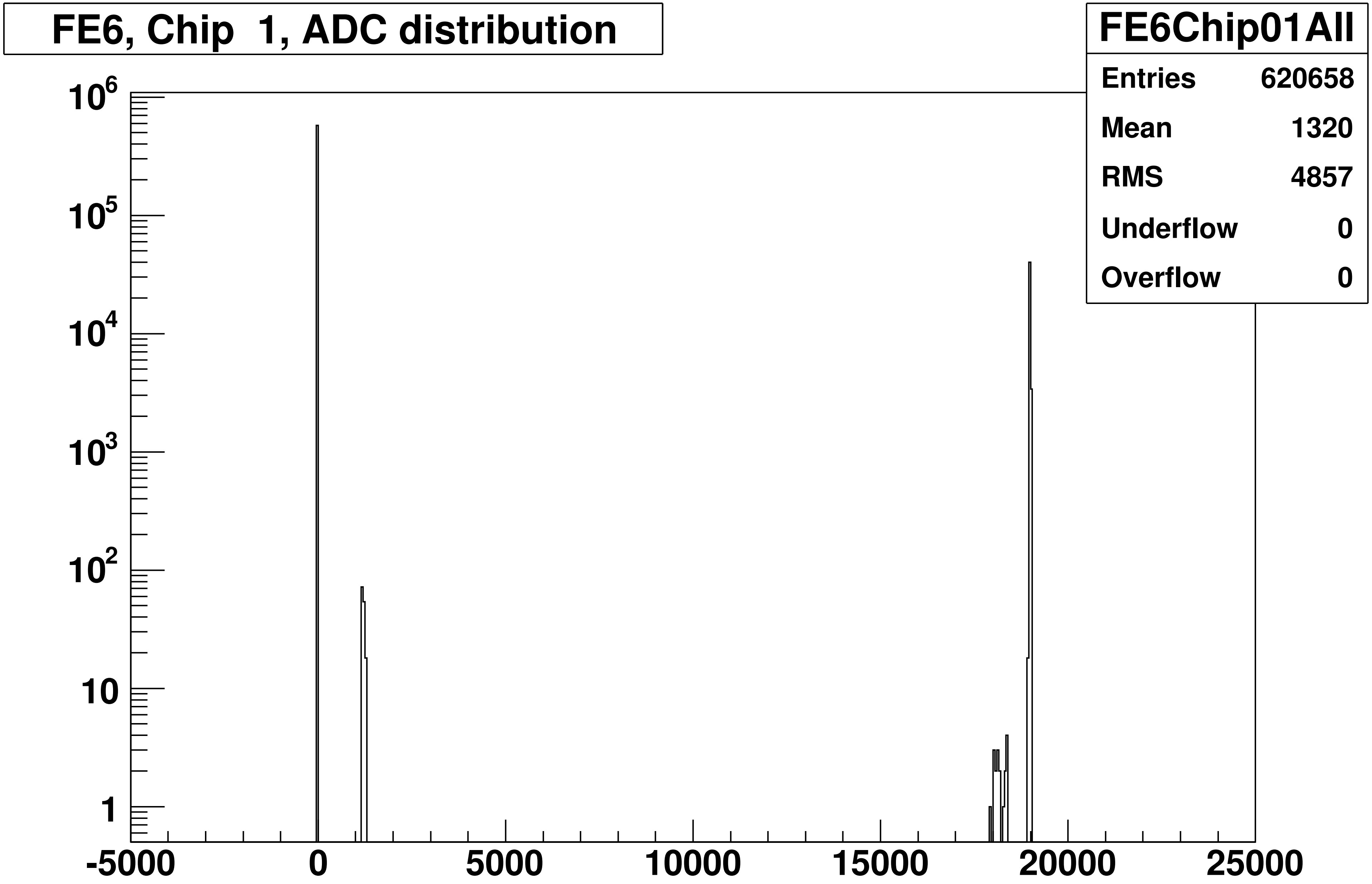
19400



FE6, Chip 1, ADC distribution
FE6Chip01All
Entries 620658
Mean 1320
RMS 4857
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
1
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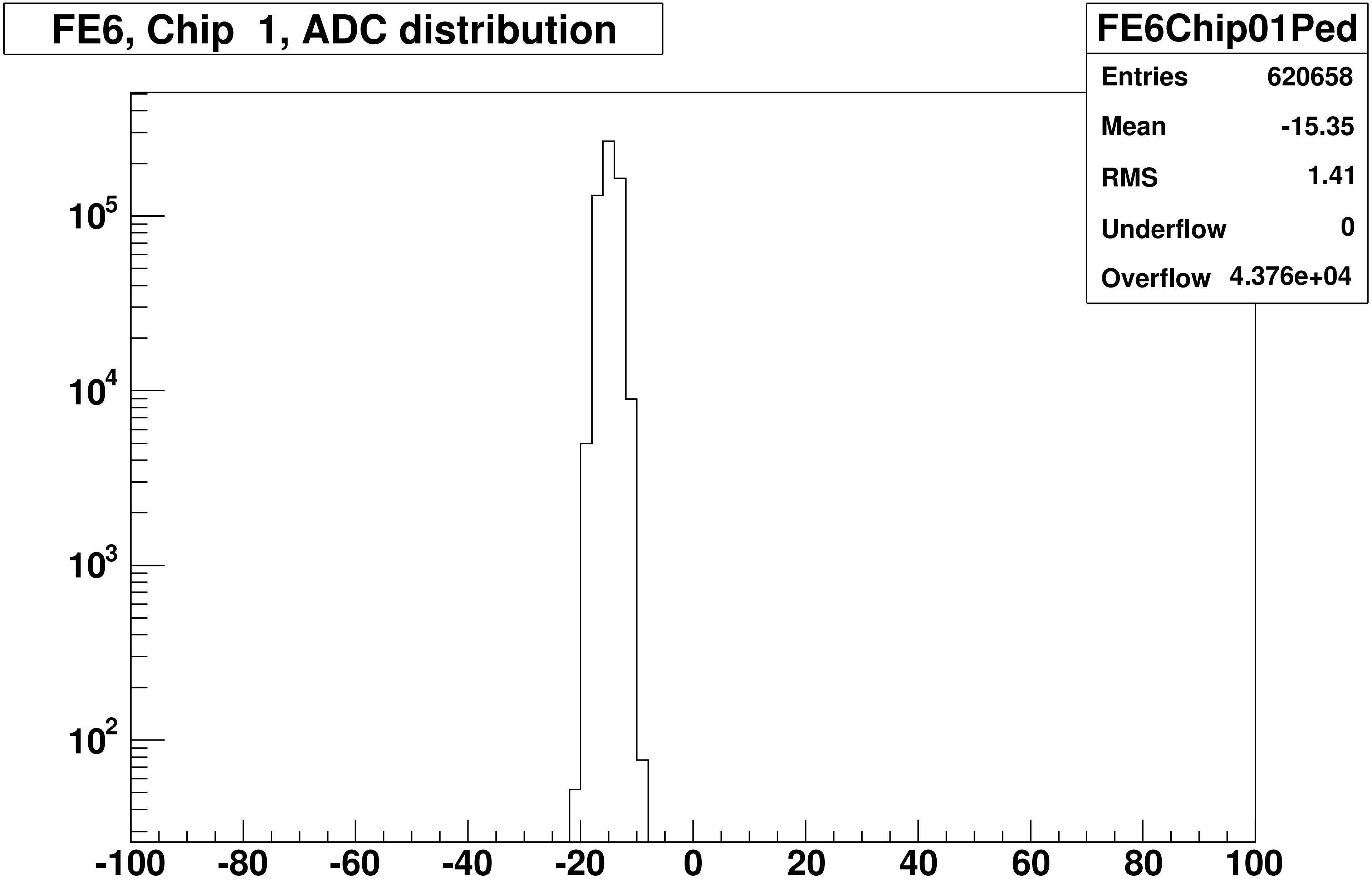
FE6, Chip 1, ADC distribution
FE6Chip01Ped
Entries 620658
Mean -15.35
RMS 1.41
Underflow 0
Overflow 4.376e+04
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10^4
10^3
10^2
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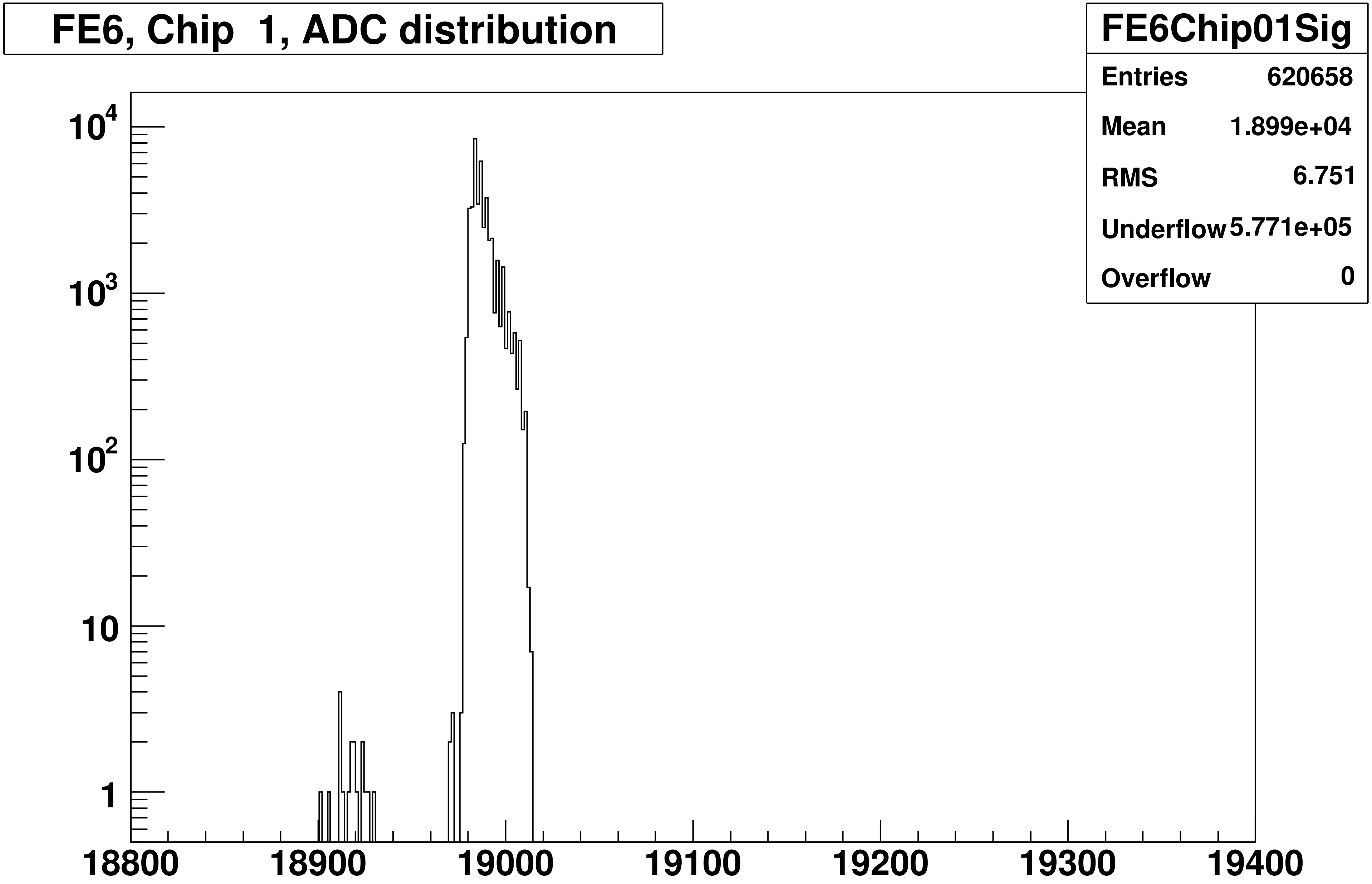
FE6, Chip 1, ADC distribution
FE6Chip01Sig
Entries 620658
Mean 1.899e+04
RMS 6.751
Underflow 5.771e+05
Overflow 0
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10^3
10^2
10
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18900
19000
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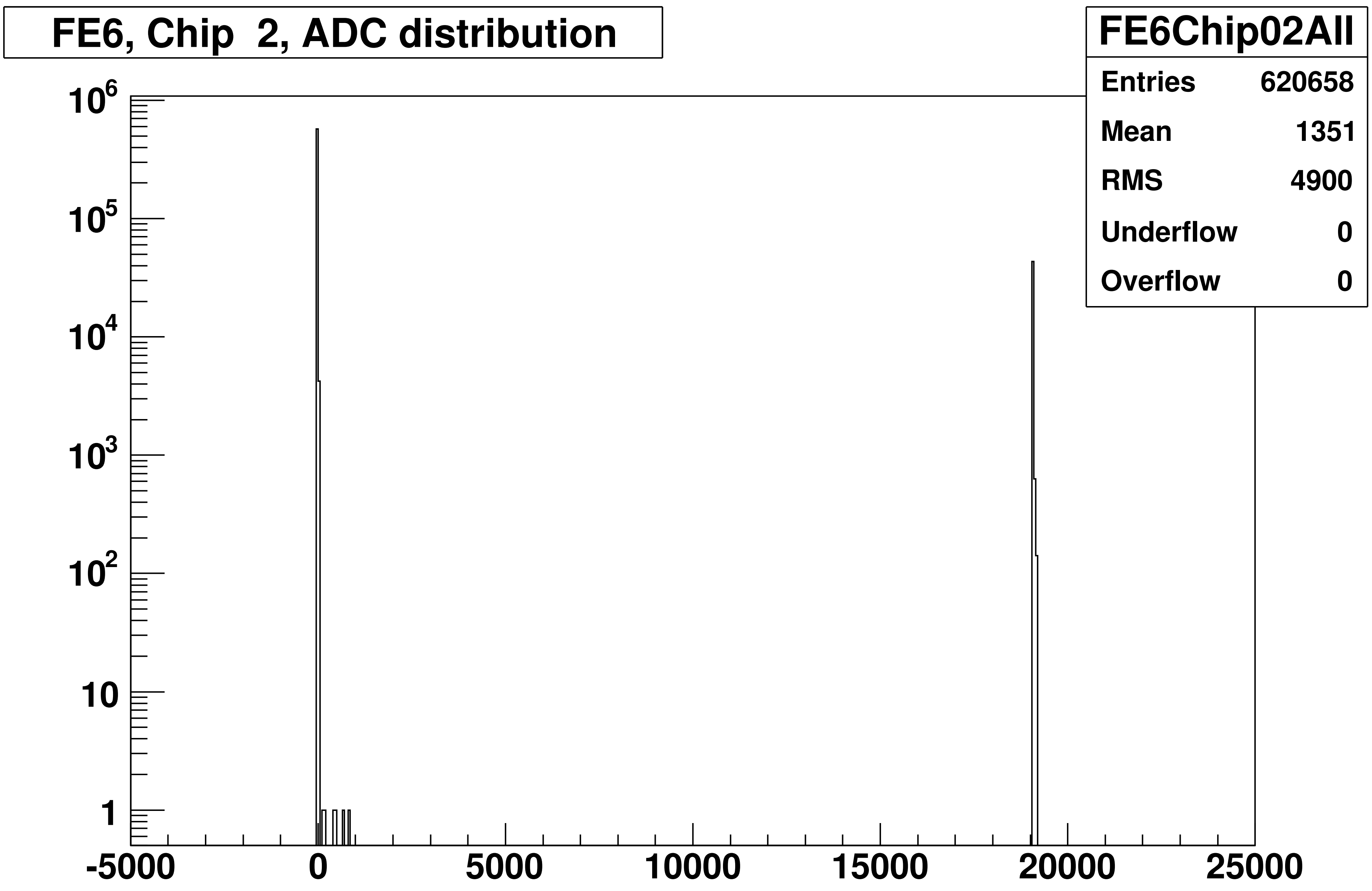
19200
19300
19400


FE6, Chip 2, ADC distribution
FE6Chip02All
Entries 620658
Mean 1351
RMS 4900
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
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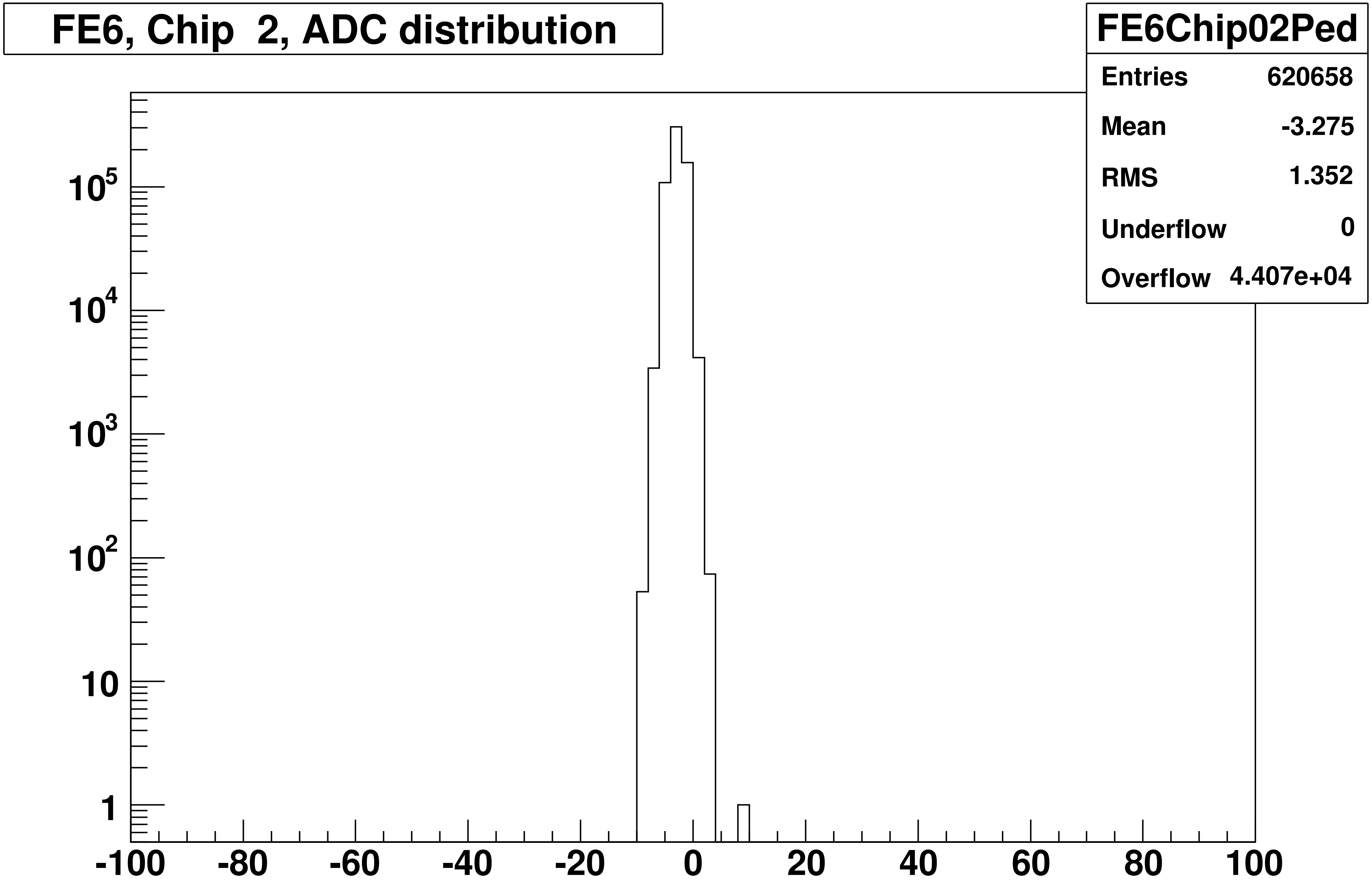
FE6, Chip 2, ADC distribution
FE6Chip02Ped
Entries 620658
Mean -3.275
RMS 1.352
Underflow 0
Overflow 4.407e+04
10^5
10^4
10^3
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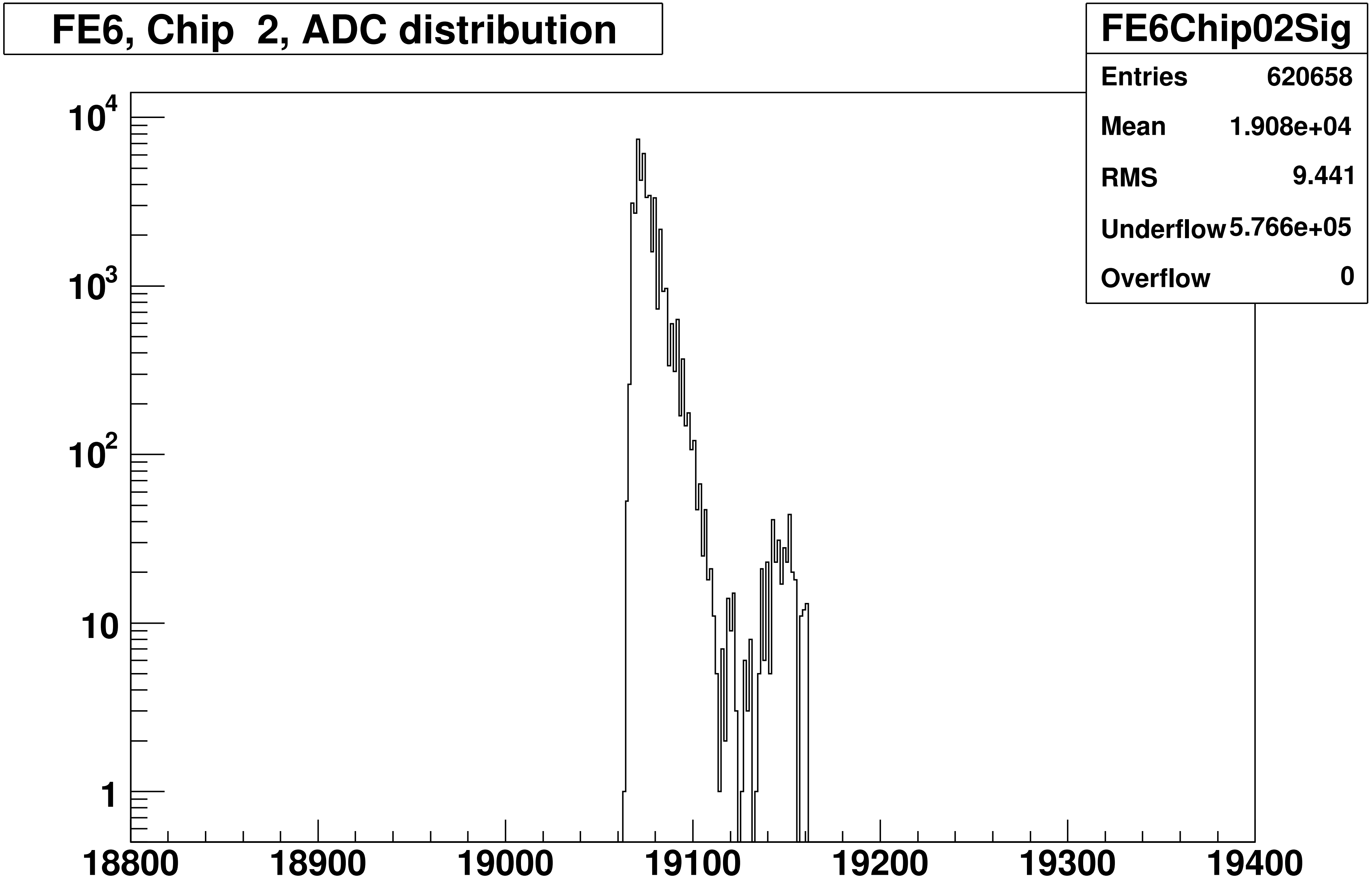
-100 -80 -60 -40 -20 0 20 40 60 80 100



FE6, Chip 2, ADC distribution
FE6Chip02Sig
Entries 620658
Mean 1.908e+04
RMS 9.441
Underflow 5.766e+05
Overflow 0
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10^3
10^2
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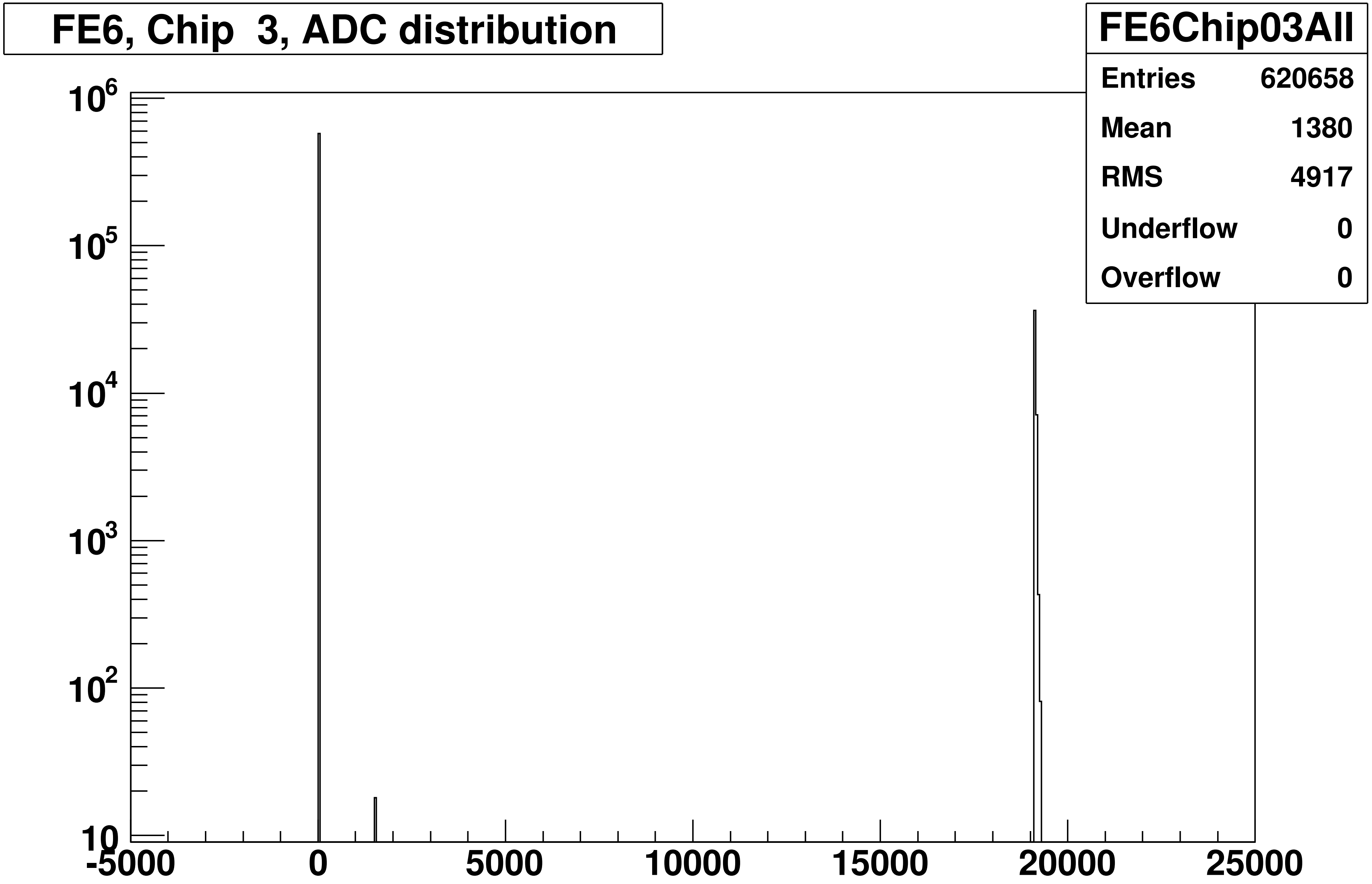
FE6, Chip 3, ADC distribution
FE6Chip03All
Entries 620658
Mean 1380
RMS 4917
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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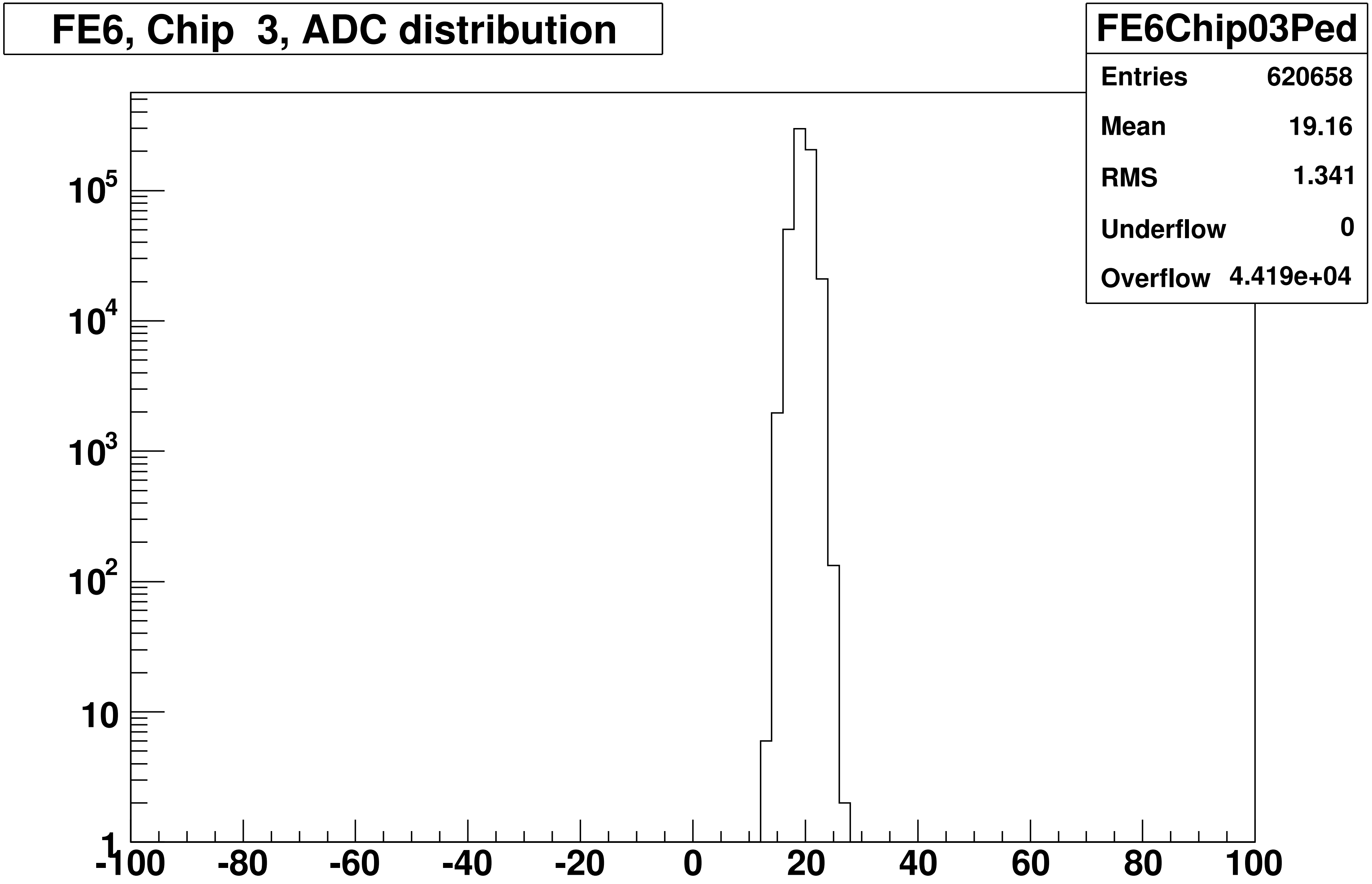
FE6, Chip 3, ADC distribution
FE6Chip03Ped
Entries 620658
Mean 19.16
RMS 1.341
Underflow 0
Overflow 4.419e+04
10^5
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10^3
10^2
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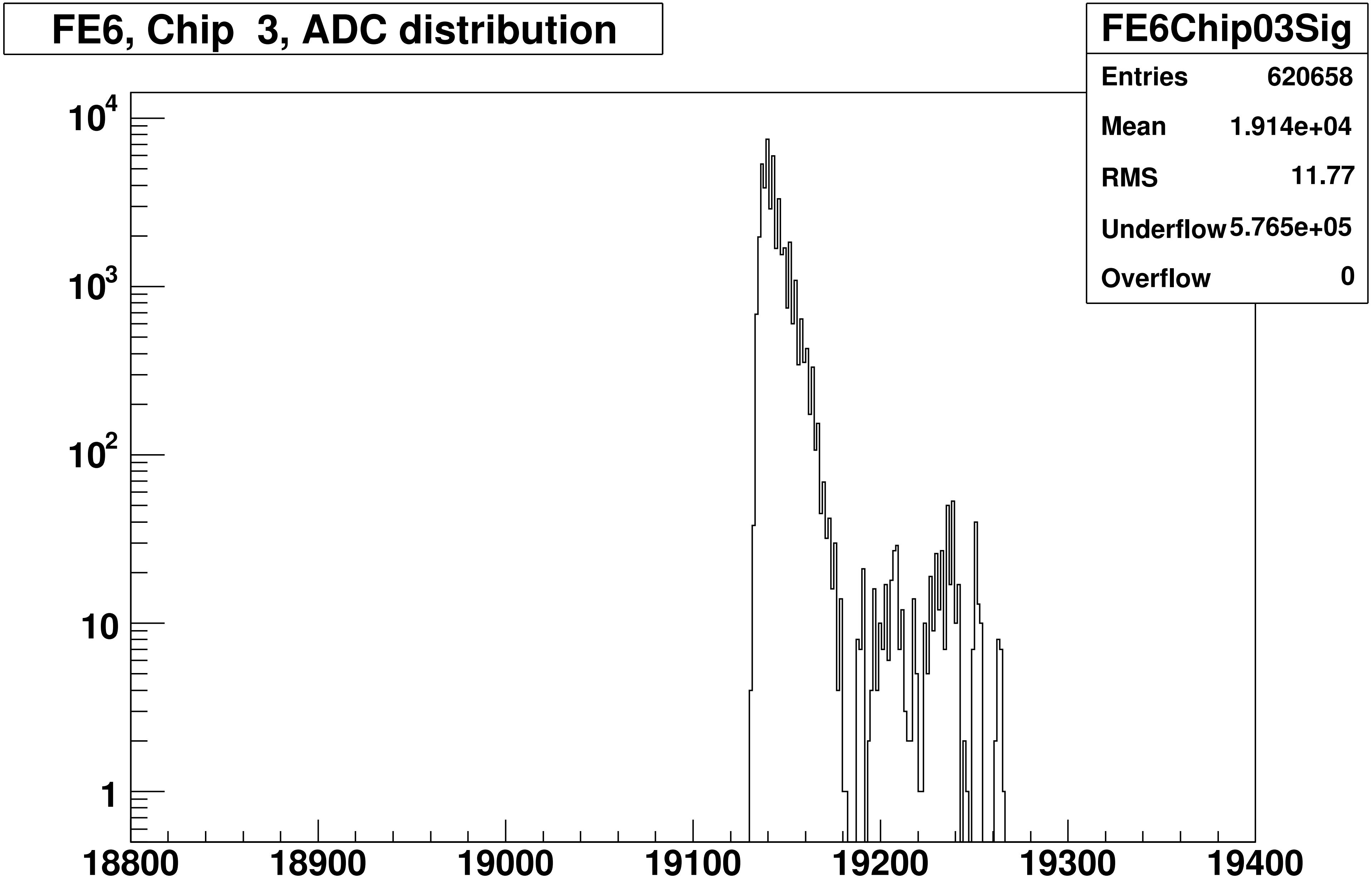

FE6, Chip 3, ADC distribution
FE6Chip03Sig
Entries 620658
Mean 1.914e+04
RMS 11.77
Underflow 5.765e+05
Overflow 0
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10^3
10^2
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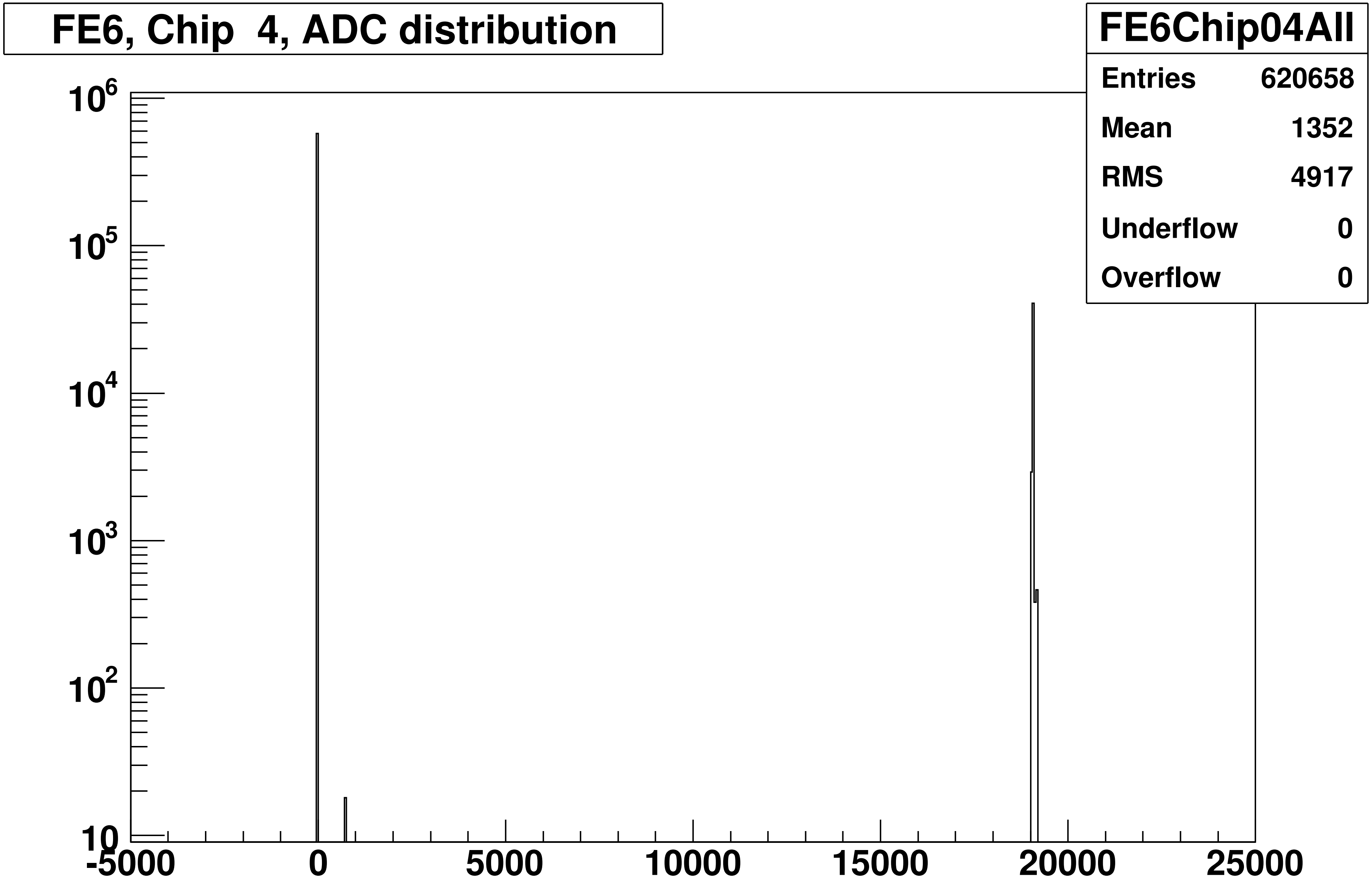
FE6, Chip 4, ADC distribution
FE6Chip04All
Entries 620658
Mean 1352
RMS 4917
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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20000
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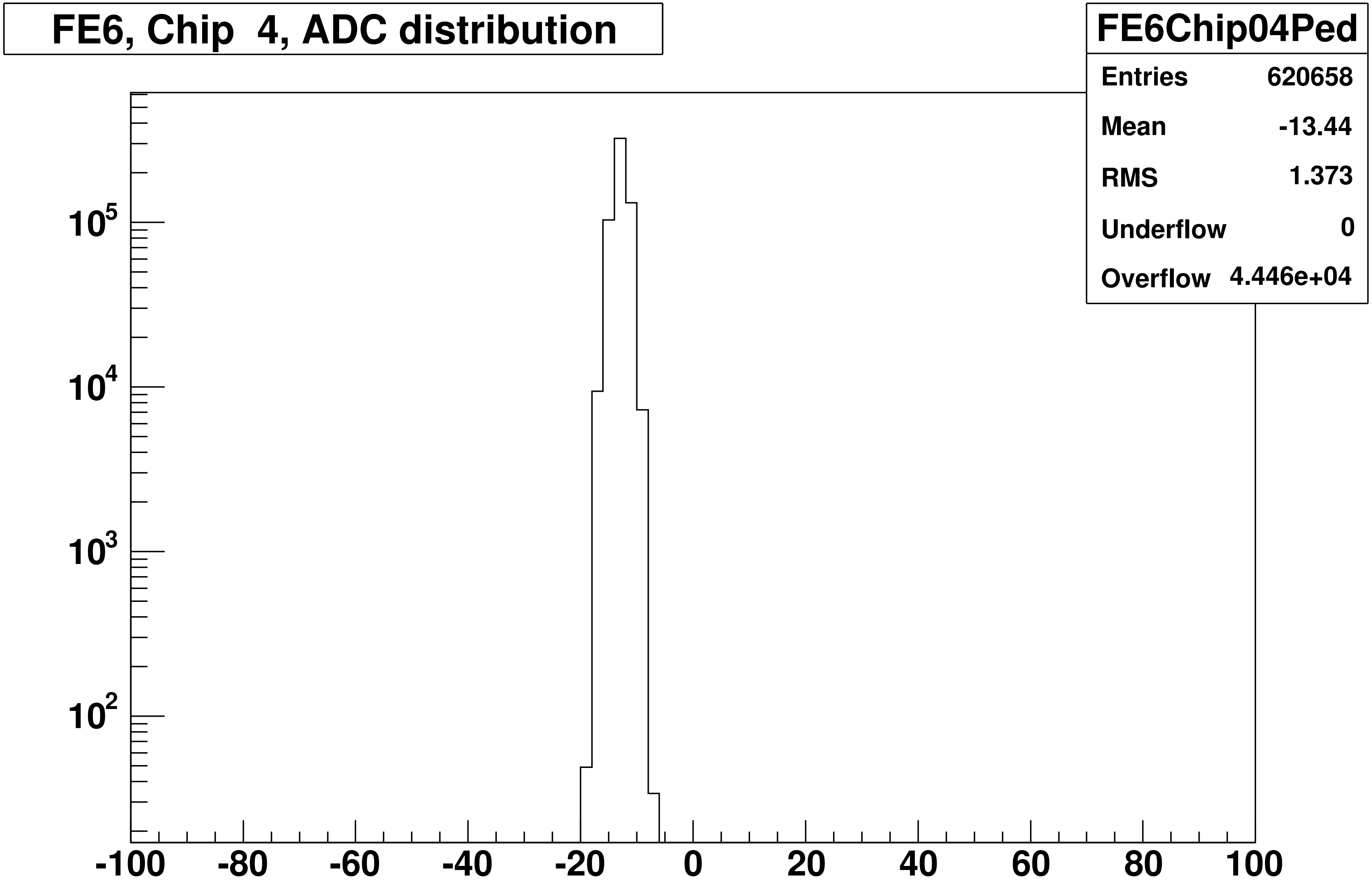
FE6, Chip 4, ADC distribution
FE6Chip04Ped
Entries 620658
Mean -13.44
RMS 1.373
Underflow 0
Overflow 4.446e+04
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10^4
10^3
10^2
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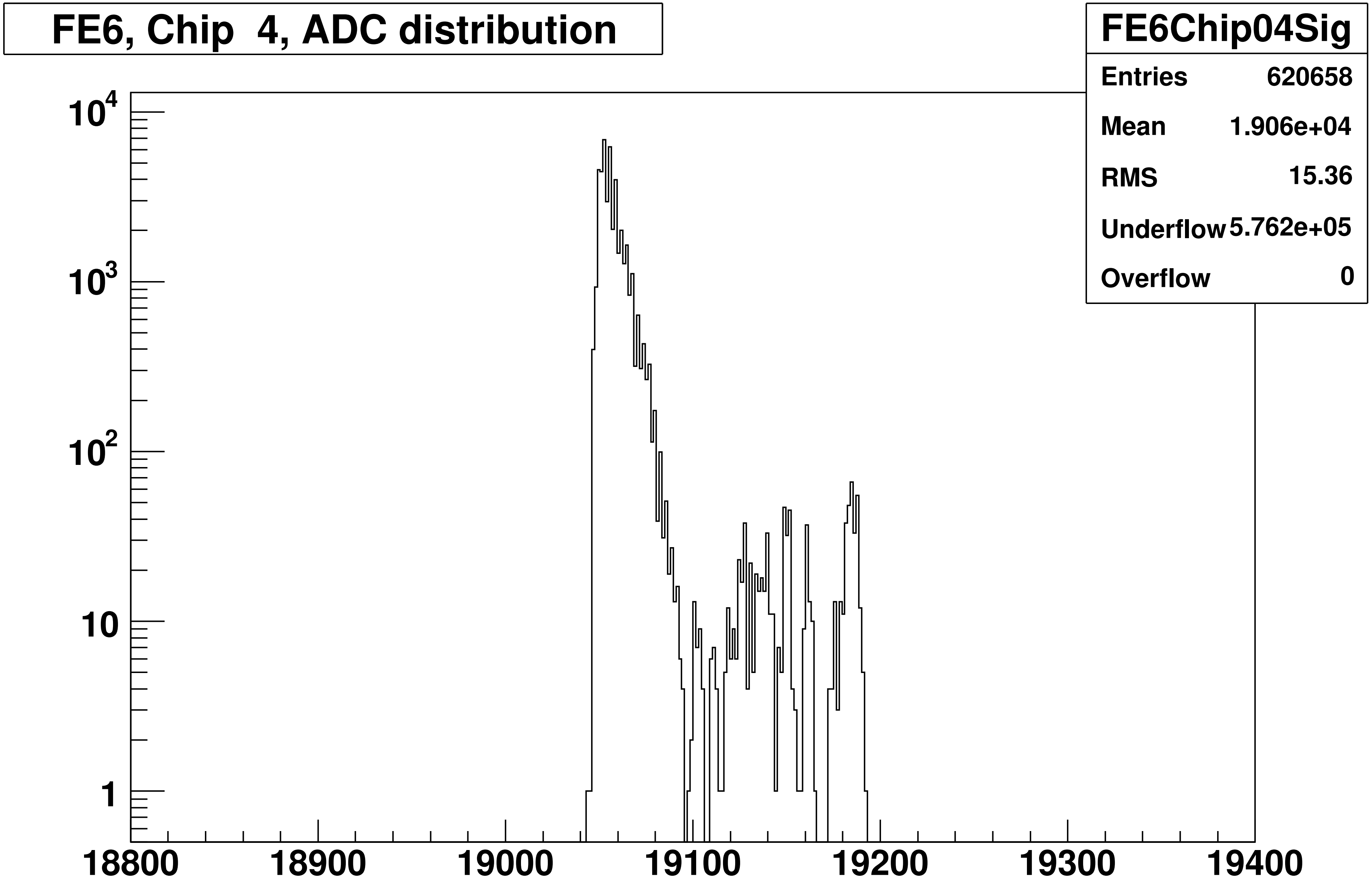
FE6, Chip 4, ADC distribution
FE6Chip04Sig
Entries 620658
Mean 1.906e+04
RMS 15.36
Underflow 5.762e+05
Overflow 0
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10^3
10^2
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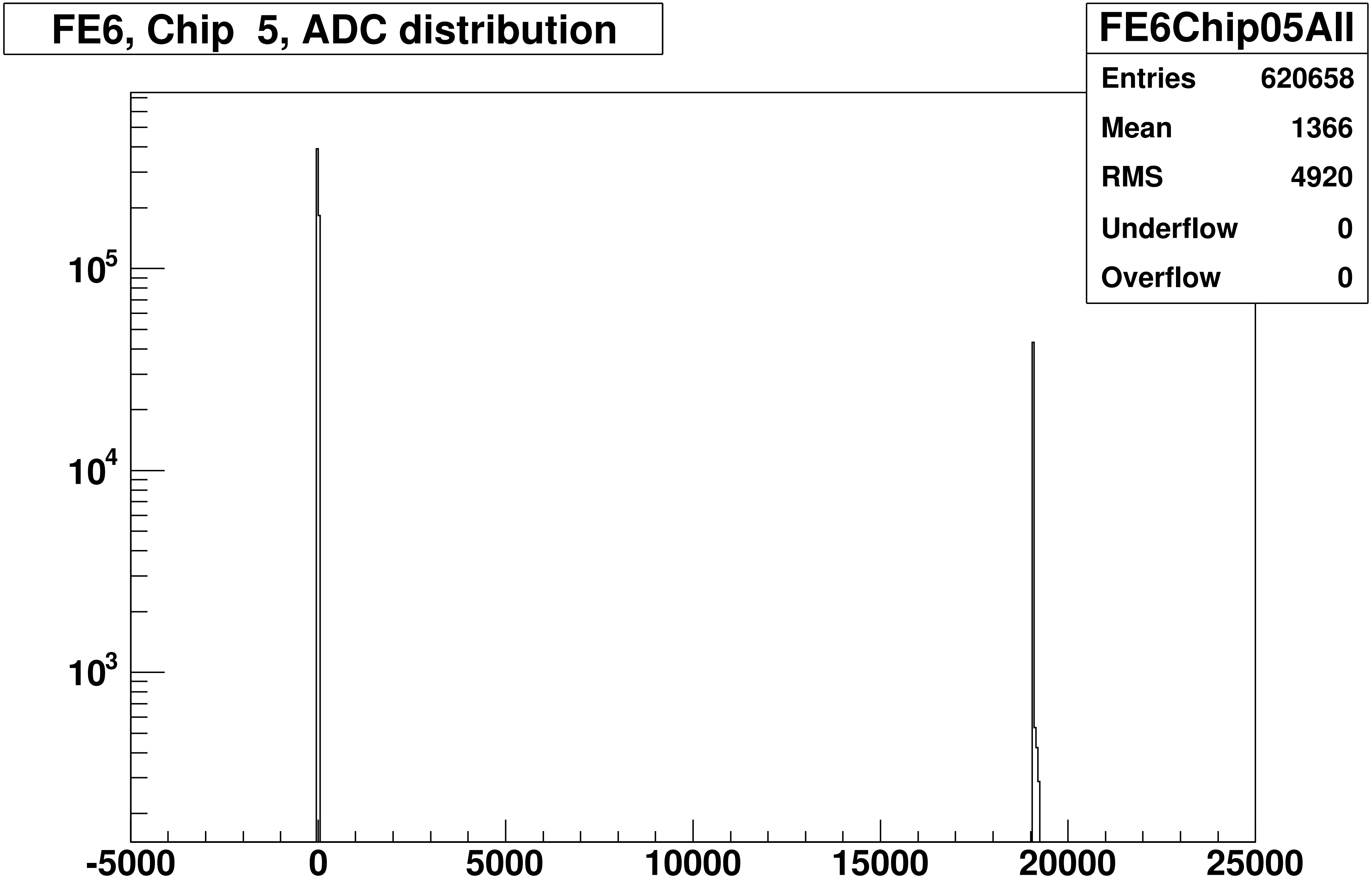
FE6, Chip 5, ADC distribution
FE6Chip05All
Entries 620658
Mean 1366
RMS 4920
Underflow 0
Overflow 0
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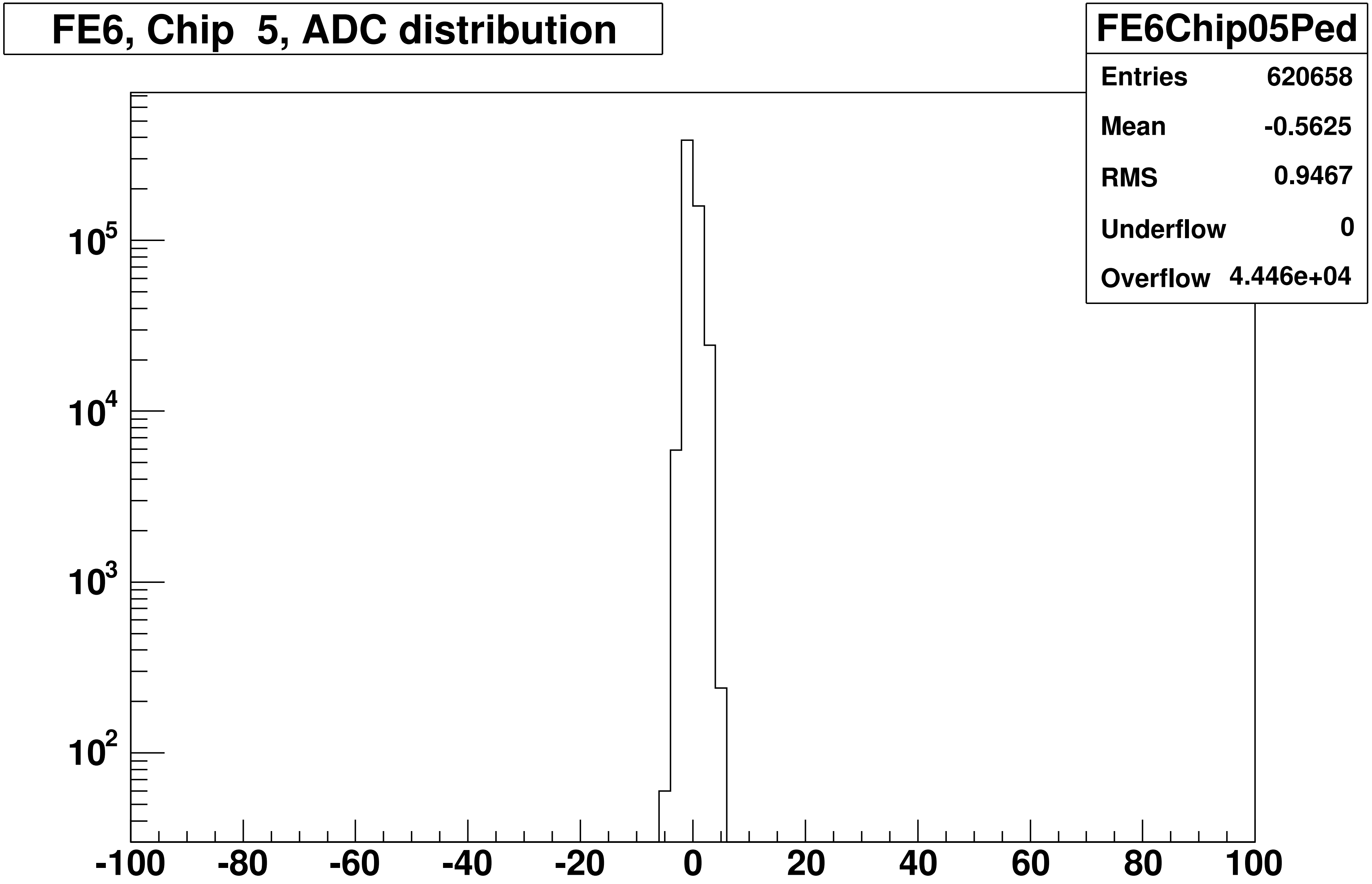
25000



FE6, Chip 5, ADC distribution
FE6Chip05Ped
Entries 620658
Mean -0.5625
RMS 0.9467
Underflow 0
Overflow 4.446e+04
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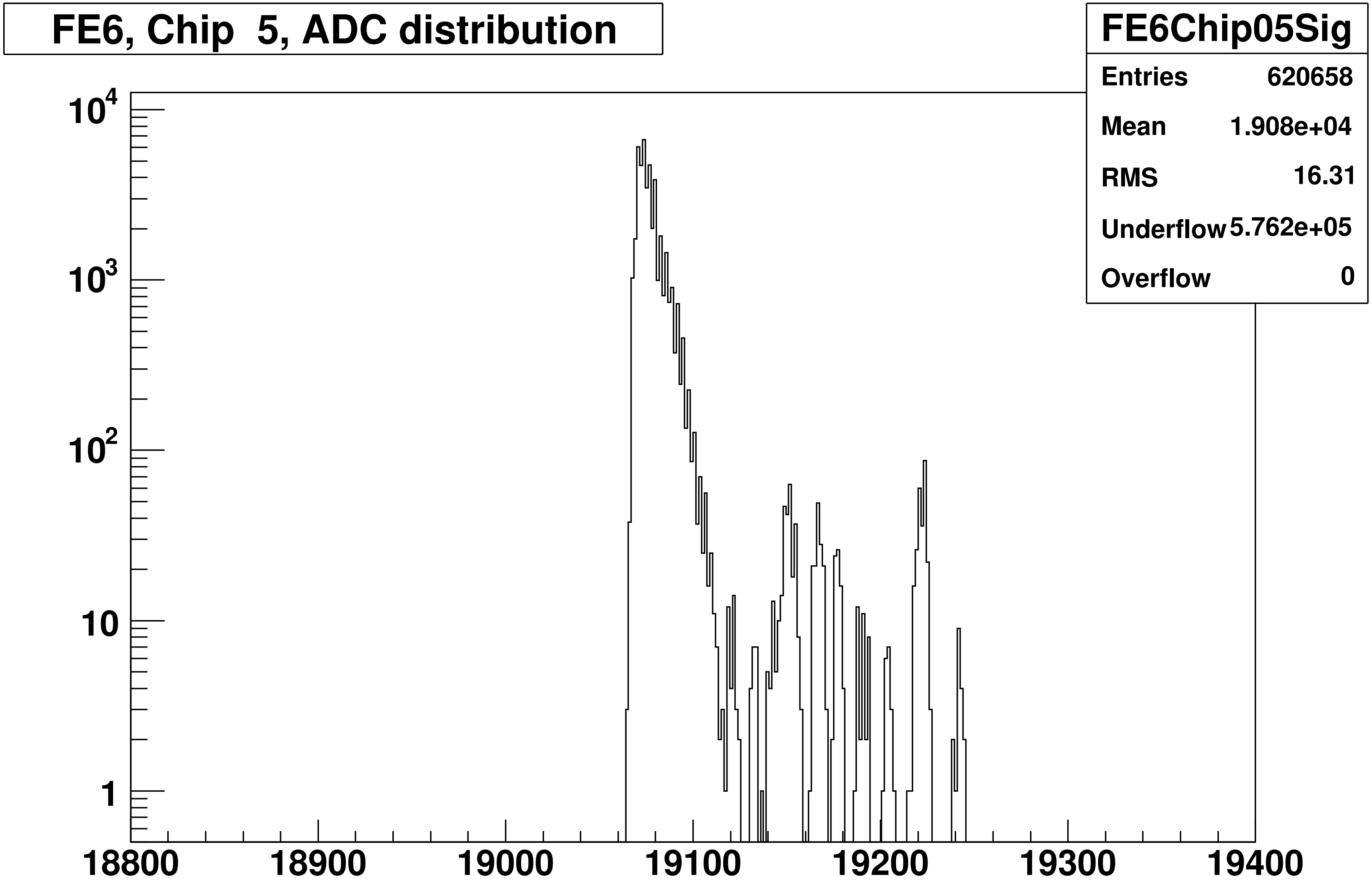

FE6, Chip 5, ADC distribution
FE6Chip05Sig
Entries 620658
Mean 1.908e+04
RMS 16.31
Underflow 5.762e+05
Overflow 0
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10^3
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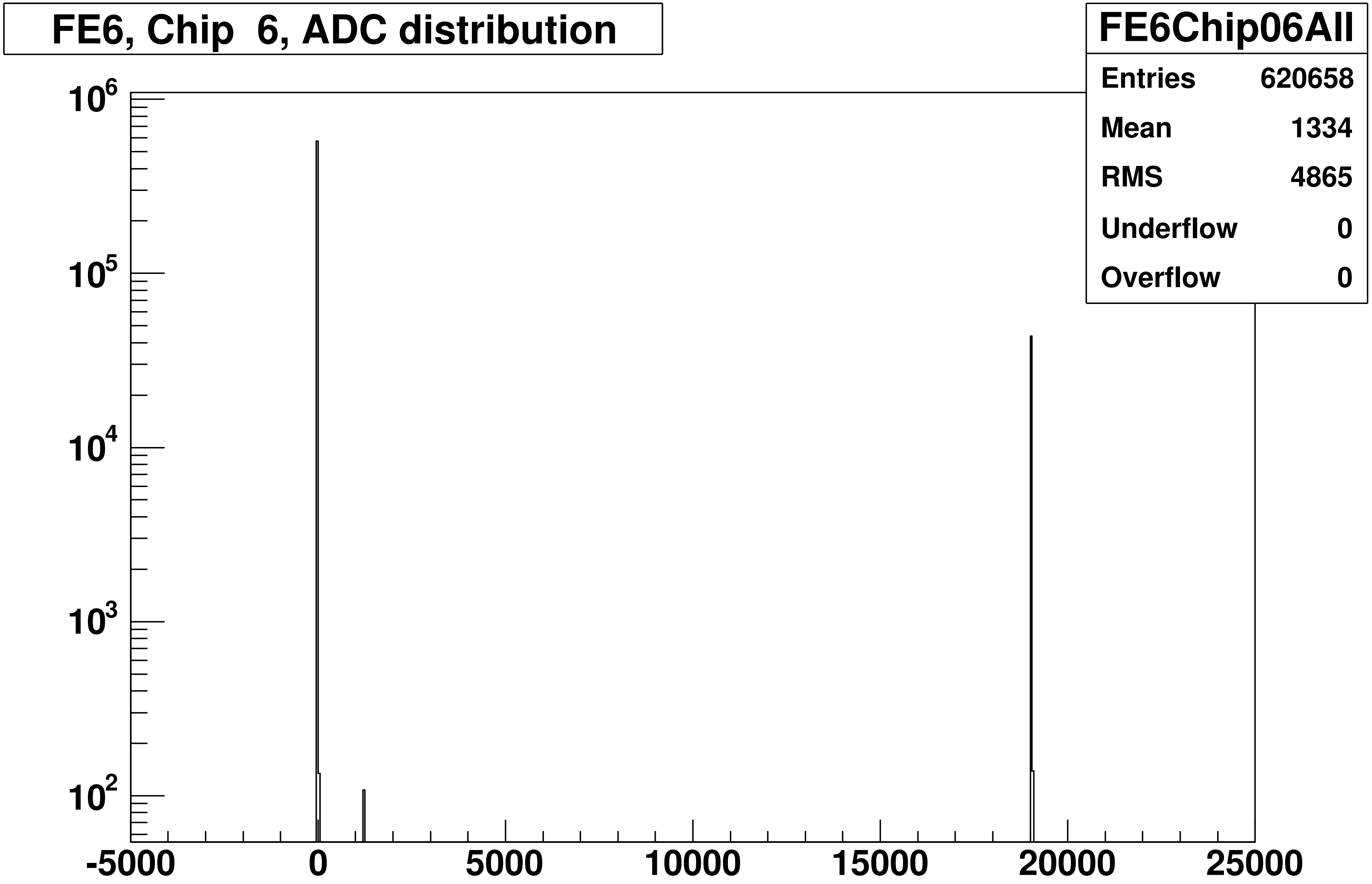

FE6, Chip 6, ADC distribution
FE6Chip06All
Entries 620658
Mean 1334
RMS 4865
Underflow 0
Overflow 0
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10^3
10^2
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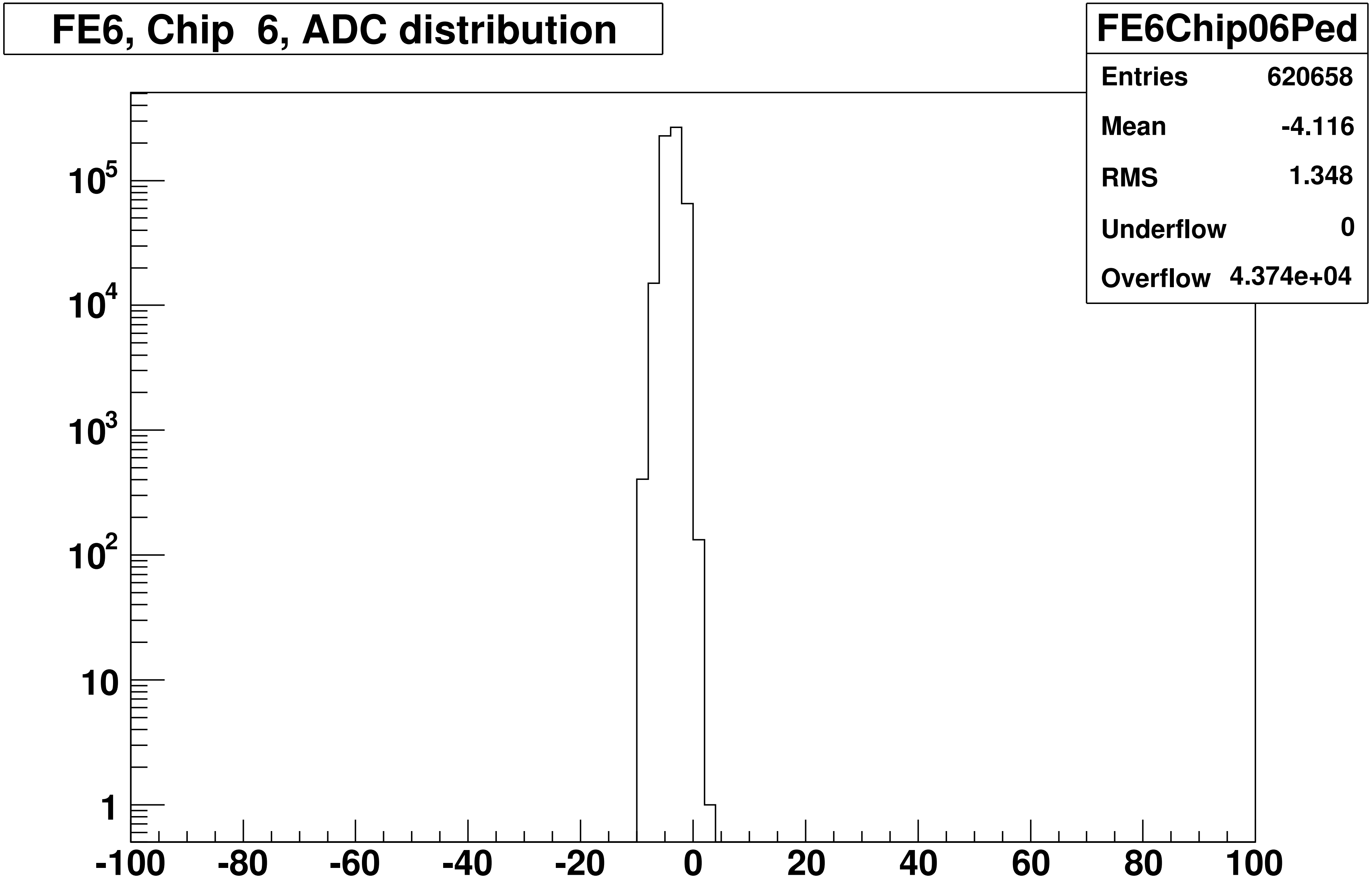
FE6, Chip 6, ADC distribution
FE6Chip06Ped
Entries 620658
Mean -4.116
RMS 1.348
Underflow 0
Overflow 4.374e+04
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10^3
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10
1
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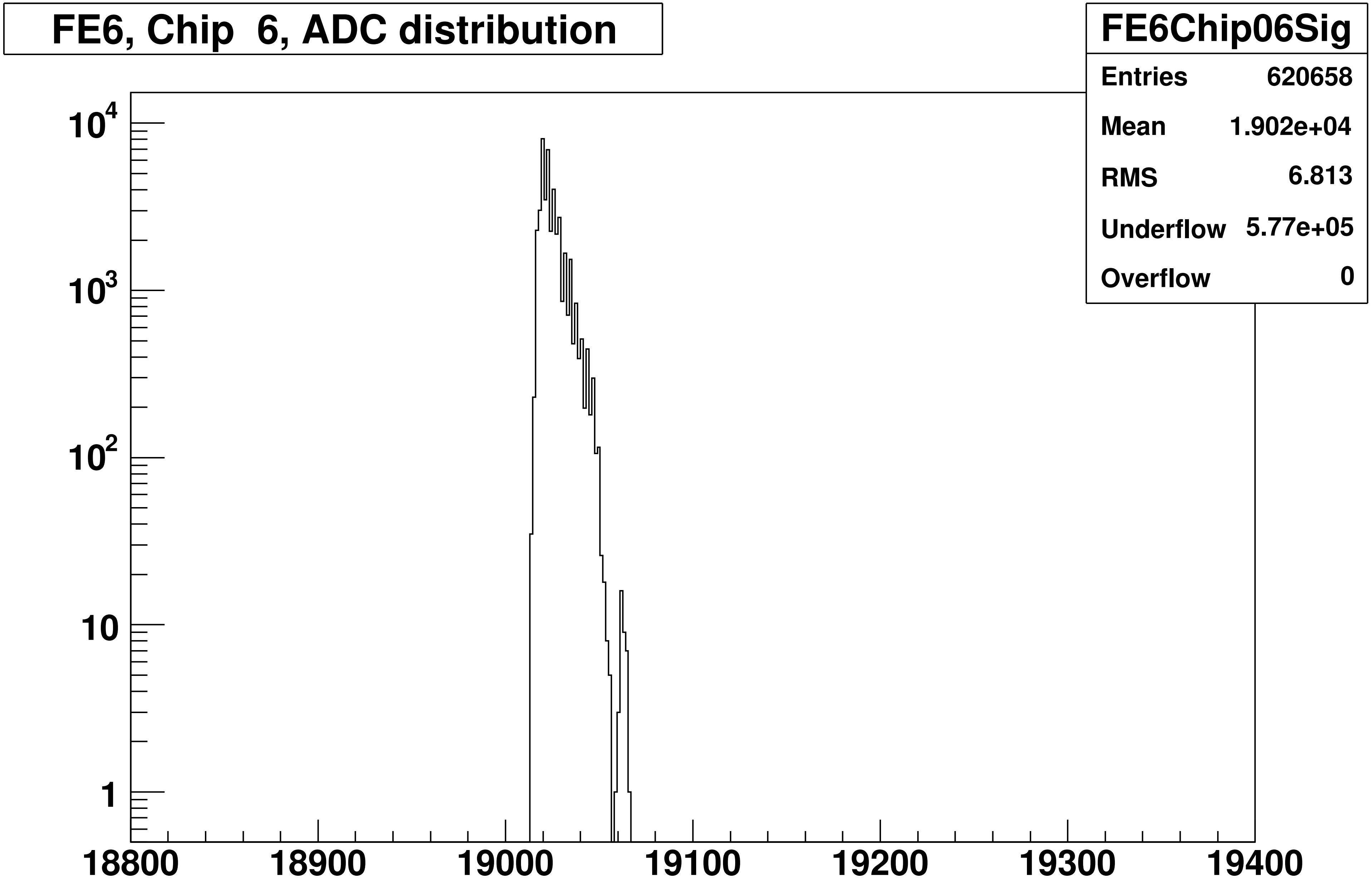
FE6, Chip 6, ADC distribution
FE6Chip06Sig
Entries 620658
Mean 1.902e+04
RMS 6.813
Underflow 5.77e+05
Overflow 0
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10^3
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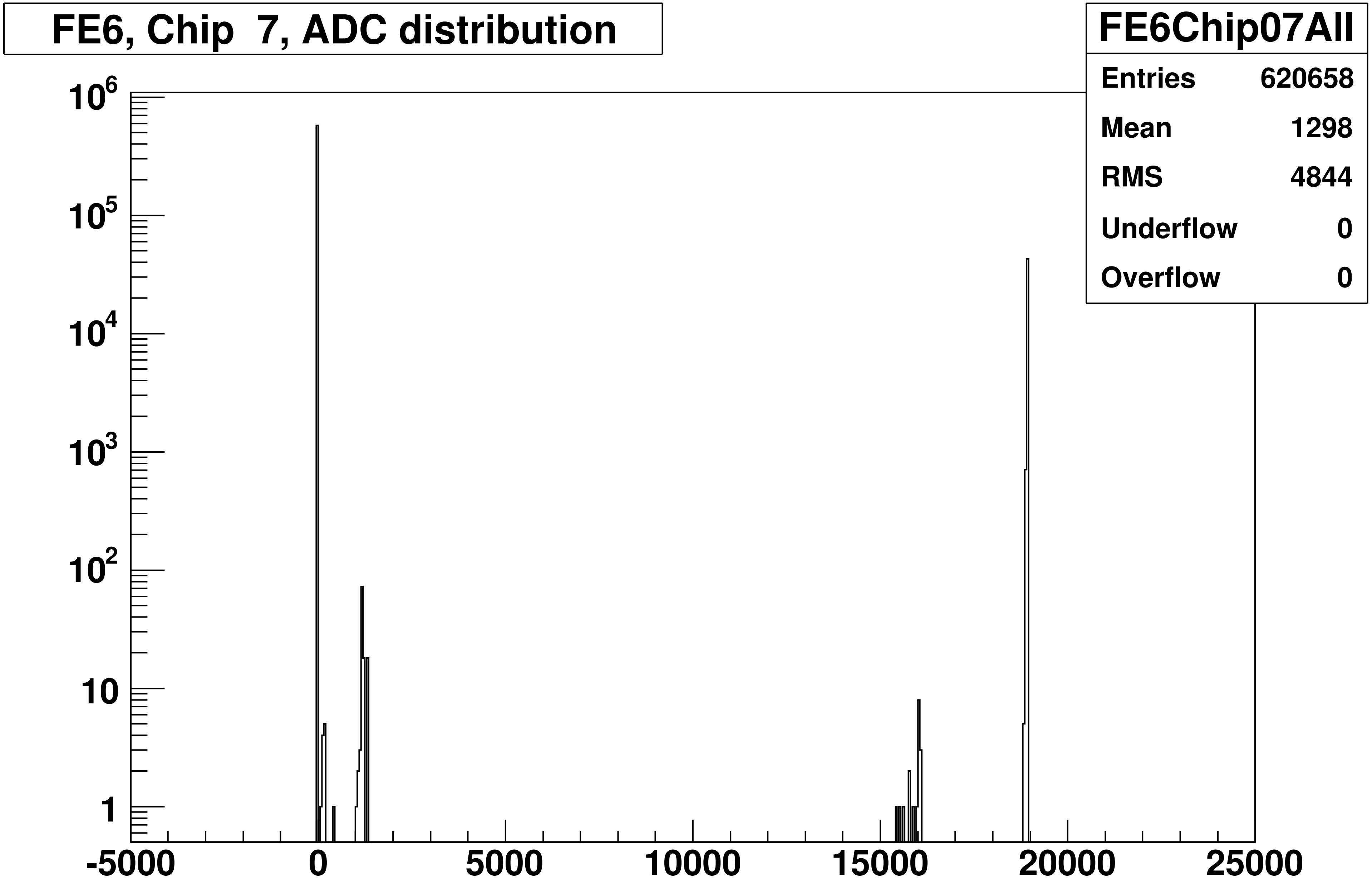
FE6, Chip 7, ADC distribution
FE6Chip07All
Entries 620658
Mean 1298
RMS 4844
Underflow 0
Overflow 0
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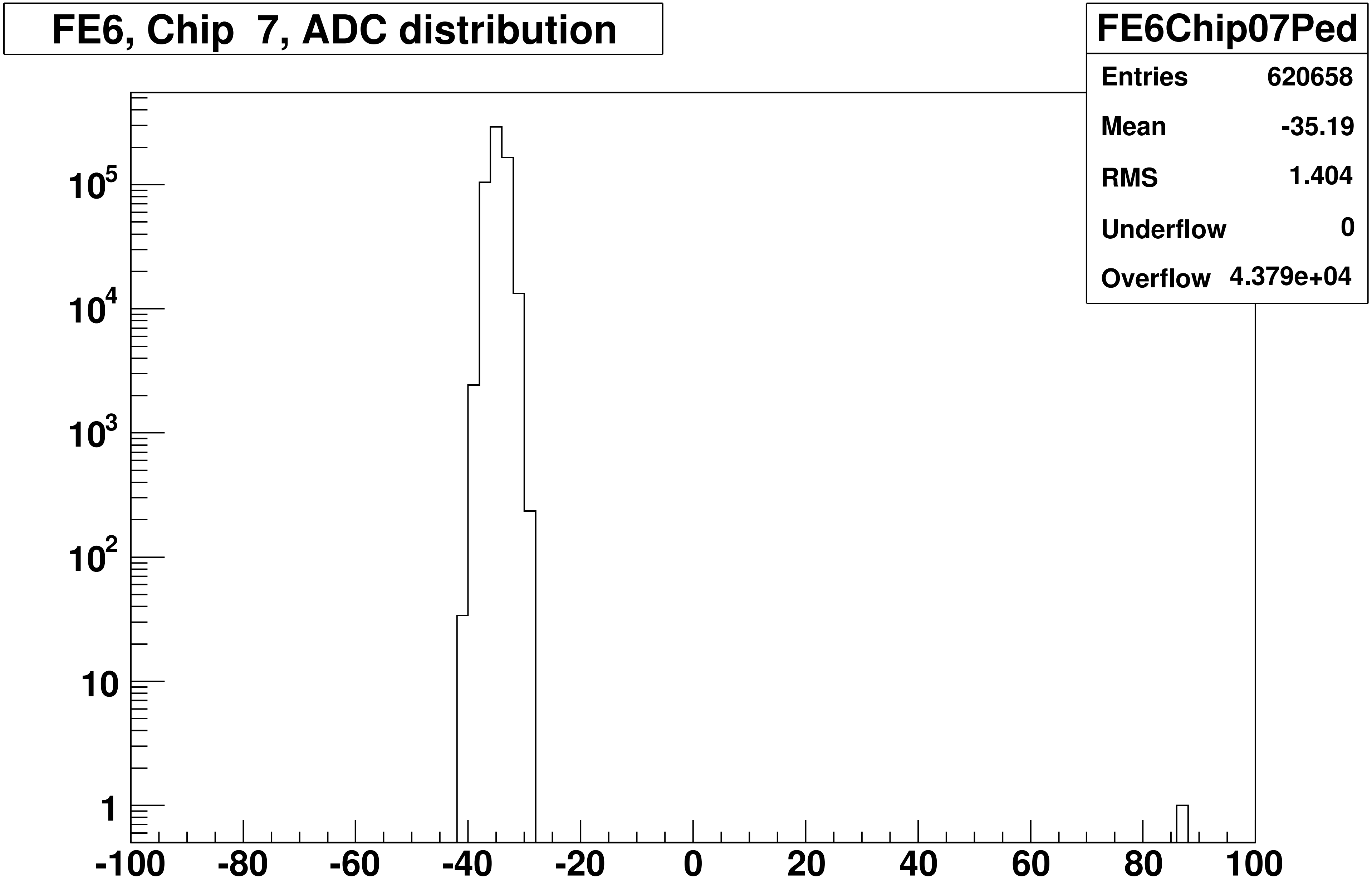
FE6, Chip 7, ADC distribution
FE6Chip07Ped
Entries 620658
Mean -35.19
RMS 1.404
Underflow 0
Overflow 4.379e+04
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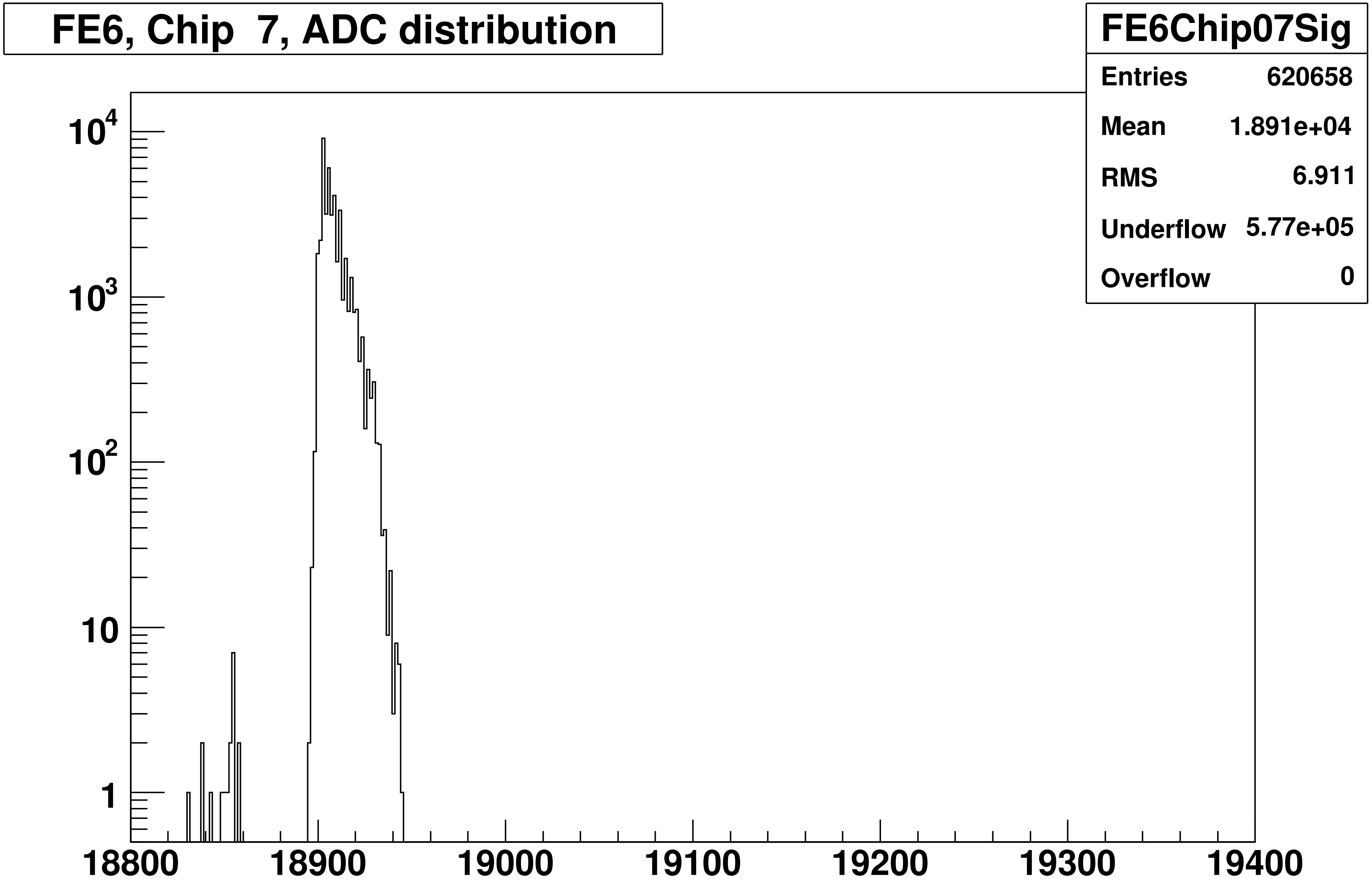
80
100


FE6, Chip 7, ADC distribution
FE6Chip07Sig
Entries 620658
Mean 1.891e+04
RMS 6.911
Underflow 5.77e+05
Overflow 0
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10^3
10^2
10
1
18800
18900
19000
19100
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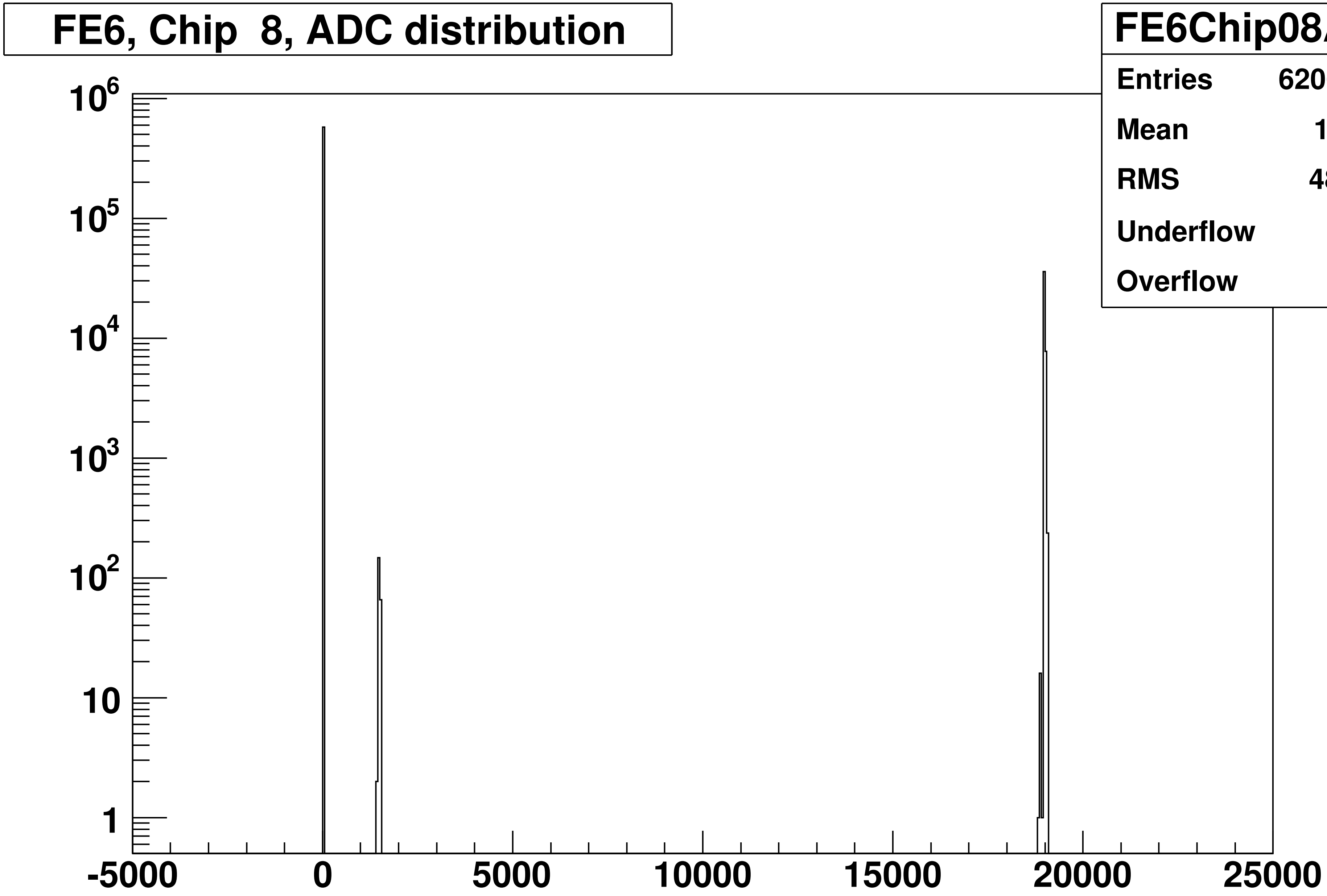
19300
19400


FE6, Chip 8, ADC distribution
FE6Chip08All
Entries 620658
Mean 1361
RMS 4866
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
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10000
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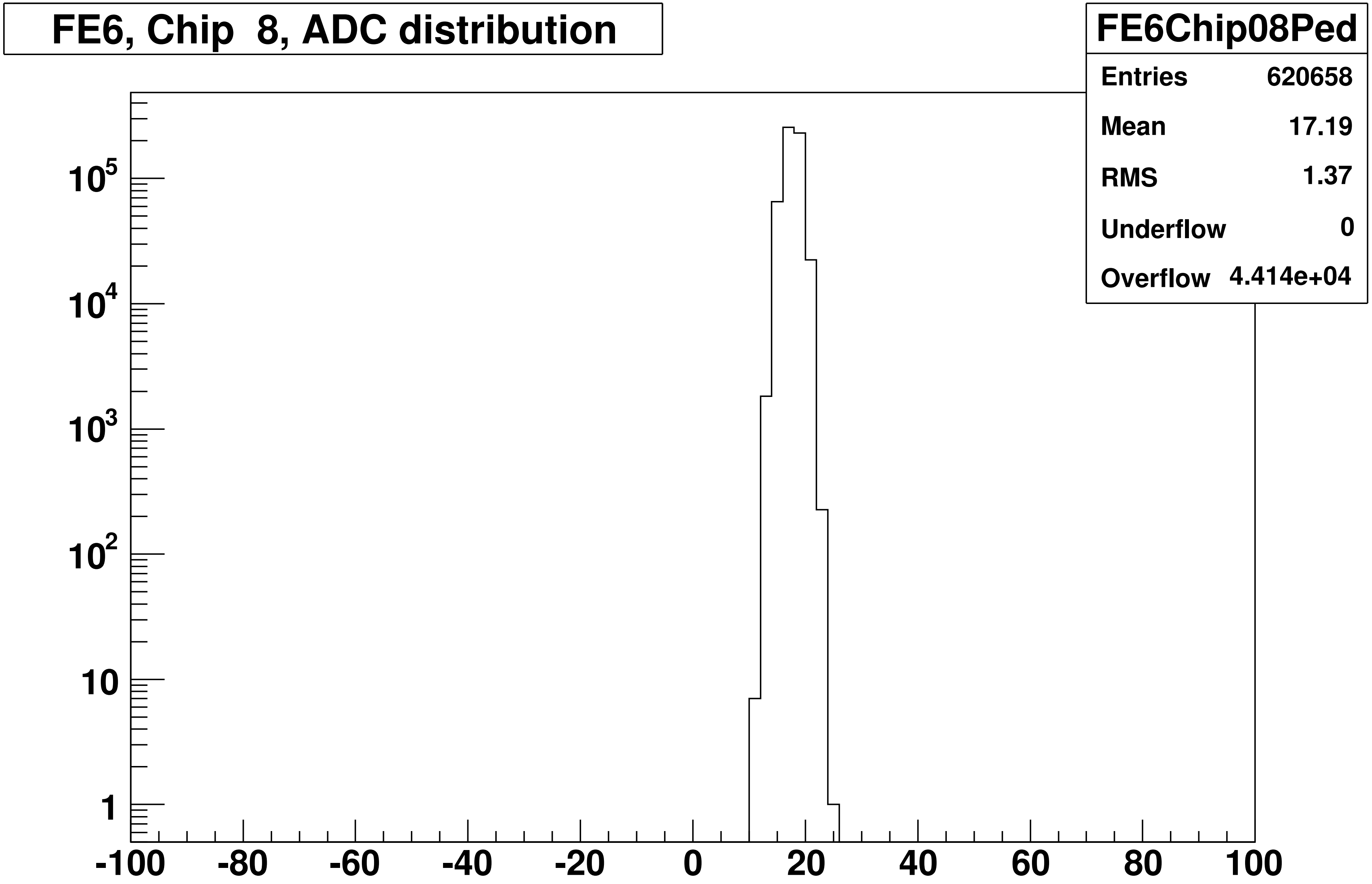

FE6, Chip 8, ADC distribution
FE6Chip08Ped
Entries 620658
Mean 17.19
RMS 1.37
Underflow 0
Overflow 4.414e+04
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10^3
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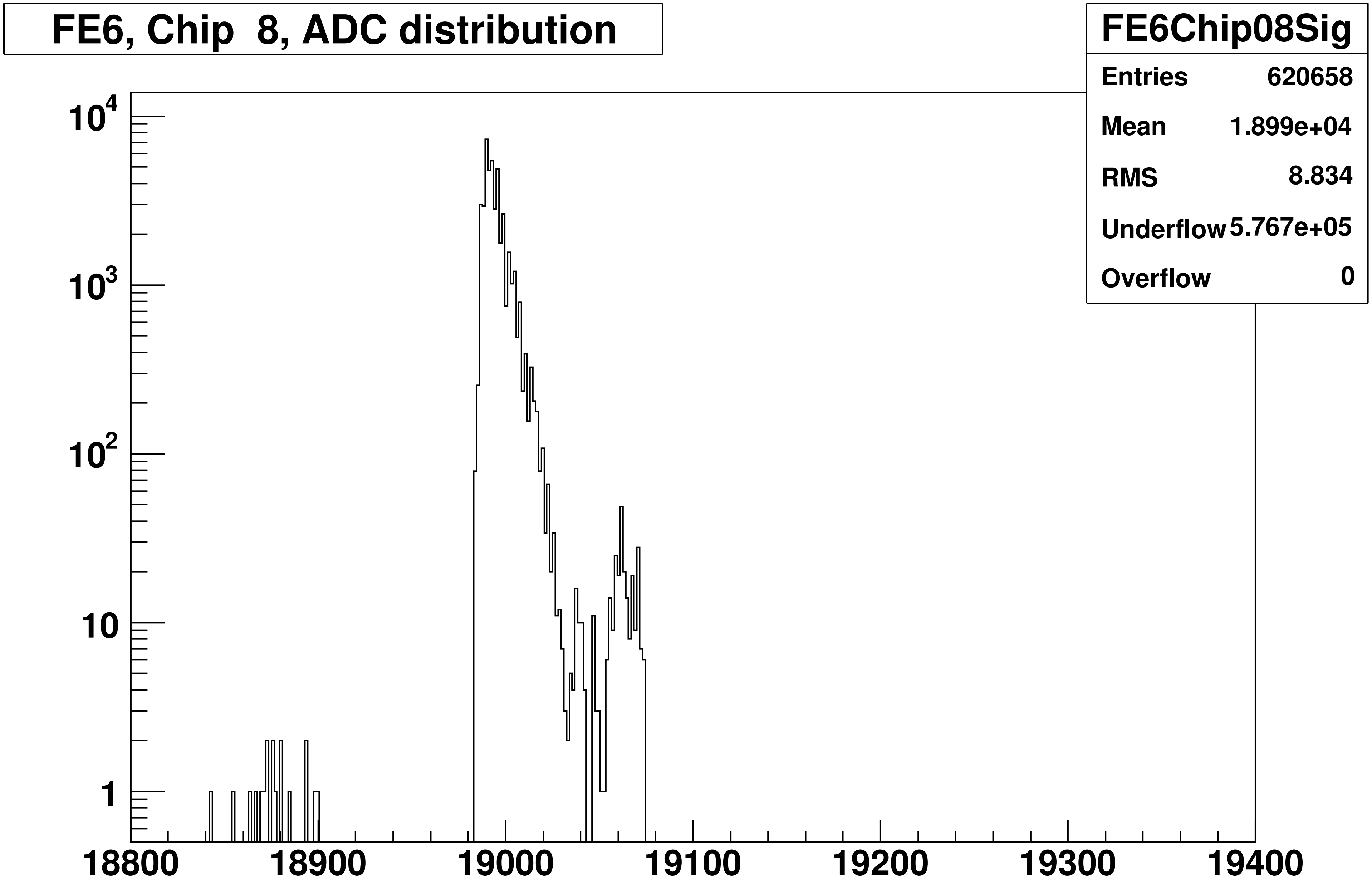
FE6, Chip 8, ADC distribution
FE6Chip08Sig
Entries 620658
Mean 1.899e+04
RMS 8.834
Underflow 5.767e+05
Overflow 0
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10^2
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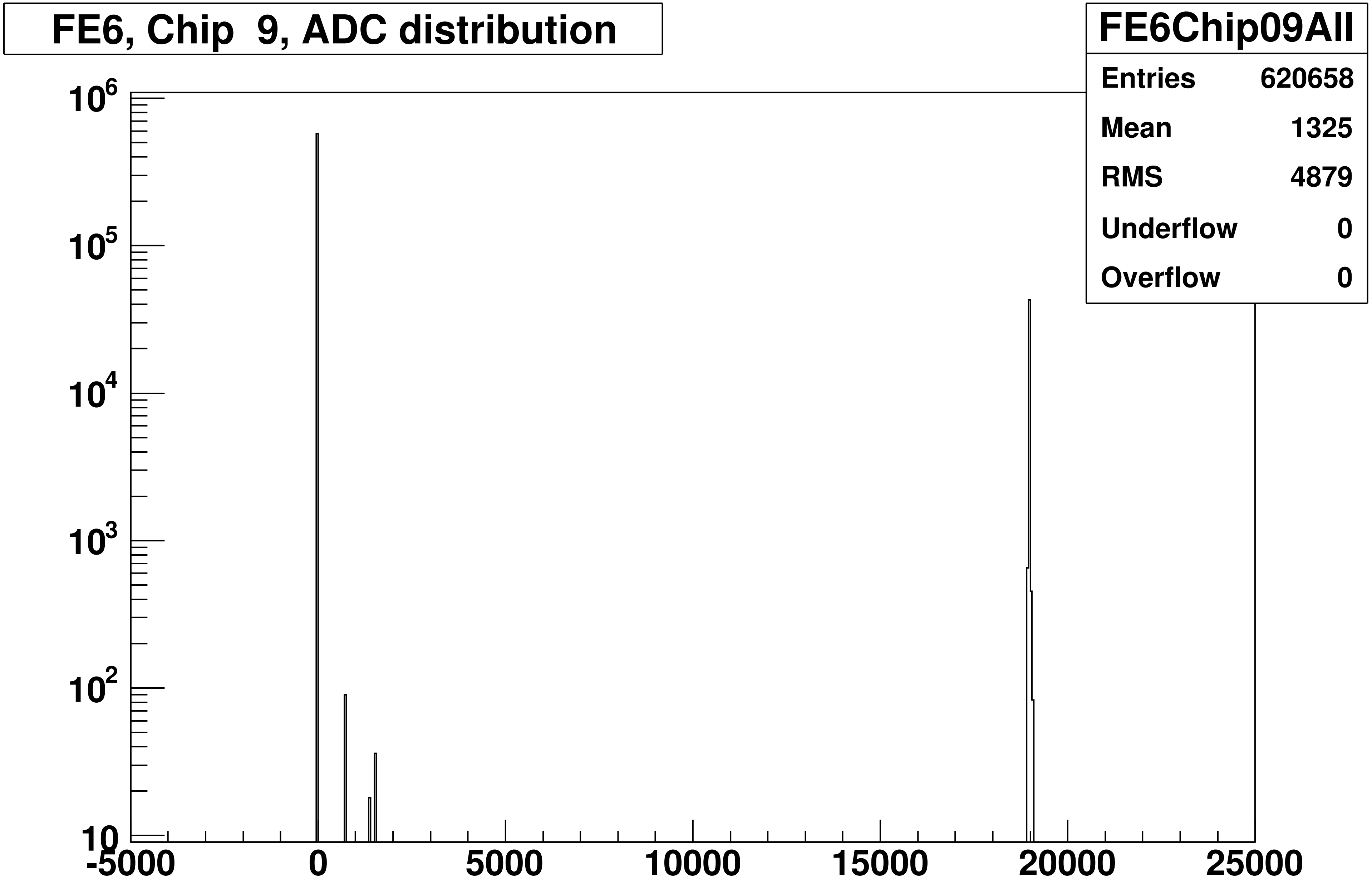
19300
19400



FE6, Chip 9, ADC distribution
FE6Chip09All
Entries 620658
Mean 1325
RMS 4879
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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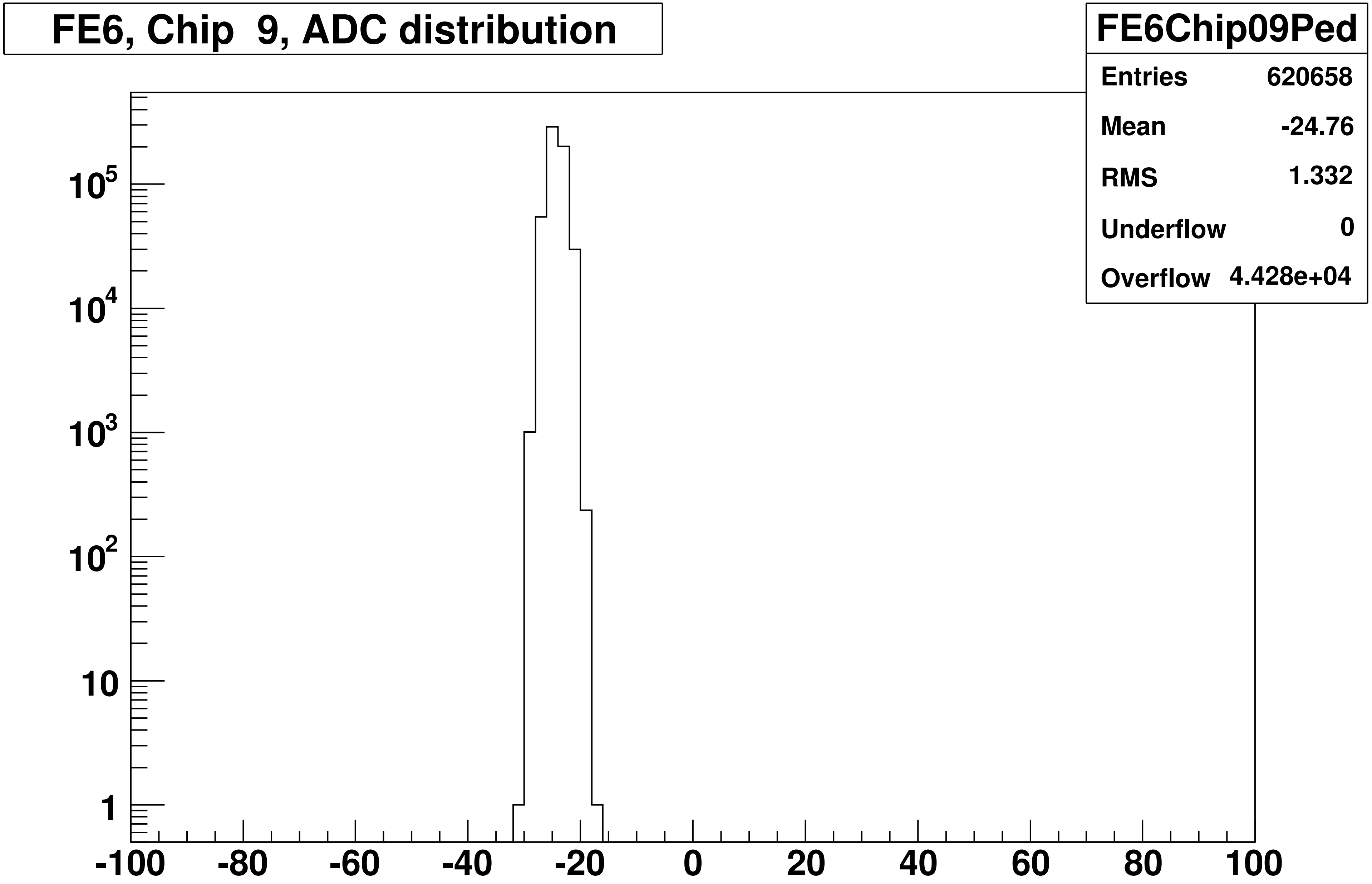

FE6, Chip 9, ADC distribution
FE6Chip09Ped
Entries 620658
Mean -24.76
RMS 1.332
Underflow 0
Overflow 4.428e+04
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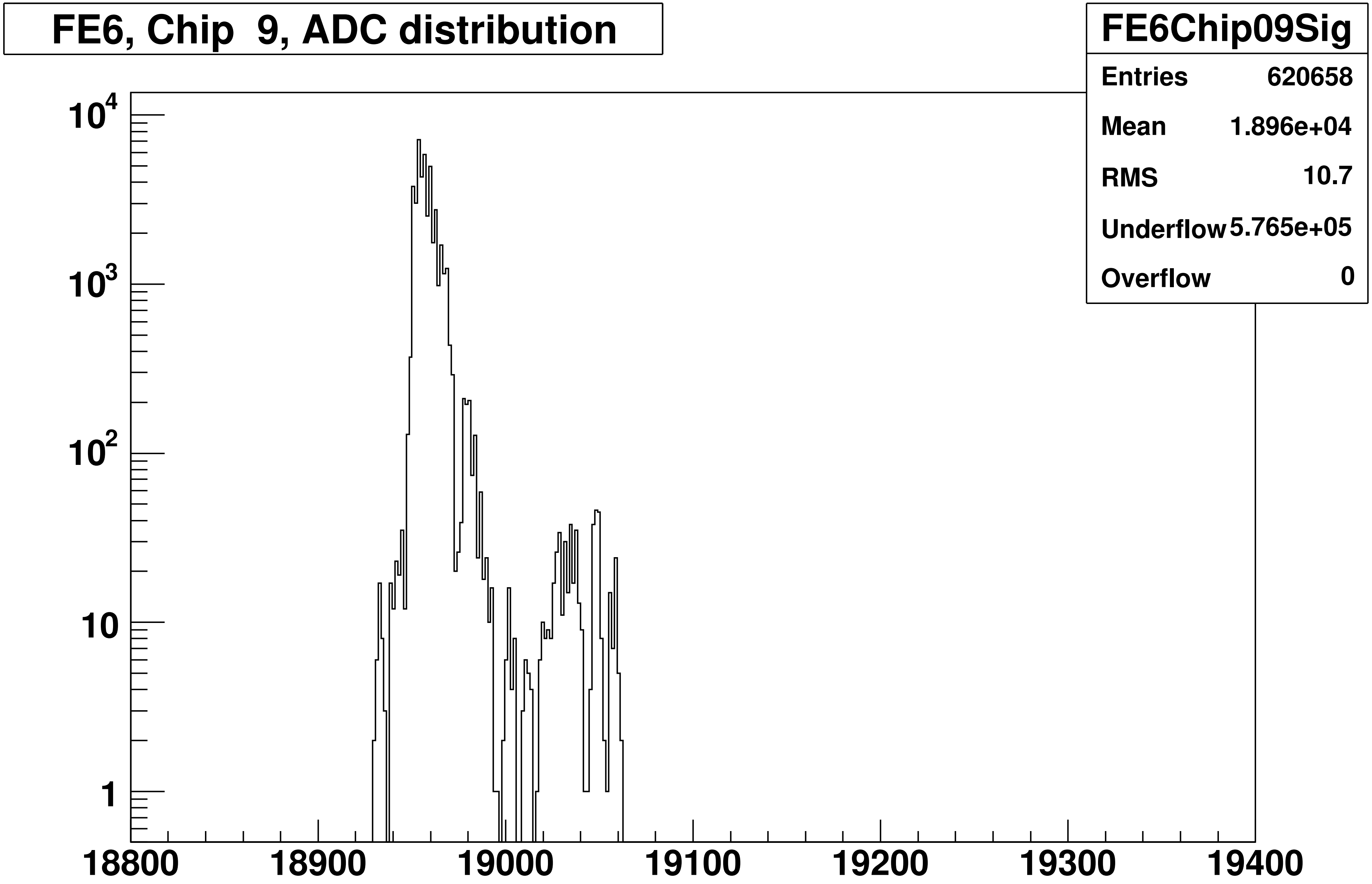
FE6, Chip 9, ADC distribution
FE6Chip09Sig
Entries 620658
Mean 1.896e+04
RMS 10.7
Underflow 5.765e+05
Overflow 0
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10^3
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18900
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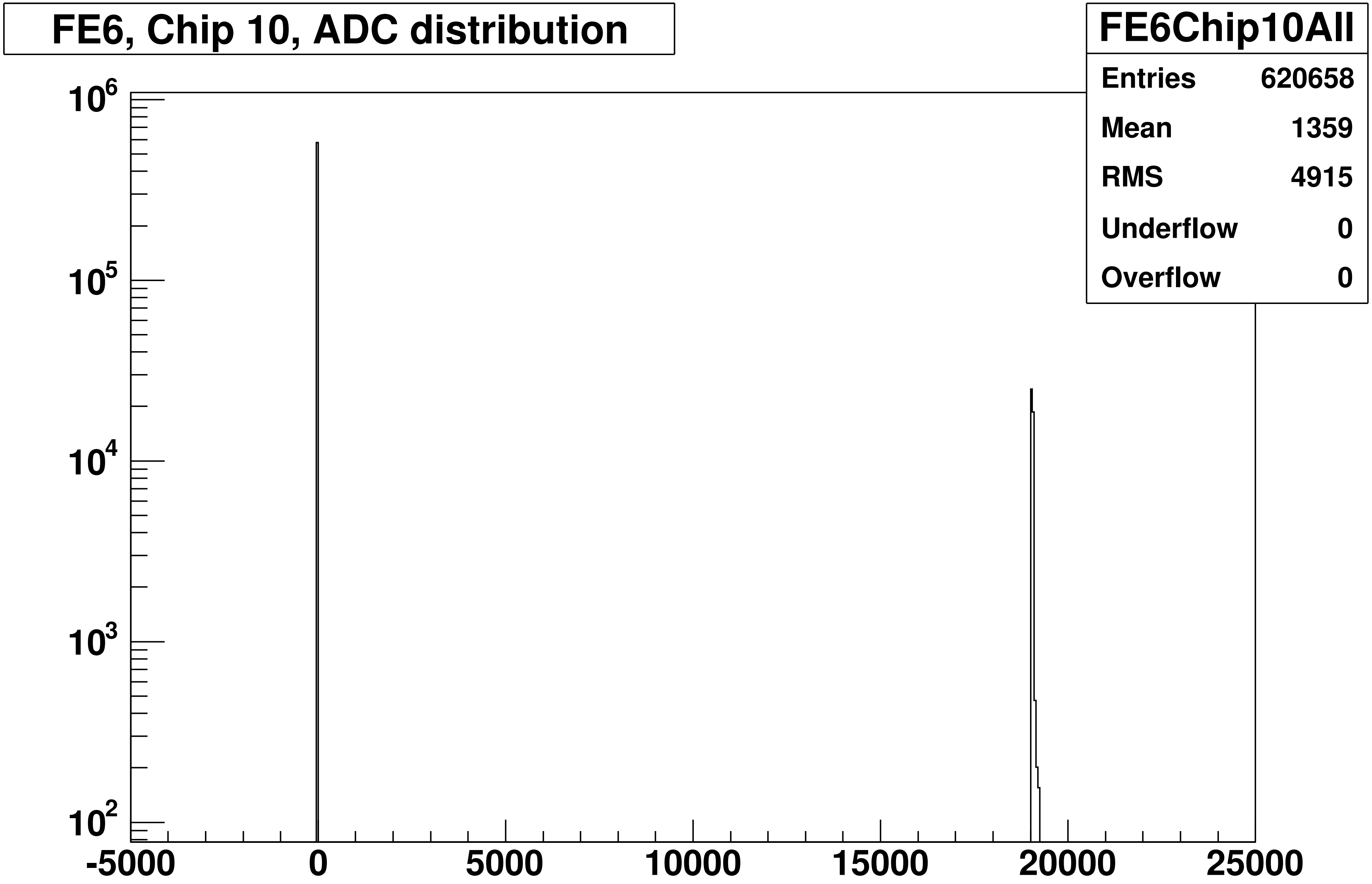
FE6, Chip 10, ADC distribution
FE6Chip10All
Entries 620658
Mean 1359
RMS 4915
Underflow 0
Overflow 0
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10^5
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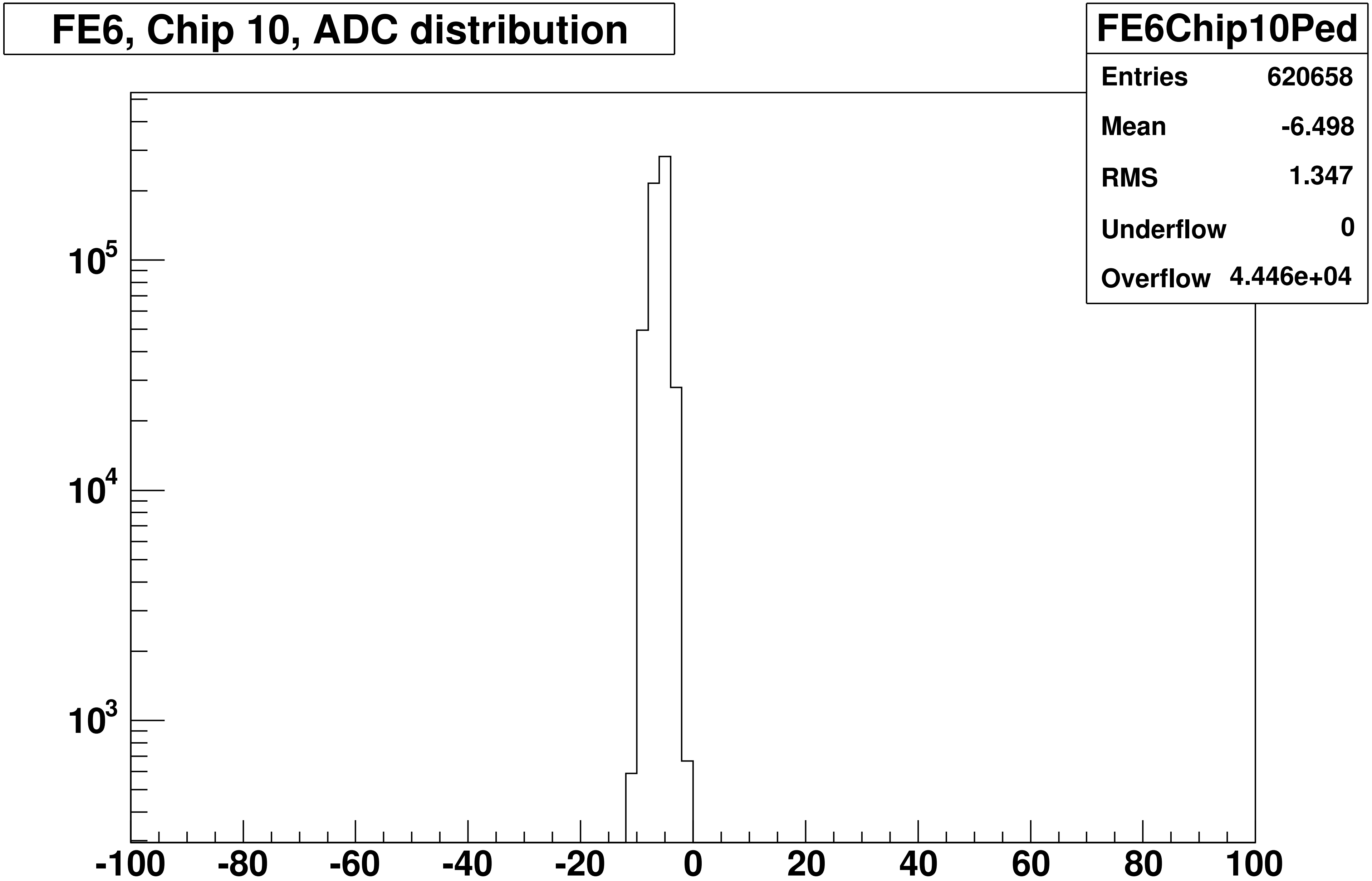
FE6, Chip 10, ADC distribution
FE6Chip10Ped
Entries 620658
Mean -6.498
RMS 1.347
Underflow 0
Overflow 4.446e+04
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10^4
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-20
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80
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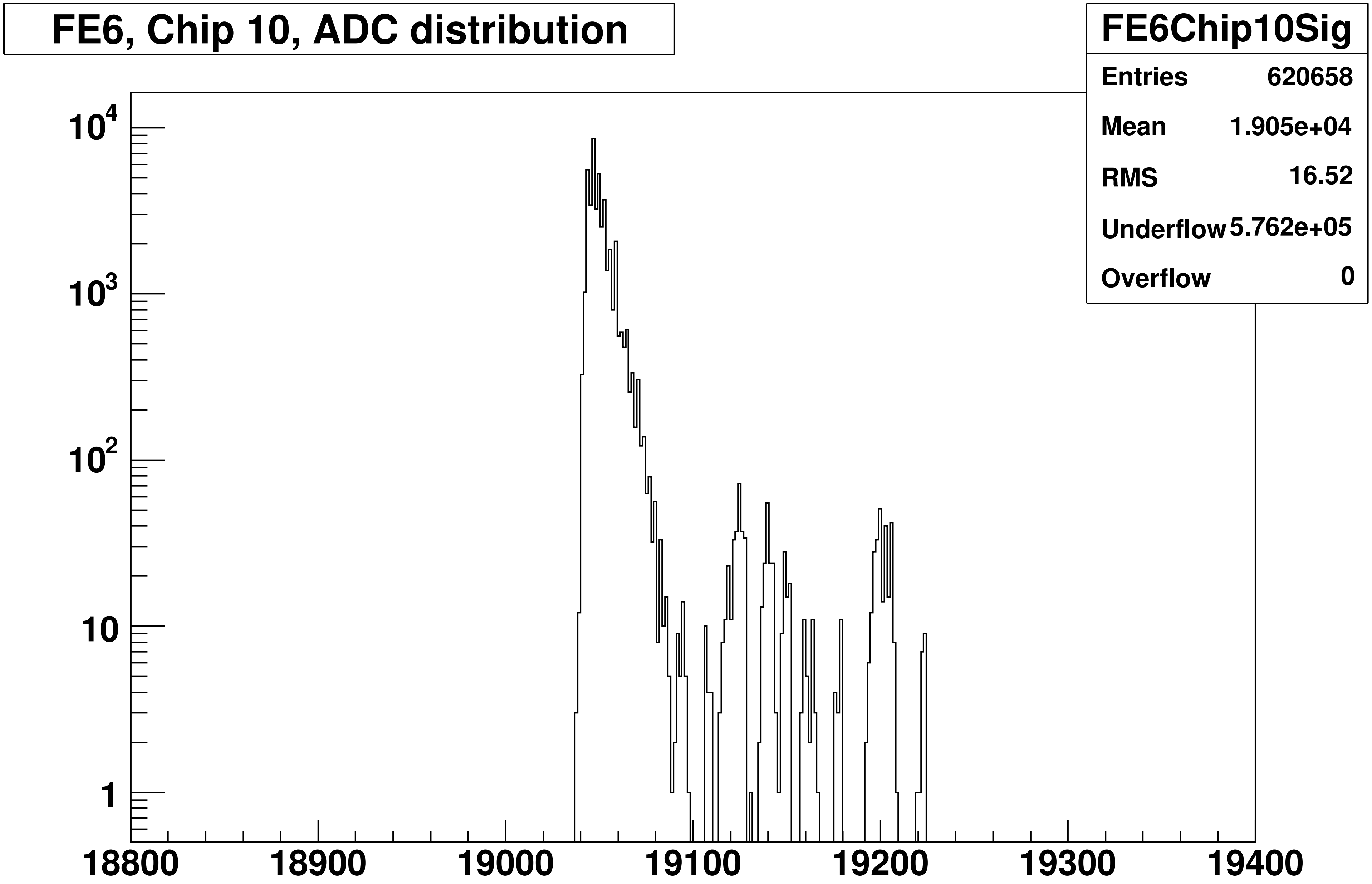
FE6, Chip 10, ADC distribution
FE6Chip10Sig
Entries 620658
Mean 1.905e+04
RMS 16.52
Underflow 5.762e+05
Overflow 0
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10^3
10^2
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1
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18900
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19100
19200
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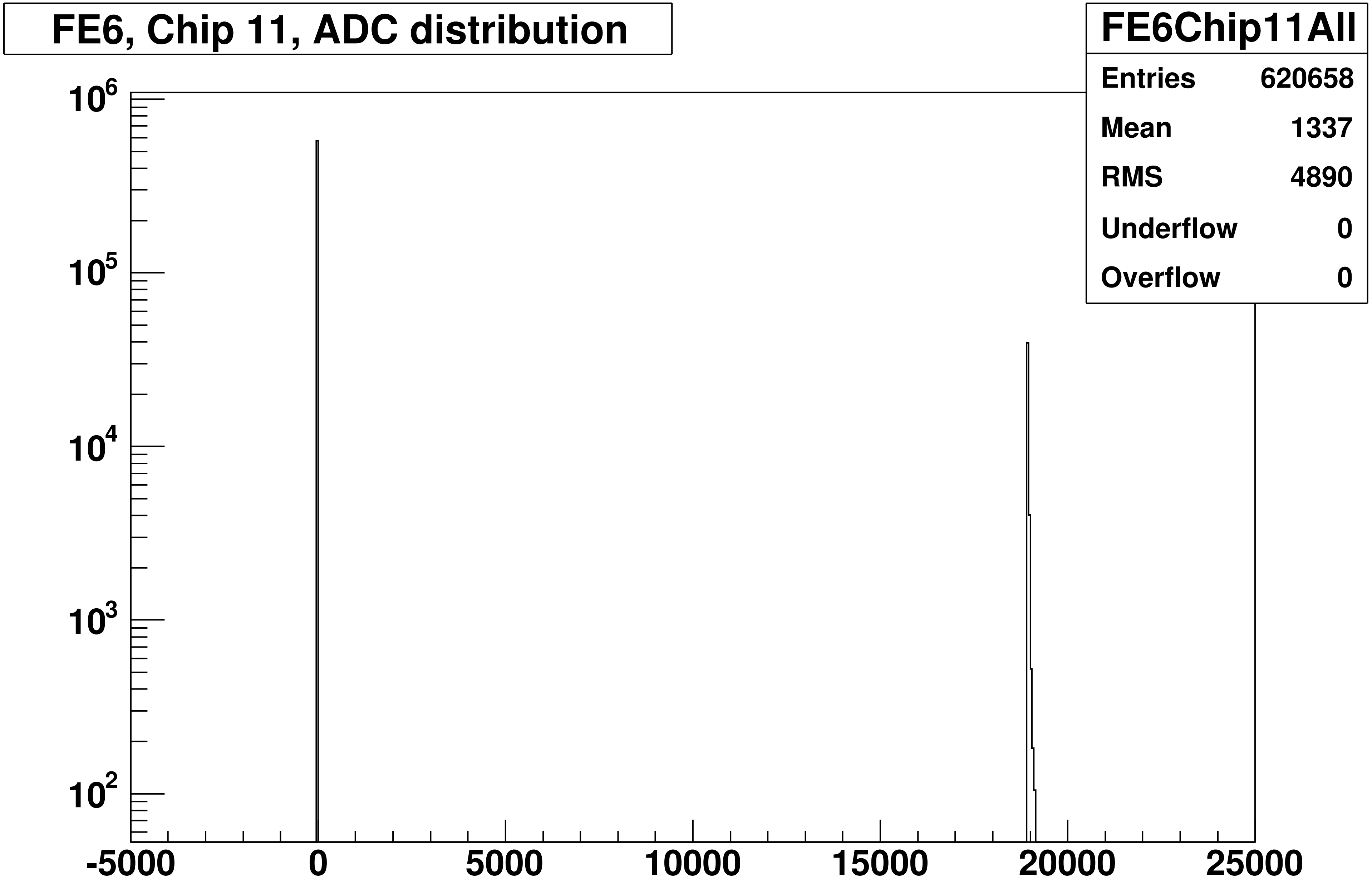
19400



FE6, Chip 11, ADC distribution
FE6Chip11All
Entries 620658
Mean 1337
RMS 4890
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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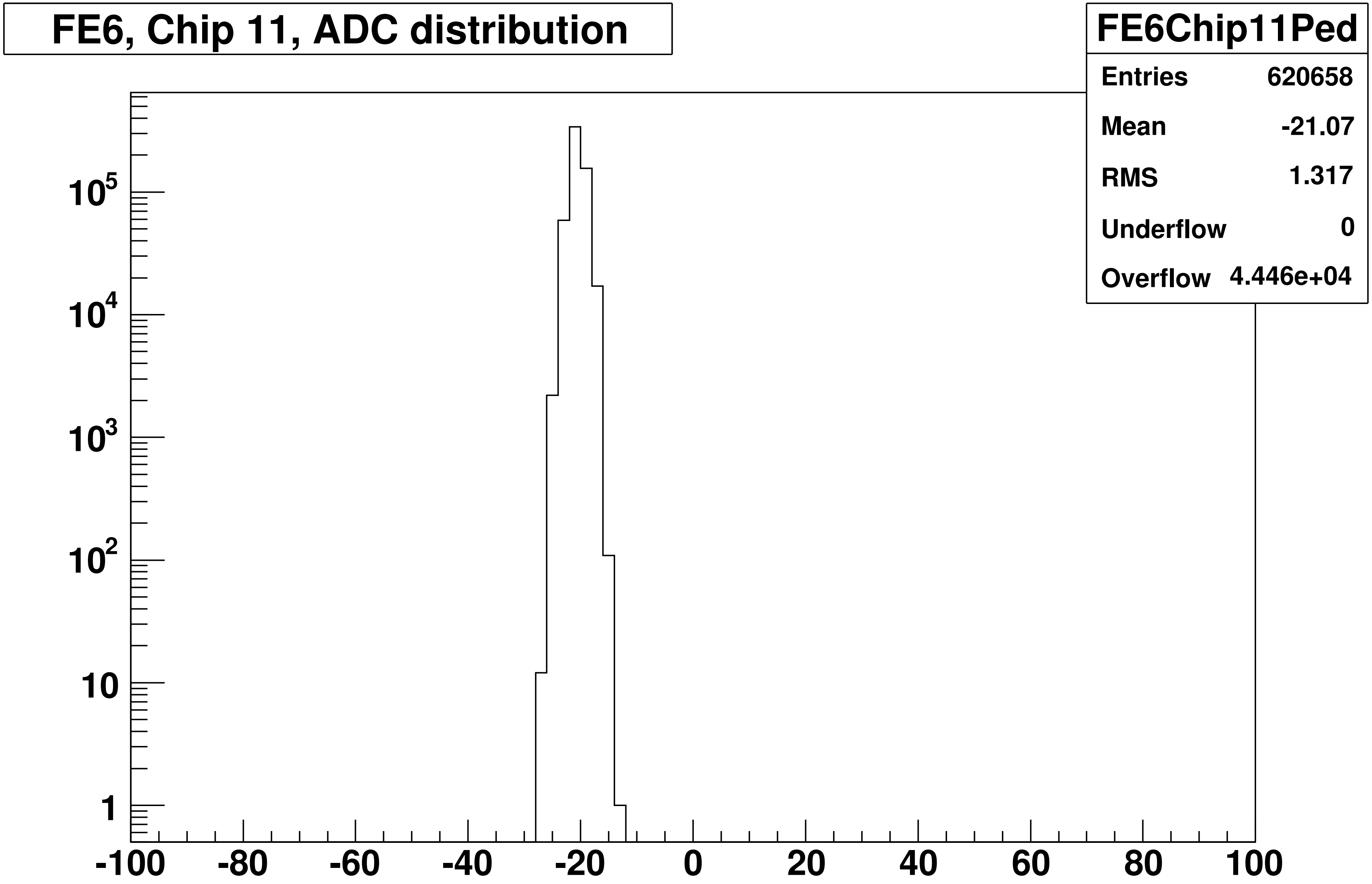
5000
10000
15000
20000
25000


FE6, Chip 11, ADC distribution
FE6Chip11Ped
Entries 620658
Mean -21.07
RMS 1.317
Underflow 0
Overflow 4.446e+04
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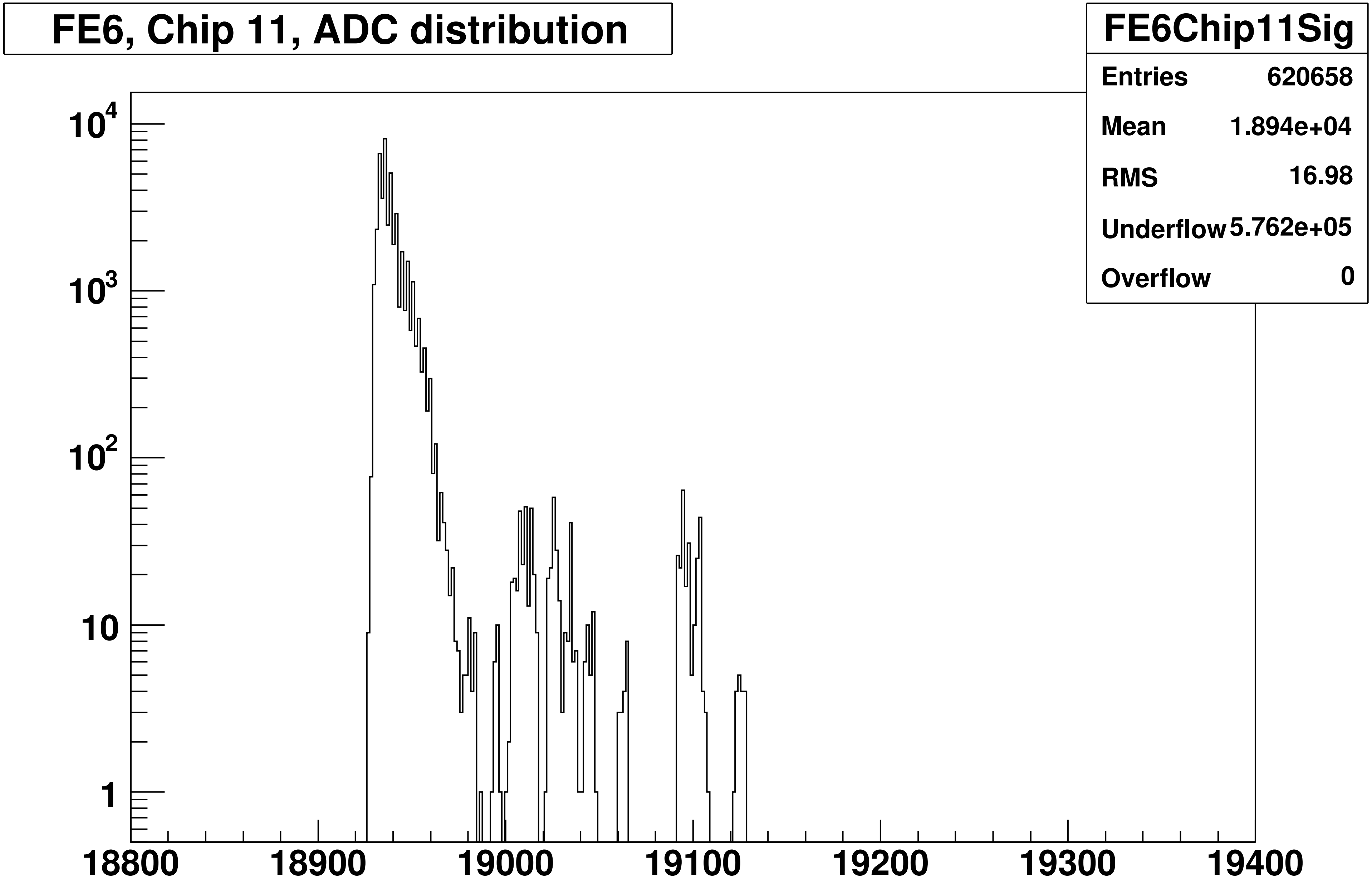
FE6, Chip 11, ADC distribution
FE6Chip11Sig
Entries 620658
Mean 1.894e+04
RMS 16.98
Underflow 5.762e+05
Overflow 0
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10^3
10^2
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18900
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19200
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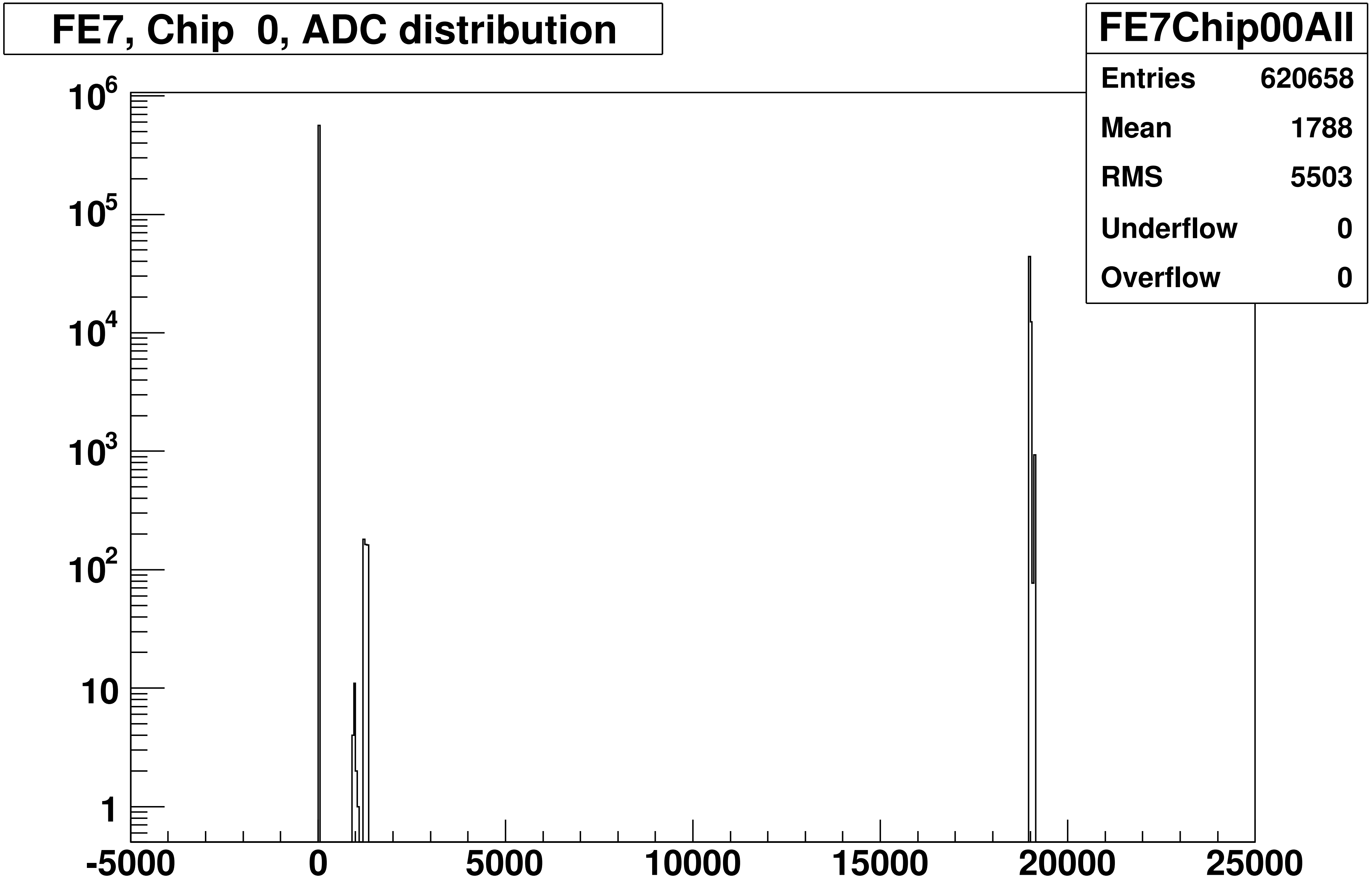
FE7, Chip 0, ADC distribution
FE7Chip00All
Entries 620658
Mean 1788
RMS 5503
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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-5000
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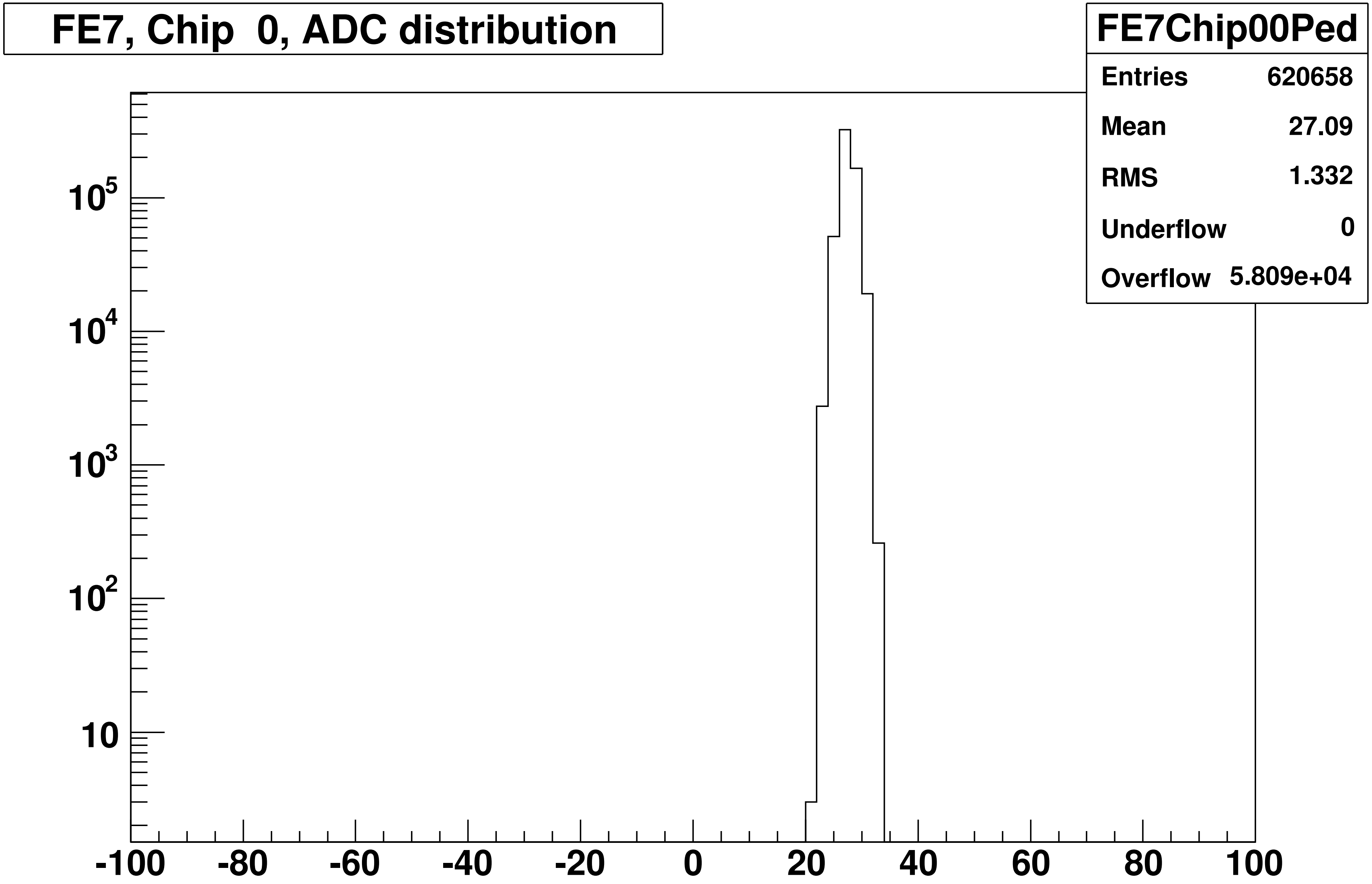
15000
20000
25000


FE7, Chip 0, ADC distribution
FE7Chip00Ped
Entries 620658
Mean 27.09
RMS 1.332
Underflow 0
Overflow 5.809e+04
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10^4
10^3
10^2
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-100
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-40
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80
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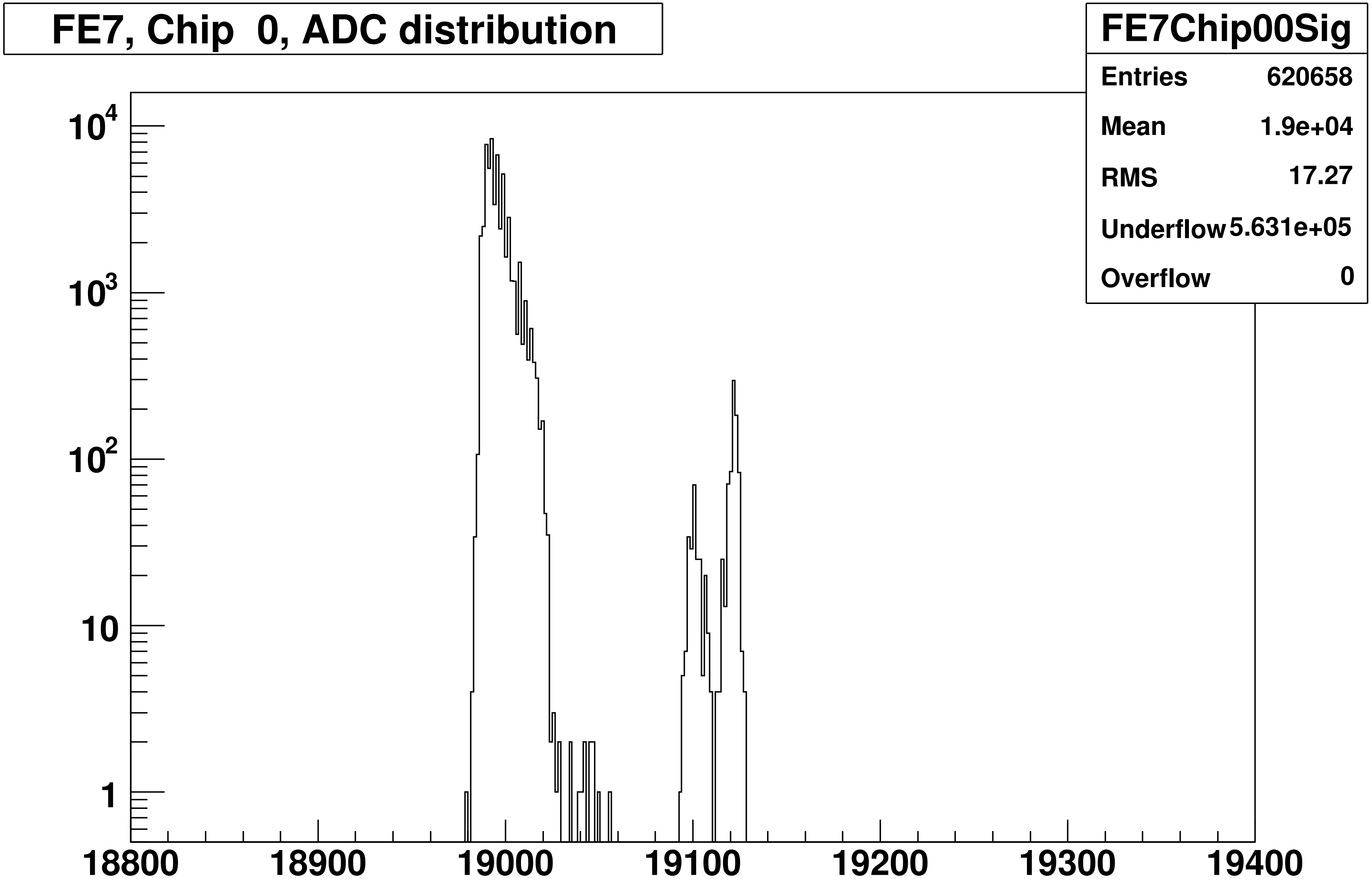
FE7, Chip 0, ADC distribution
FE7Chip00Sig
Entries 620658
Mean 1.9e+04
RMS 17.27
Underflow 5.631e+05
Overflow 0
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10^3
10^2
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18900
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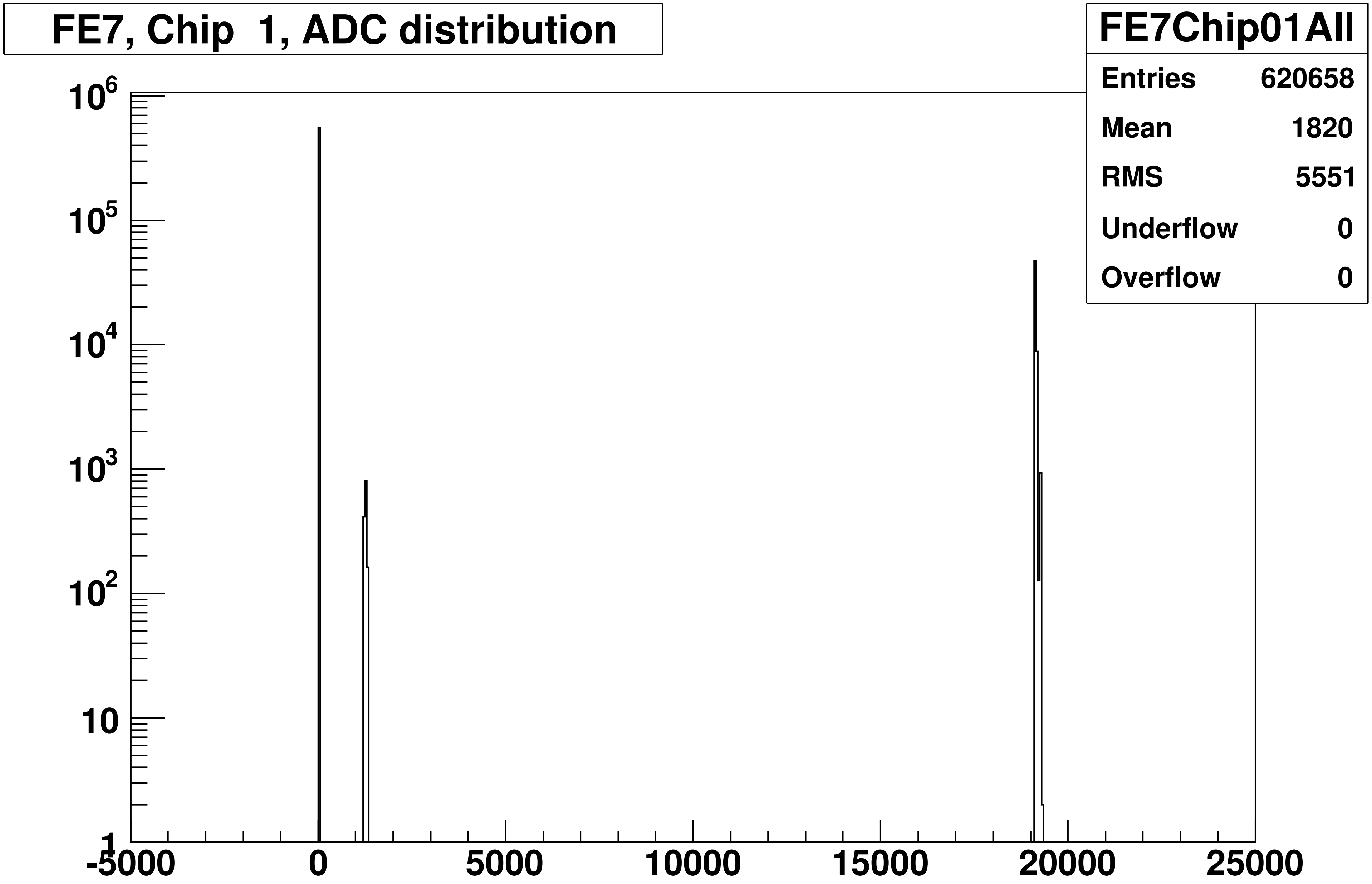
19200
19300
19400



FE7, Chip 1, ADC distribution
FE7Chip01All
Entries 620658
Mean 1820
RMS 5551
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
10
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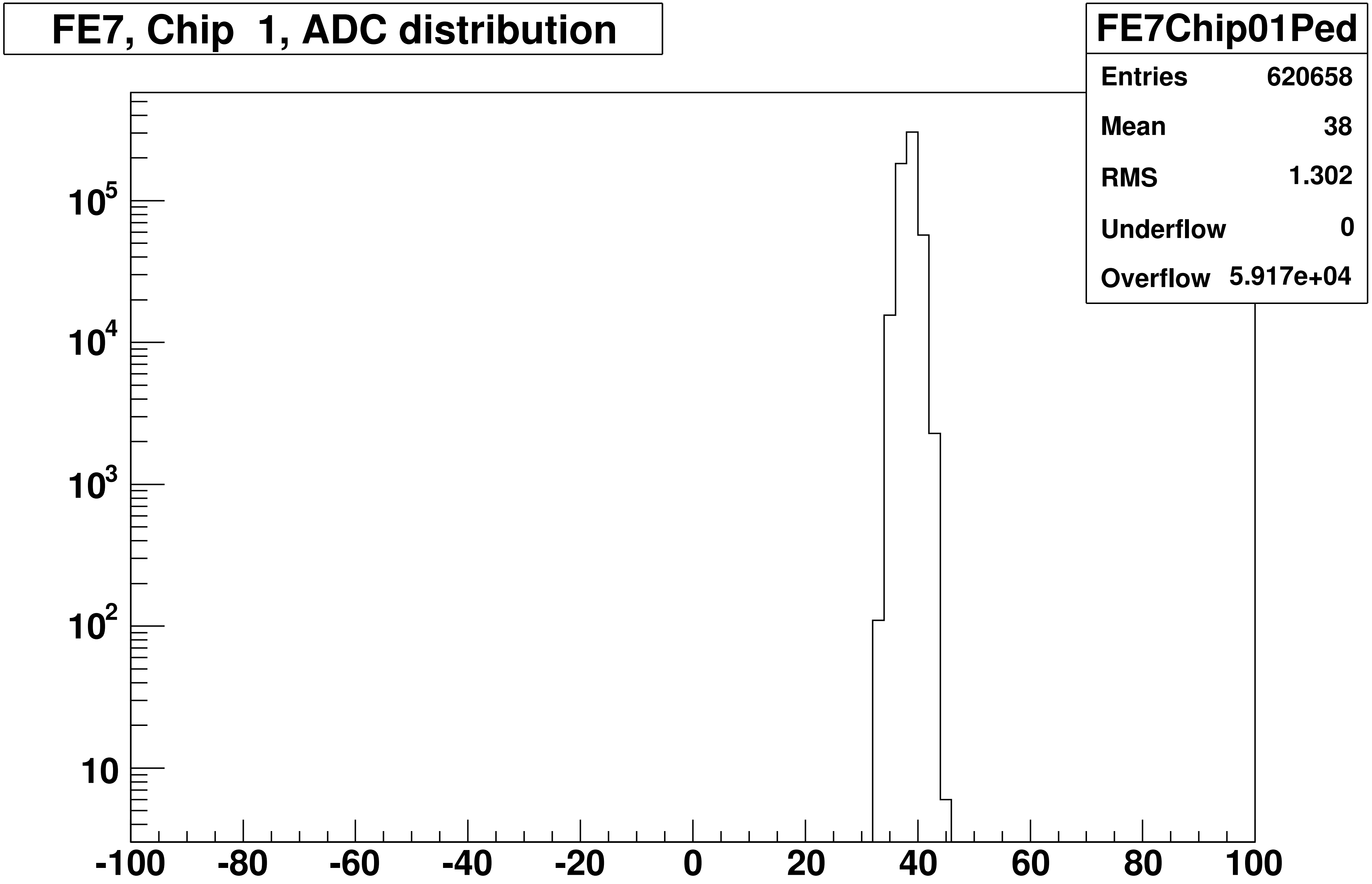
0
5000
10000
15000
20000
25000



FE7, Chip 1, ADC distribution
FE7Chip01Ped
Entries 620658
Mean 38
RMS 1.302
Underflow 0
Overflow 5.917e+04
10^5
10^4
10^3
10^2
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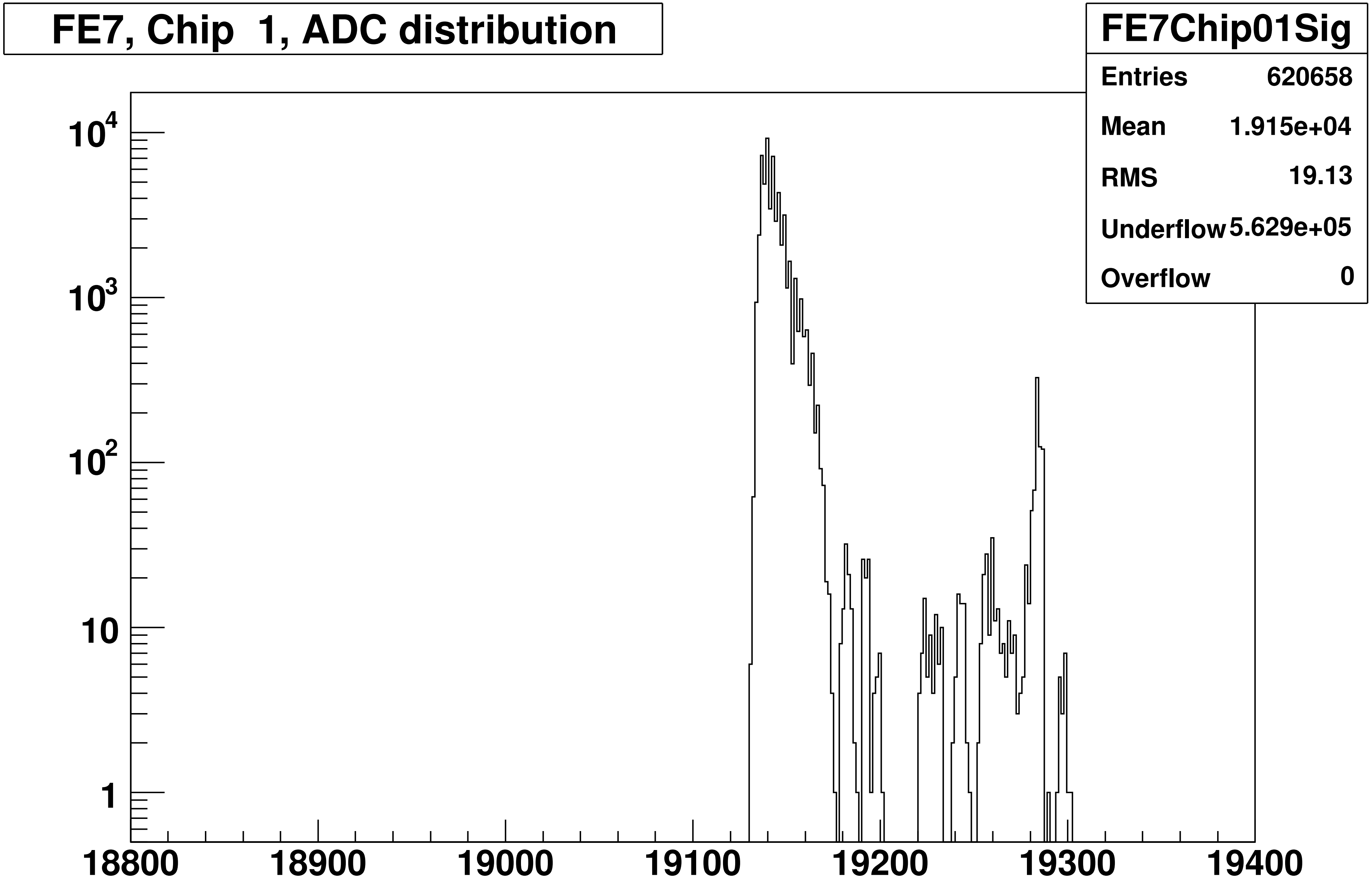
FE7, Chip 1, ADC distribution
FE7Chip01Sig
Entries 620658
Mean 1.915e+04
RMS 19.13
Underflow 5.629e+05
Overflow 0
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10^3
10^2
10
1
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18900
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19200
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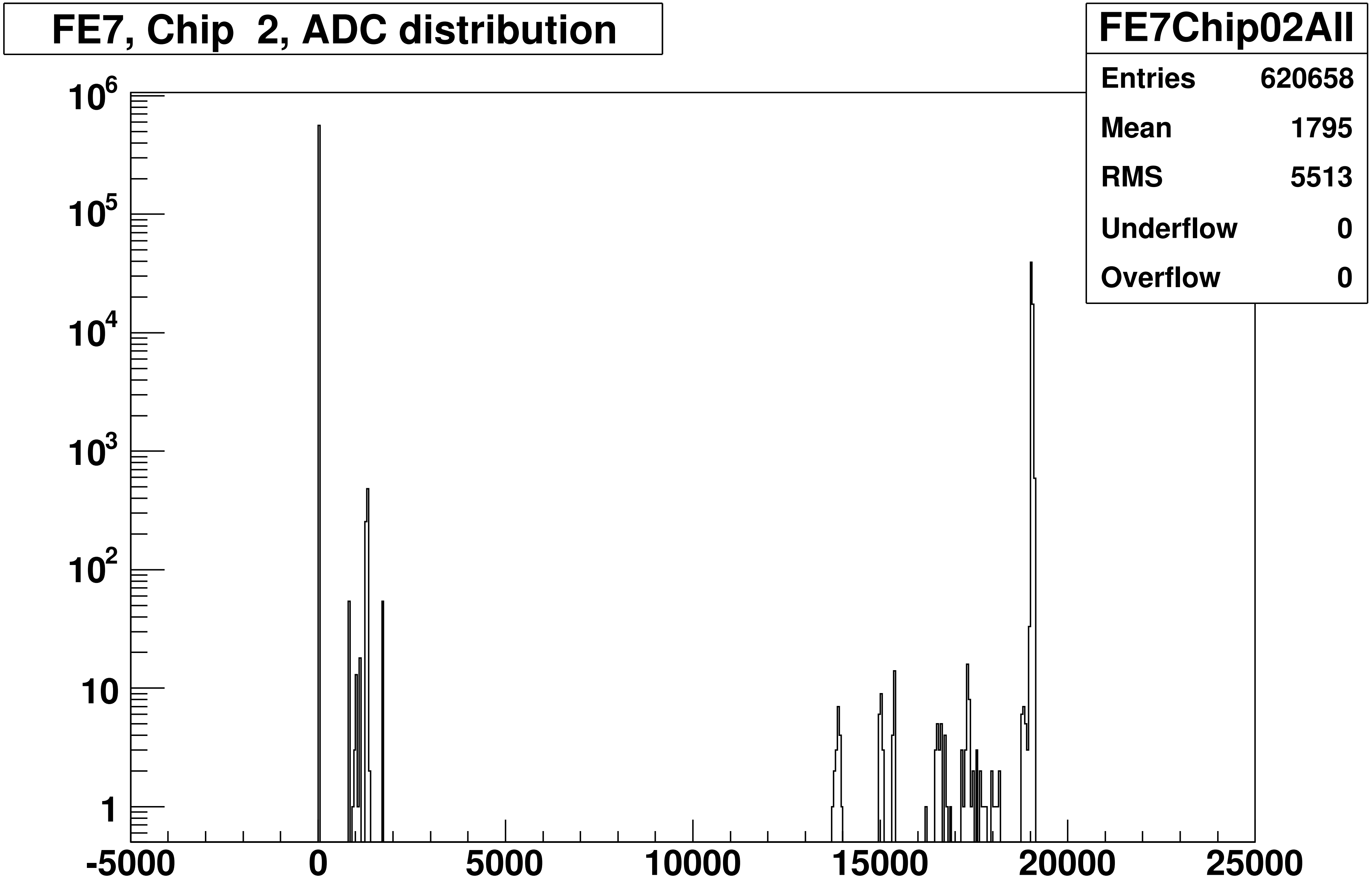
19400


FE7, Chip 2, ADC distribution
FE7Chip02All
Entries 620658
Mean 1795
RMS 5513
Underflow 0
Overflow 0
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10^4
10^3
10^2
10
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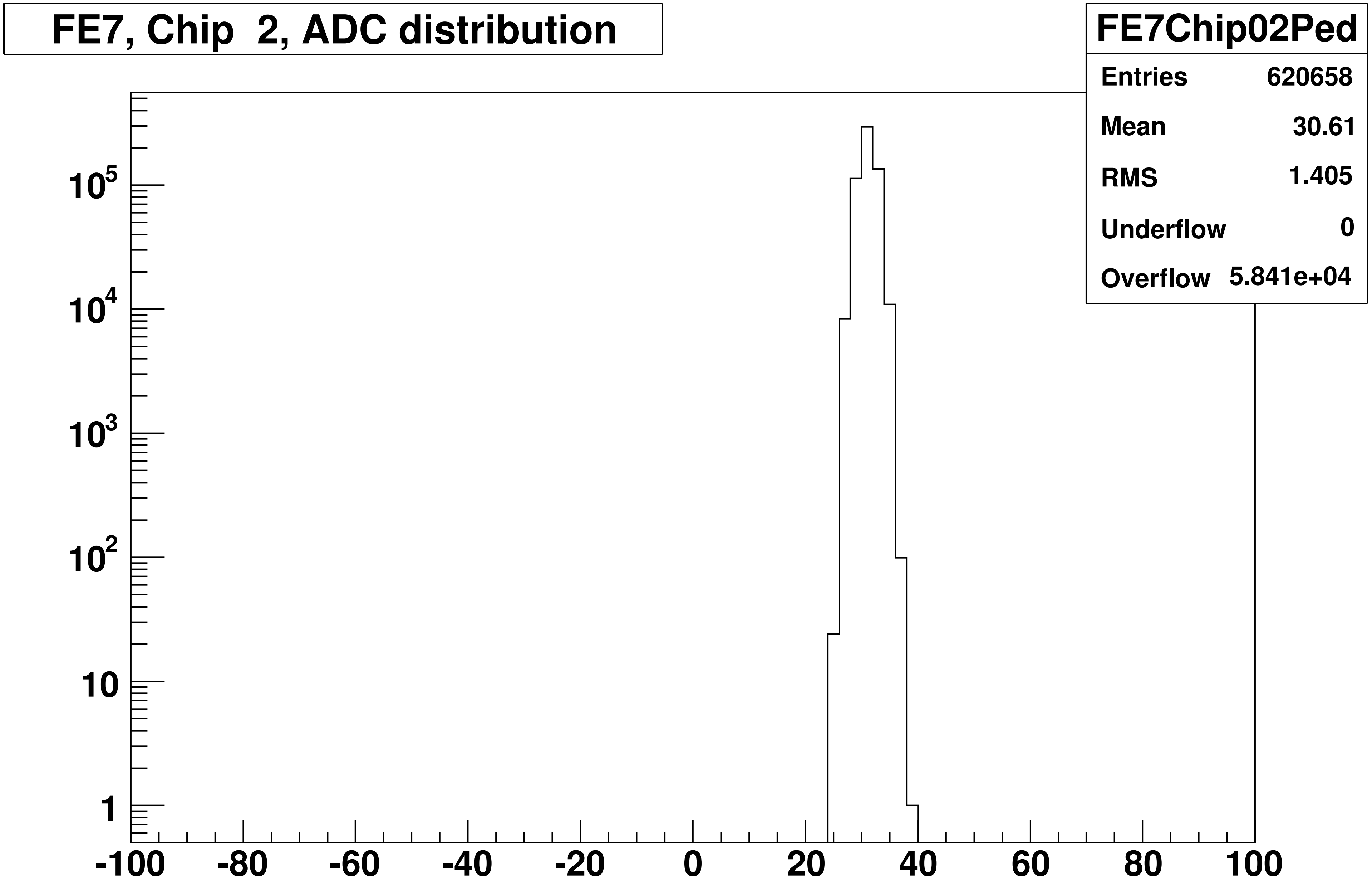
20000
25000


FE7, Chip 2, ADC distribution
FE7Chip02Ped
Entries 620658
Mean 30.61
RMS 1.405
Underflow 0
Overflow 5.841e+04
10^5
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10^3
10^2
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-80
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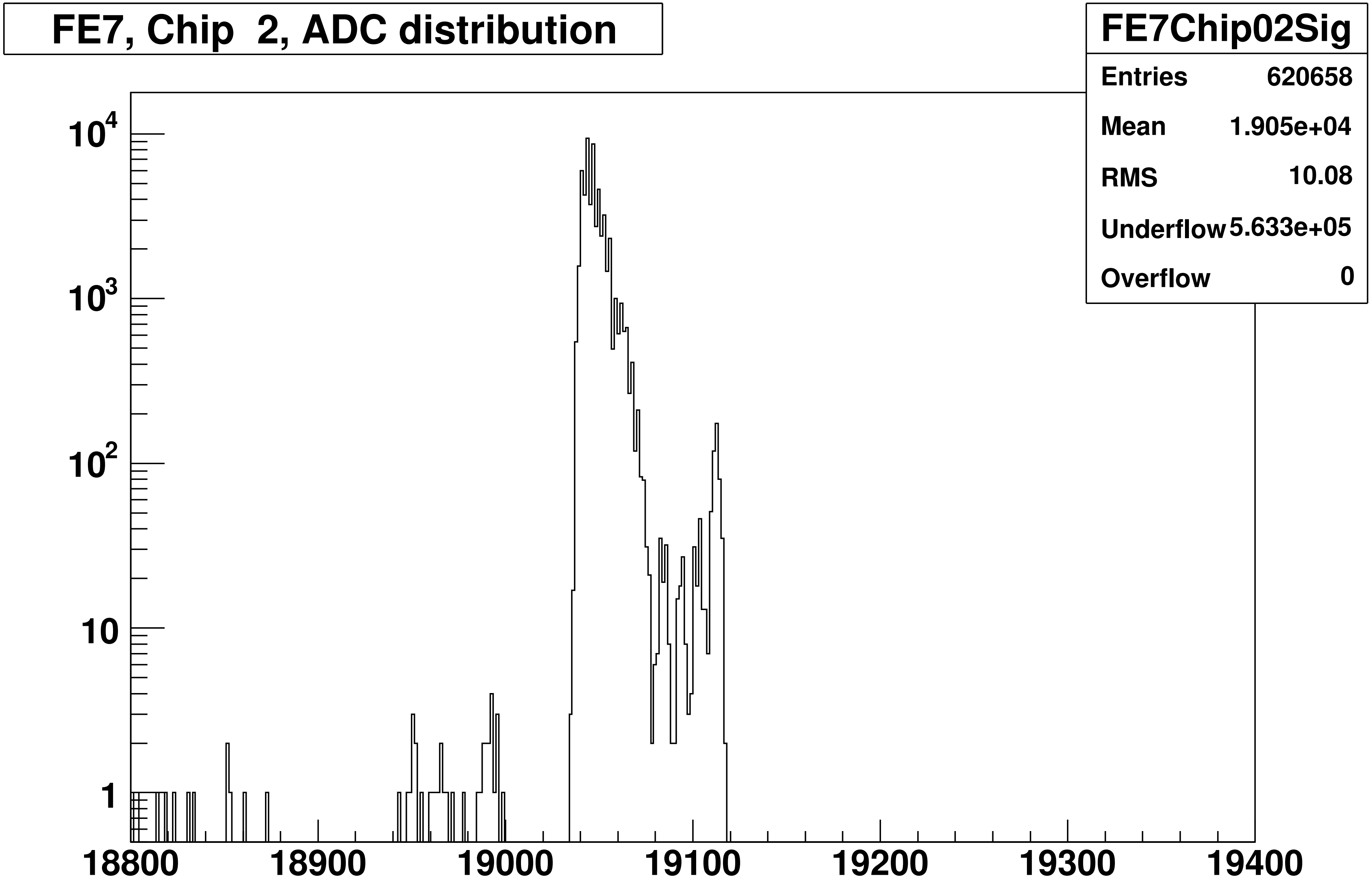
FE7, Chip 2, ADC distribution
FE7Chip02Sig
Entries 620658
Mean 1.905e+04
RMS 10.08
Underflow 5.633e+05
Overflow 0
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10^3
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18900
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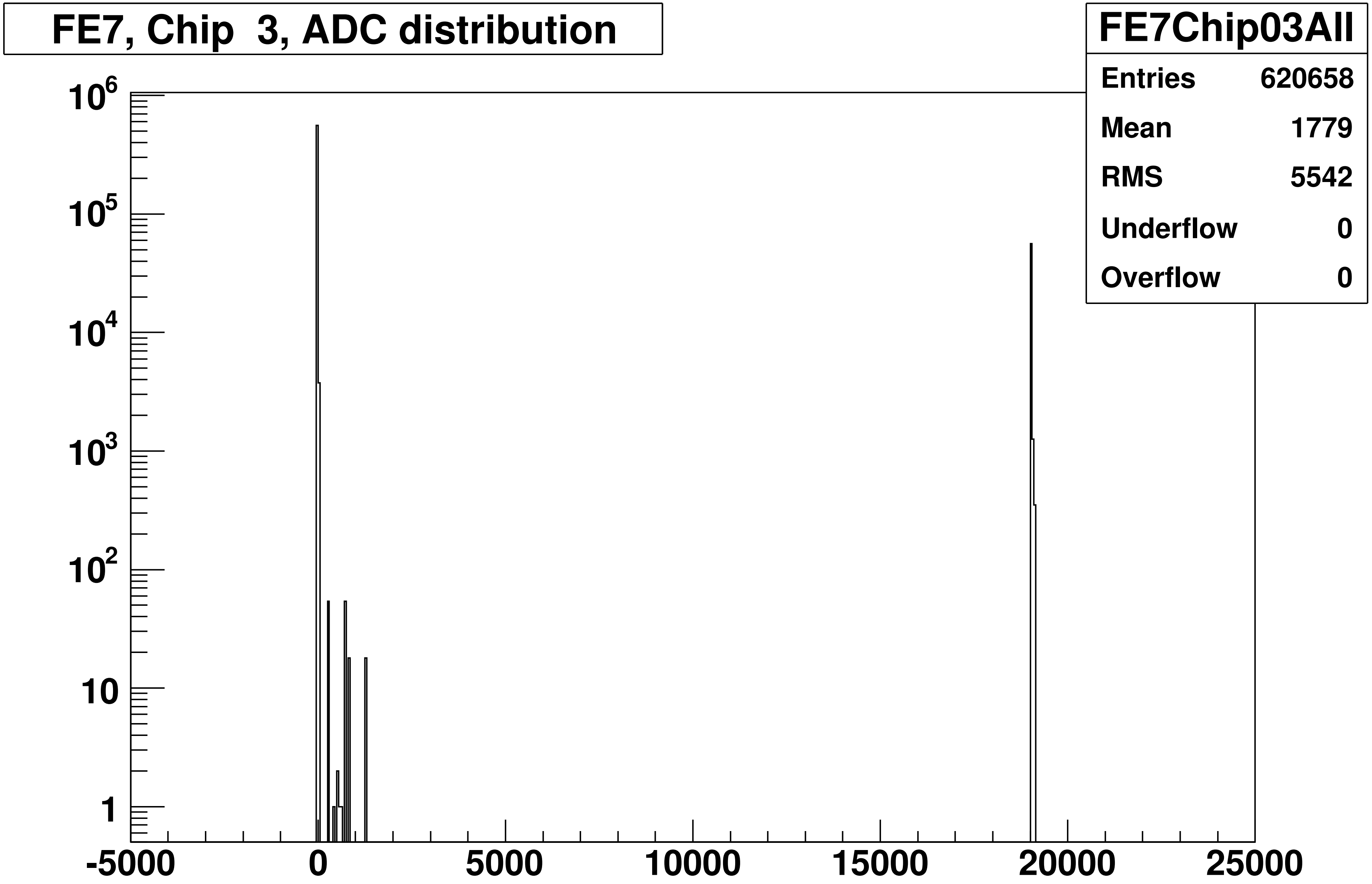
FE7, Chip 3, ADC distribution
FE7Chip03All
Entries 620658
Mean 1779
RMS 5542
Underflow 0
Overflow 0
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10^3
10^2
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15000
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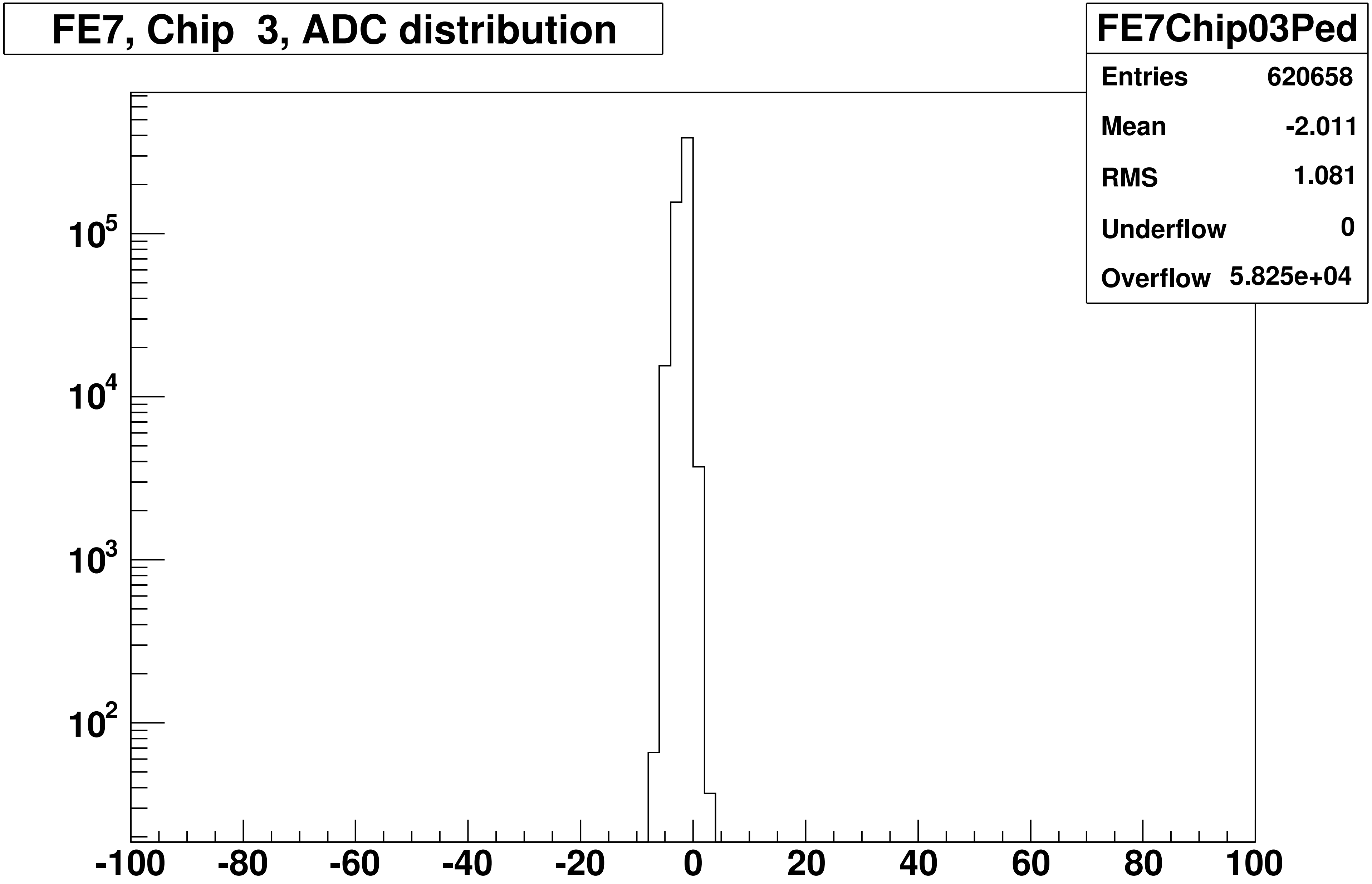
25000


FE7, Chip 3, ADC distribution
FE7Chip03Ped
Entries 620658
Mean -2.011
RMS 1.081
Underflow 0
Overflow 5.825e+04
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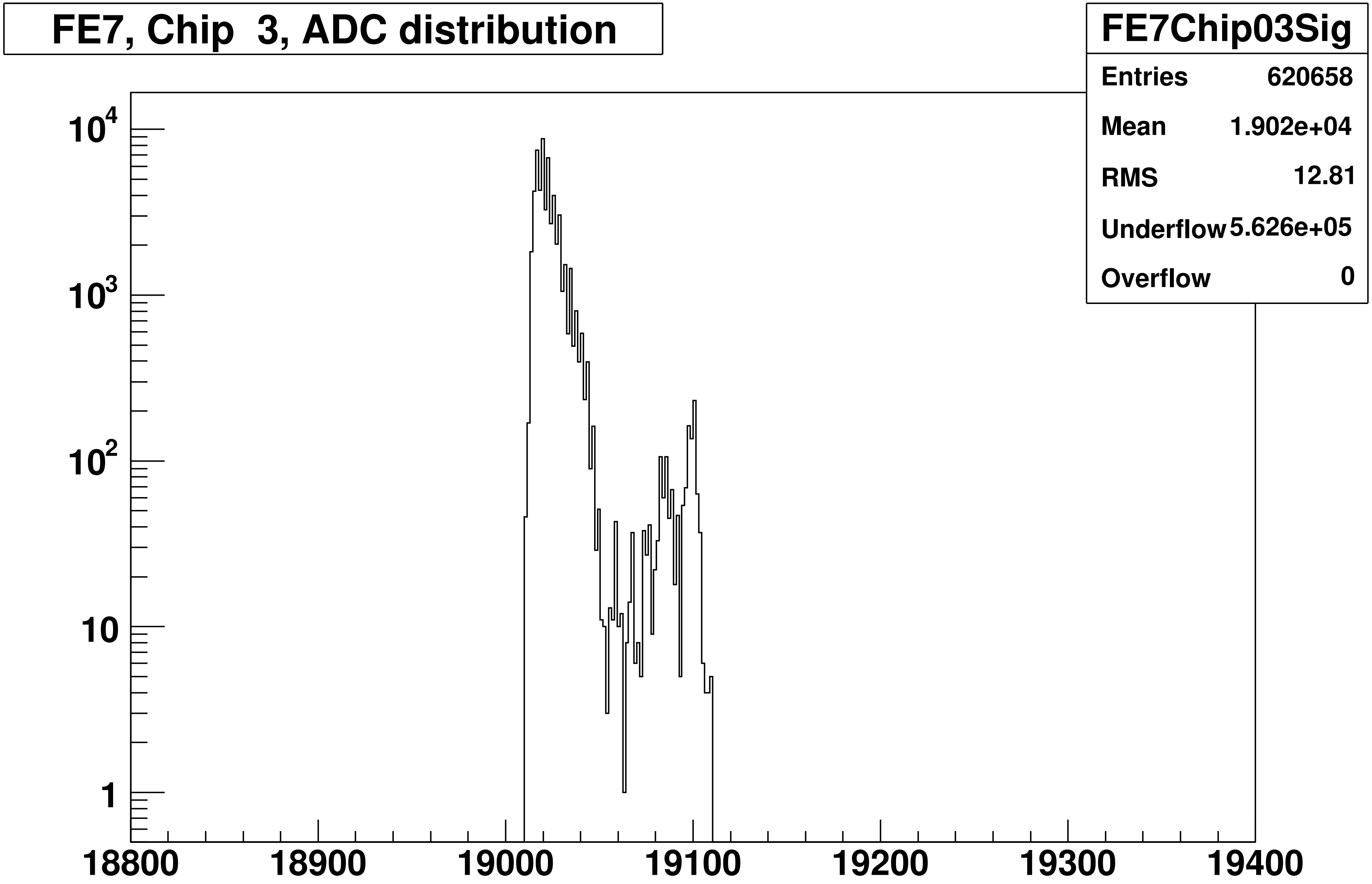

FE7, Chip 3, ADC distribution
FE7Chip03Sig
Entries 620658
Mean 1.902e+04
RMS 12.81
Underflow 5.626e+05
Overflow 0
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10^3
10^2
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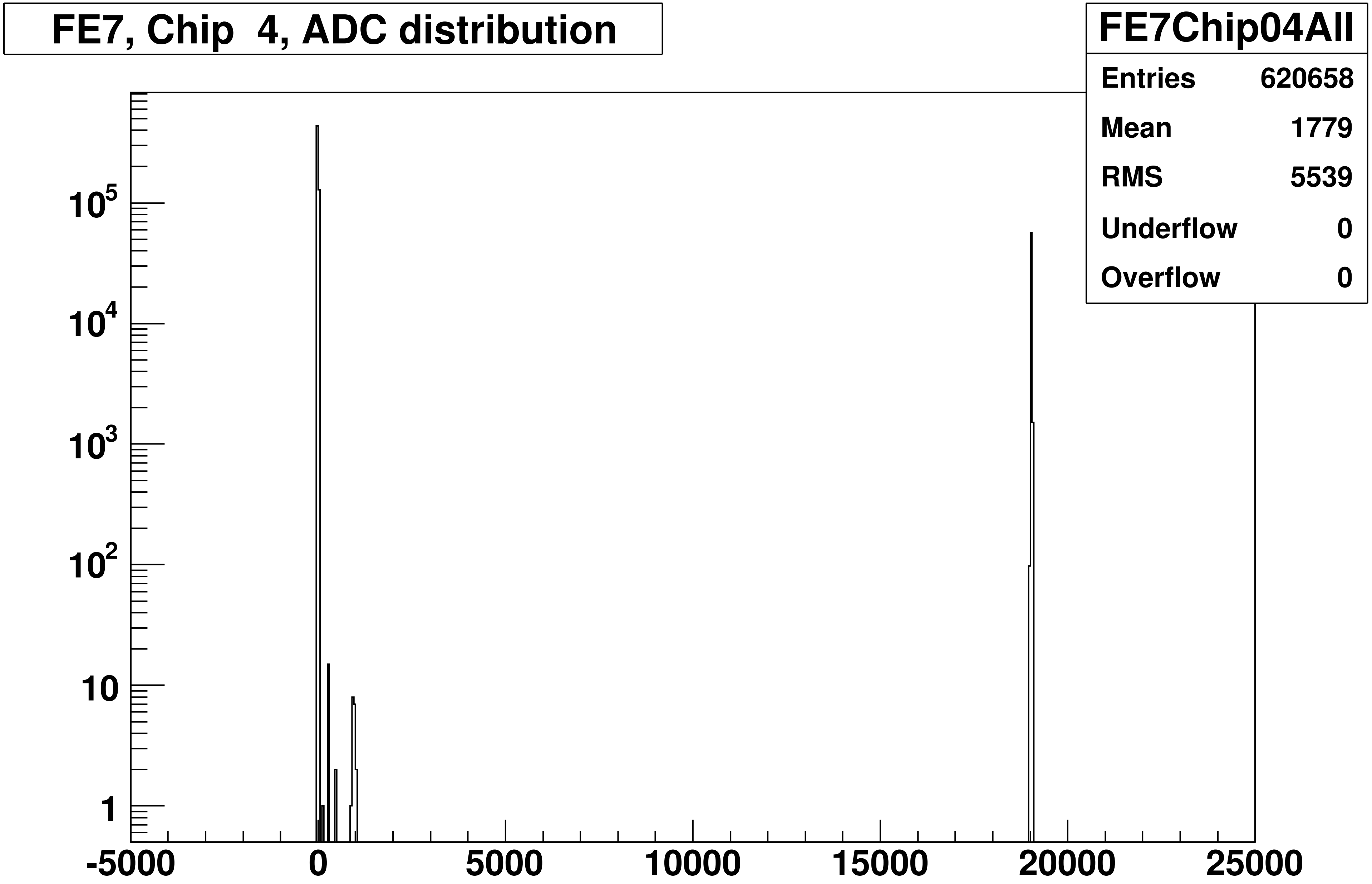
FE7, Chip 4, ADC distribution
FE7Chip04All
Entries 620658
Mean 1779
RMS 5539
Underflow 0
Overflow 0
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10^4
10^3
10^2
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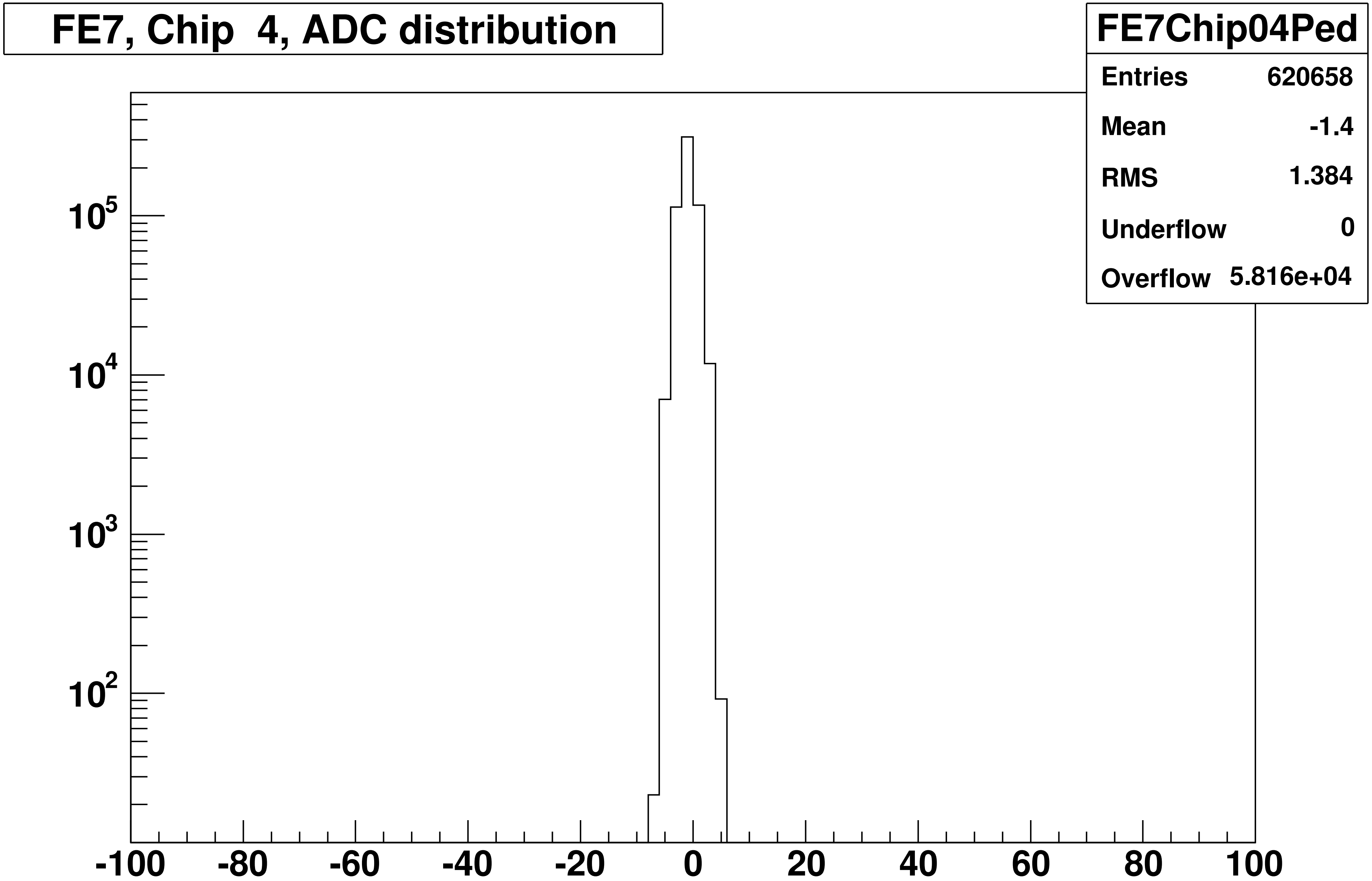
FE7, Chip 4, ADC distribution
FE7Chip04Ped
Entries 620658
Mean -1.4
RMS 1.384
Underflow 0
Overflow 5.816e+04
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10^3
10^2
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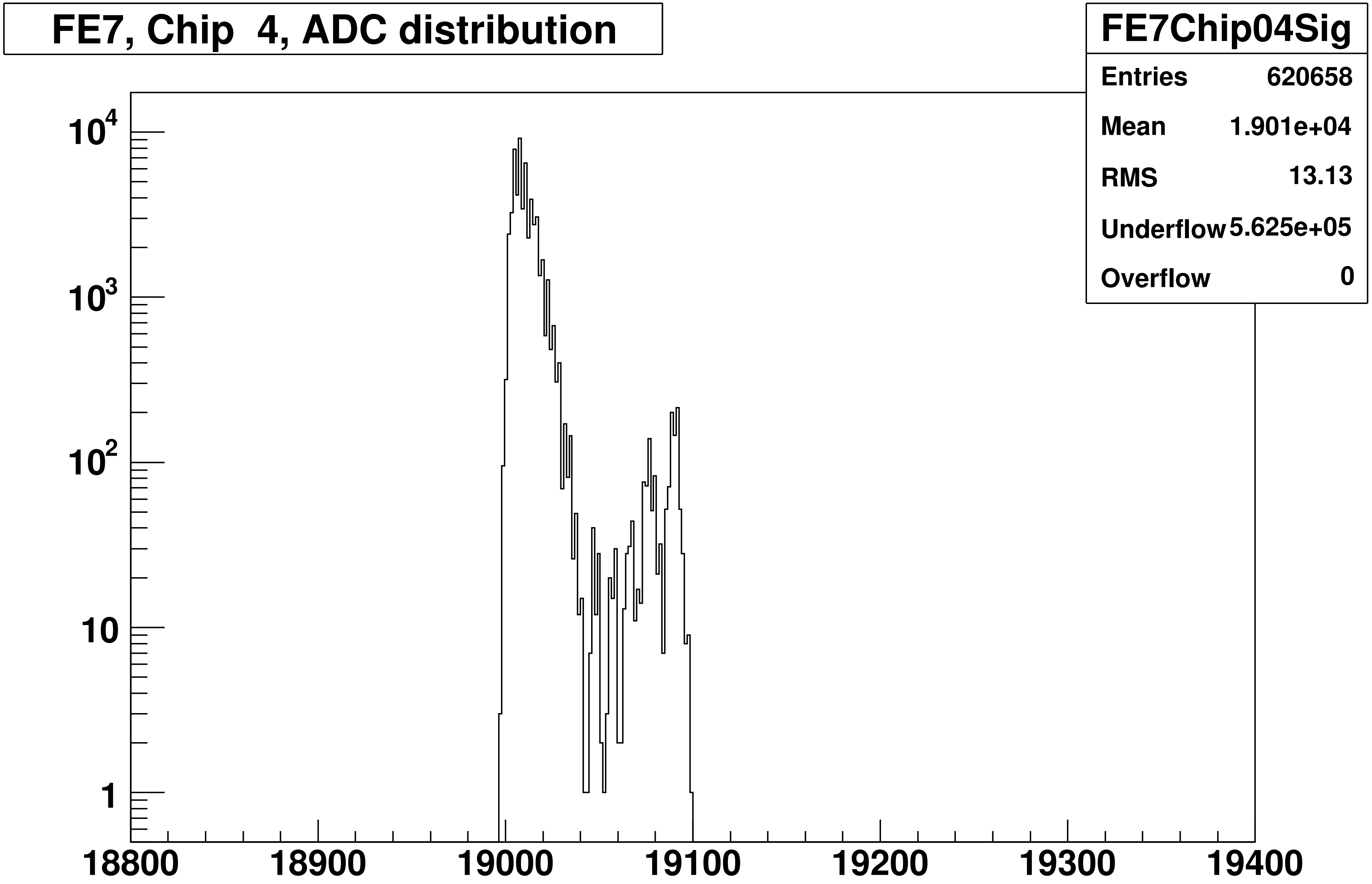

FE7, Chip 4, ADC distribution
FE7Chip04Sig
Entries 620658
Mean 1.901e+04
RMS 13.13
Underflow 5.625e+05
Overflow 0
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10^3
10^2
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18900
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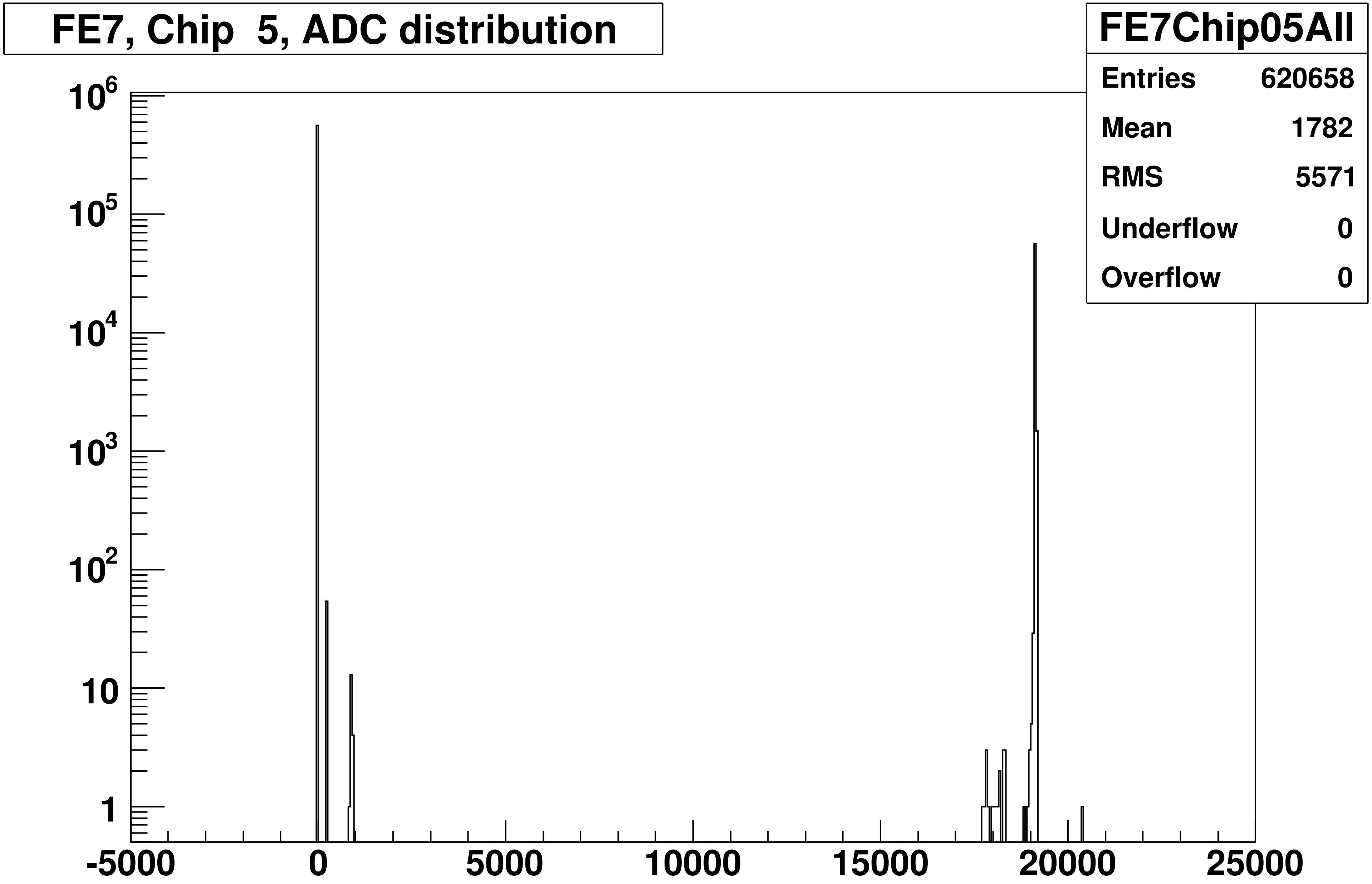
FE7, Chip 5, ADC distribution
FE7Chip05All
Entries 620658
Mean 1782
RMS 5571
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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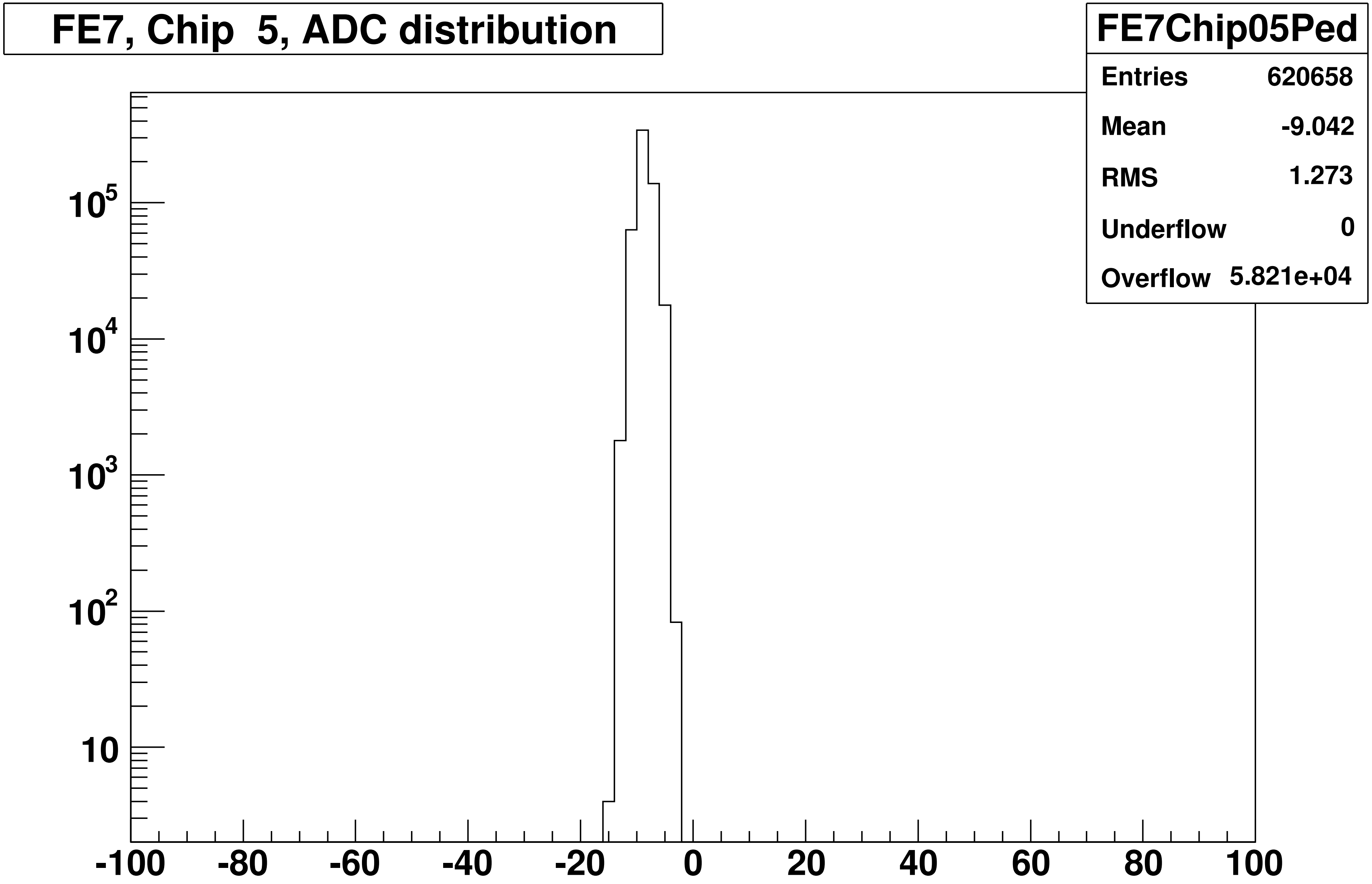
25000


FE7, Chip 5, ADC distribution
FE7Chip05Ped
Entries 620658
Mean -9.042
RMS 1.273
Underflow 0
Overflow 5.821e+04
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10^2
10^3
10^4
10^5
-100
-80
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-40
-20
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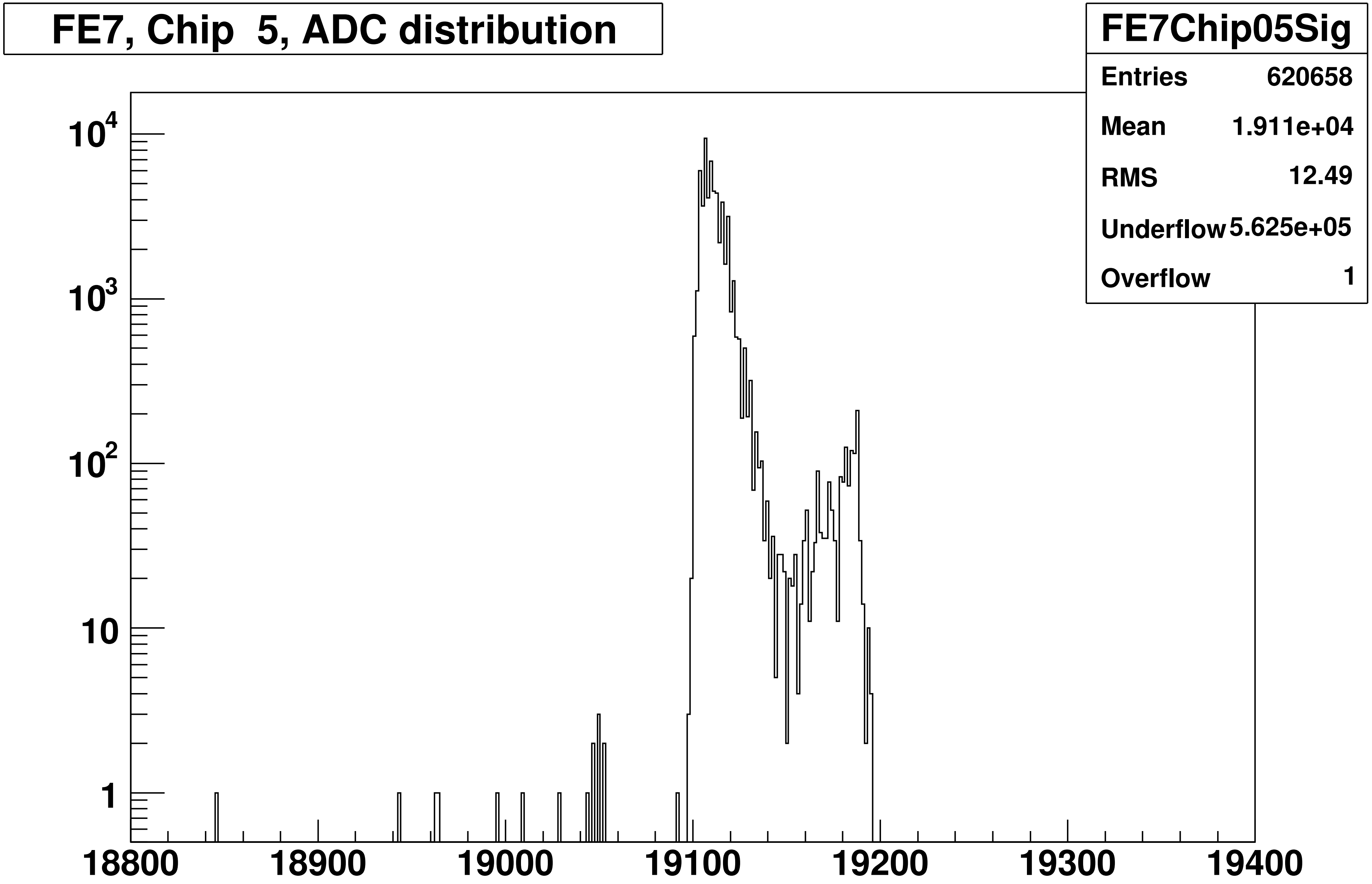
40
60
80
100


FE7, Chip 5, ADC distribution
FE7Chip05Sig
Entries 620658
Mean 1.911e+04
RMS 12.49
Underflow 5.625e+05
Overflow 1
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10^3
10^2
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18900
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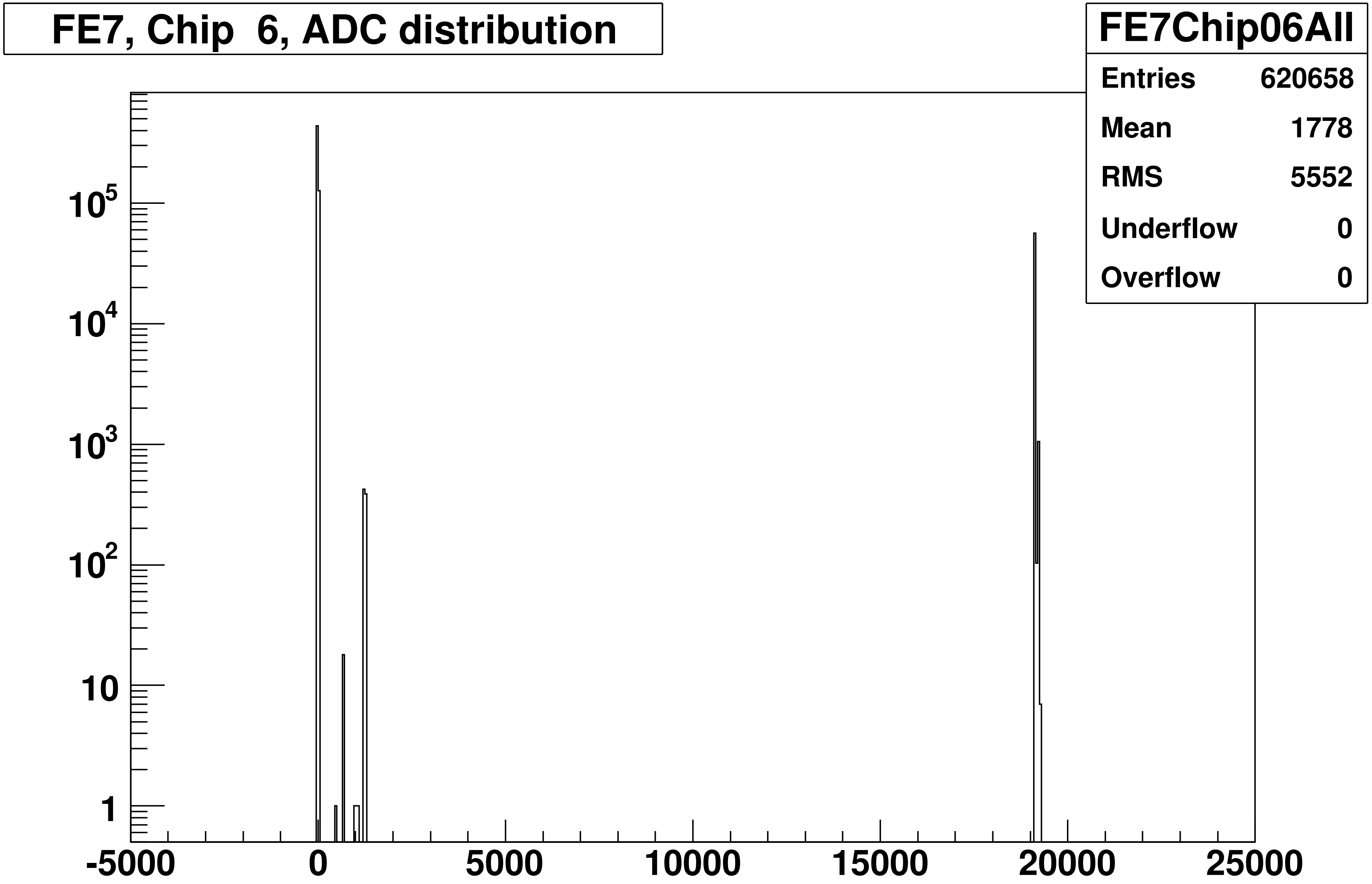
19400



FE7, Chip 6, ADC distribution
FE7Chip06All
Entries 620658
Mean 1778
RMS 5552
Underflow 0
Overflow 0
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10^4
10^3
10^2
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-5000
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5000
10000
15000
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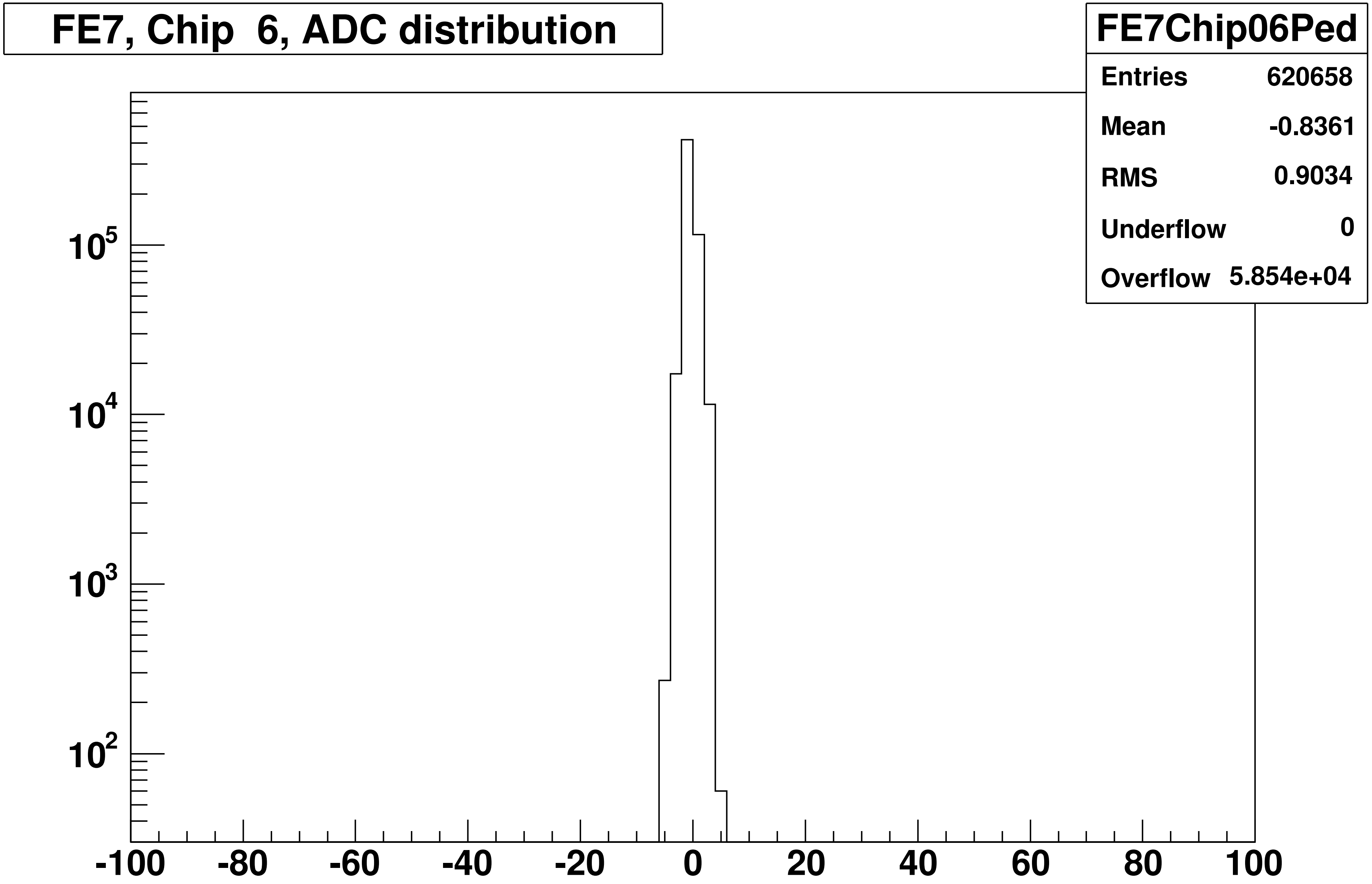
FE7, Chip 6, ADC distribution
FE7Chip06Ped
Entries 620658
Mean -0.8361
RMS 0.9034
Underflow 0
Overflow 5.854e+04
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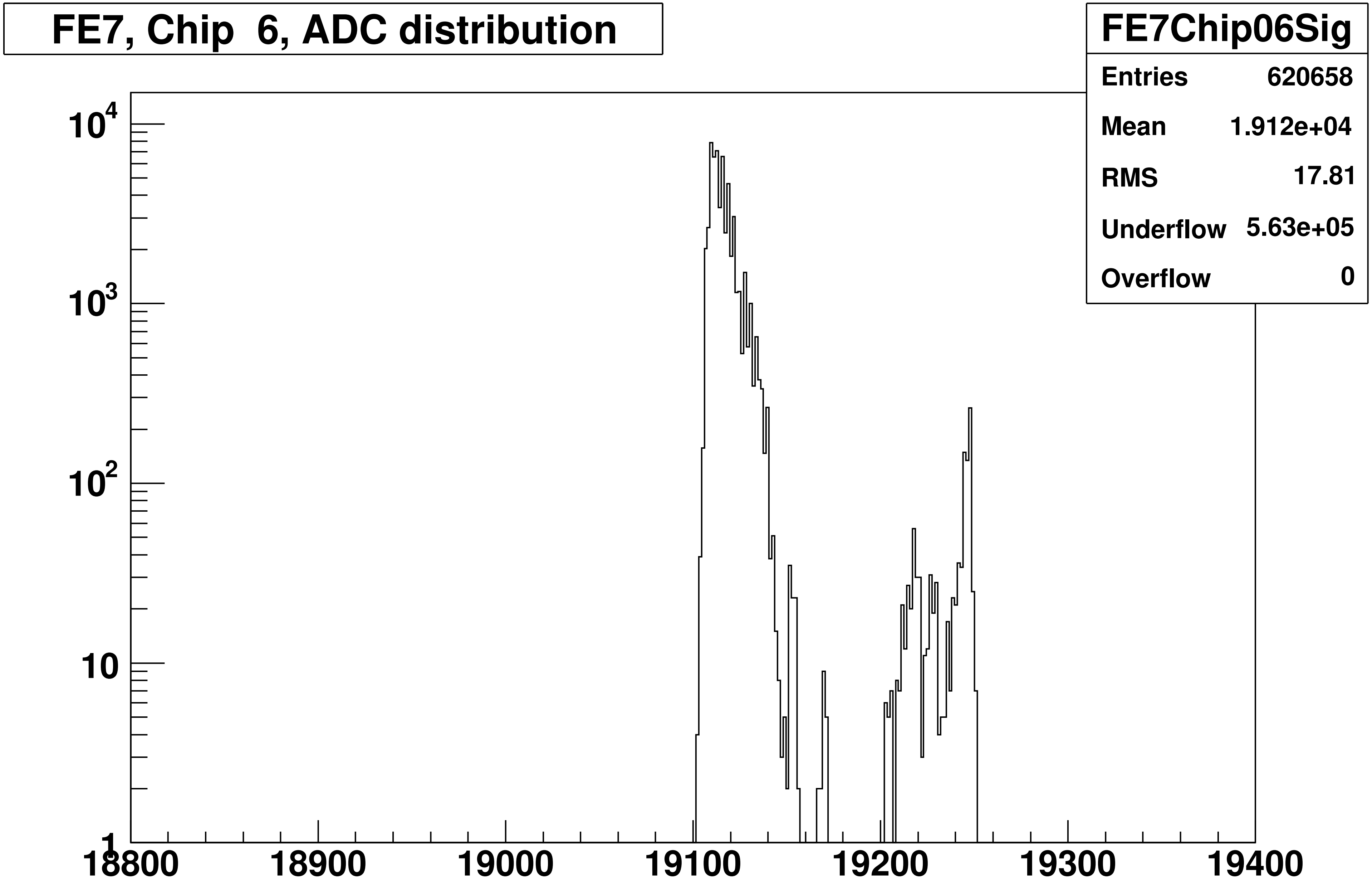
10^4
10^3
10^2
-100 -80 -60 -40 -20 0 20 40 60 80 100


FE7, Chip 6, ADC distribution
FE7Chip06Sig
Entries 620658
Mean 1.912e+04
RMS 17.81
Underflow 5.63e+05
Overflow 0
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10^3
10^2
10
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18900
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19200
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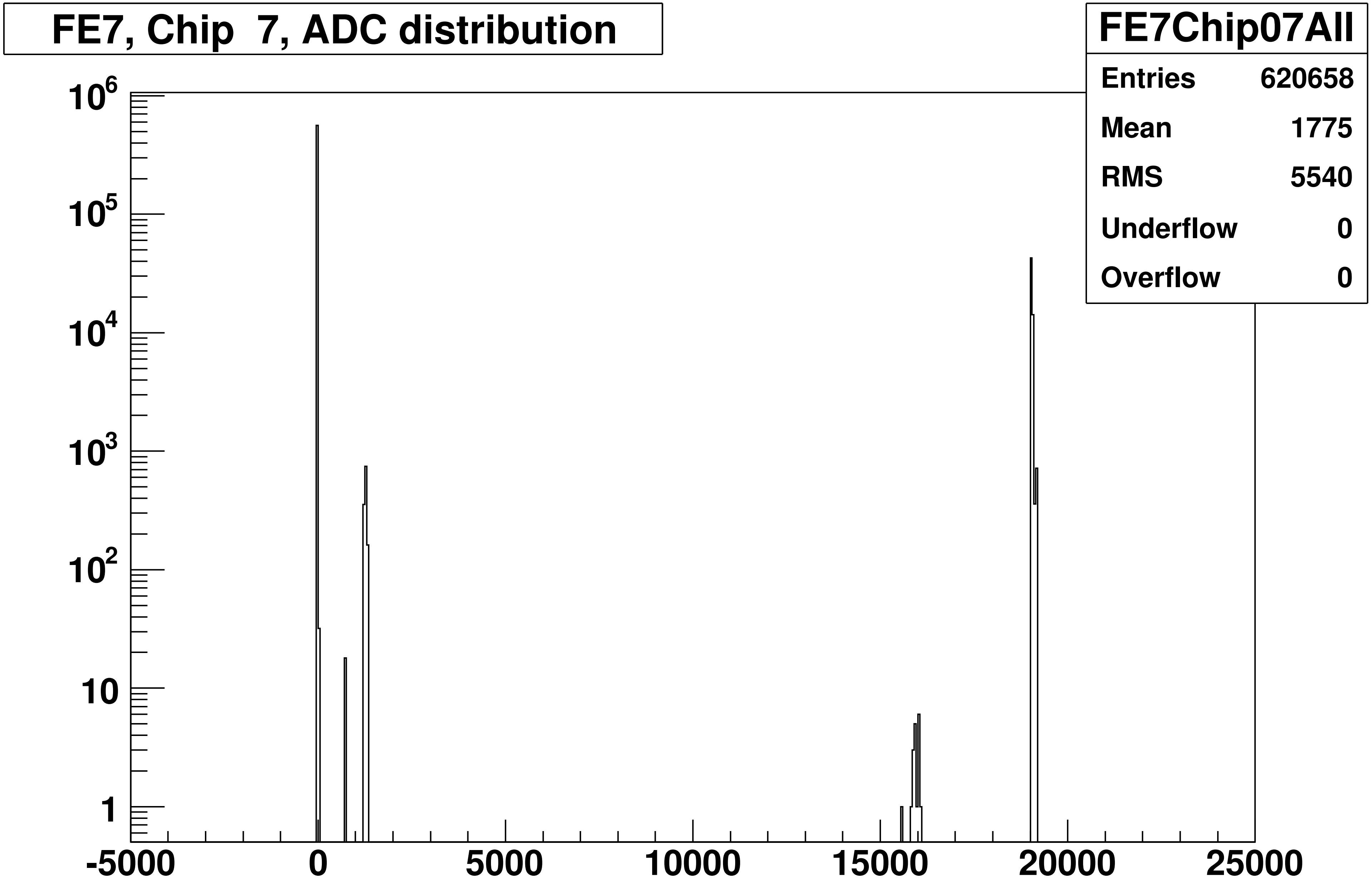
FE7, Chip 7, ADC distribution
FE7Chip07All
Entries 620658
Mean 1775
RMS 5540
Underflow 0
Overflow 0
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10^5
10^4
10^3
10^2
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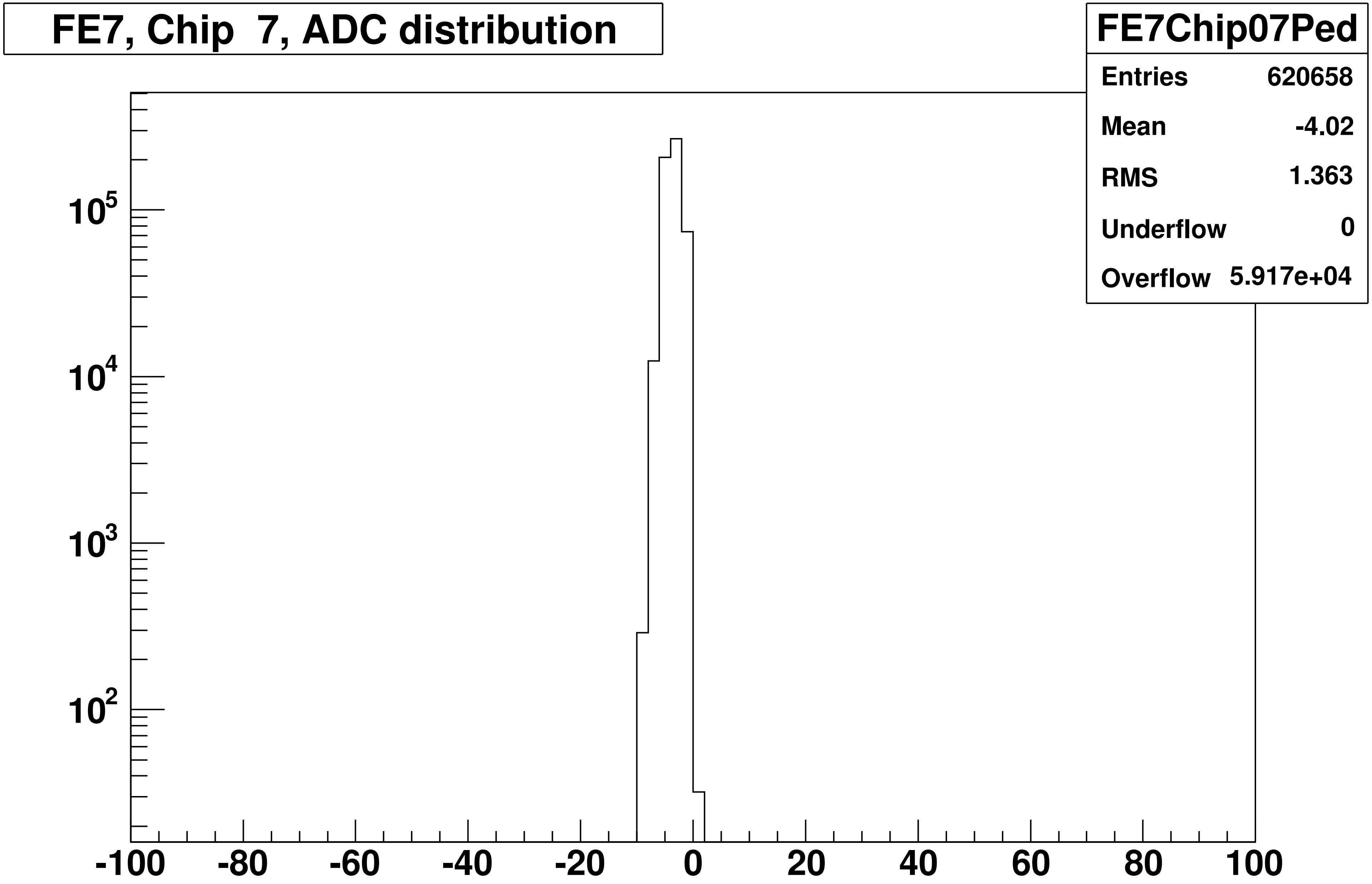

FE7, Chip 7, ADC distribution
FE7Chip07Ped
Entries 620658
Mean -4.02
RMS 1.363
Underflow 0
Overflow 5.917e+04
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10^3
10^2
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80
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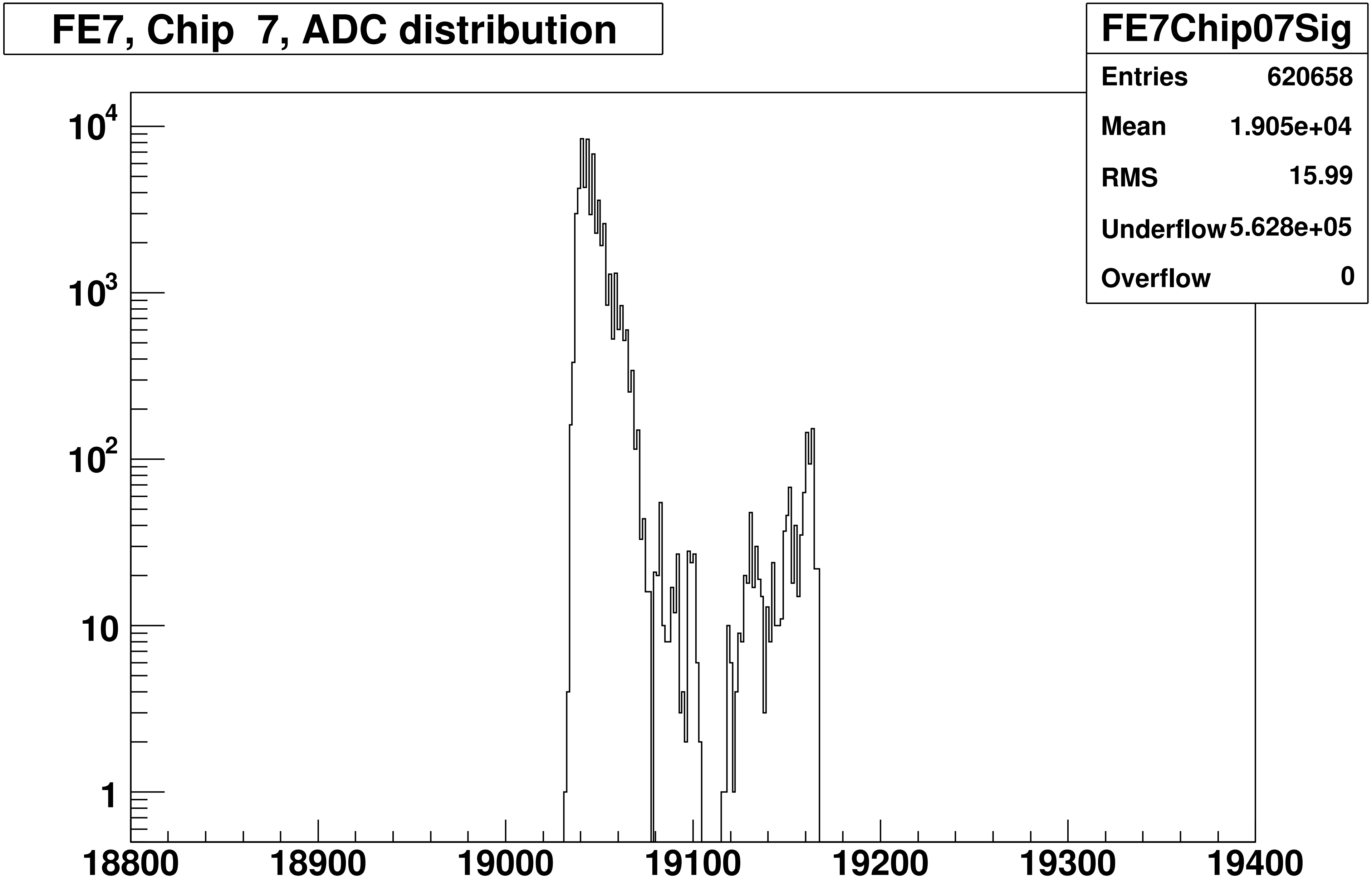
FE7, Chip 7, ADC distribution
FE7Chip07Sig
Entries 620658
Mean 1.905e+04
RMS 15.99
Underflow 5.628e+05
Overflow 0
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10^3
10^2
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18900
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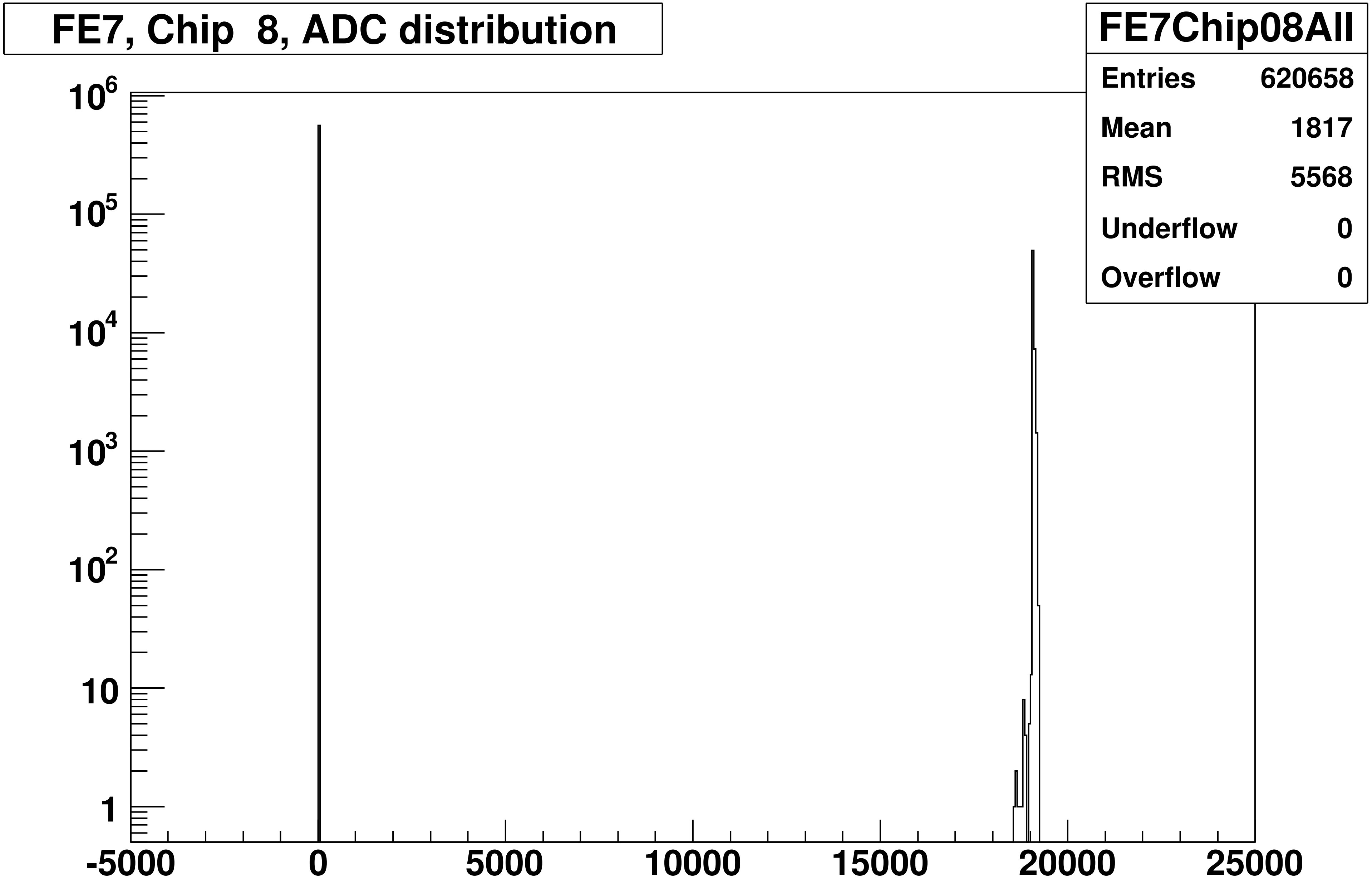
FE7, Chip 8, ADC distribution
FE7Chip08All
Entries 620658
Mean 1817
RMS 5568
Underflow 0
Overflow 0
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10^3
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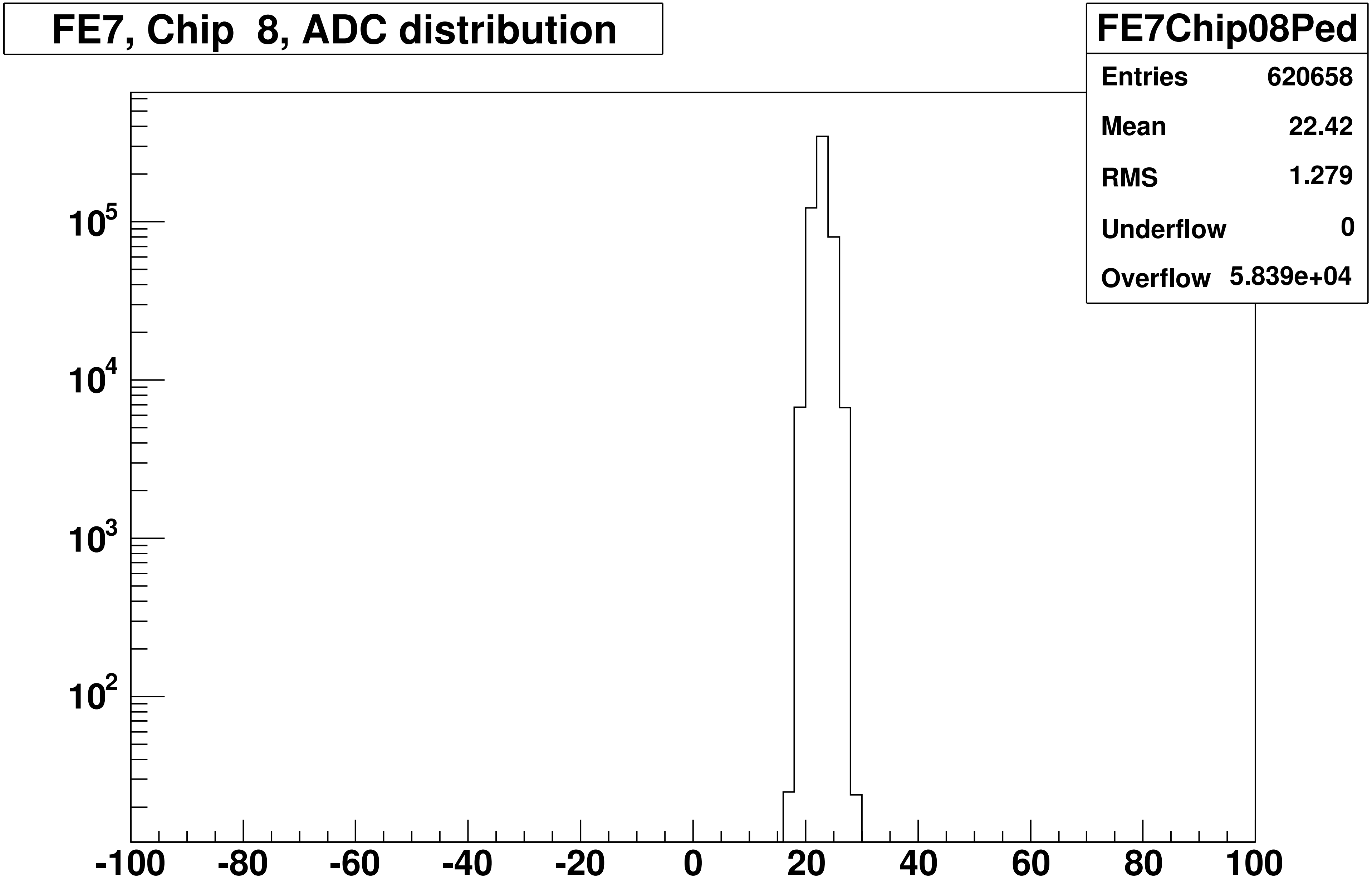

FE7, Chip 8, ADC distribution
FE7Chip08Ped
Entries 620658
Mean 22.42
RMS 1.279
Underflow 0
Overflow 5.839e+04
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10^3
10^2
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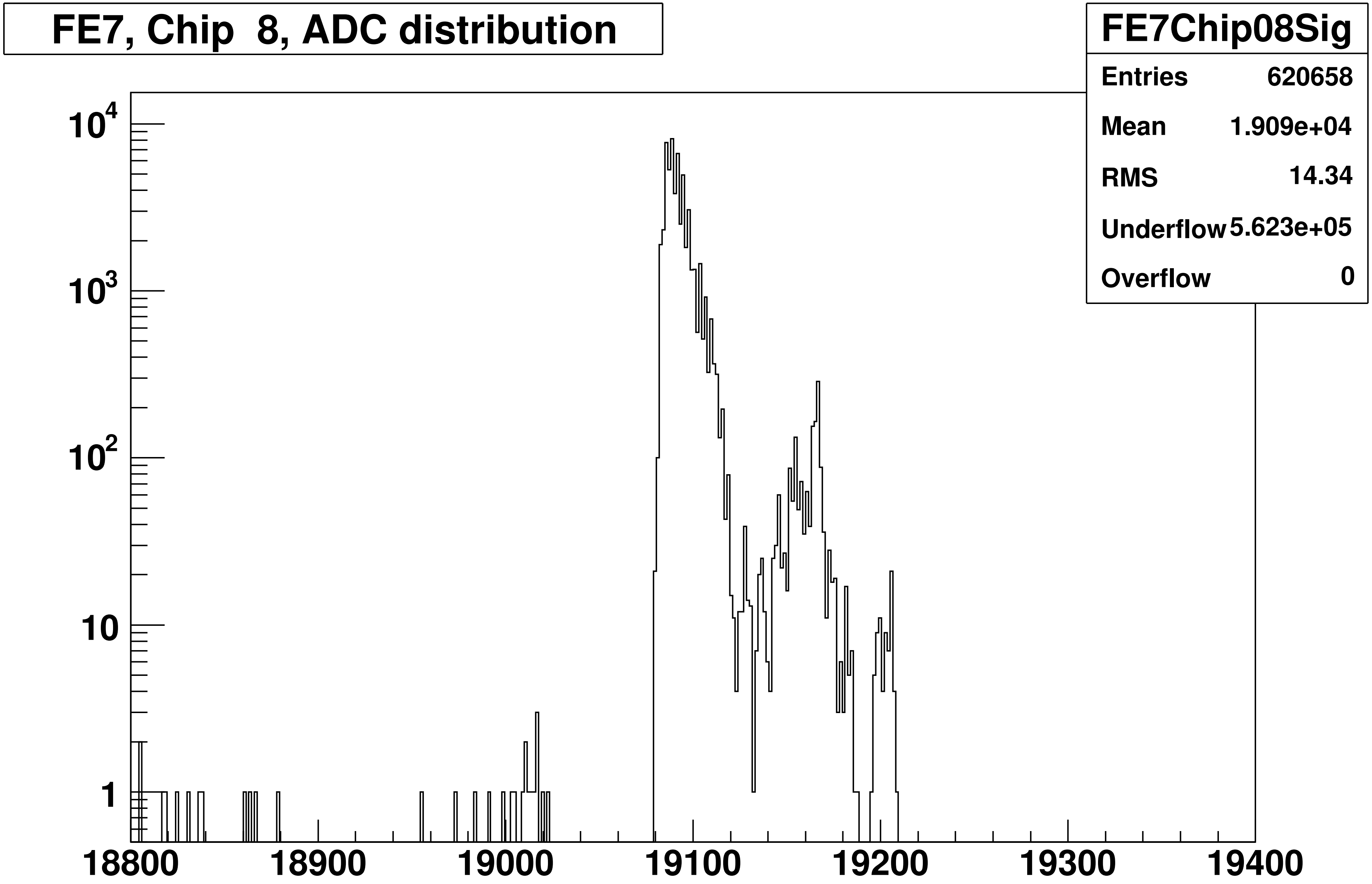
FE7, Chip 8, ADC distribution
FE7Chip08Sig
Entries 620658
Mean 1.909e+04
RMS 14.34
Underflow 5.623e+05
Overflow 0
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10^3
10^2
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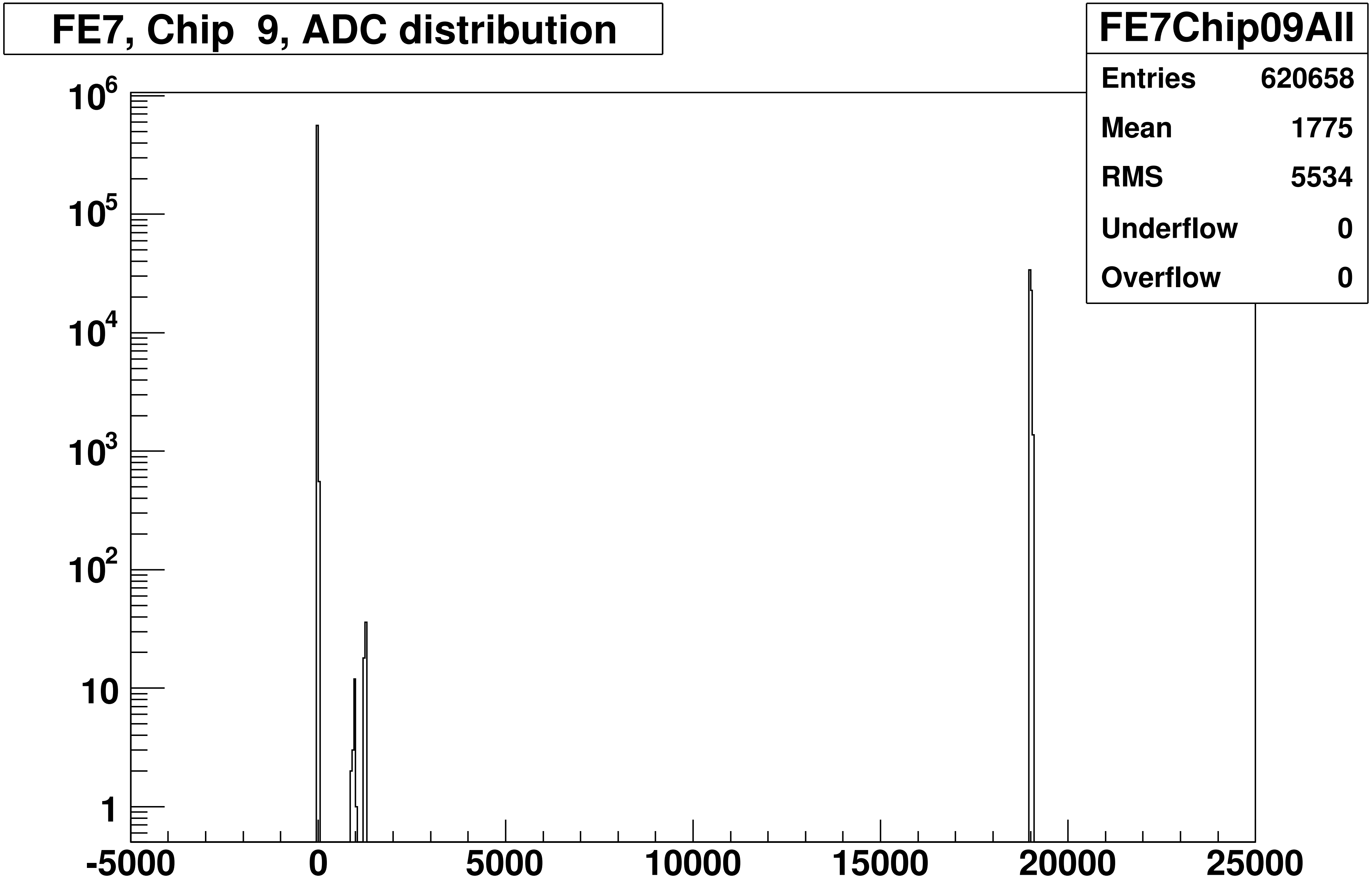
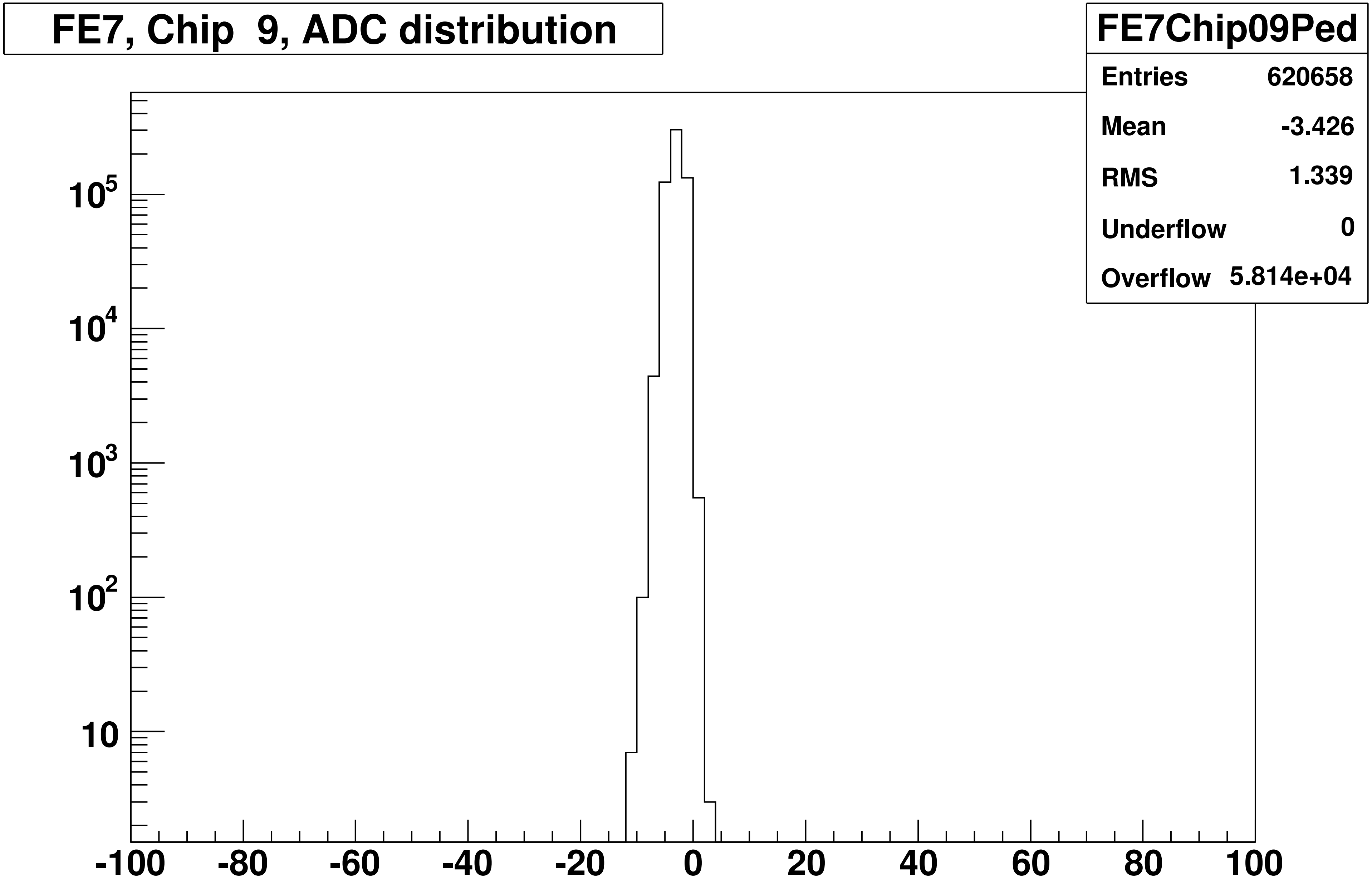
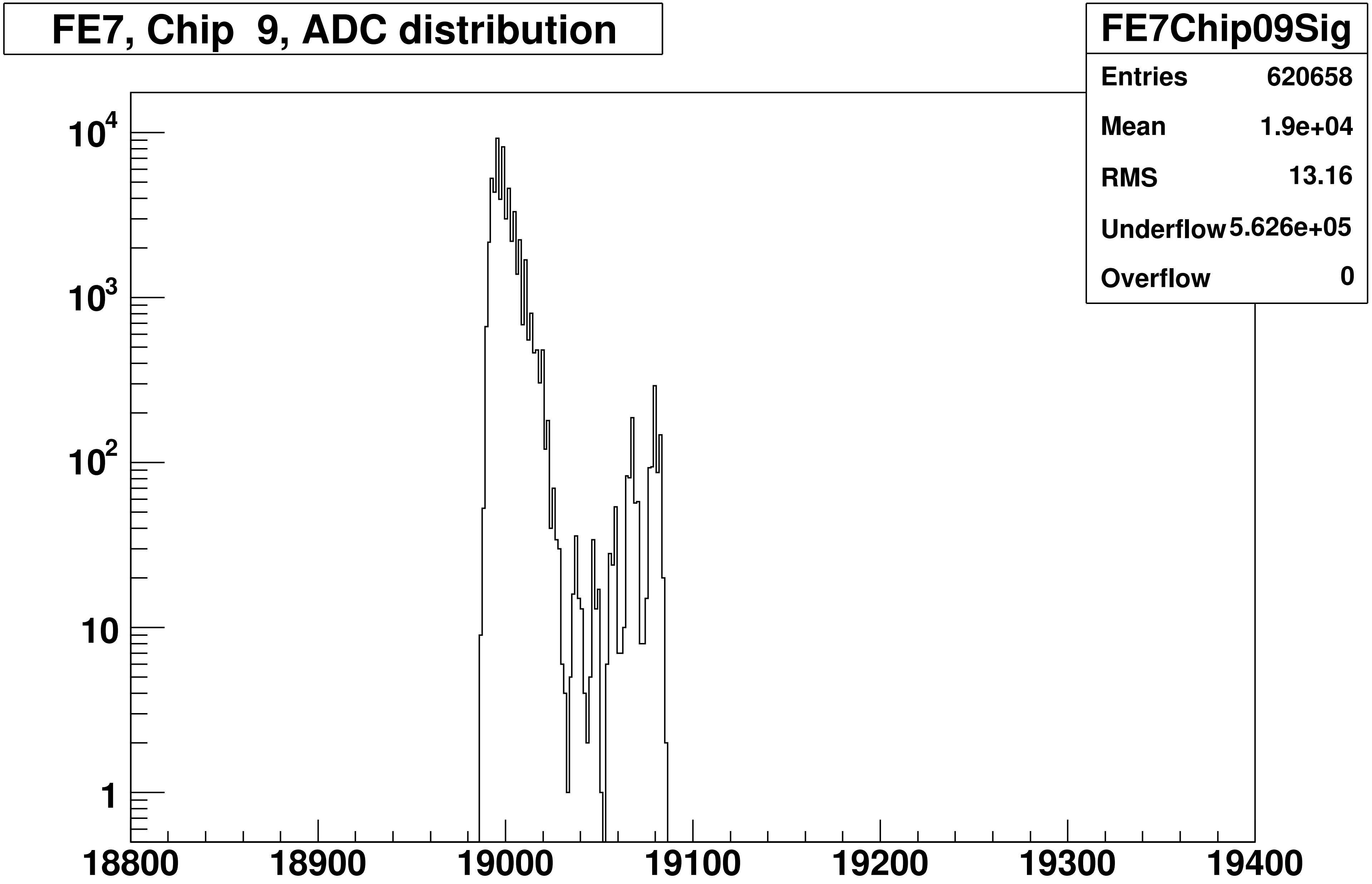
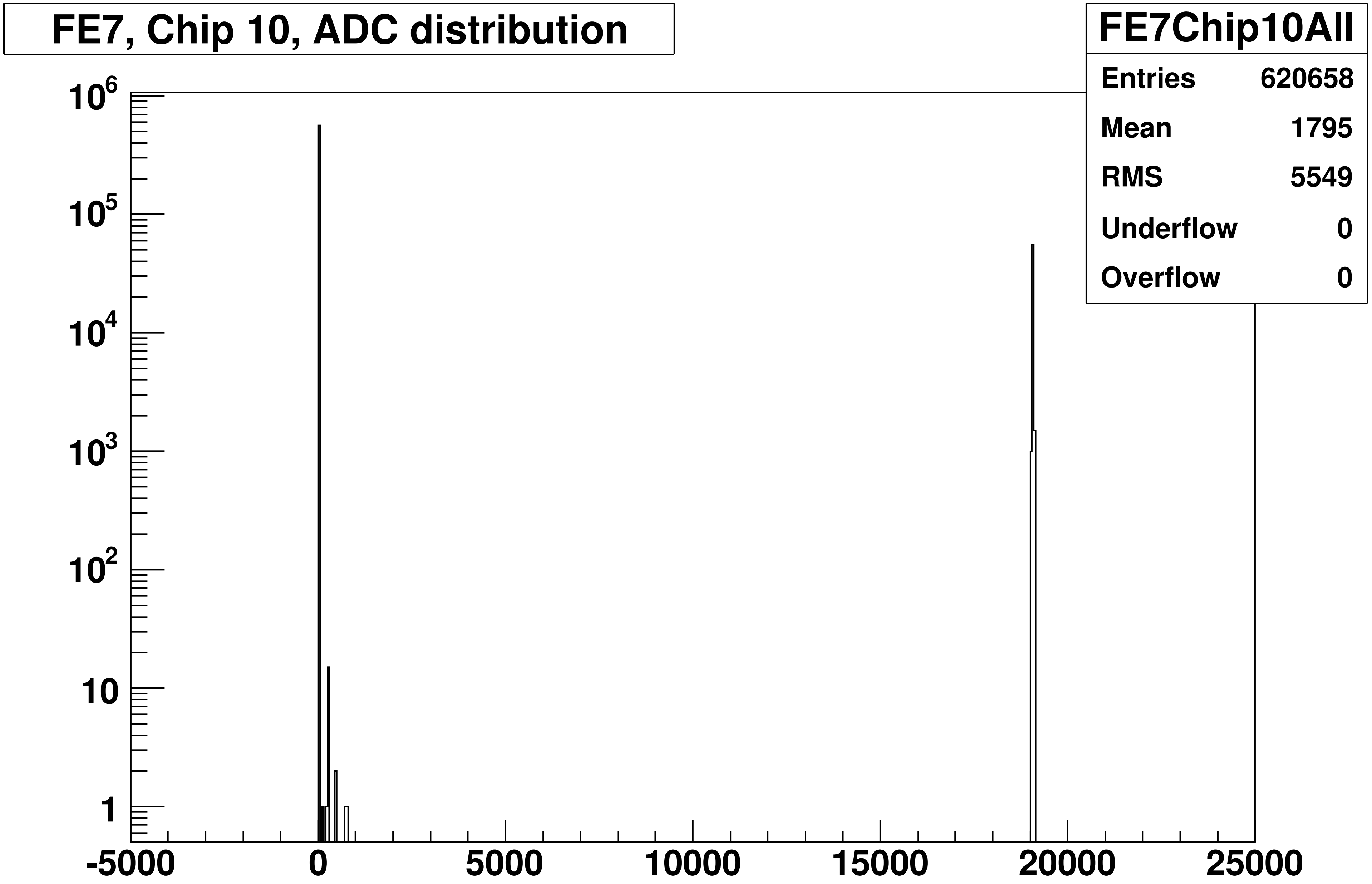
19400

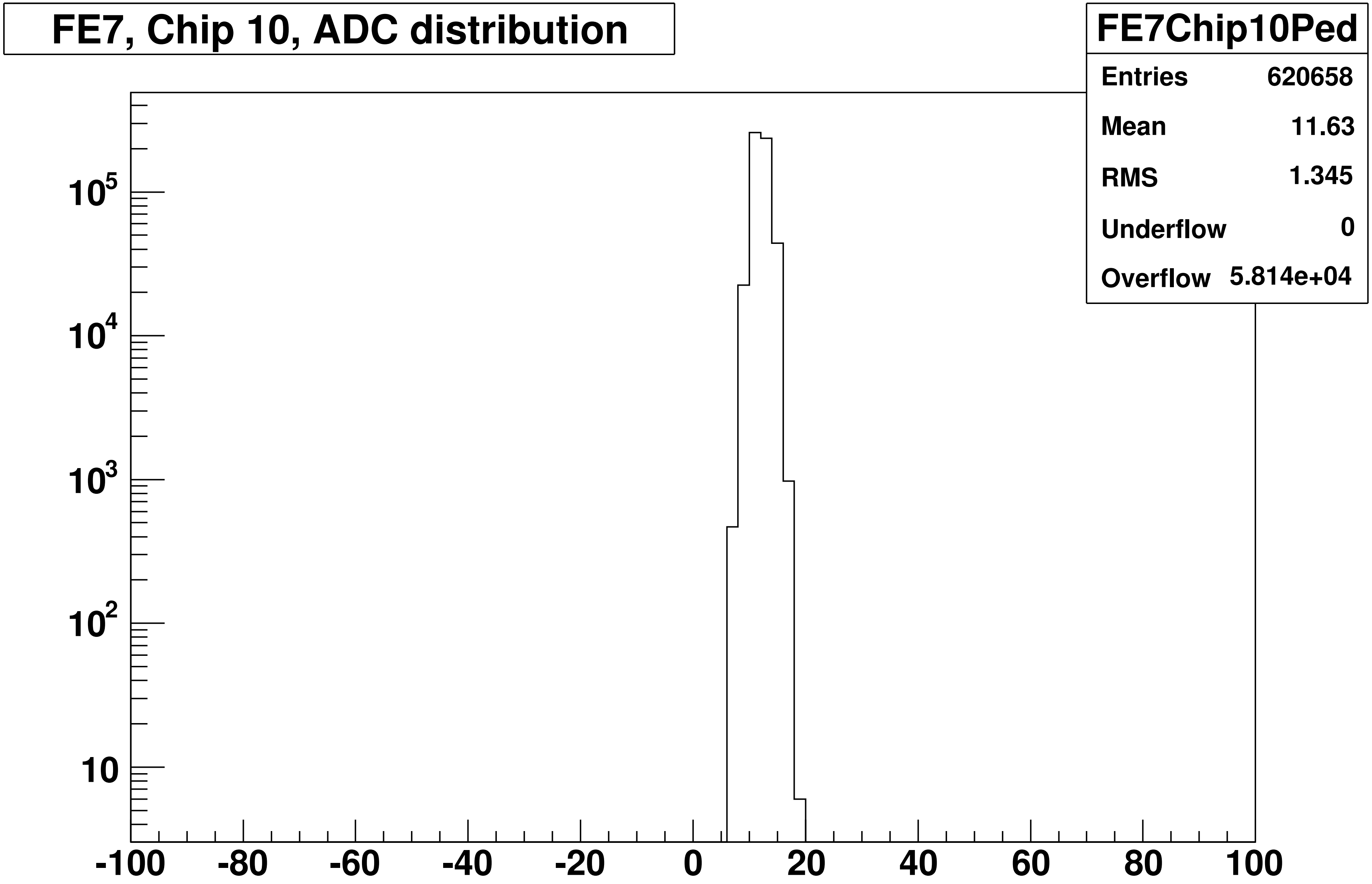


FE7, Chip 9, ADC distribution
FE7Chip09All
Entries 620658
Mean 1775
RMS 5534
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

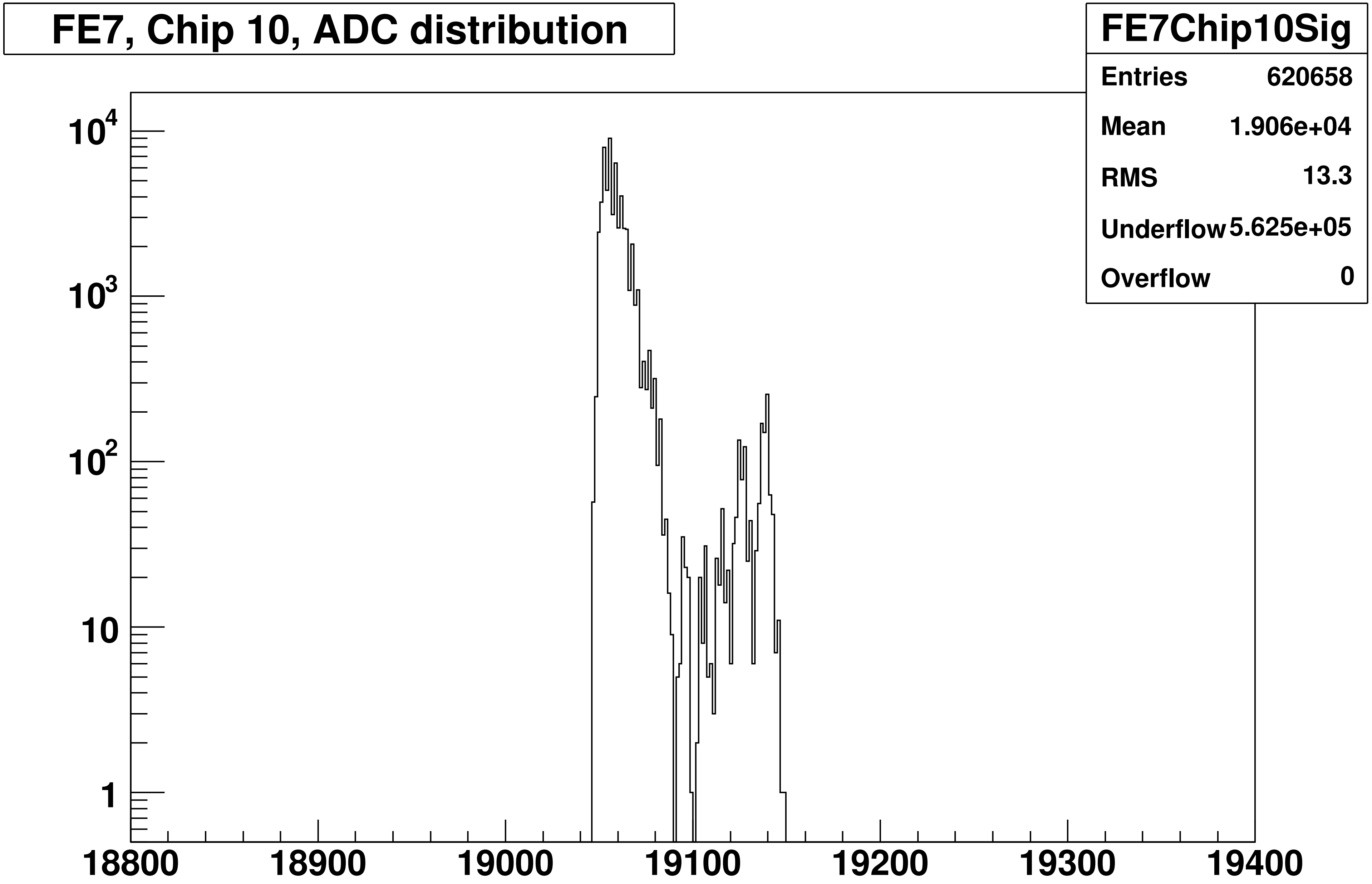

FE7, Chip 9, ADC distribution
FE7Chip09Ped
Entries 620658
Mean -3.426
RMS 1.339
Underflow 0
Overflow 5.814e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

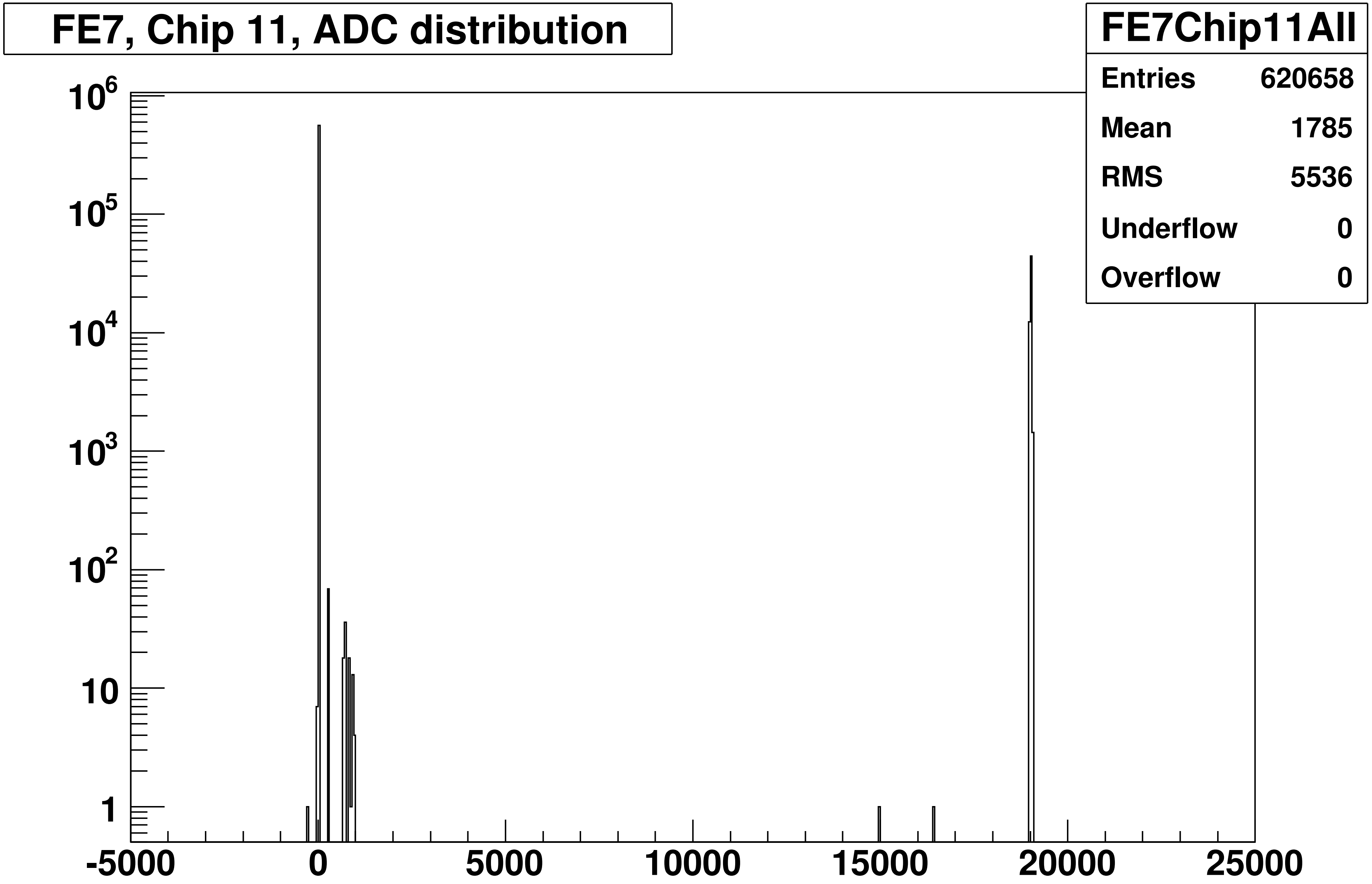

FE7, Chip 9, ADC distribution
FE7Chip09Sig
Entries 620658
Mean 1.9e+04
RMS 13.16
Underflow 5.626e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400

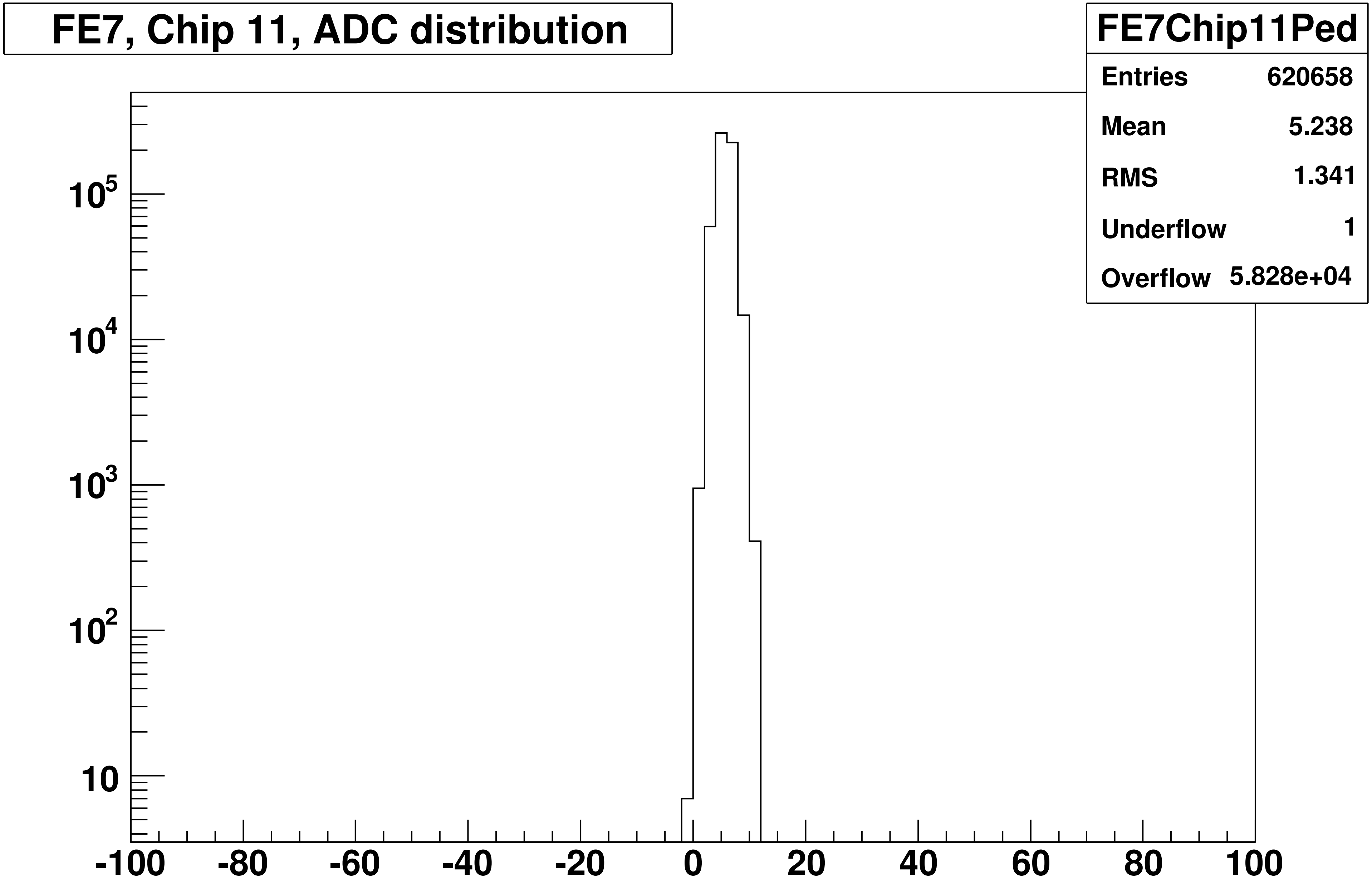

FE7, Chip 10, ADC distribution
FE7Chip10All
Entries 620658
Mean 1795
RMS 5549
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

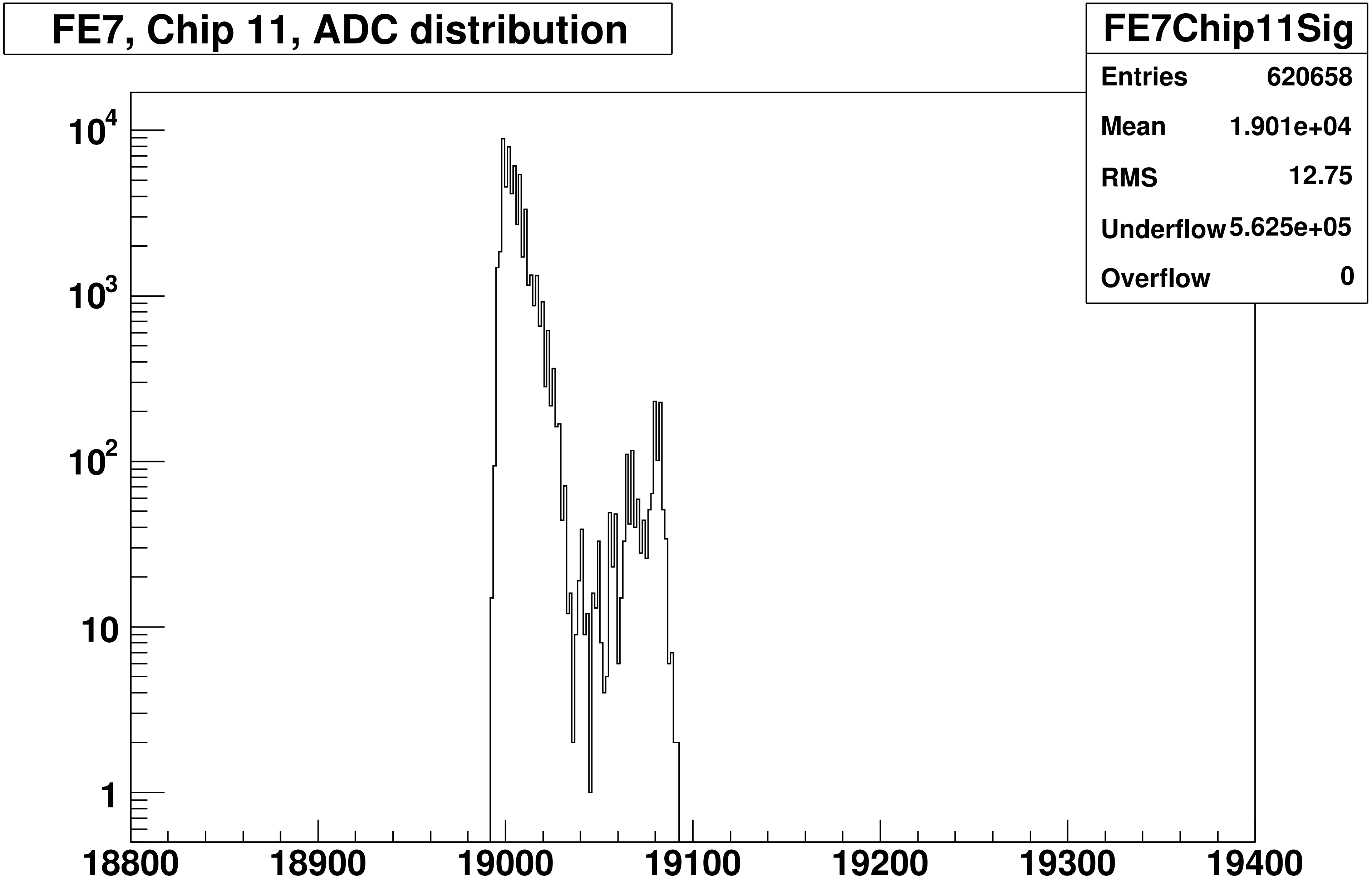


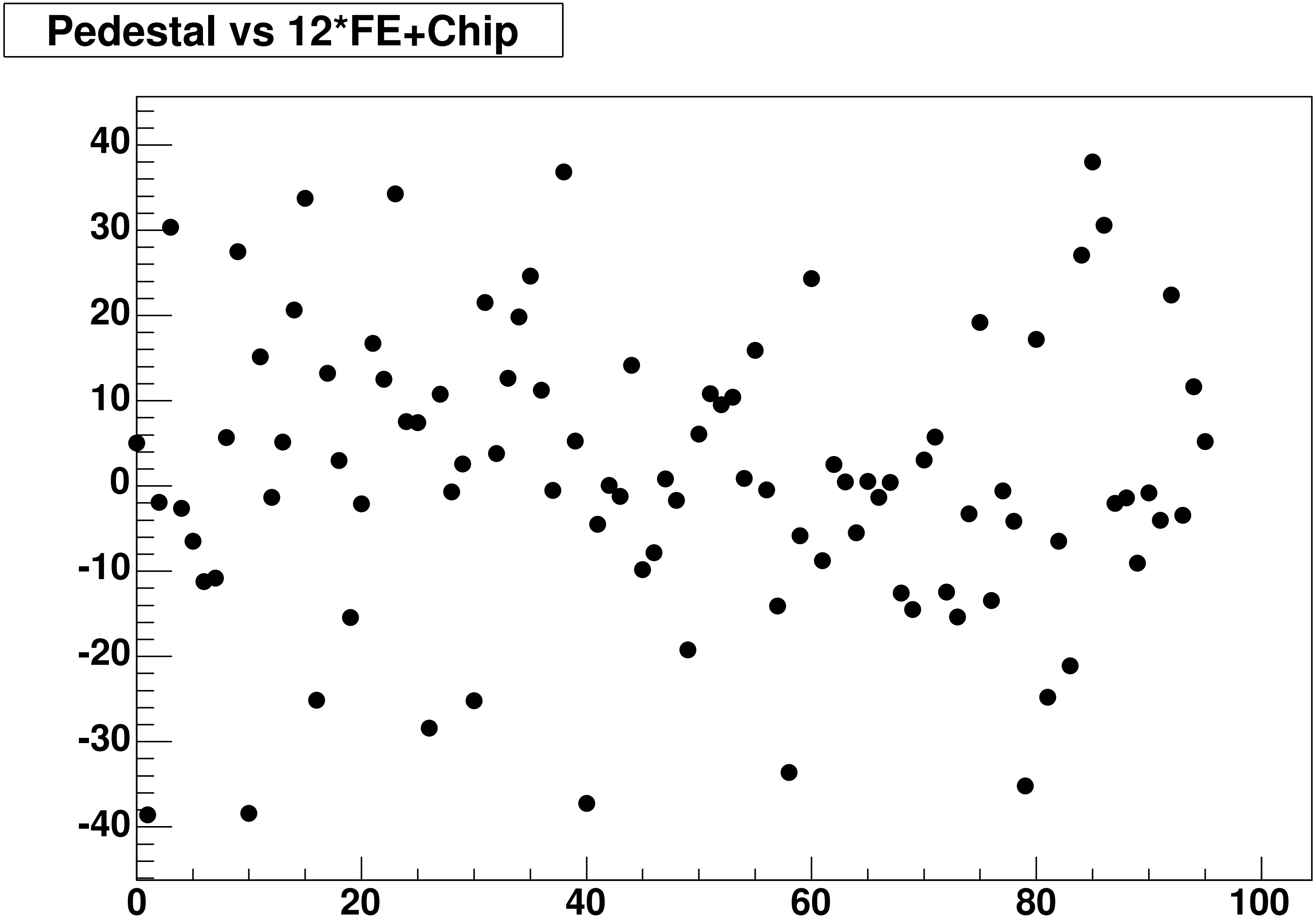
FE7, Chip 10, ADC distribution
FE7Chip10Ped
Entries 620658
Mean 11.63
RMS 1.345
Underflow 0
Overflow 5.814e+04
10^5
10^4
10^3
10^2
10
-100 -80 -60 -40 -20 0 20 40 60 80 100

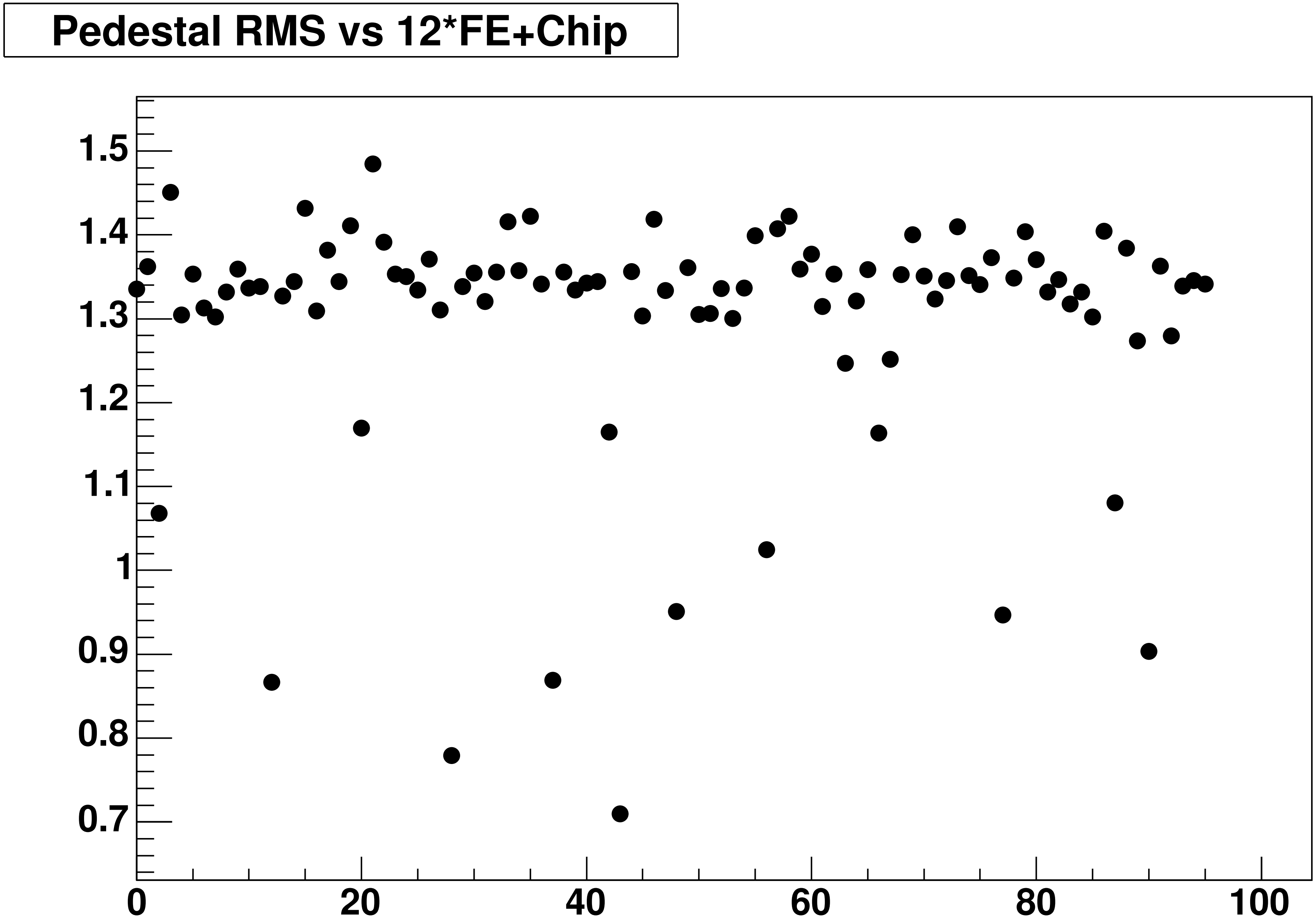

FE7, Chip 10, ADC distribution
FE7Chip10Sig
Entries 620658
Mean 1.906e+04
RMS 13.3
Underflow 5.625e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


FE7, Chip 11, ADC distribution
FE7Chip11All
Entries 620658
Mean 1785
RMS 5536
Underflow 0
Overflow 0
10^6
10^5
10^4
10^3
10^2
10
1
-5000
0
5000
10000
15000
20000
25000

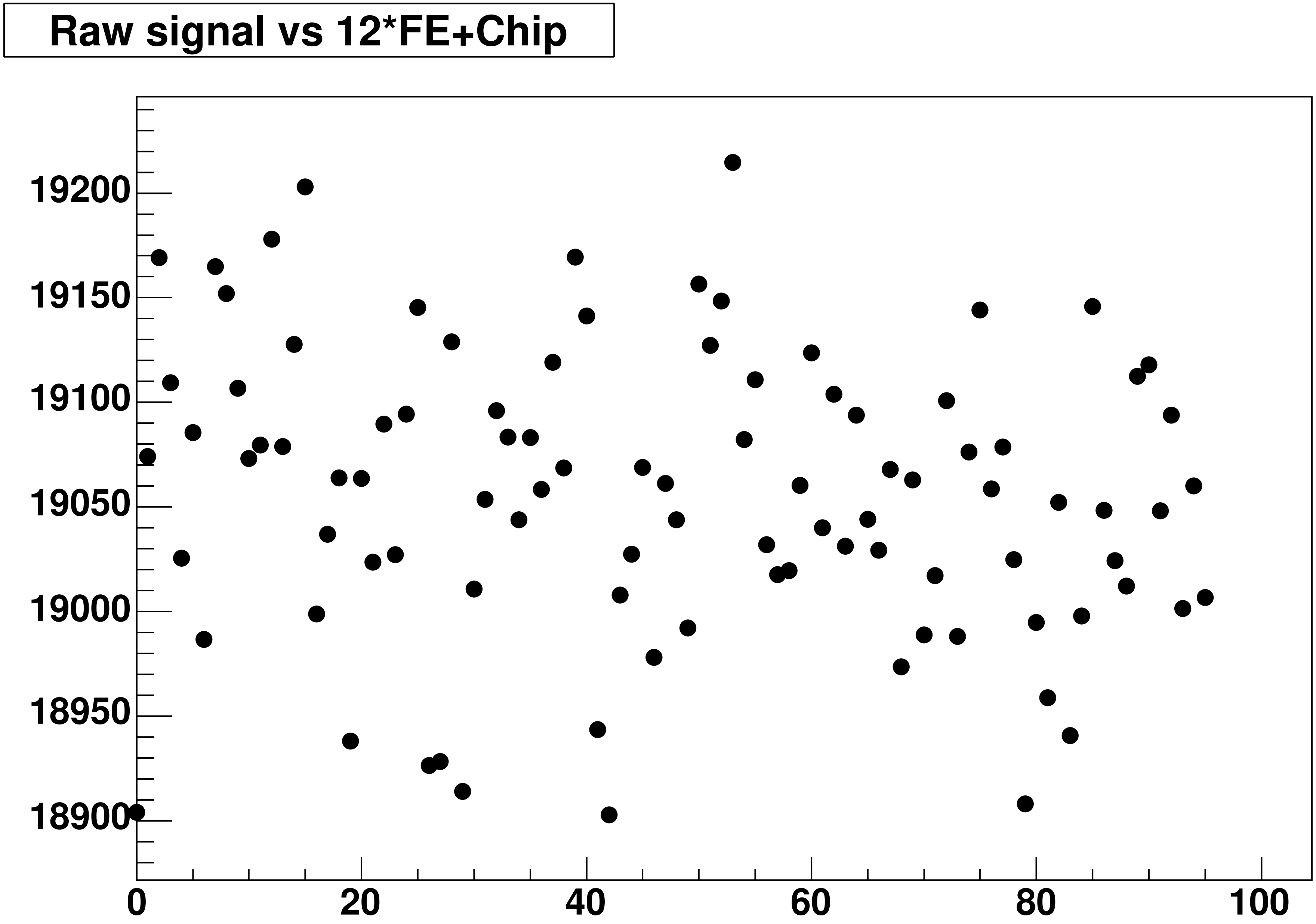

FE7, Chip 11, ADC distribution
FE7Chip11Ped
Entries 620658
Mean 5.238
RMS 1.341
Underflow 1
Overflow 5.828e+04
10
10^2
10^3
10^4
10^5
-100
-80
-60
-40
-20
0
20
40
60
80
100

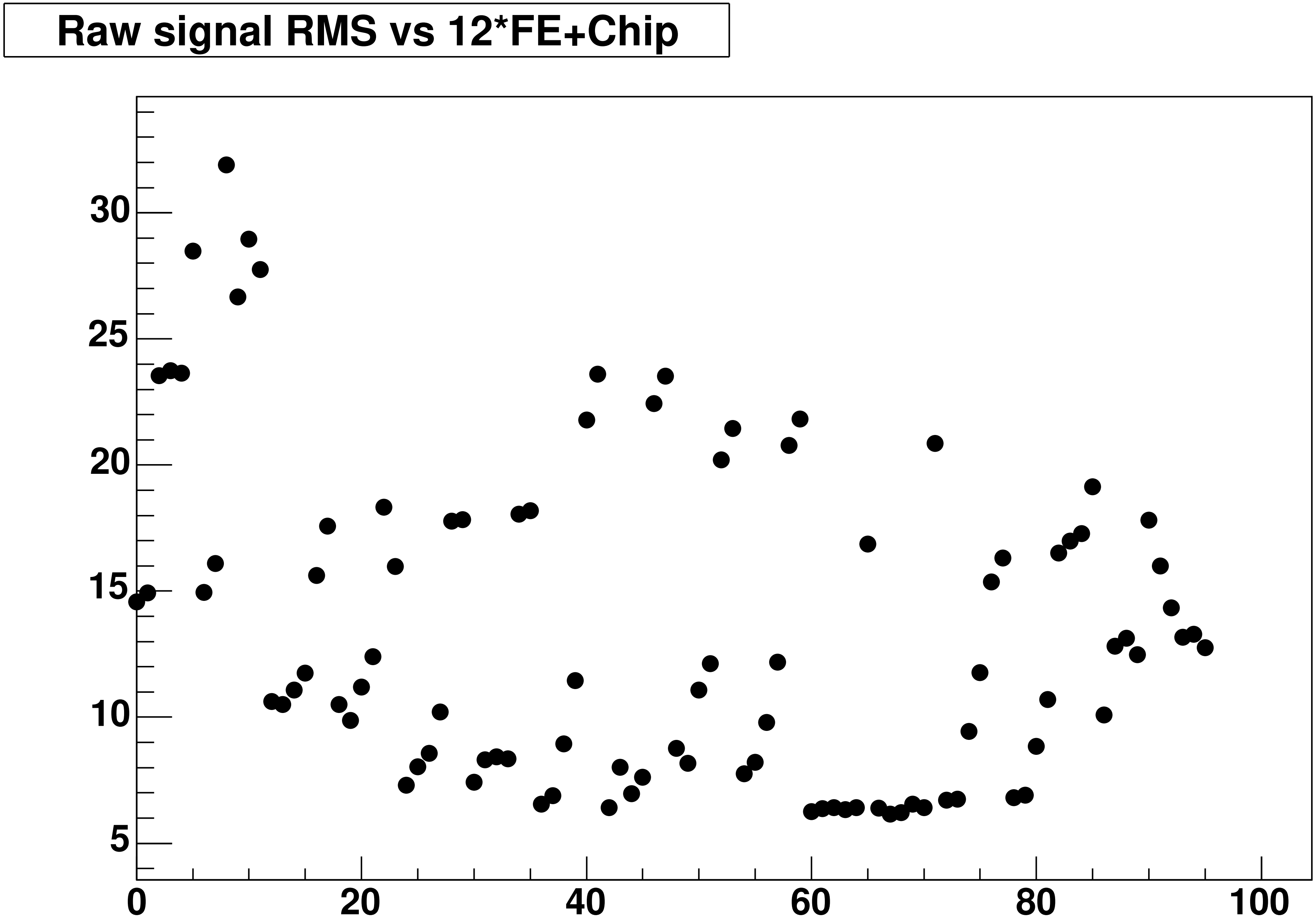

FE7, Chip 11, ADC distribution
FE7Chip11Sig
Entries 620658
Mean 1.901e+04
RMS 12.75
Underflow 5.625e+05
Overflow 0
10^4
10^3
10^2
10
1
18800
18900
19000
19100
19200
19300
19400


Pedestal vs 12*FE+Chip
40
30
20
10
0
-10
-20
-30
-40
0
20
40
60
80
100

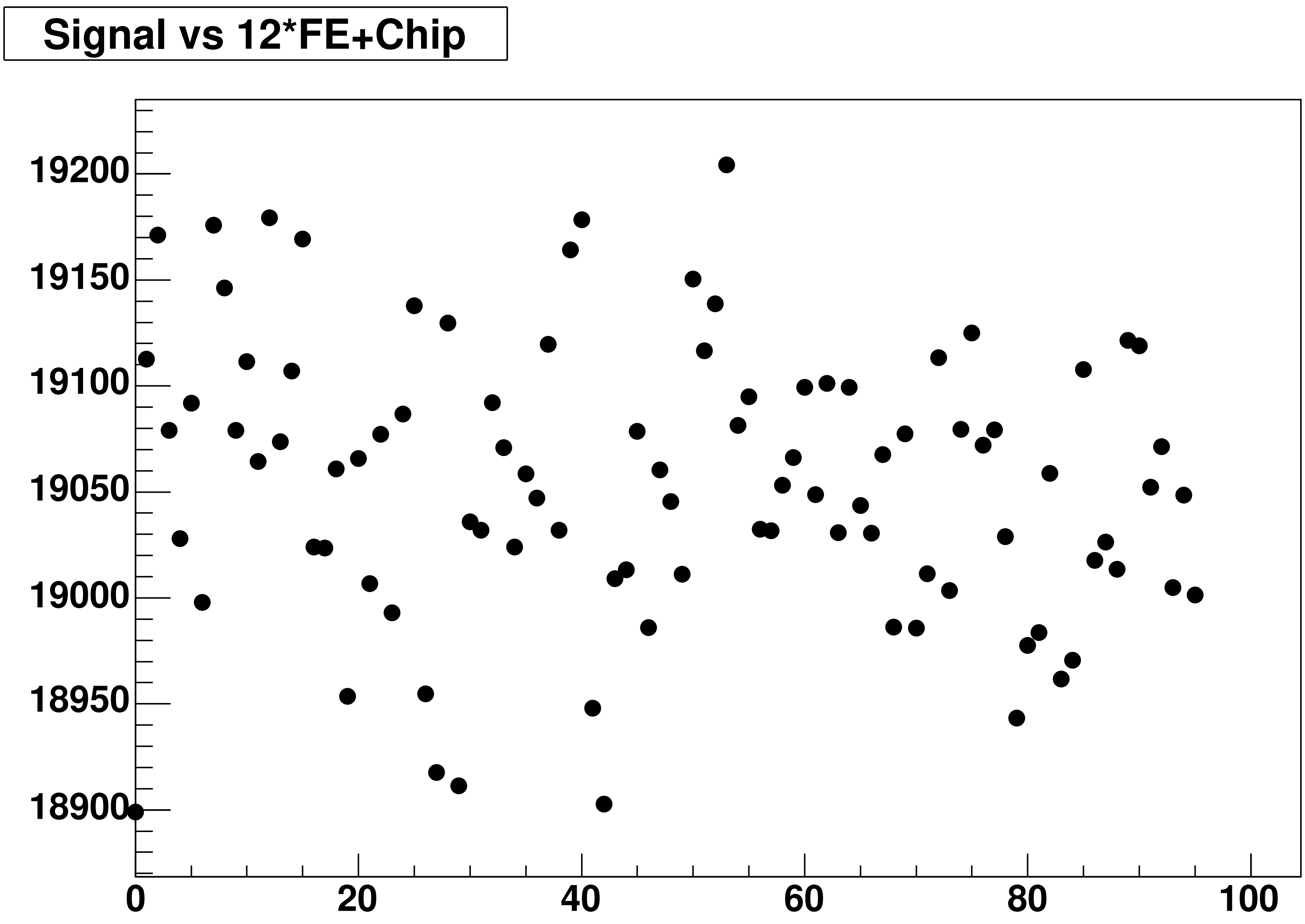


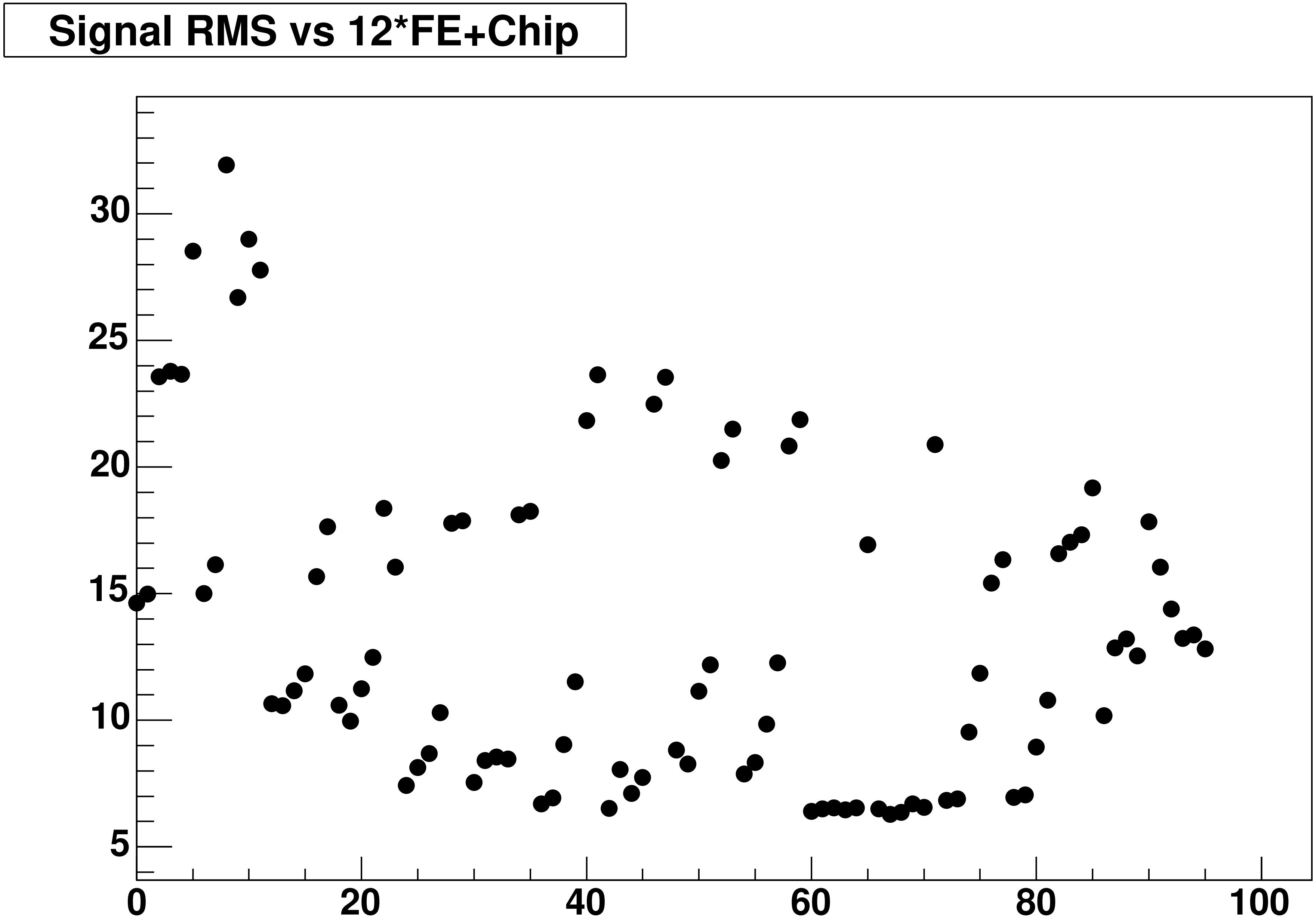
Pedestal RMS vs 12*FE+Chip
1.5
1.4
1.3
1.2
1.1
1
0.9
0.8
0.7
0
20
40
60
80
100


Raw signal vs 12*FE+Chip
19200
19150
19100
19050
19000
18950
18900
0
20
40
60
80
100



Raw signal RMS vs 12*FE+Chip
30
25
20
15
10
5
0
20
40
60
80
100


Signal vs 12*FE+Chip
19200
19150
19100
19050
19000
18950
18900
0
20
40
60
80
100


Signal RMS vs 12*FE+Chip
5
10
15
20
25
30
0
20
40
60
80
100