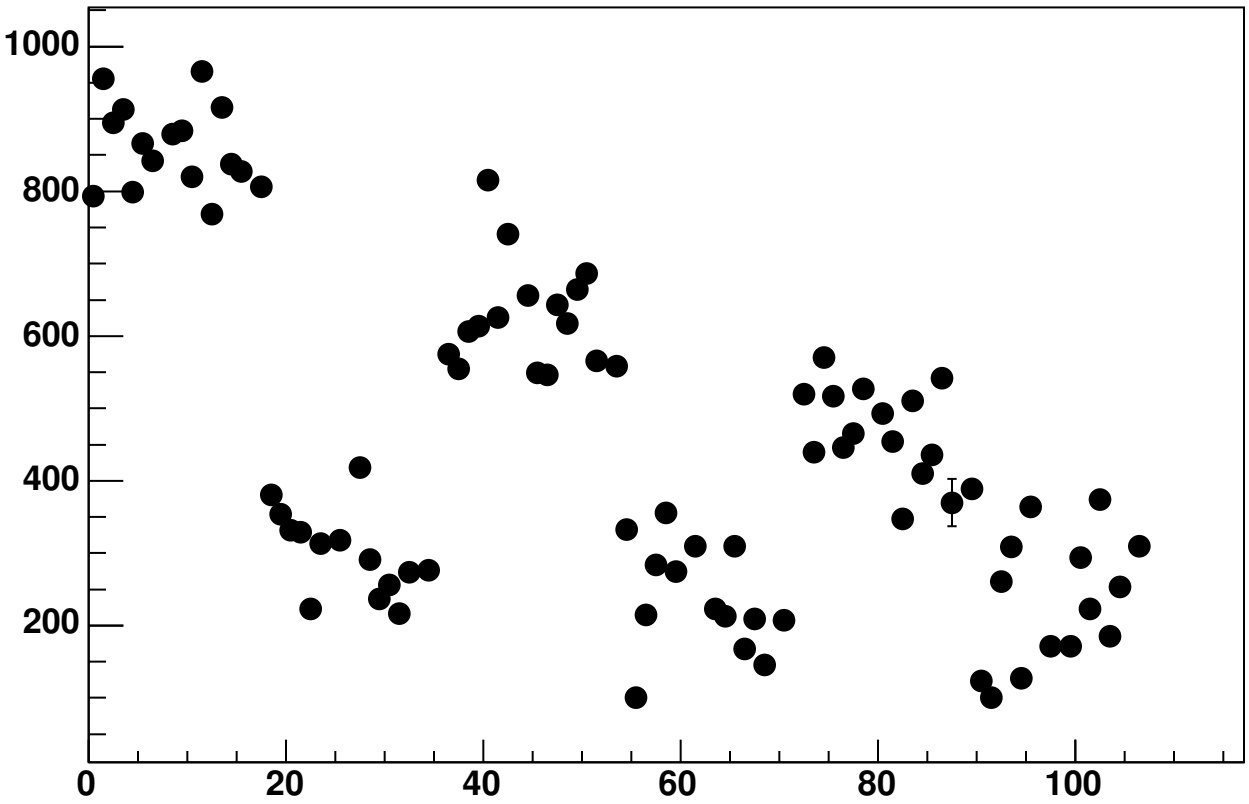
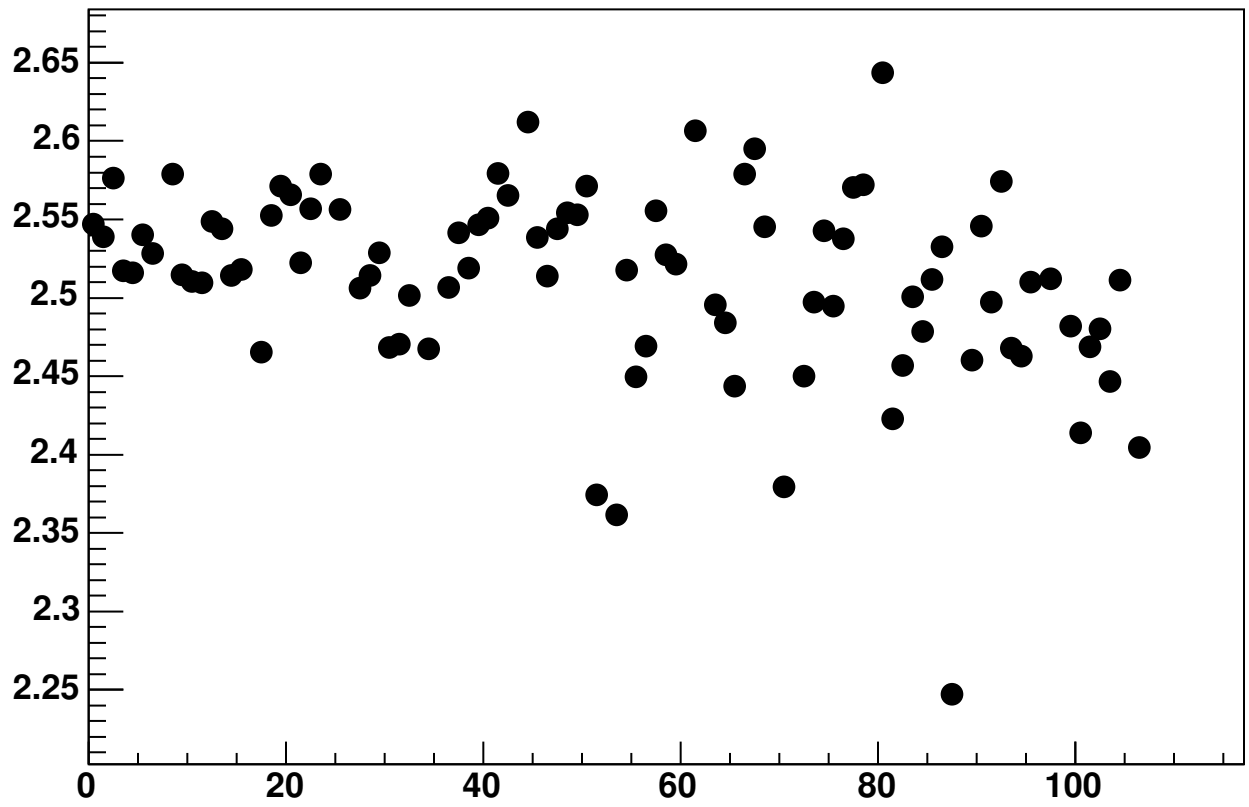
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



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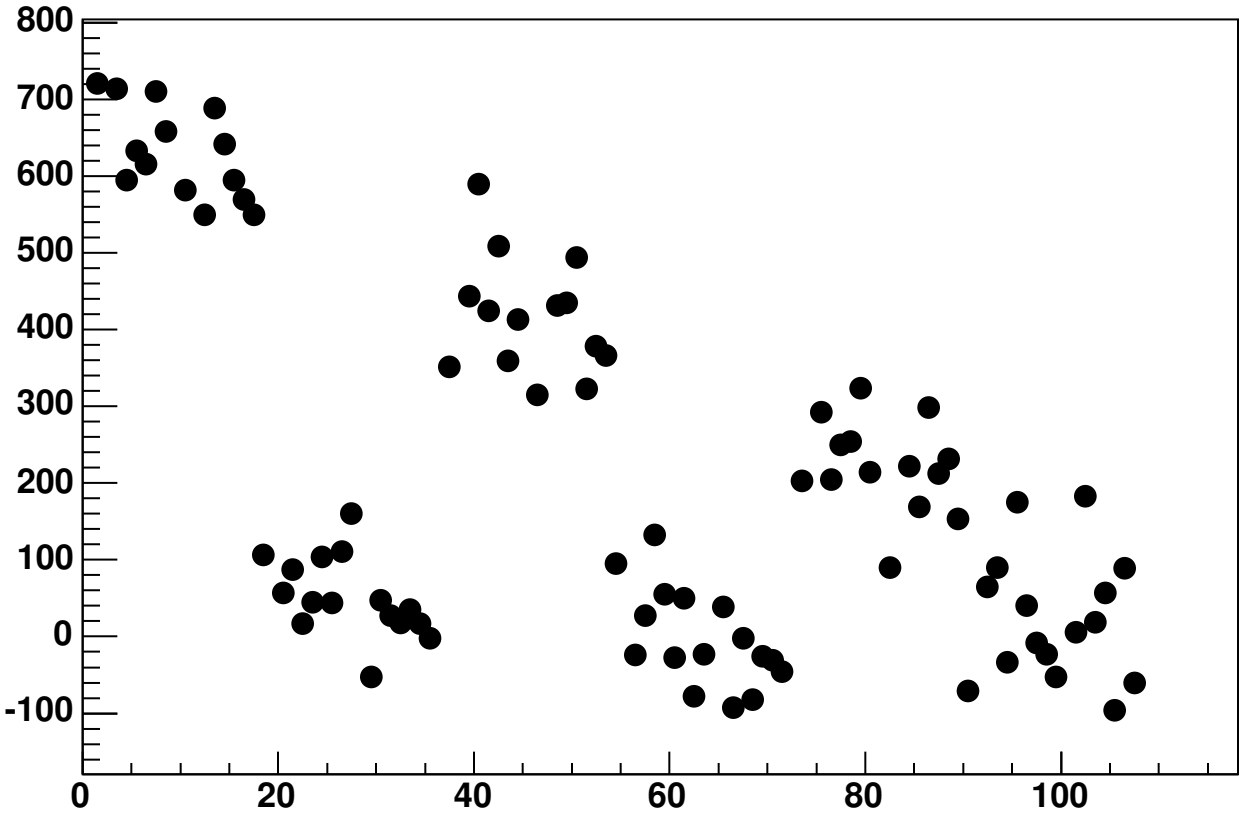
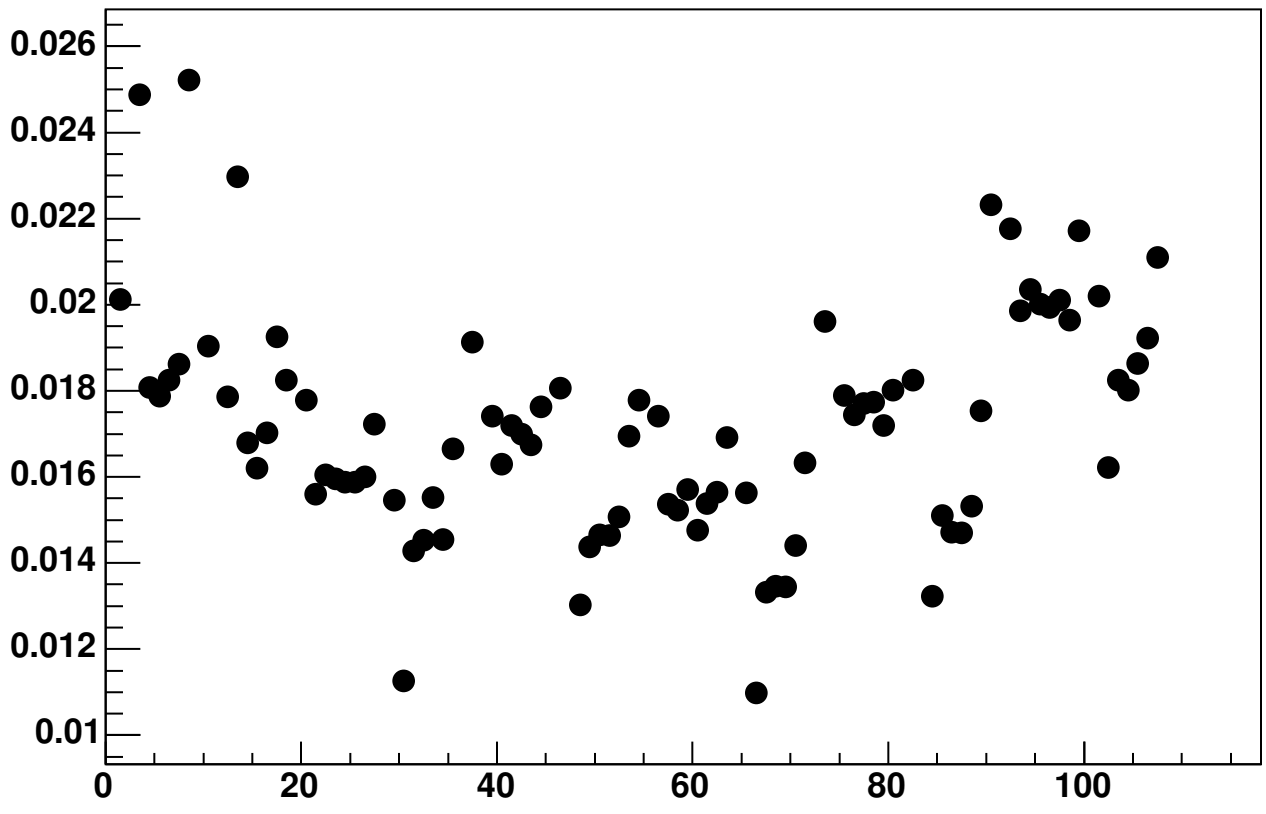
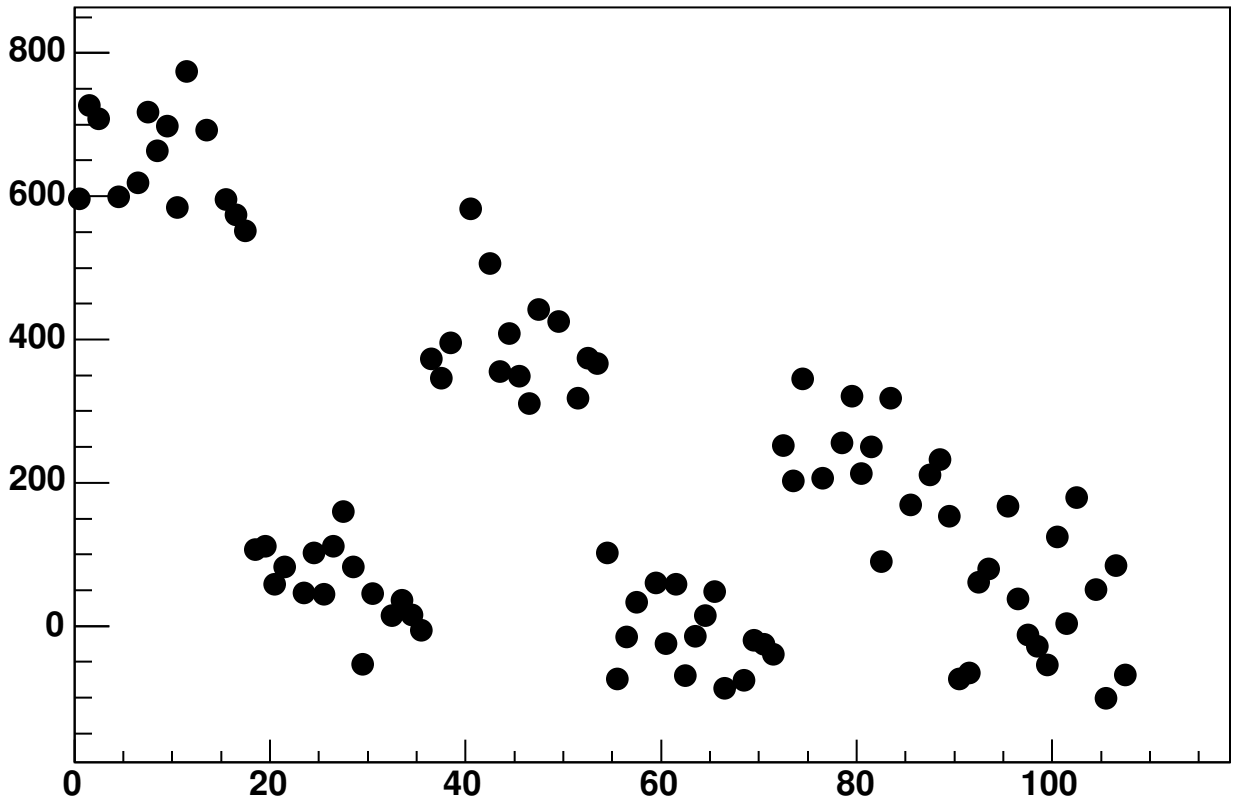


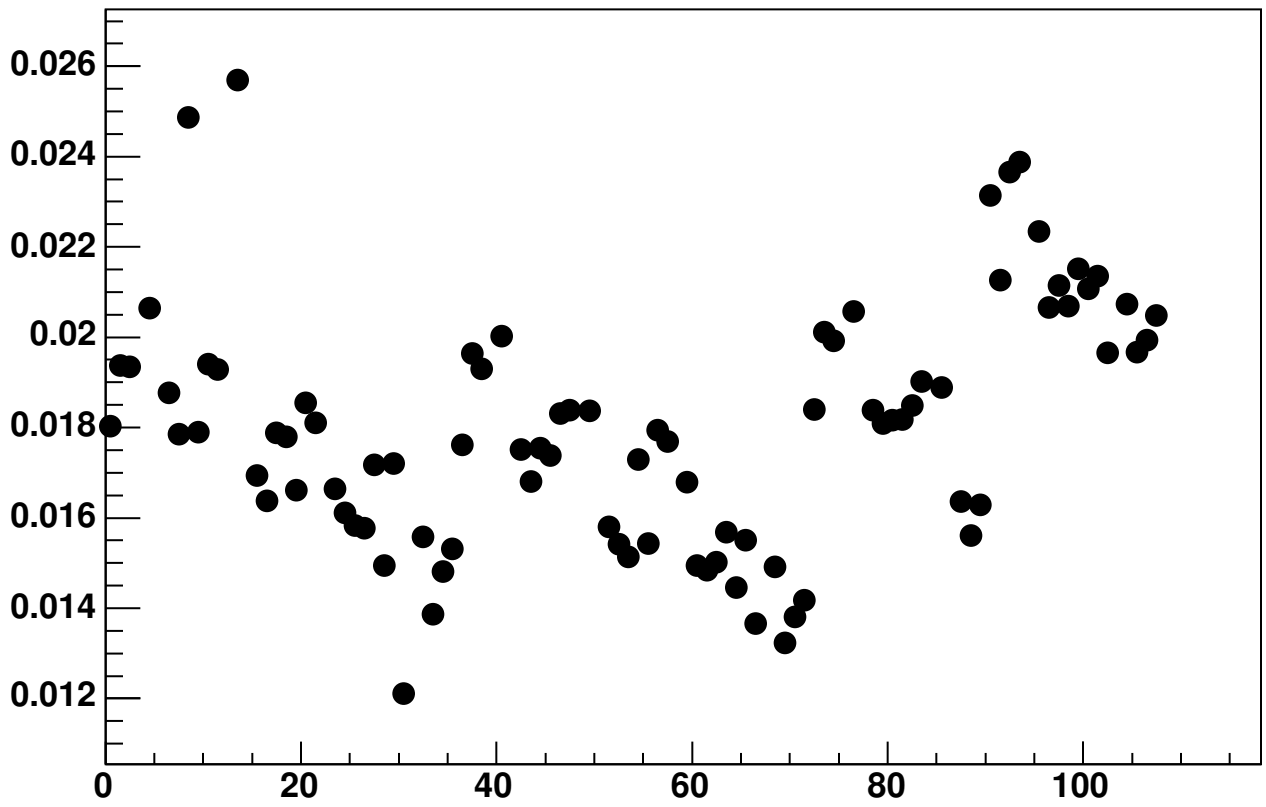
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



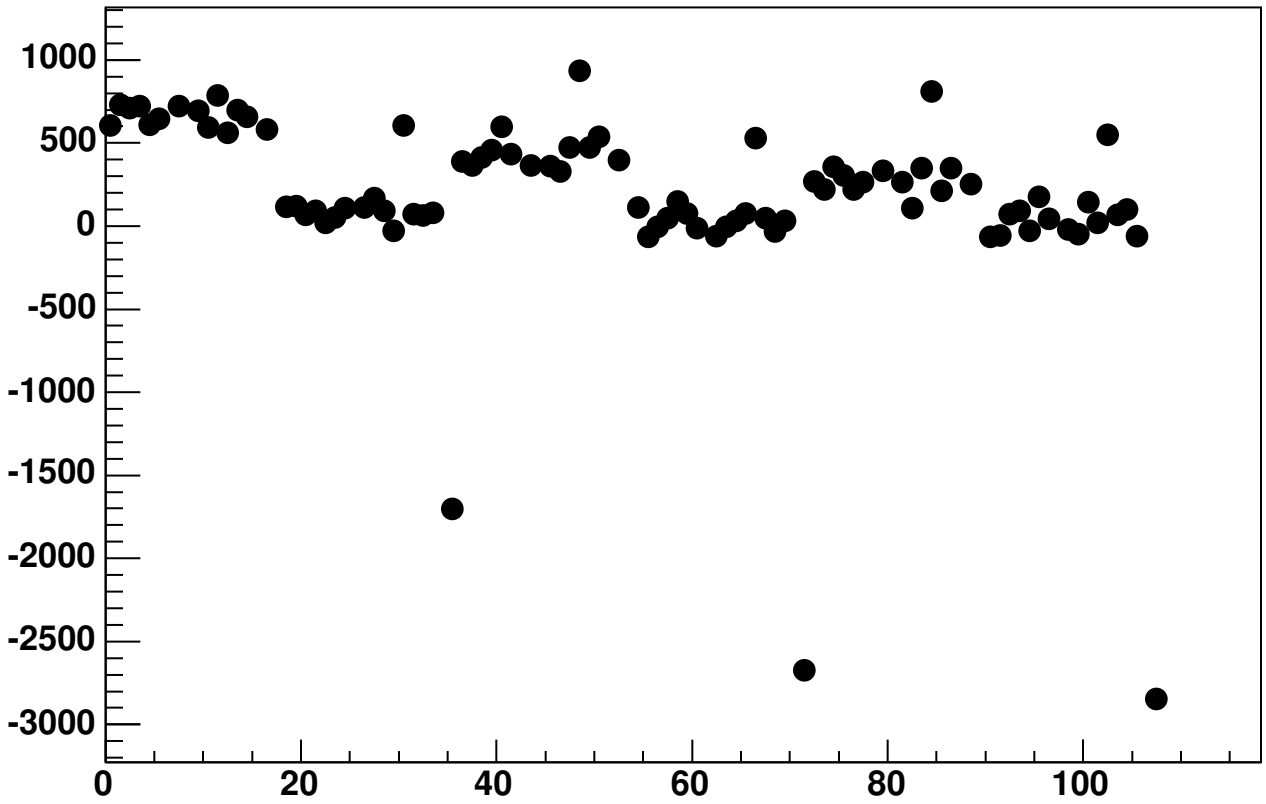
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



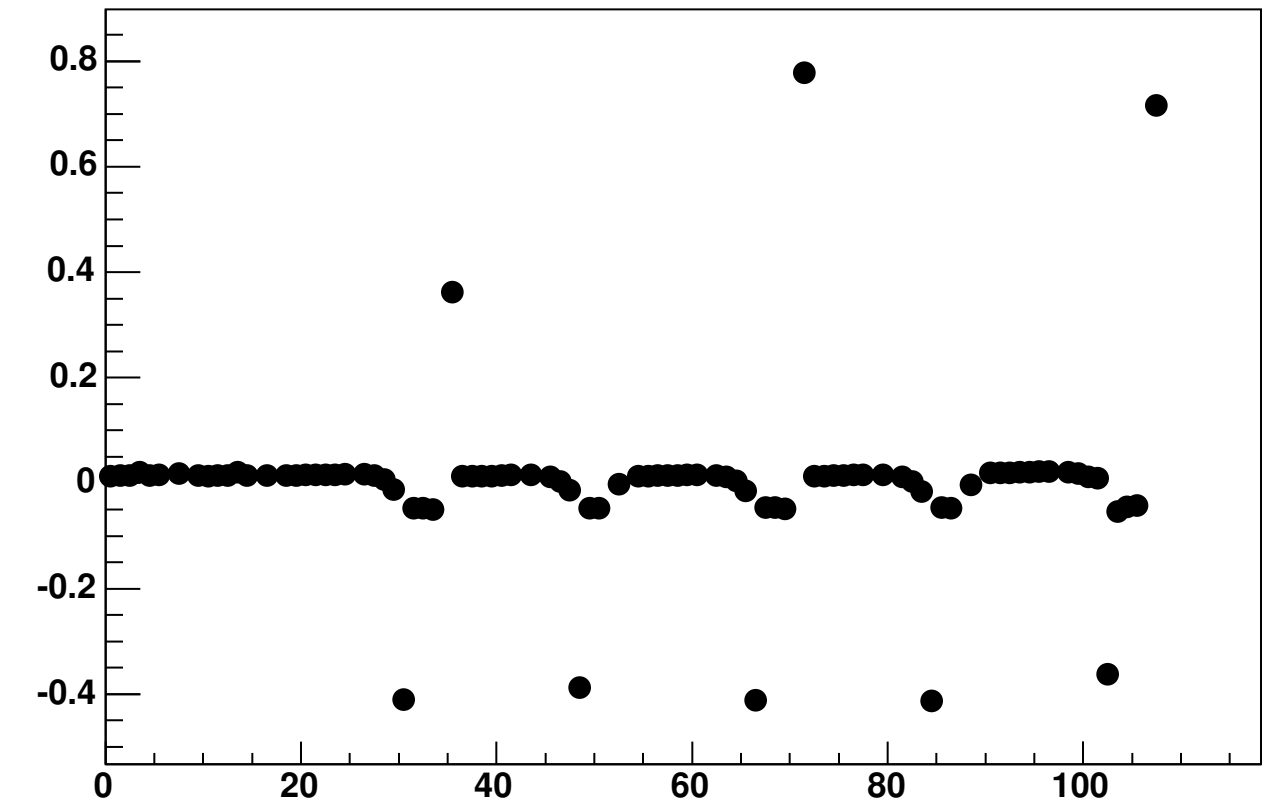
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



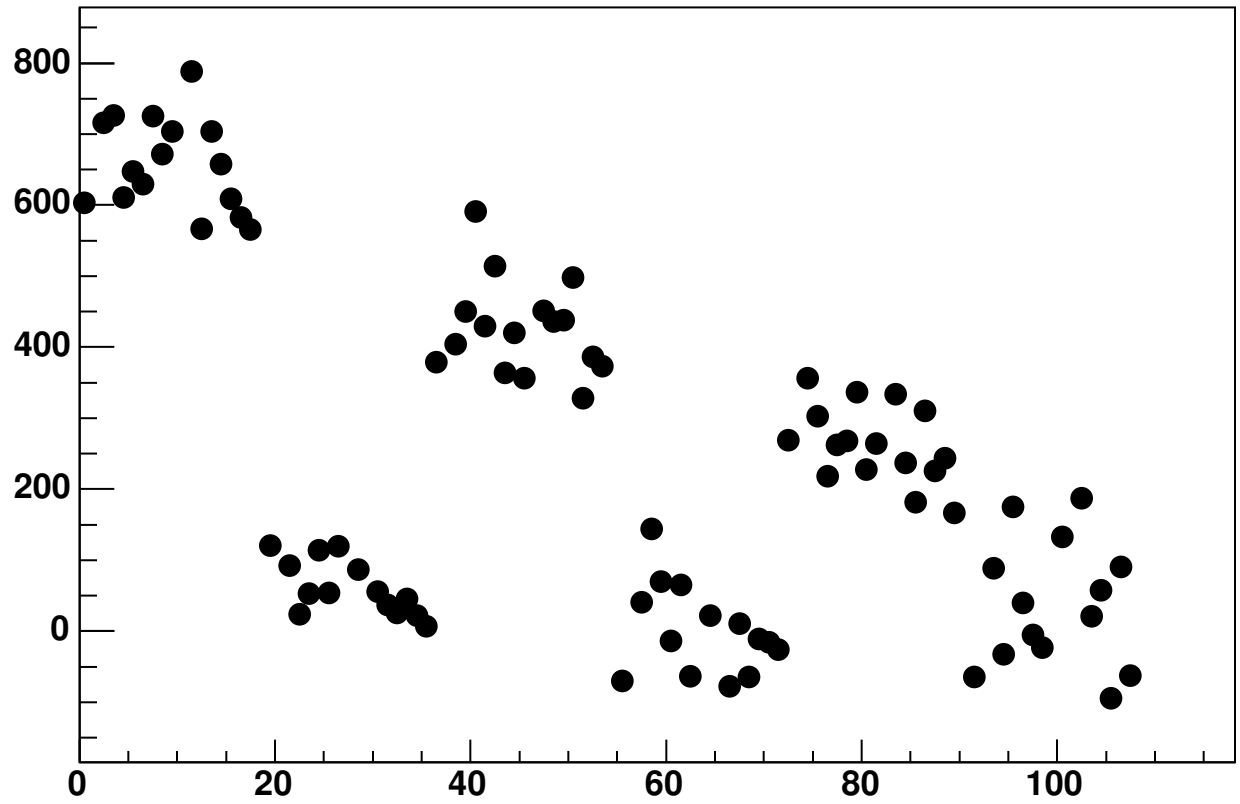
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



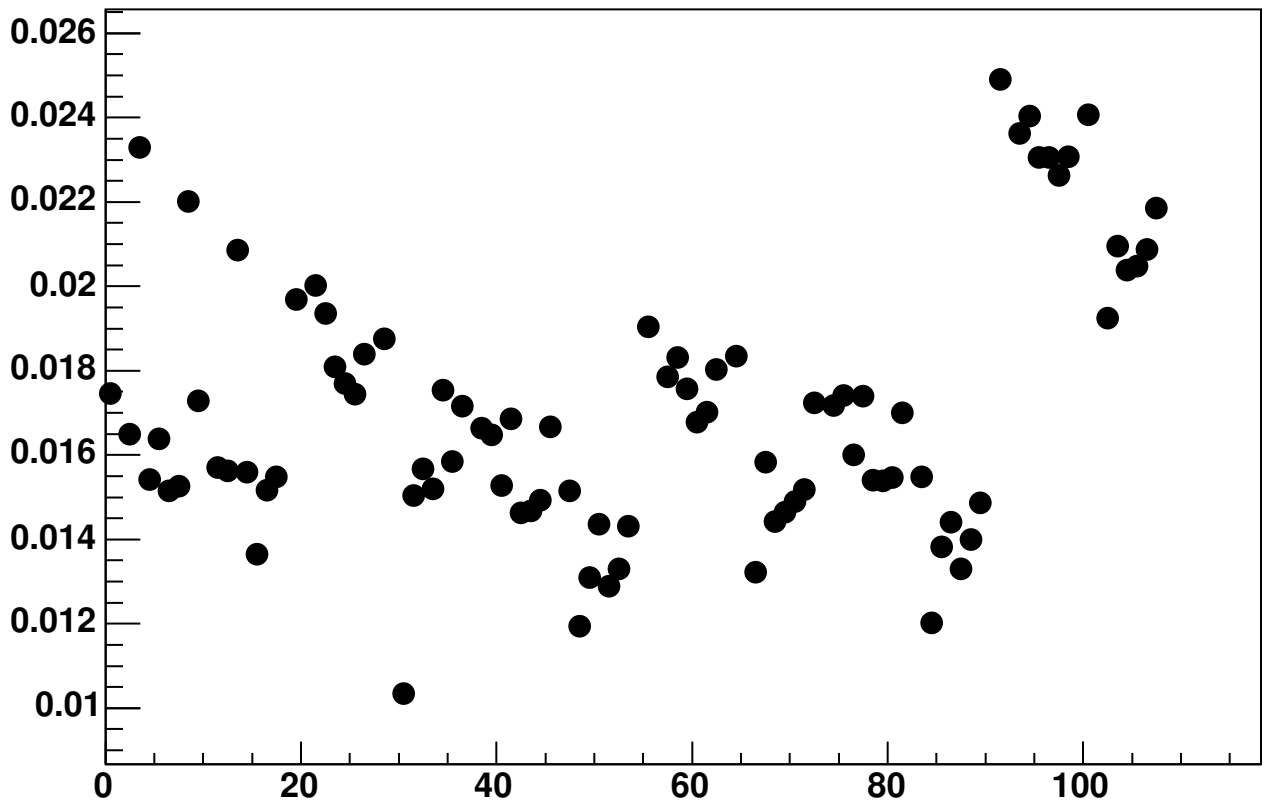
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



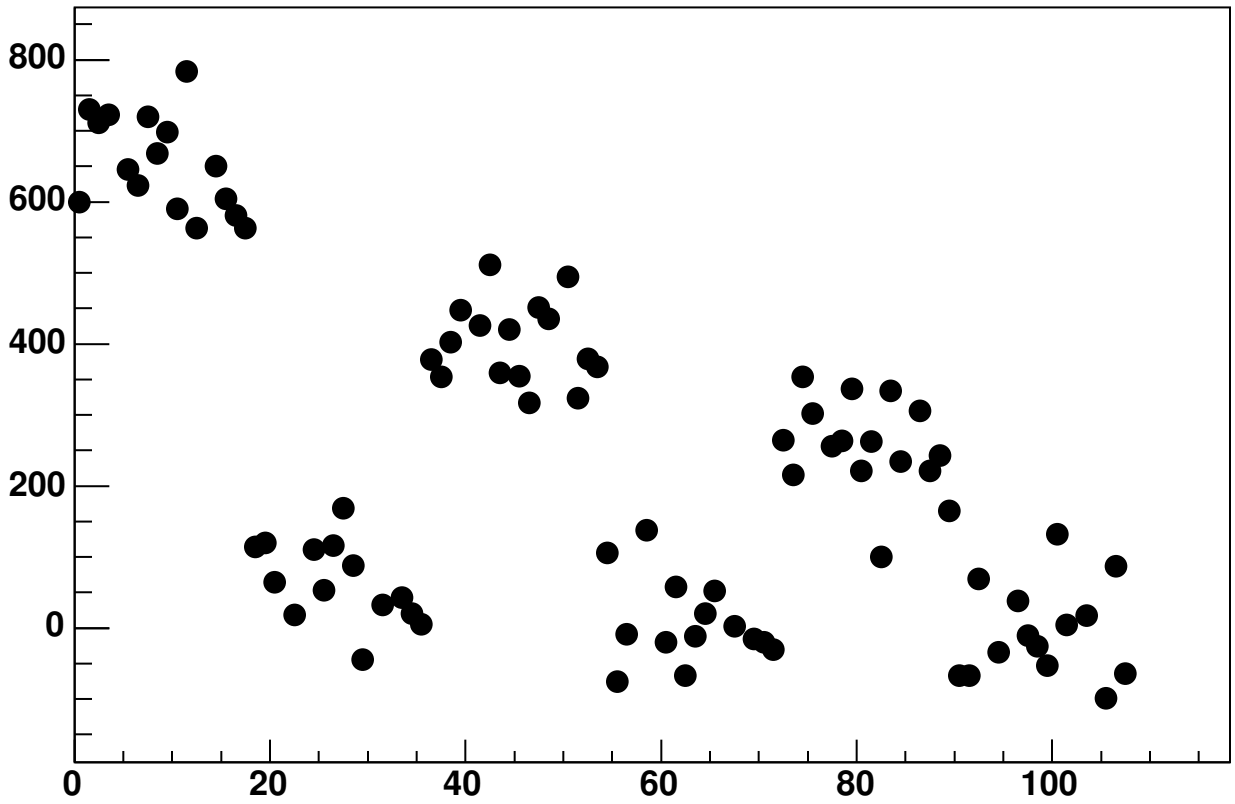
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



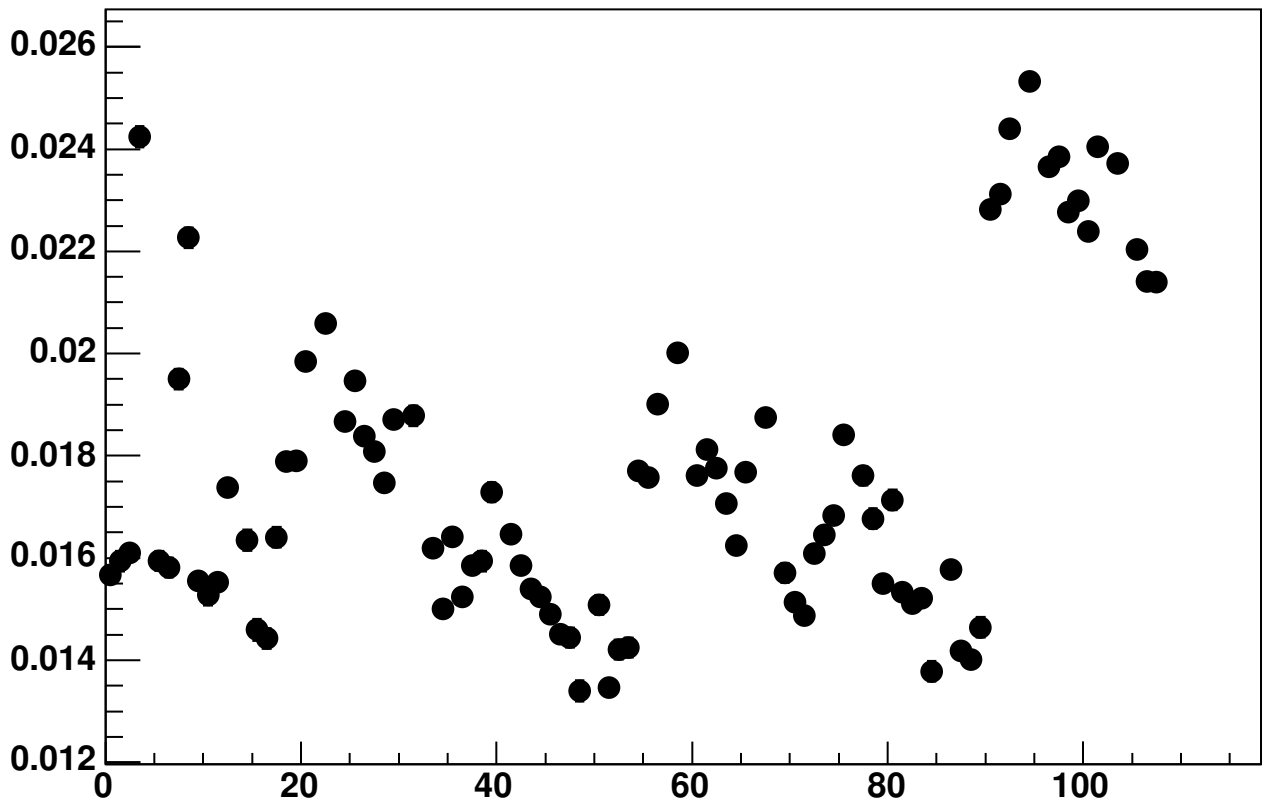
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



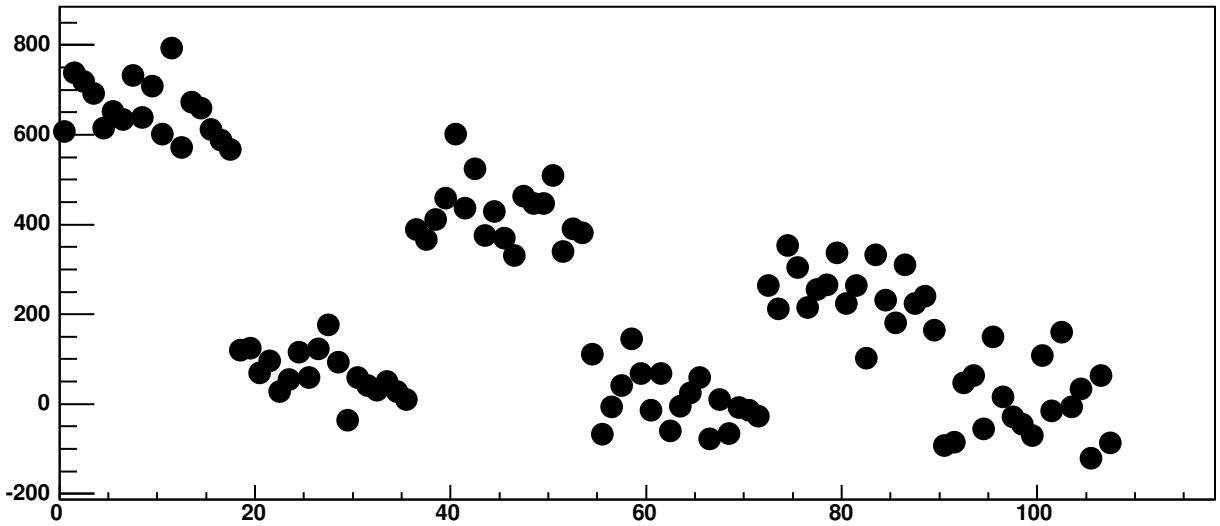
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



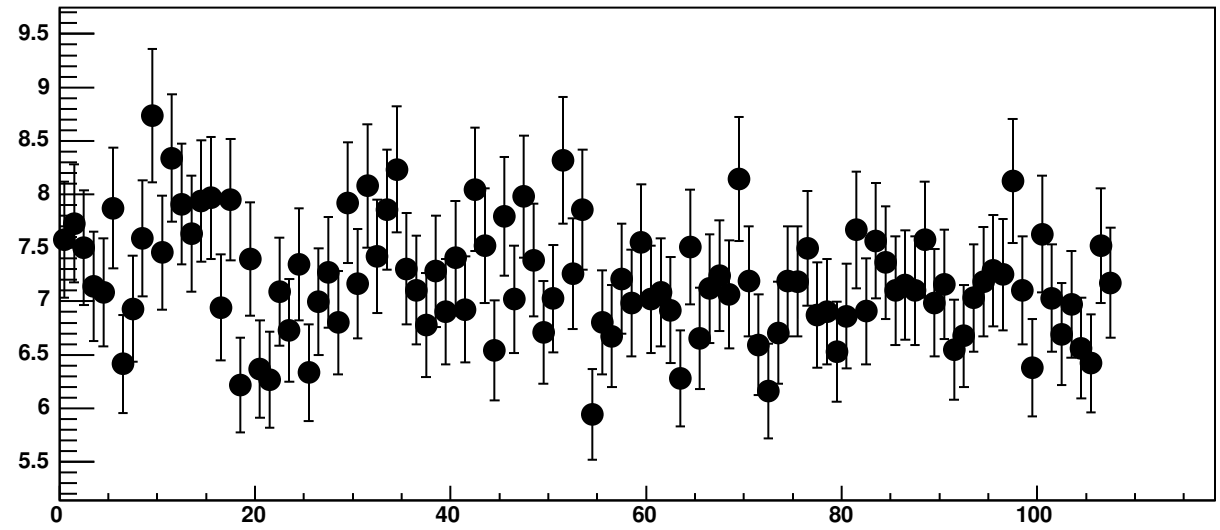
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



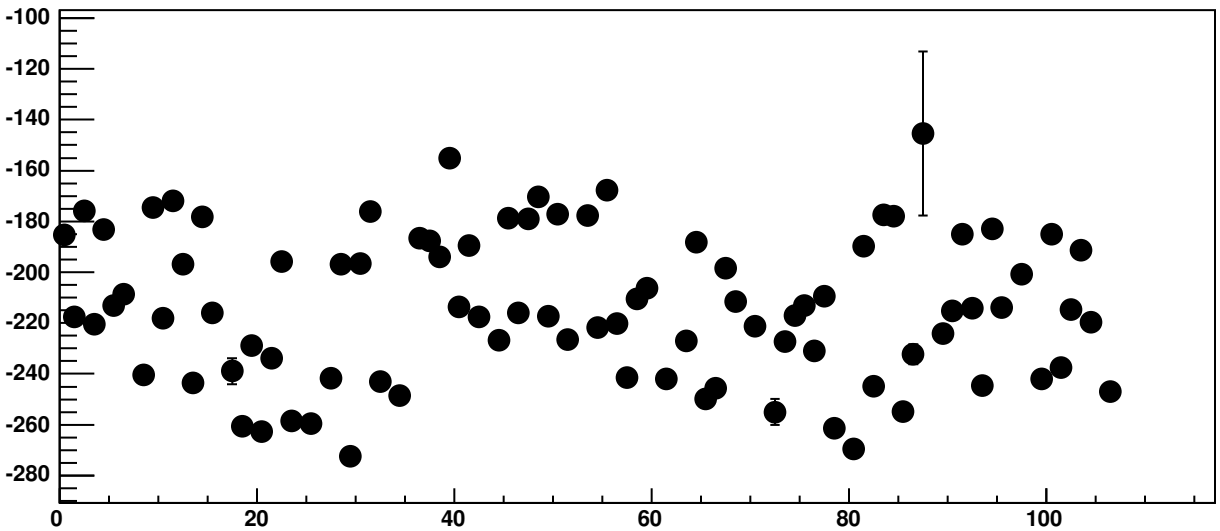
Enables off, ADC Mean vs 18*Chip+Chan



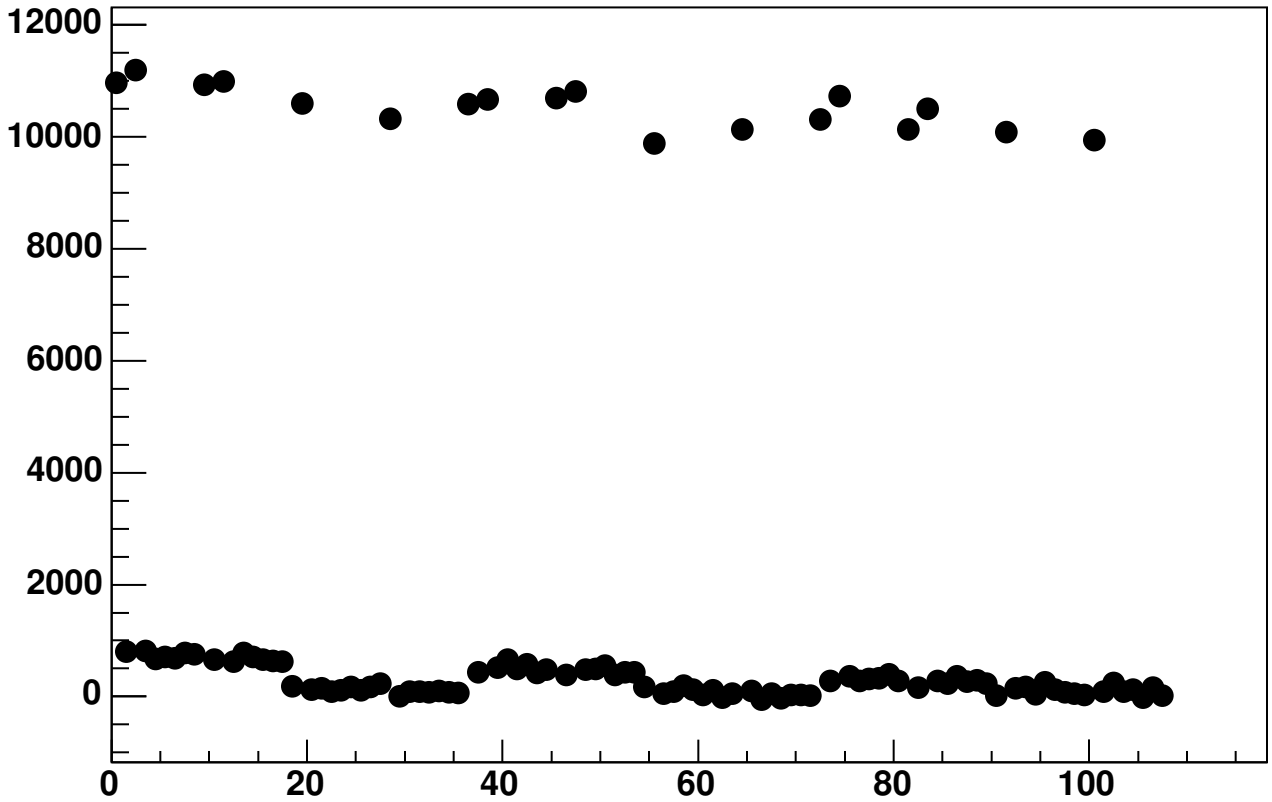
Enables off, ADC Noise vs 18*Chip+Chan



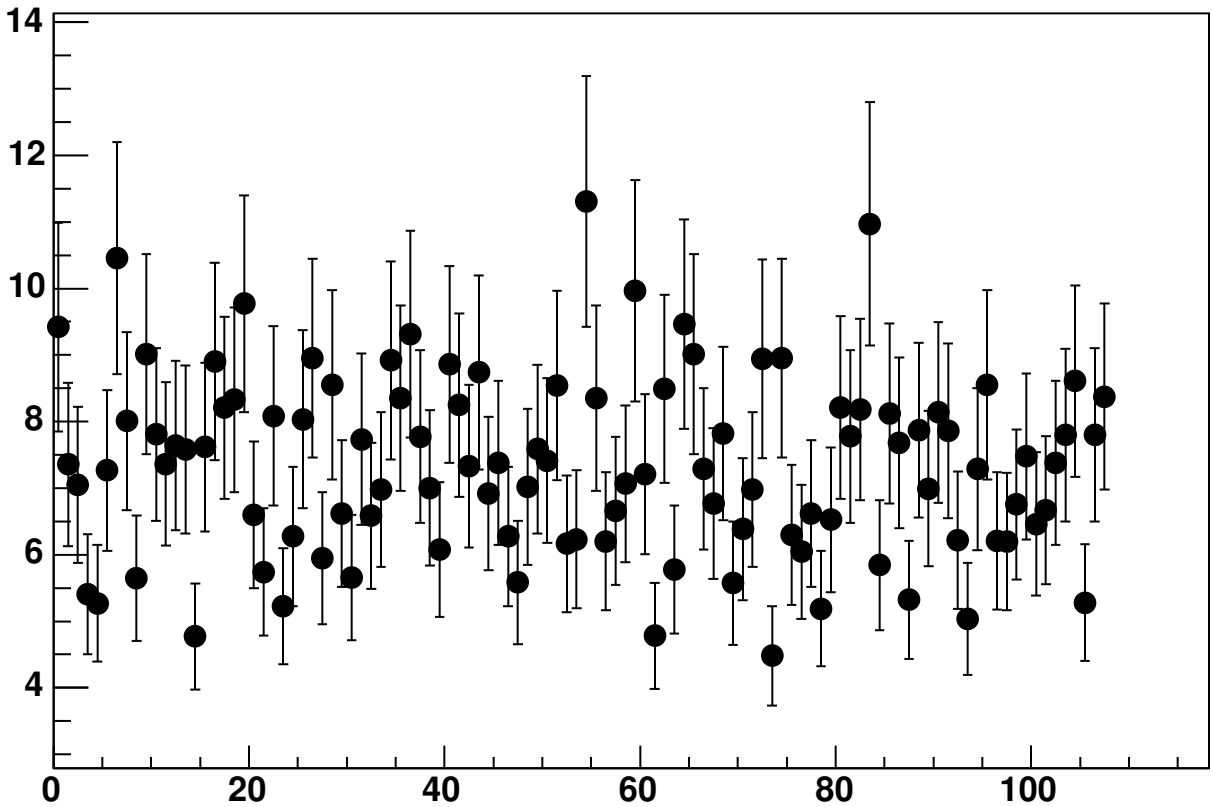
Enables off, ADC Residuals vs 18*Chip+Chan



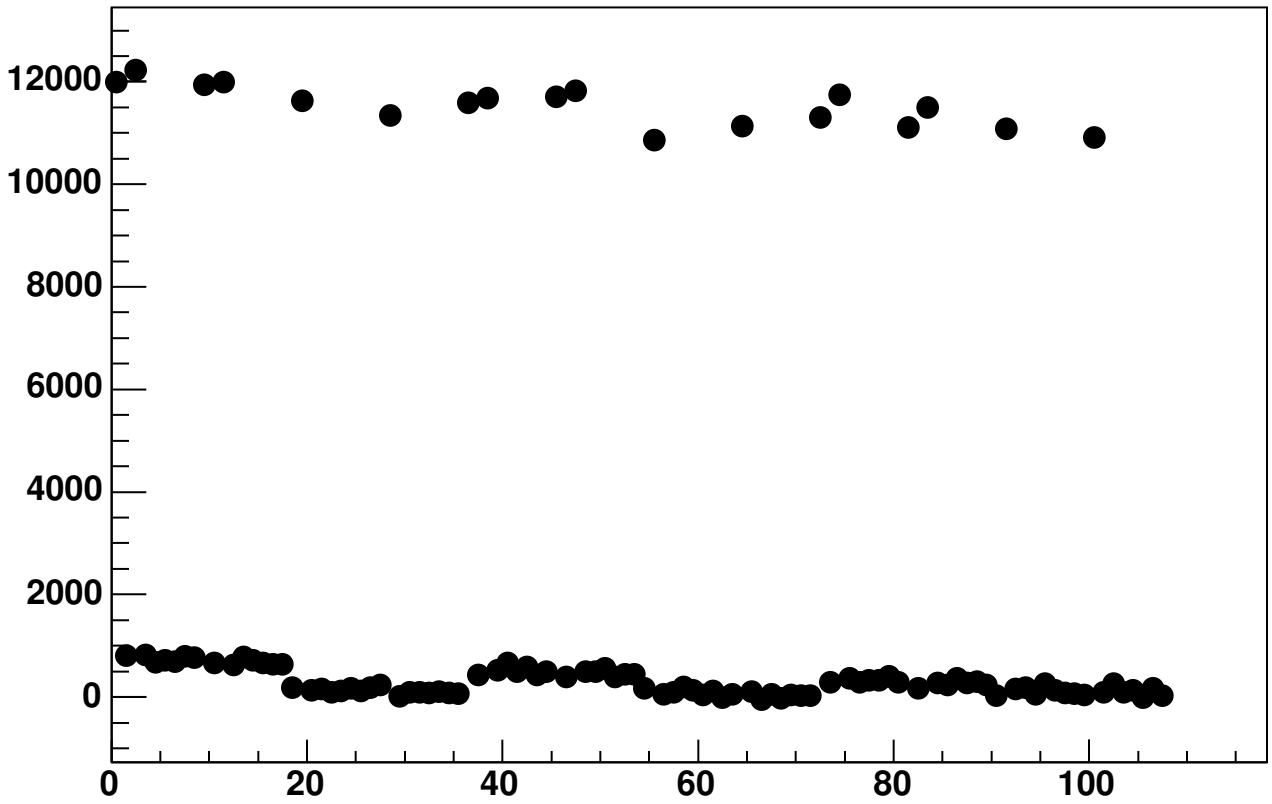
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



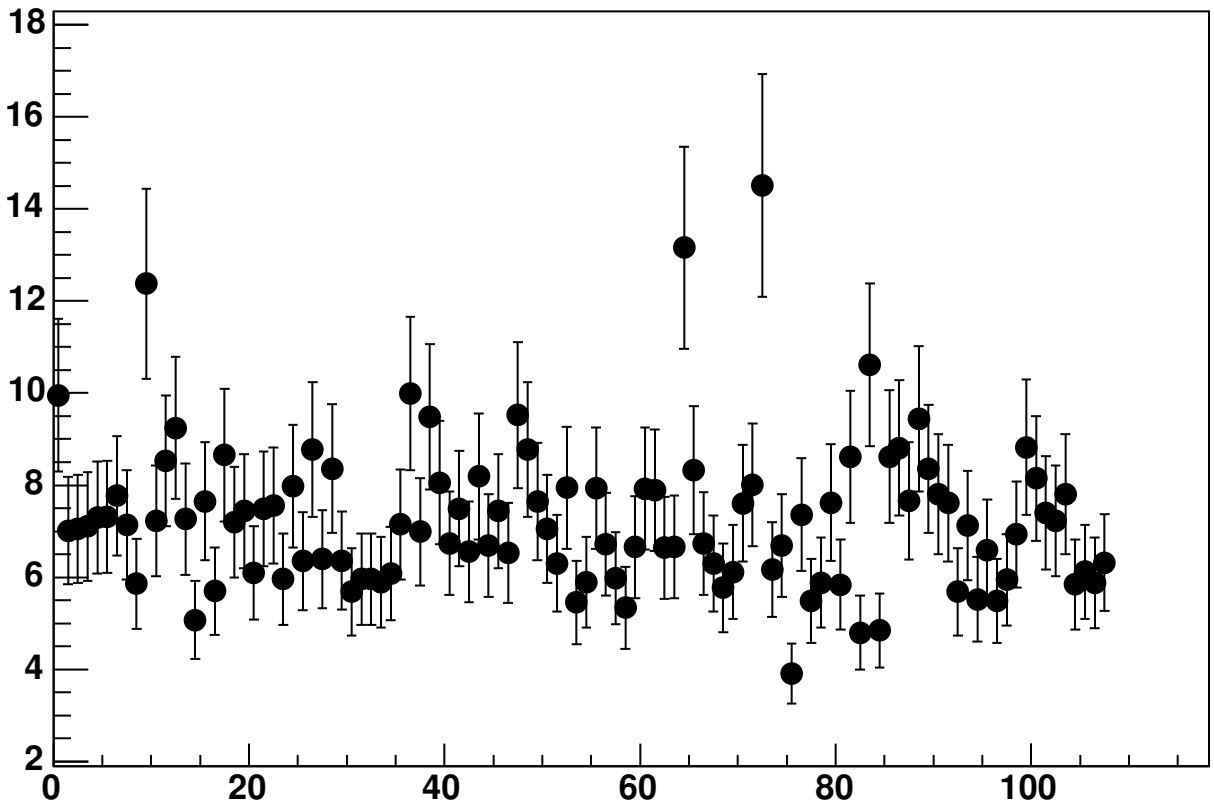
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



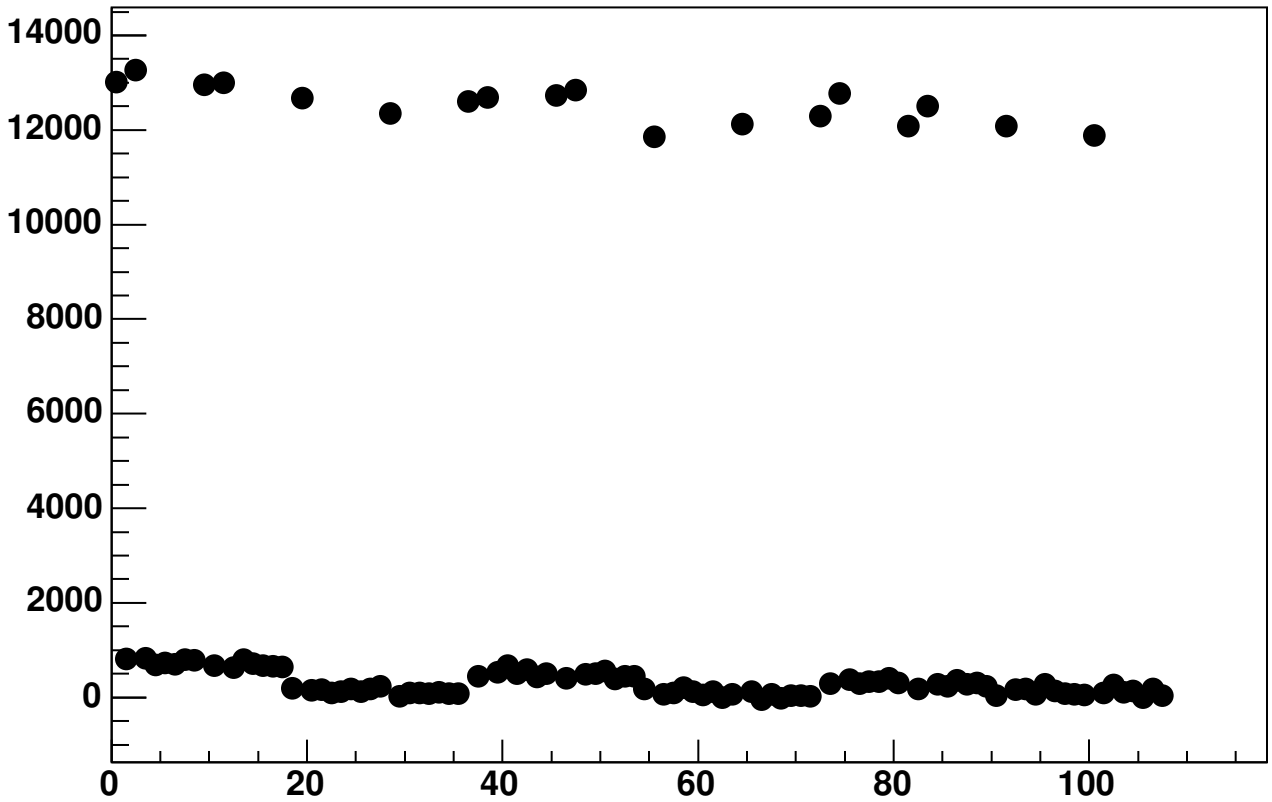
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



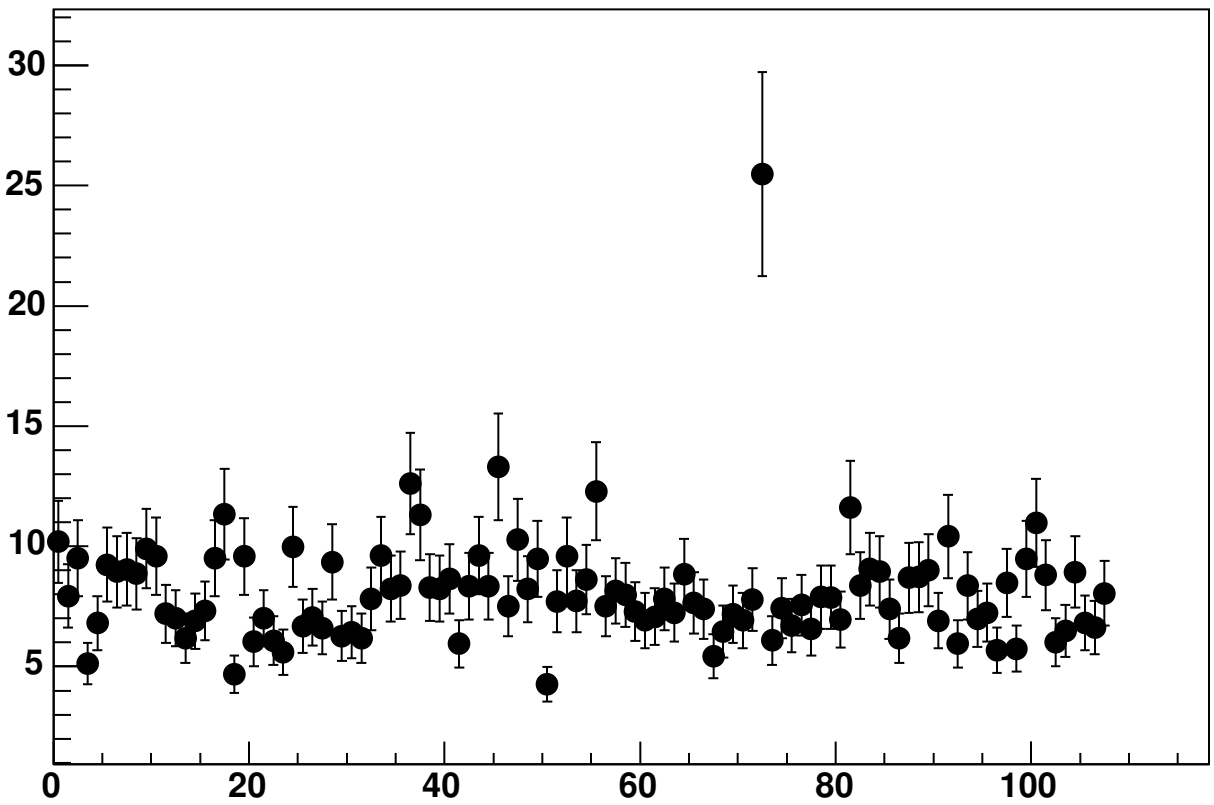
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



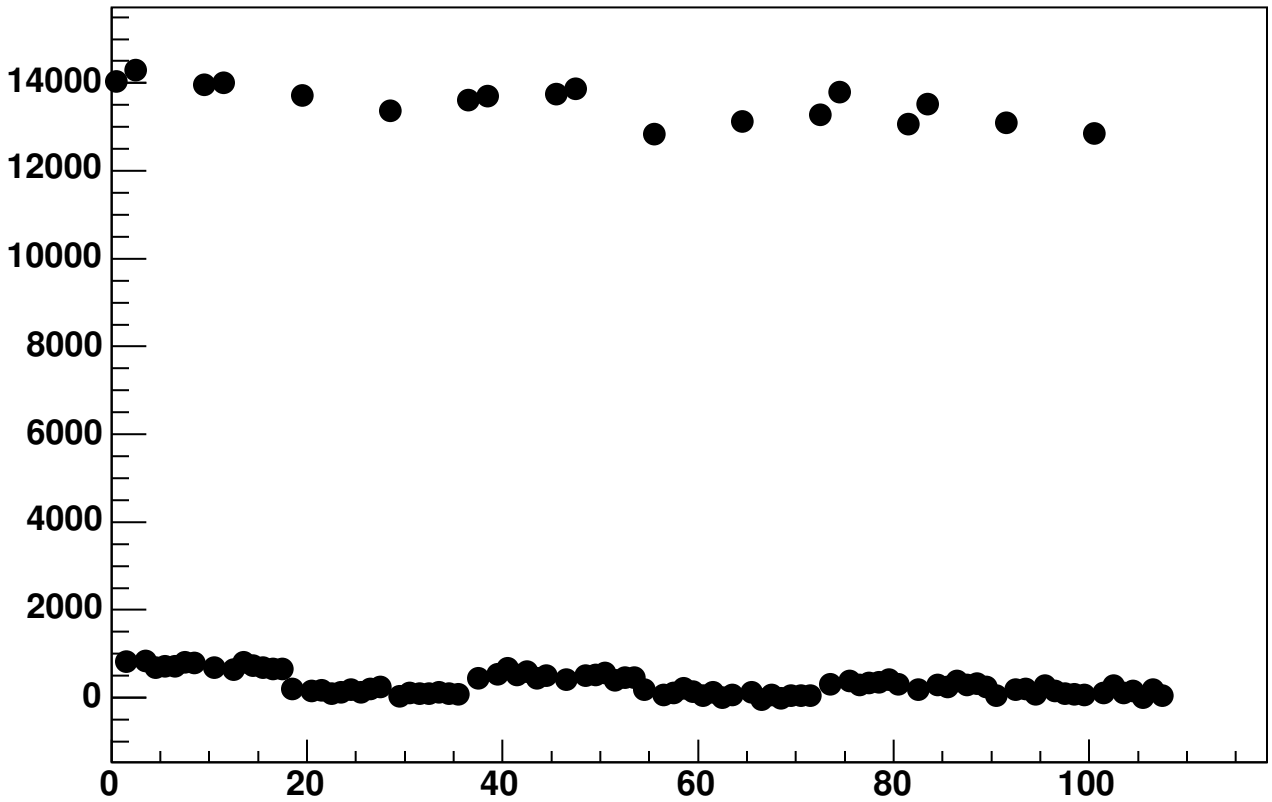
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



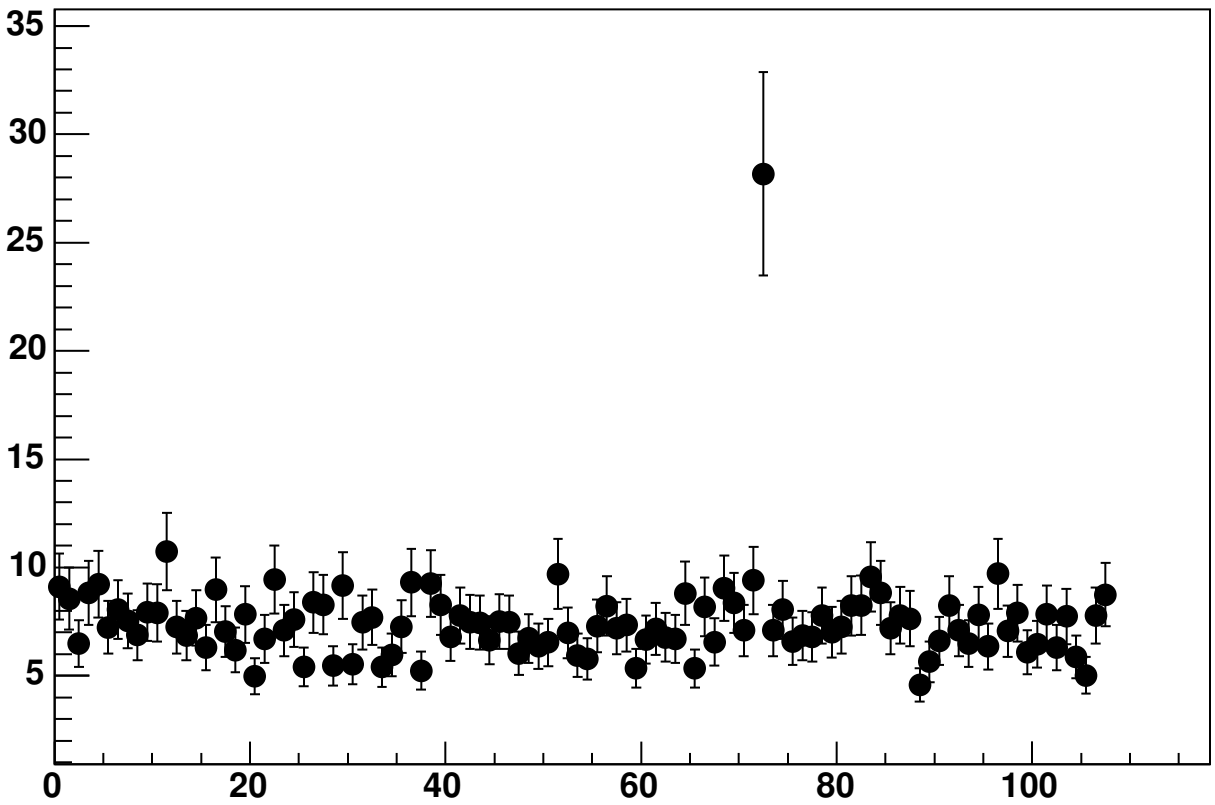
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



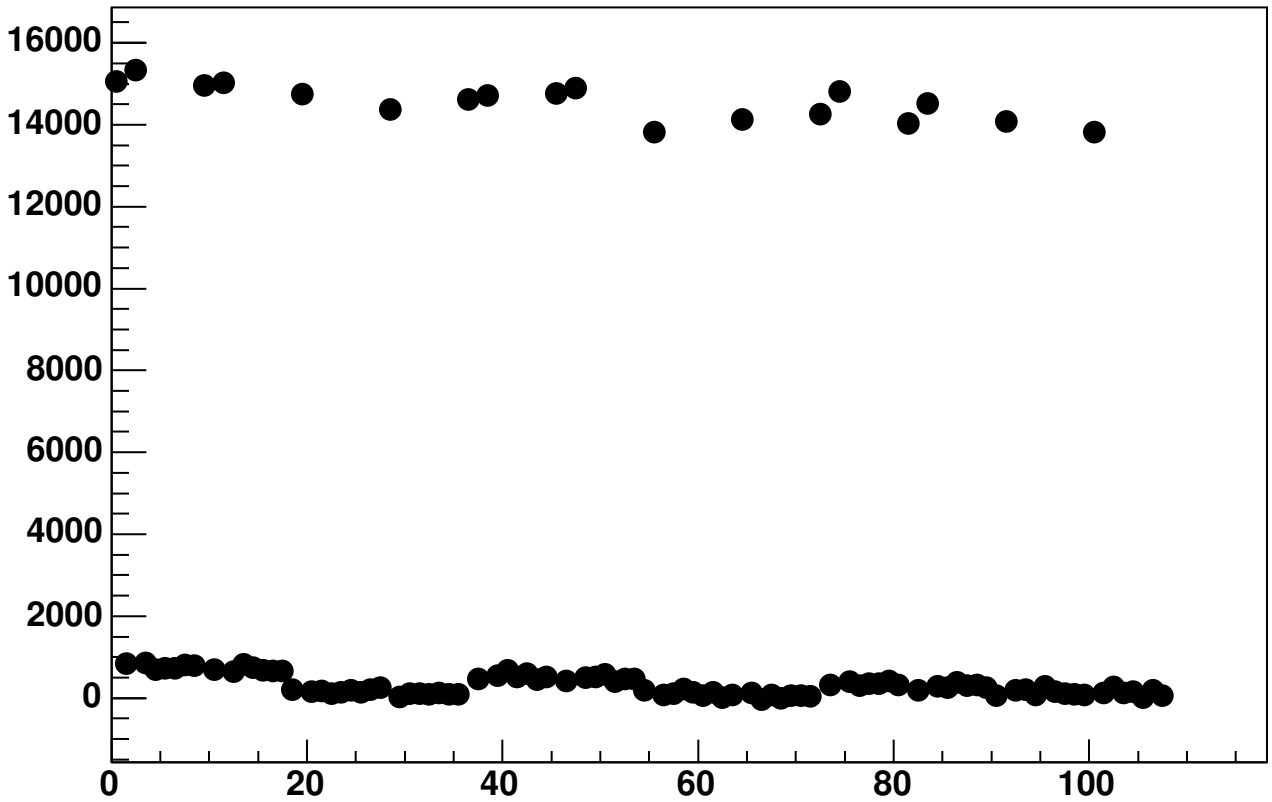
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



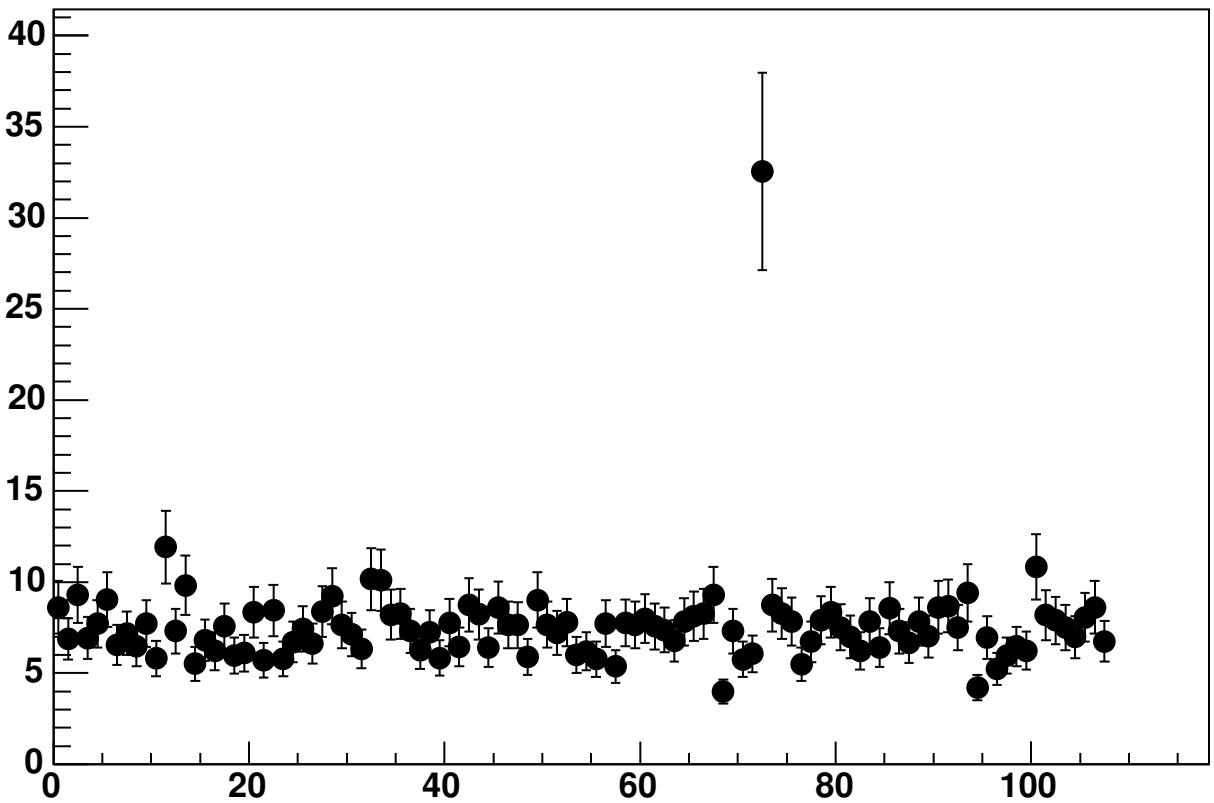
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



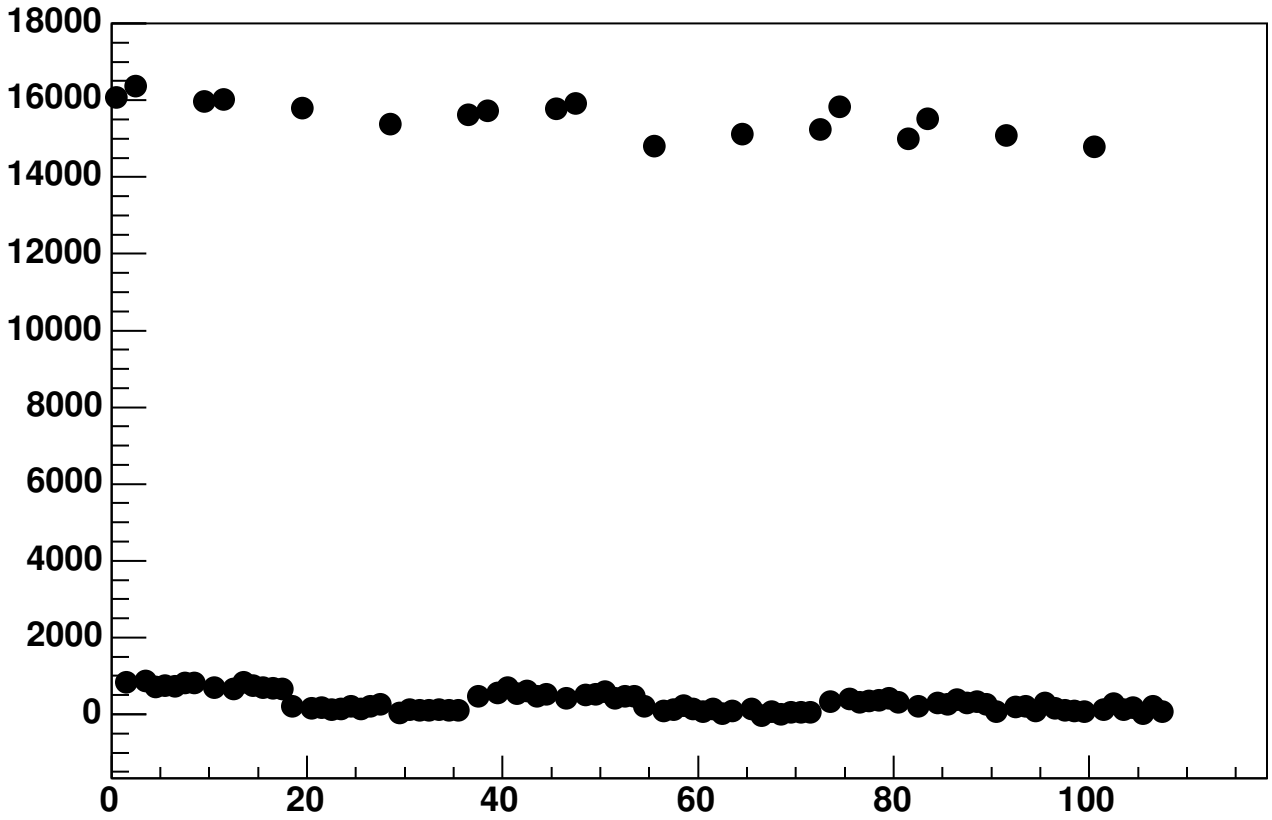
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



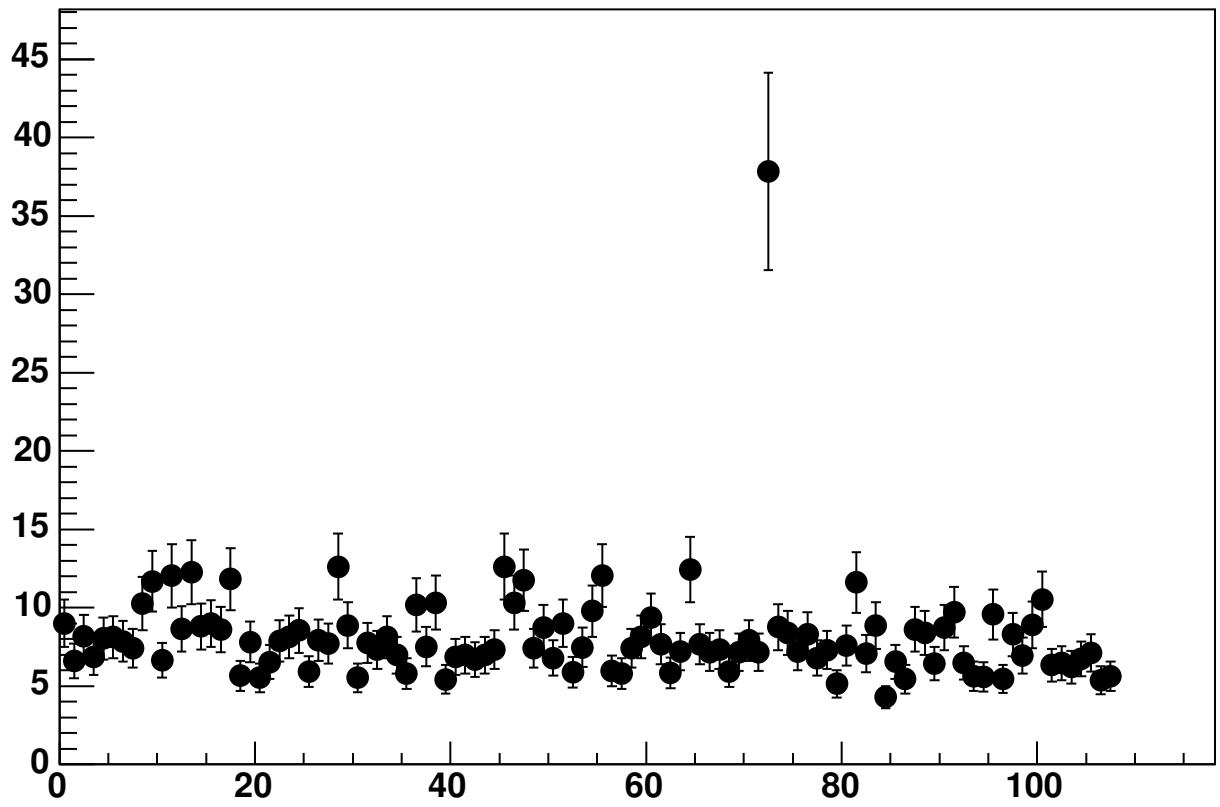
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



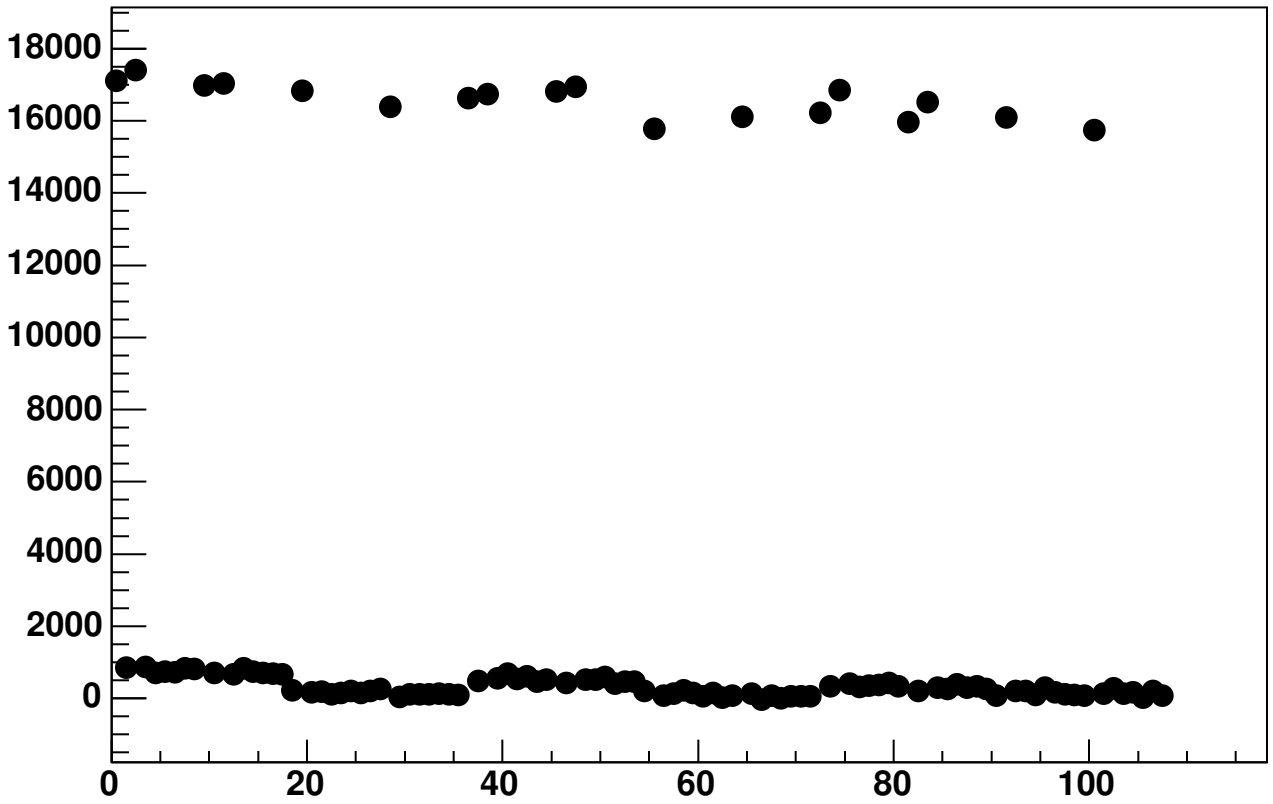
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



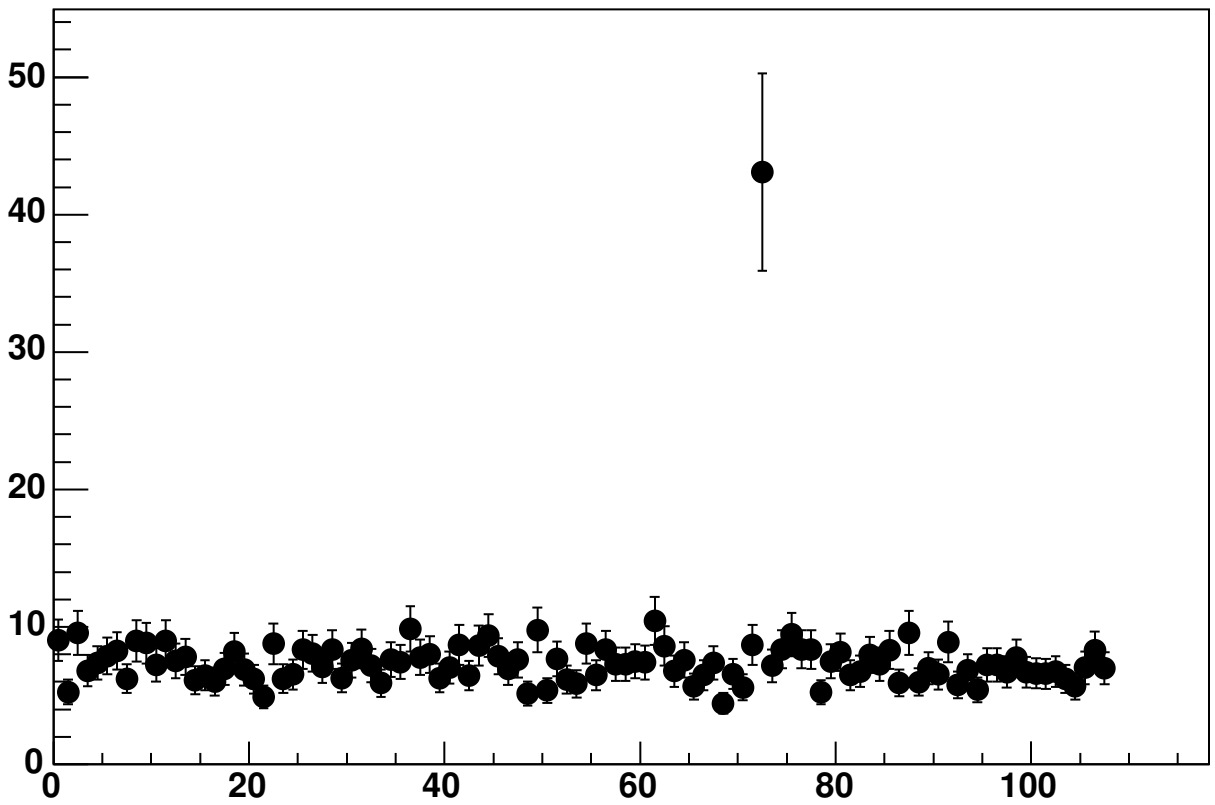
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



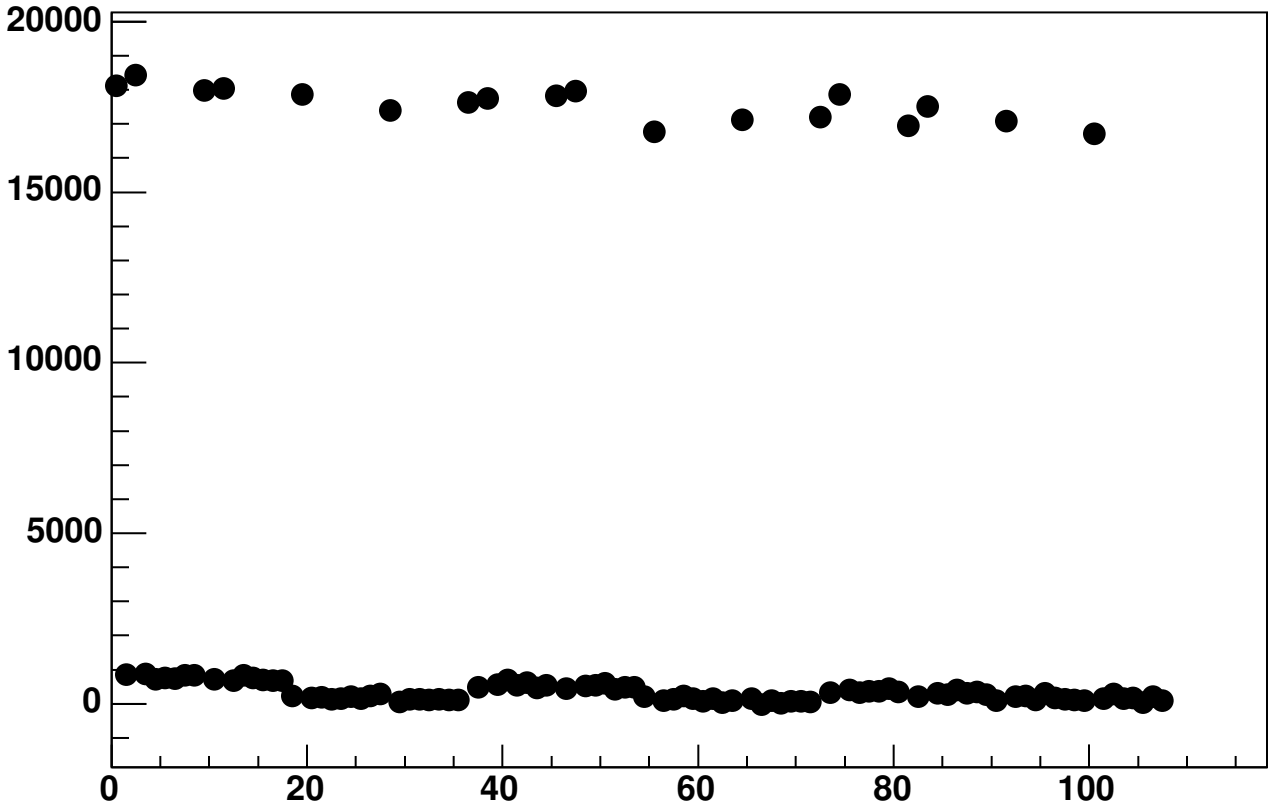
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



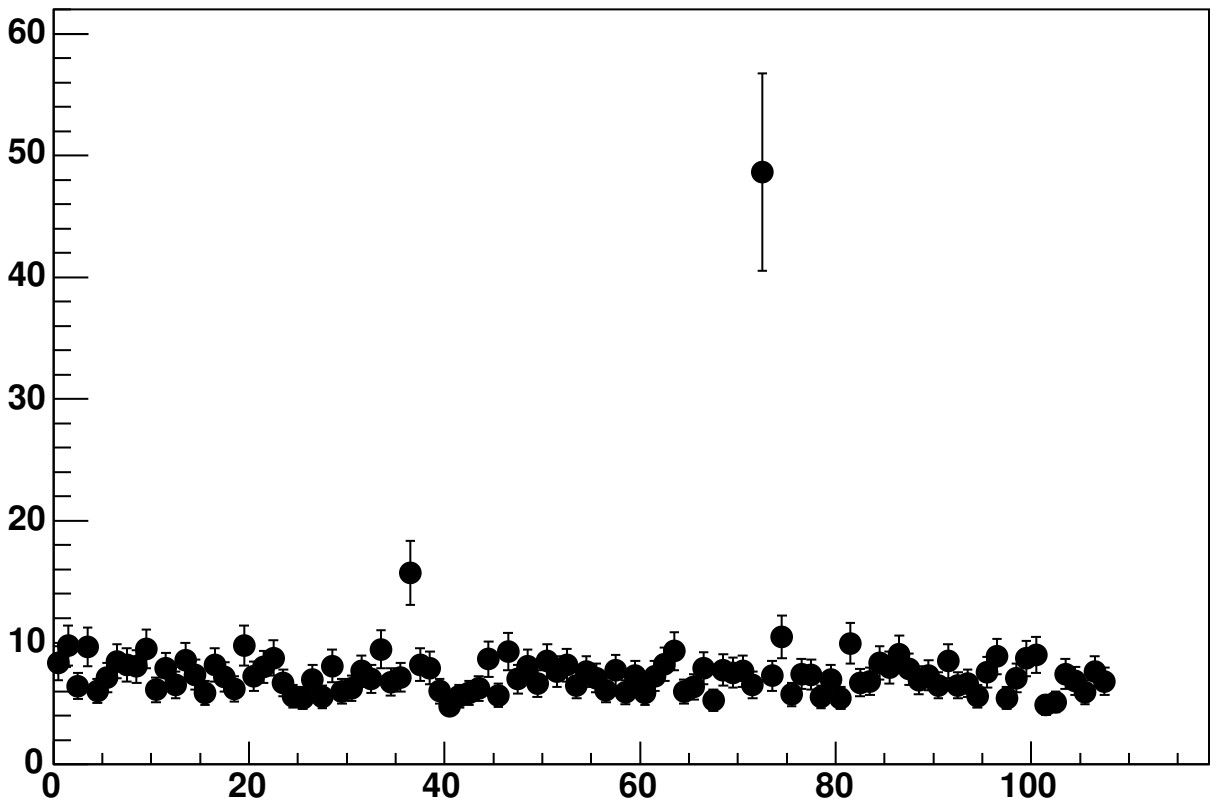
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



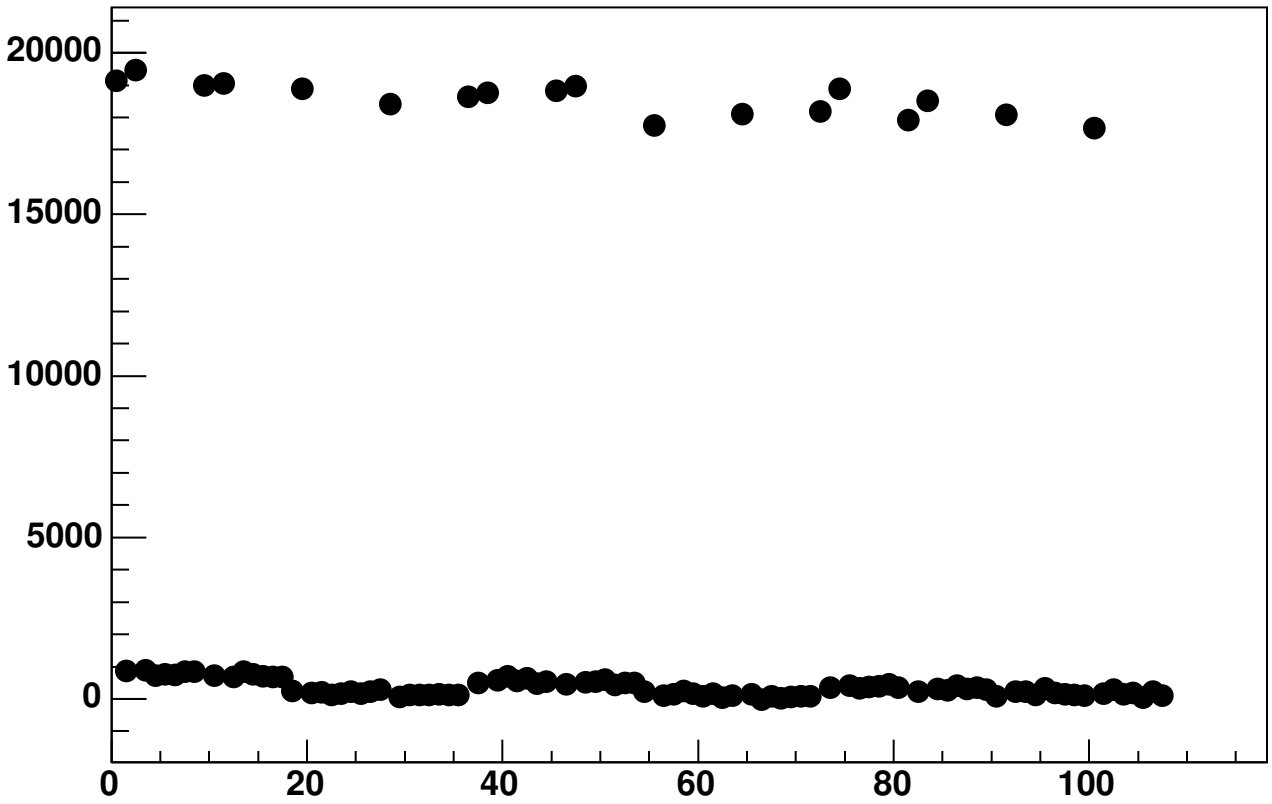
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



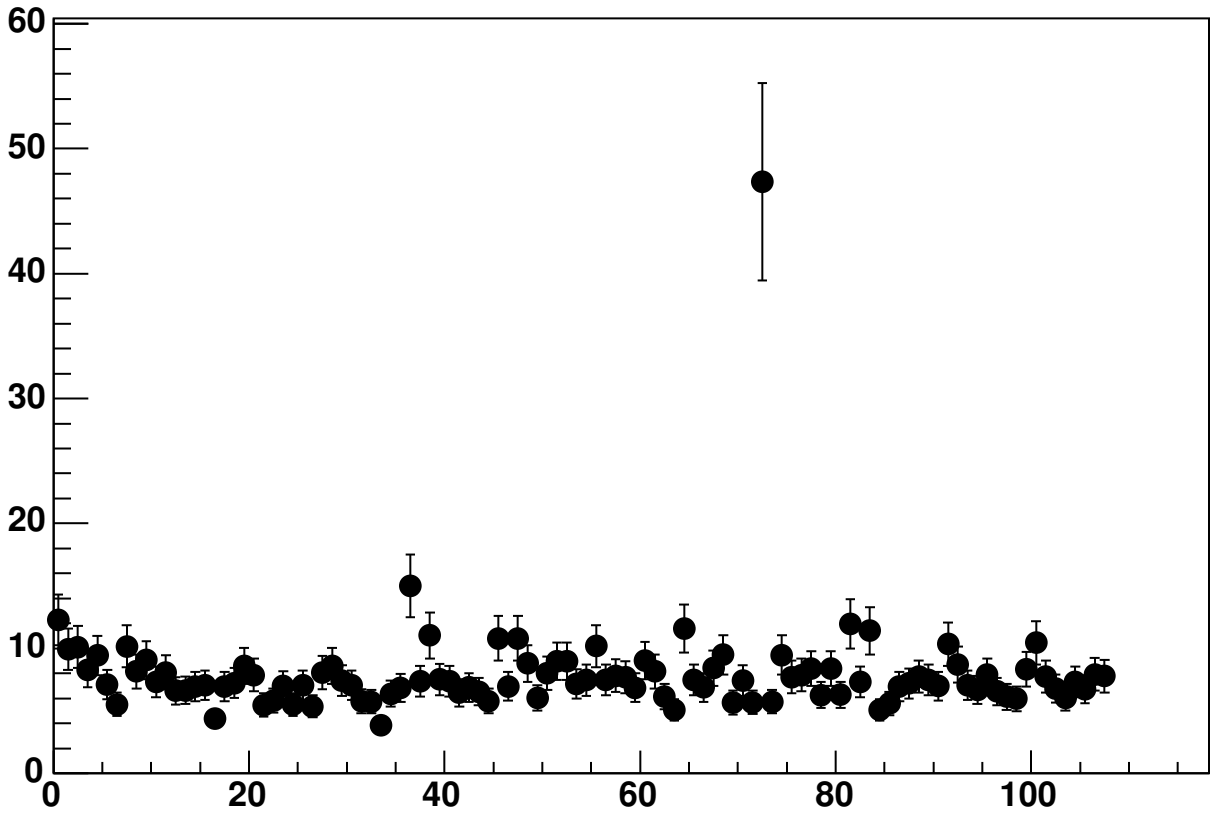
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



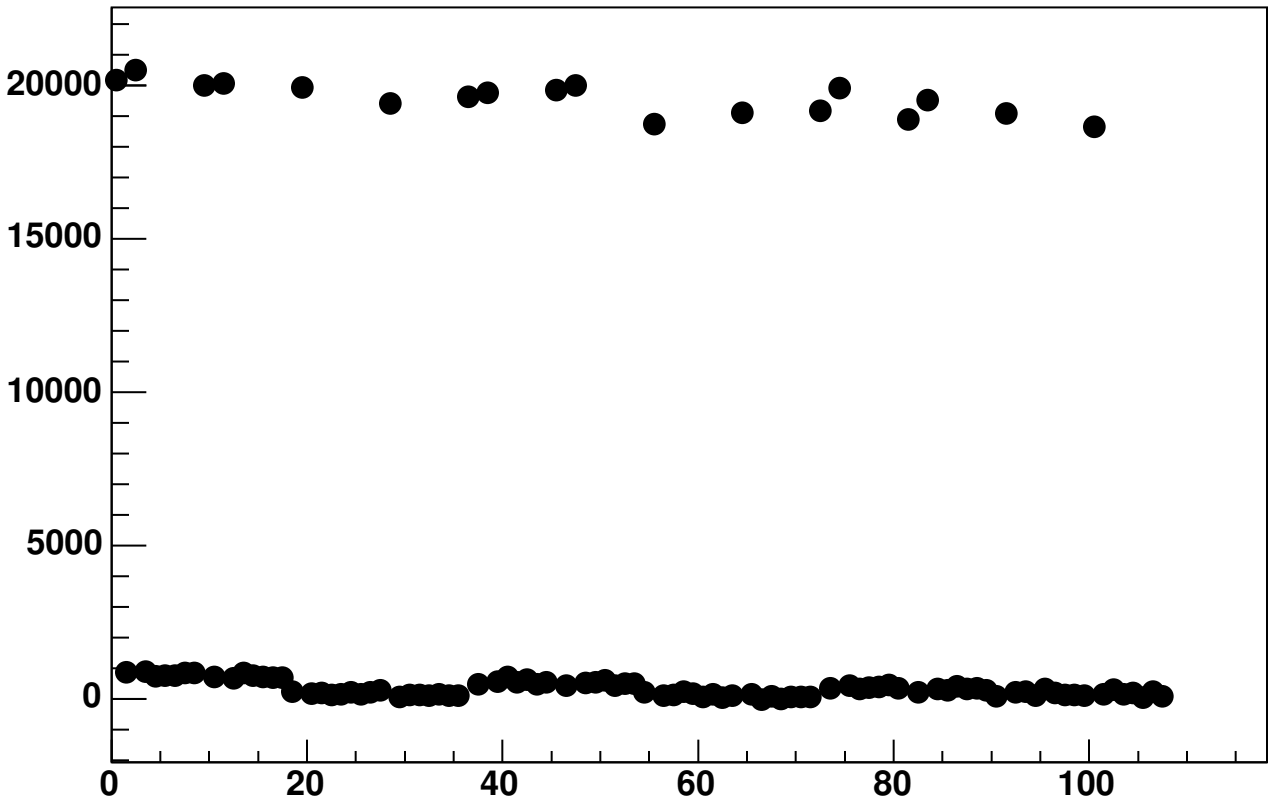
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



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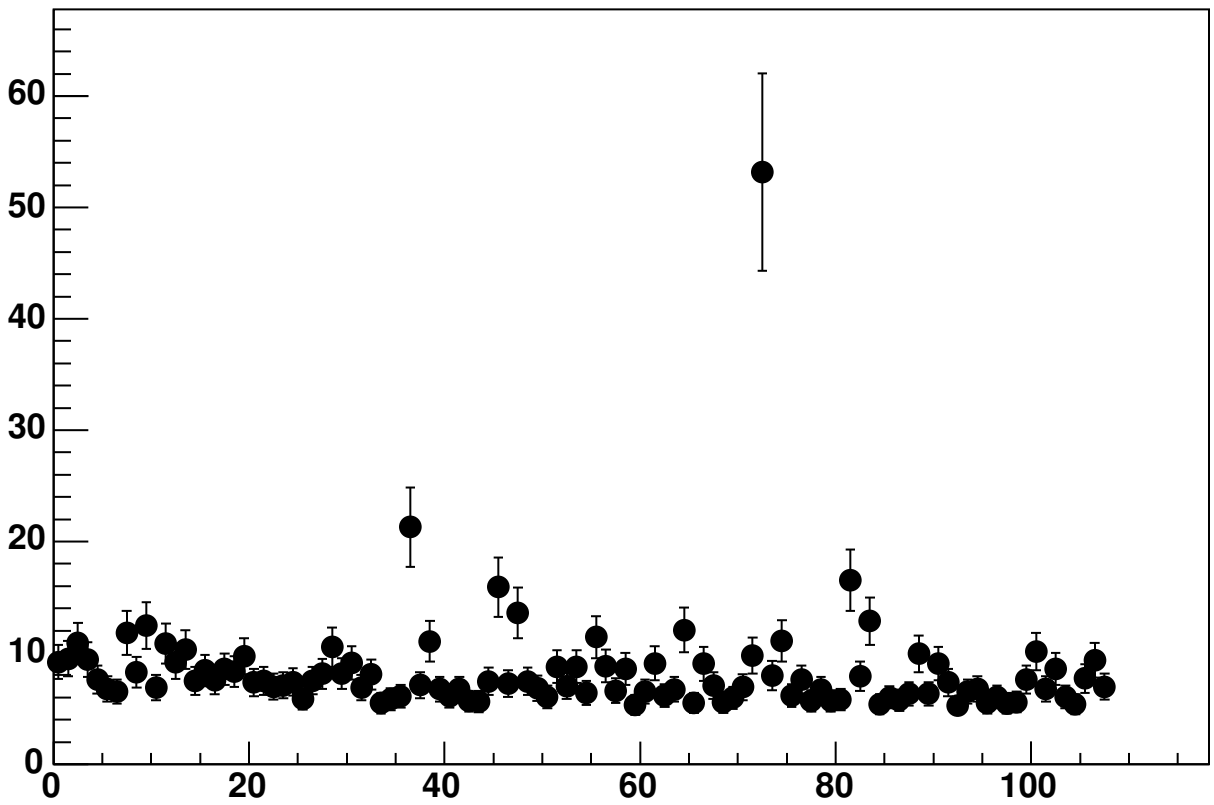
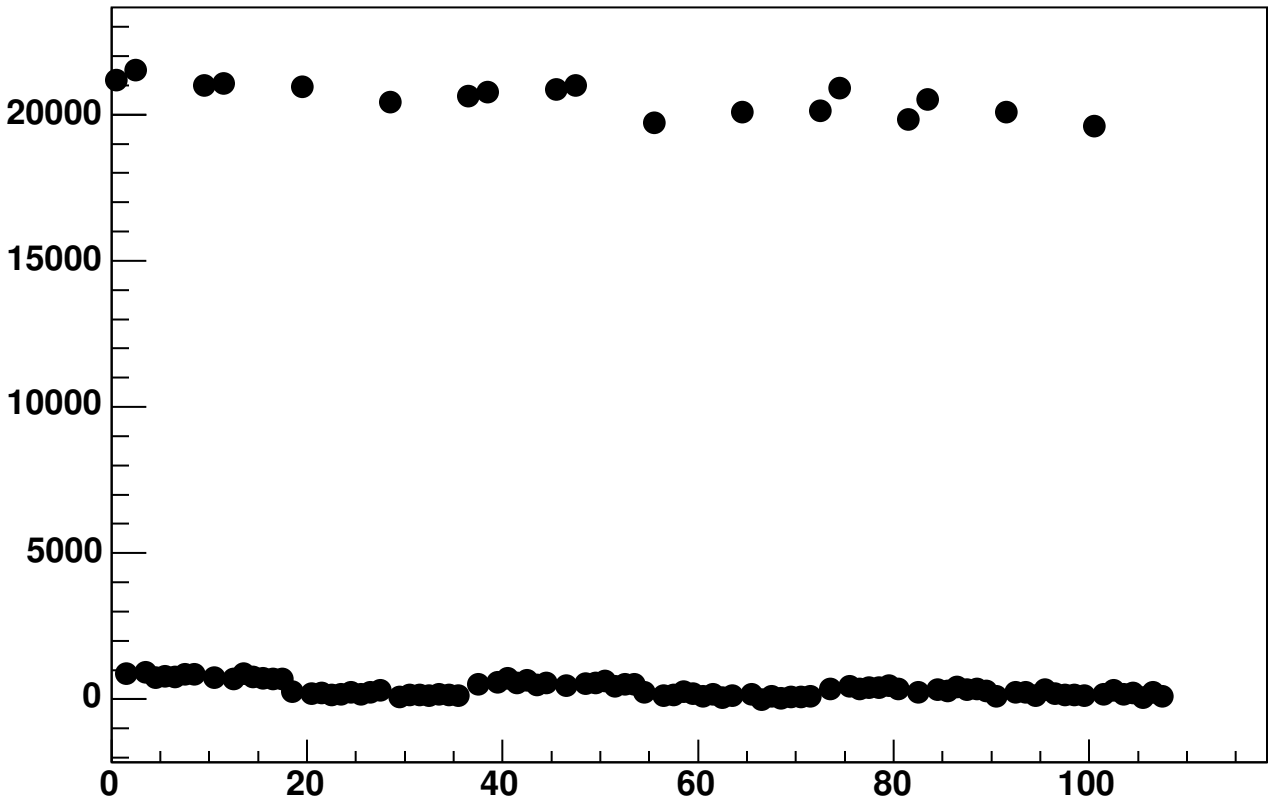
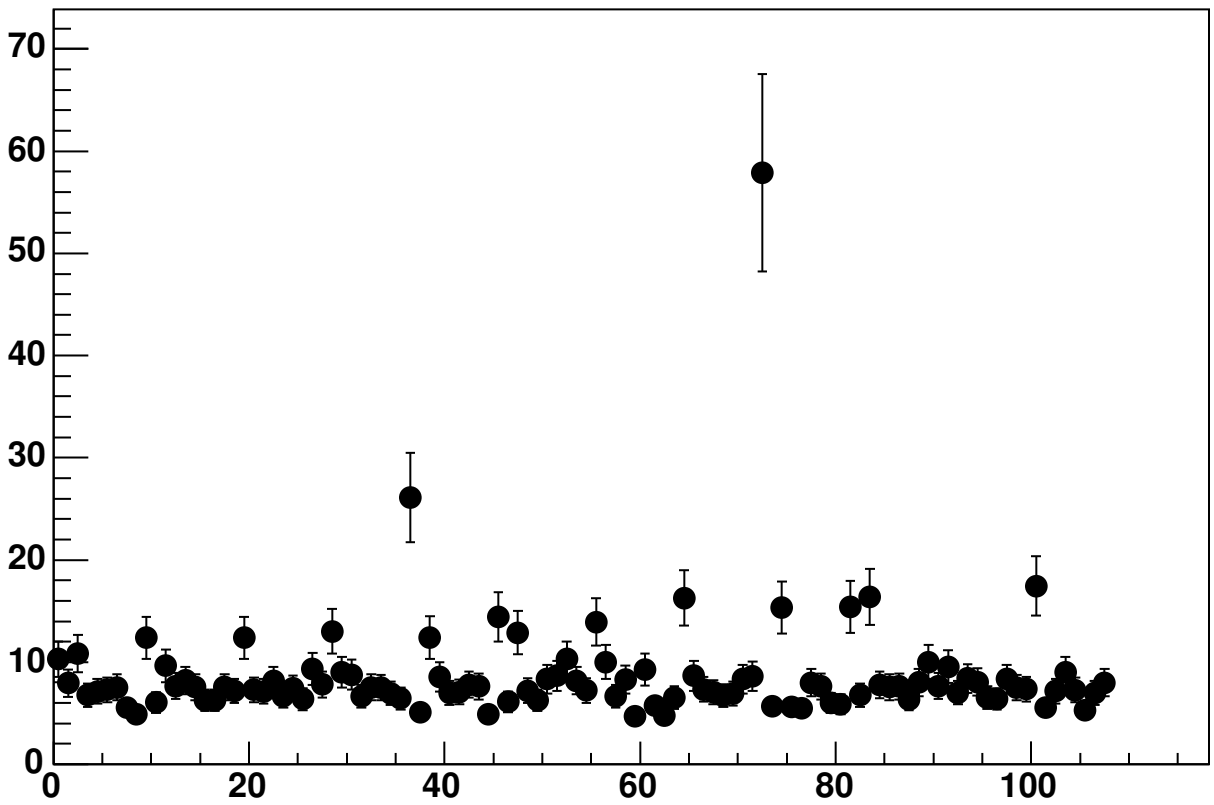


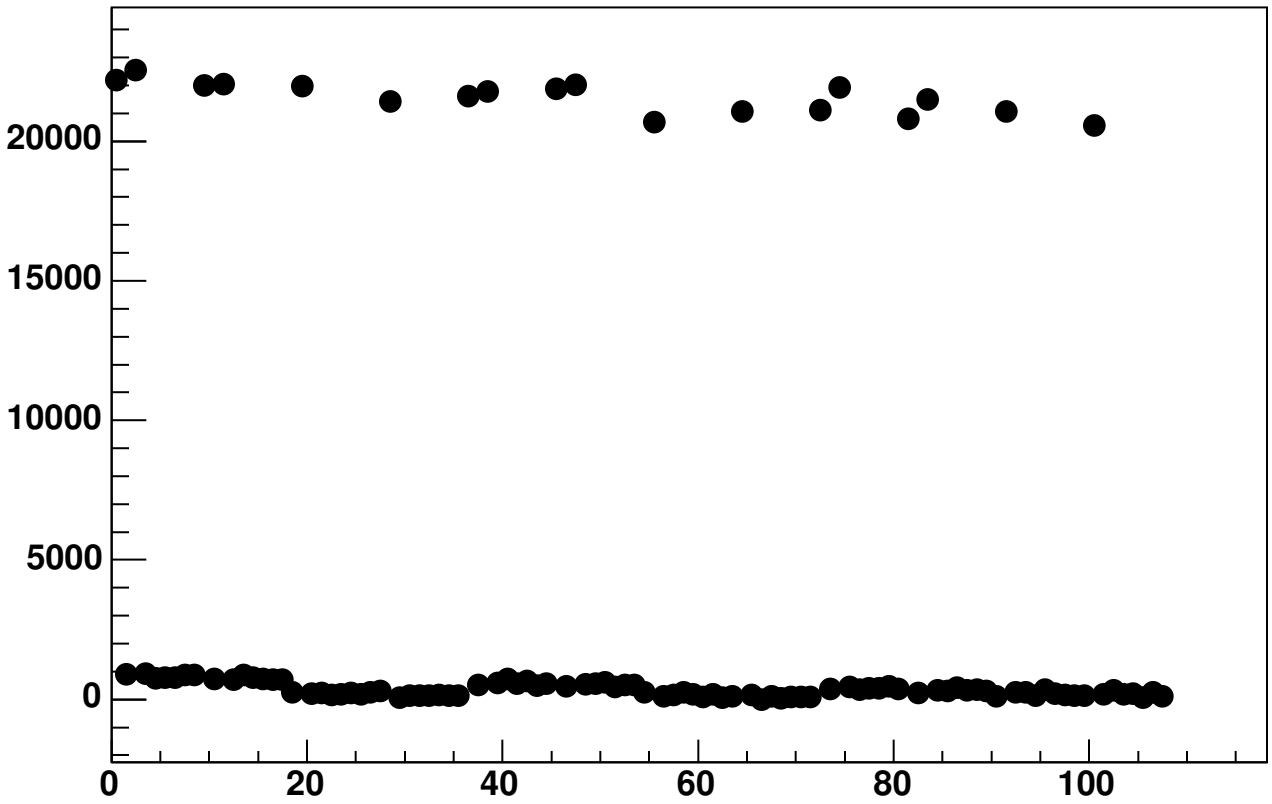
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



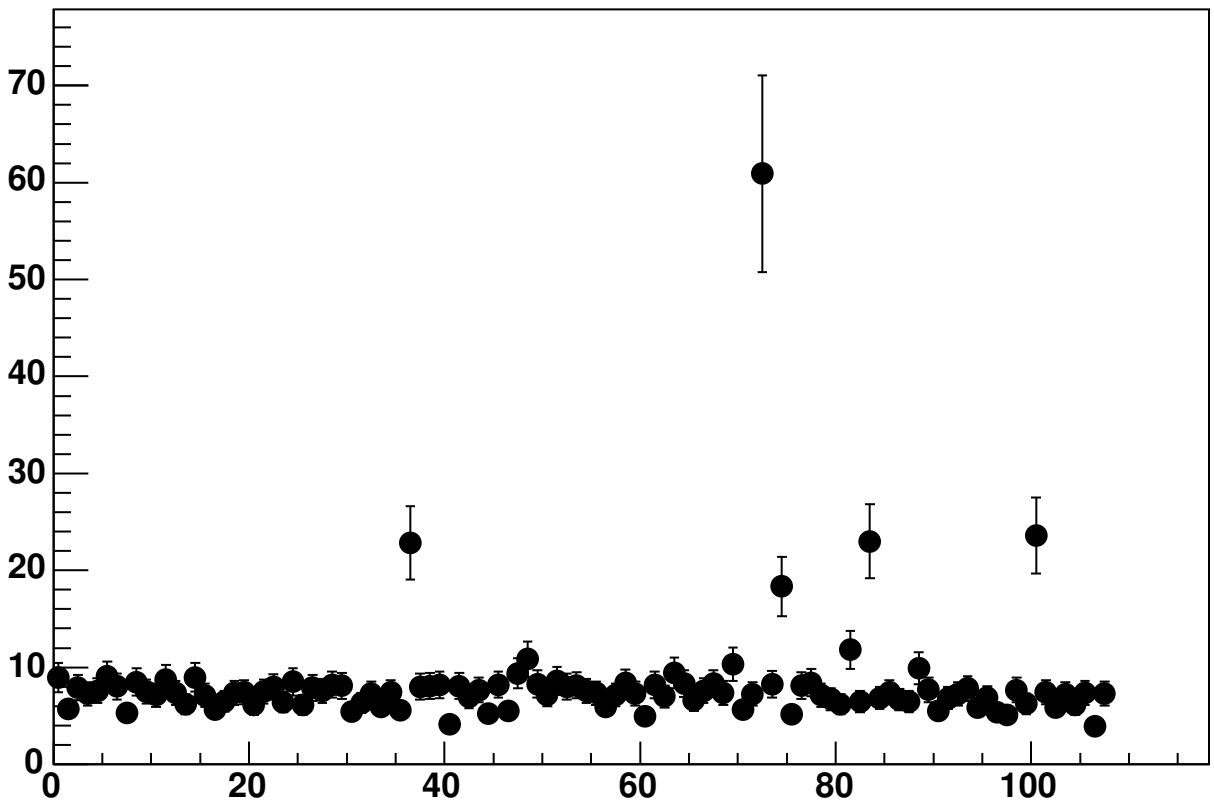
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



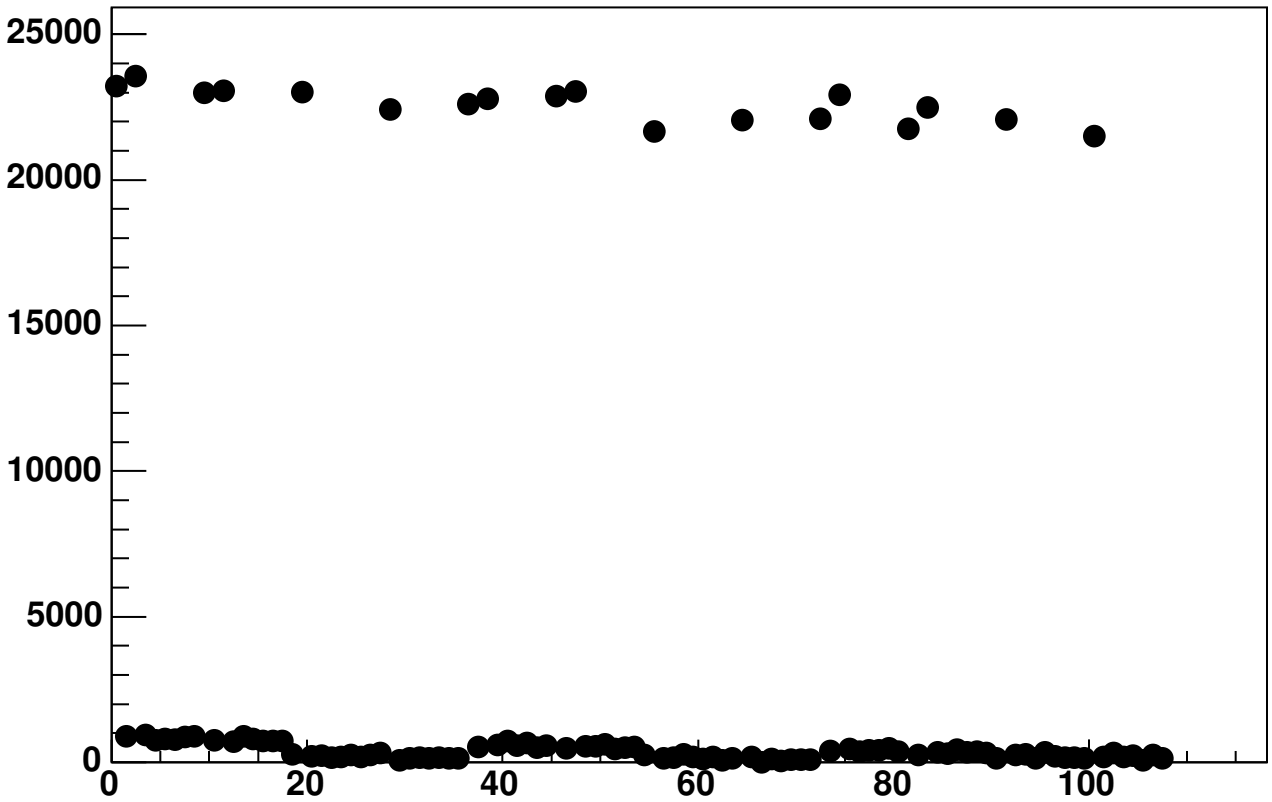
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



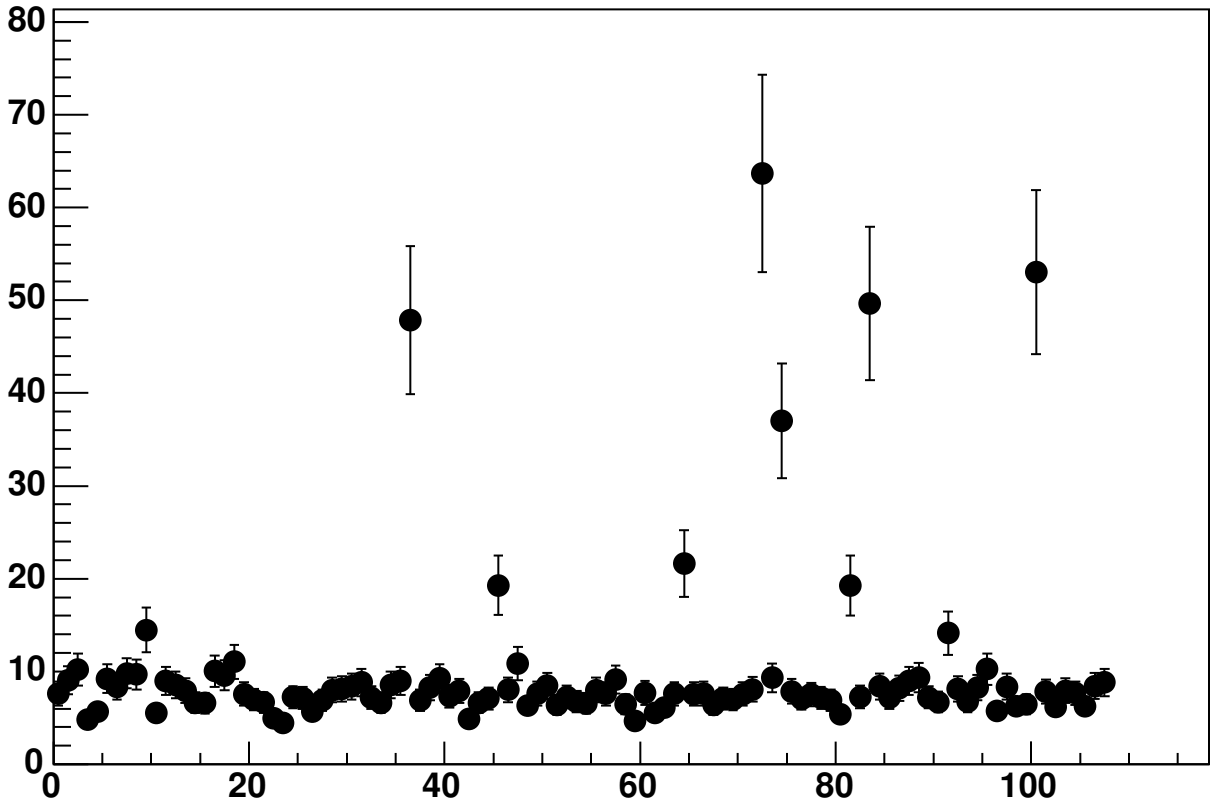
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



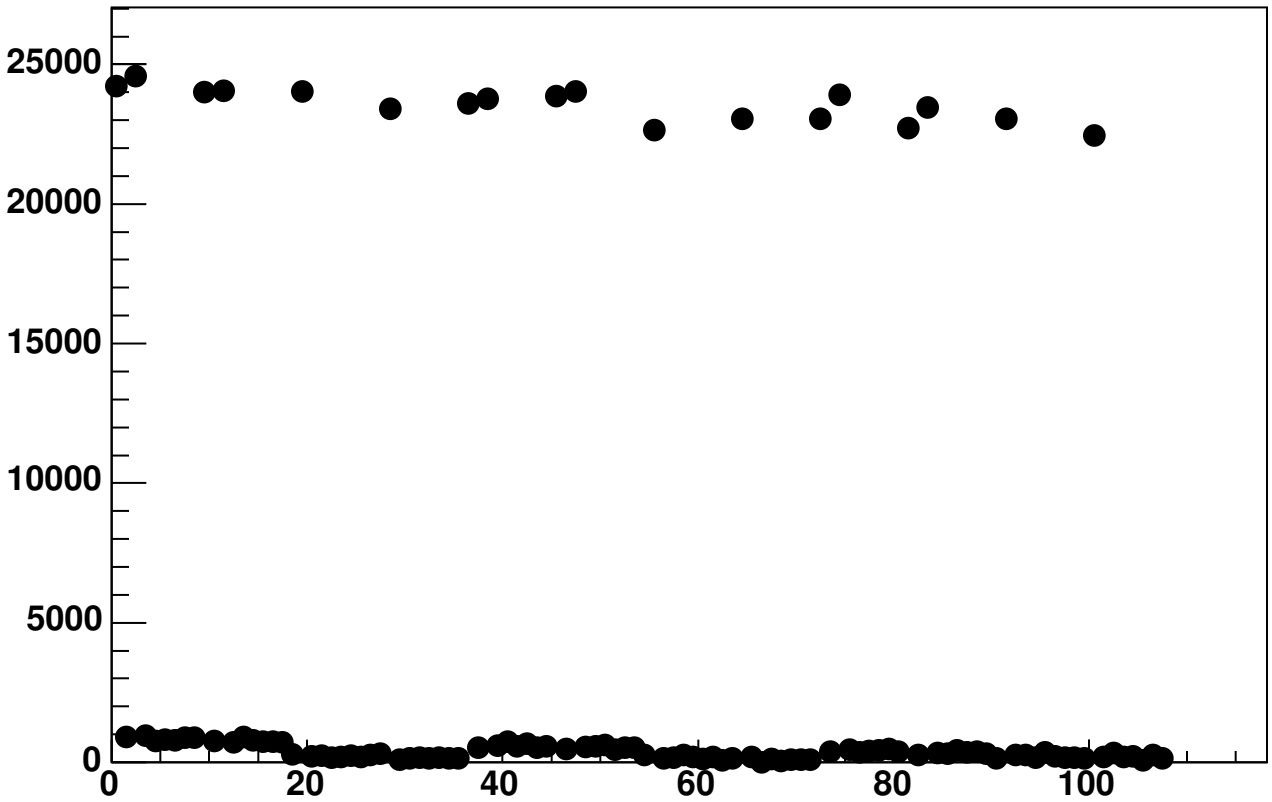
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



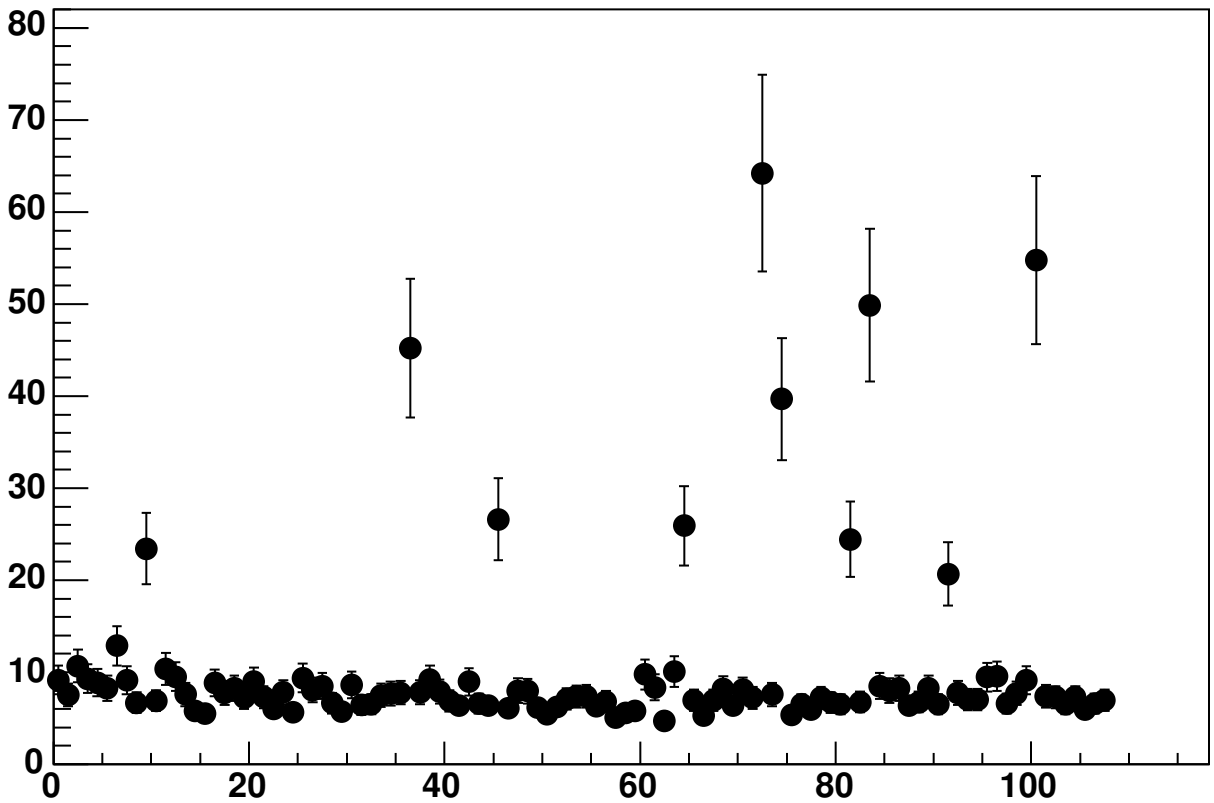
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



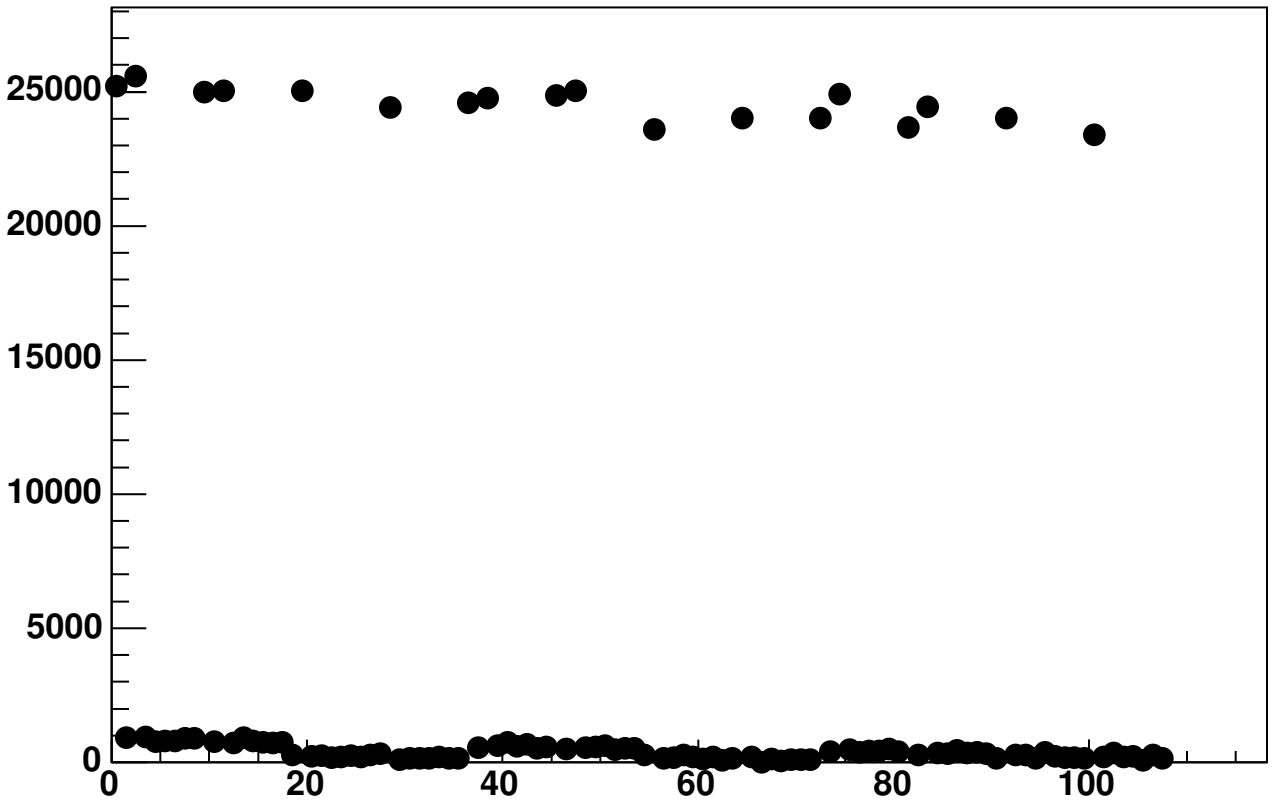
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



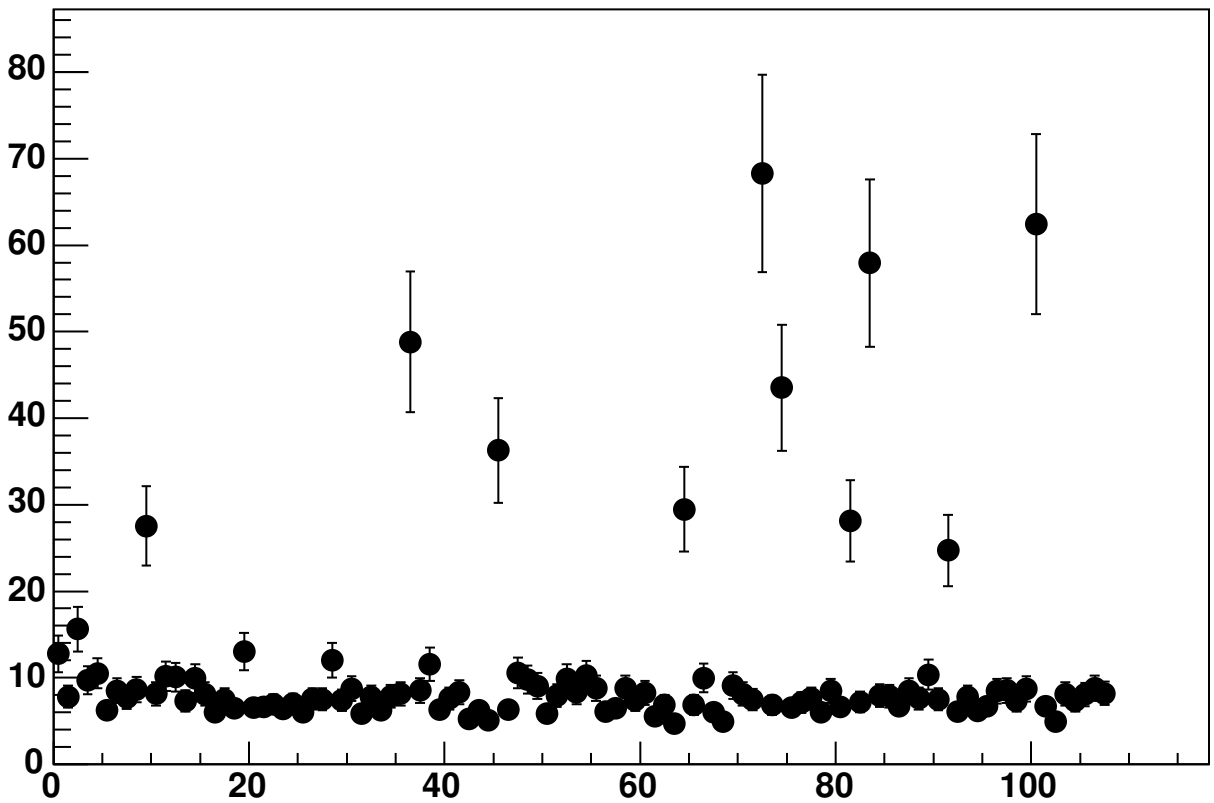
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



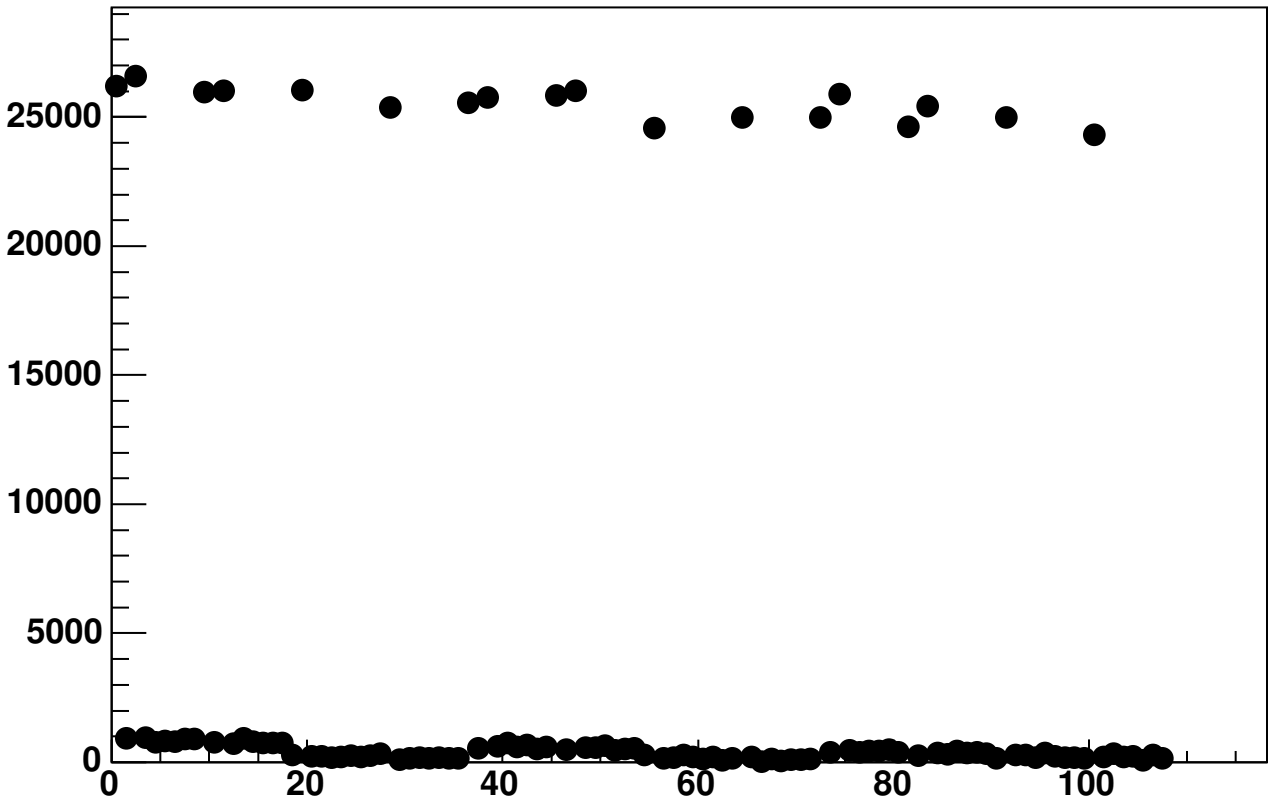
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



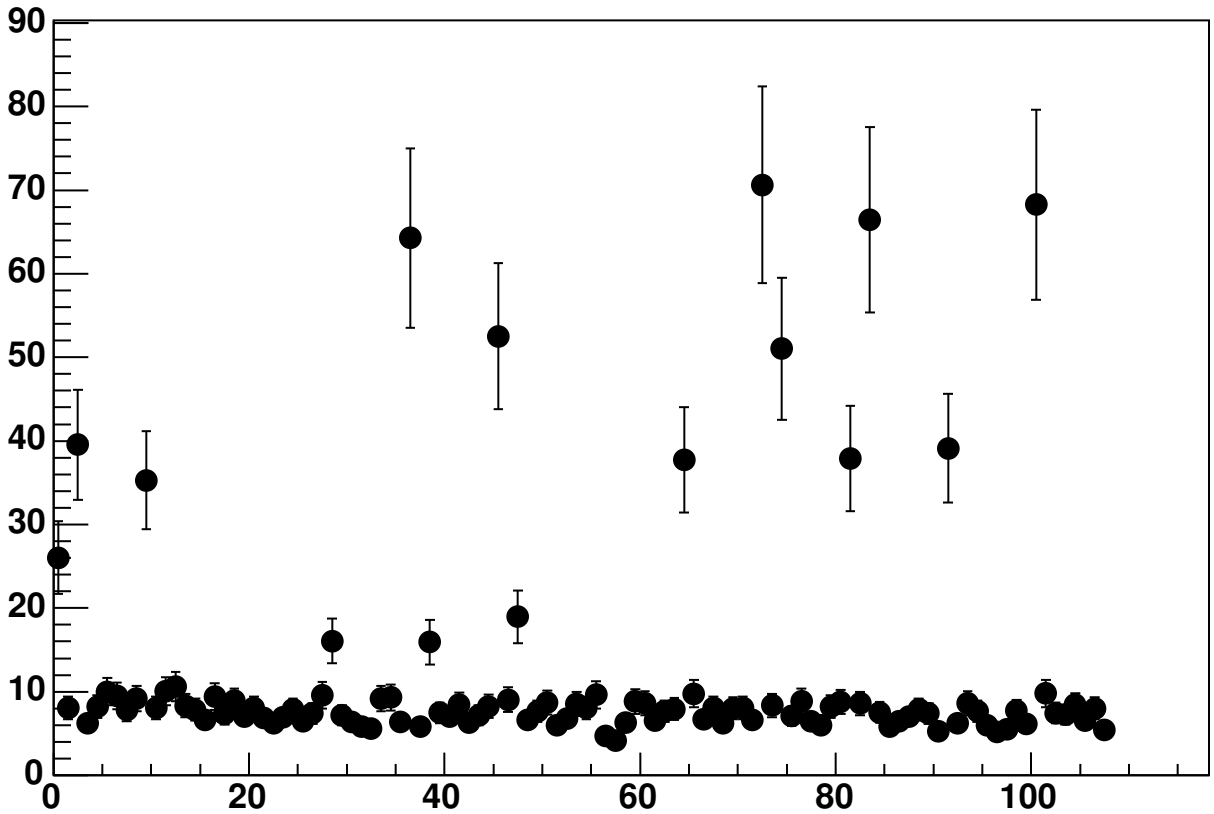
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



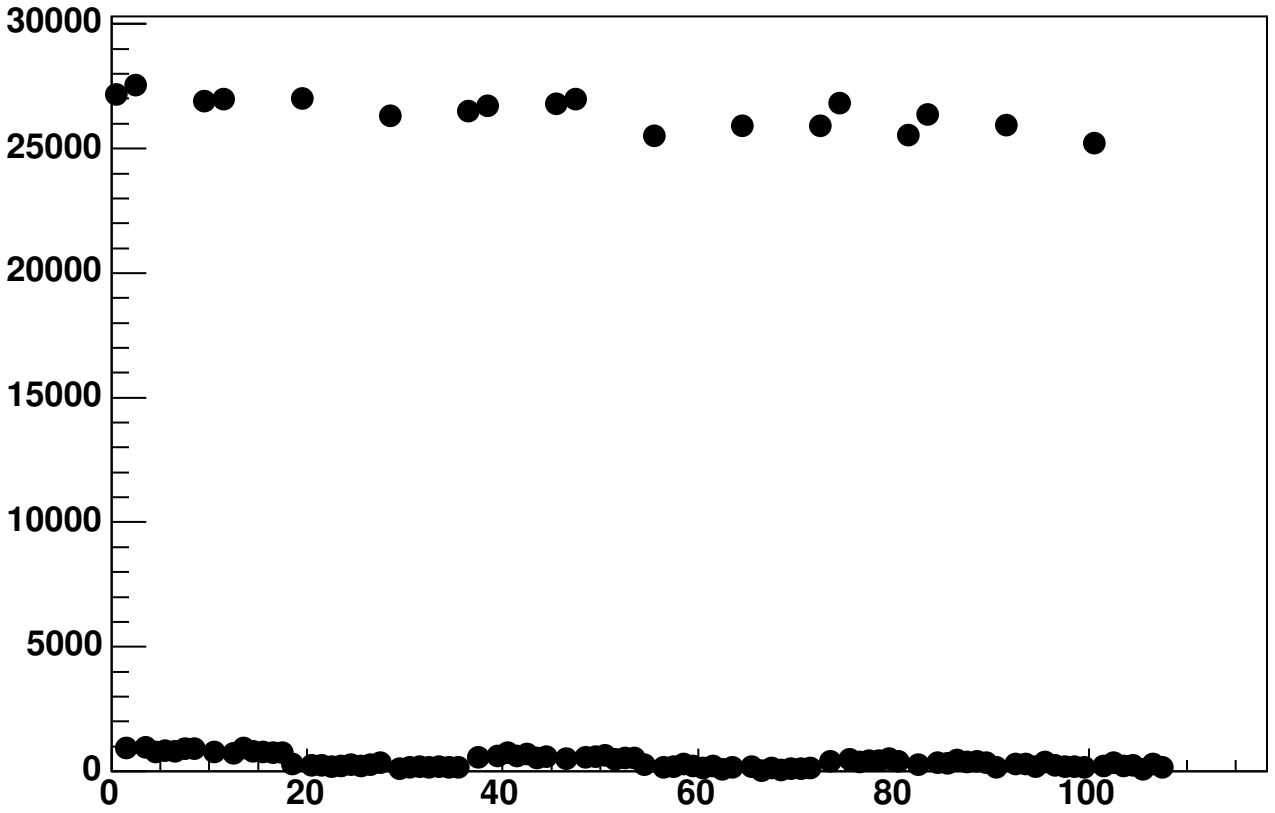
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



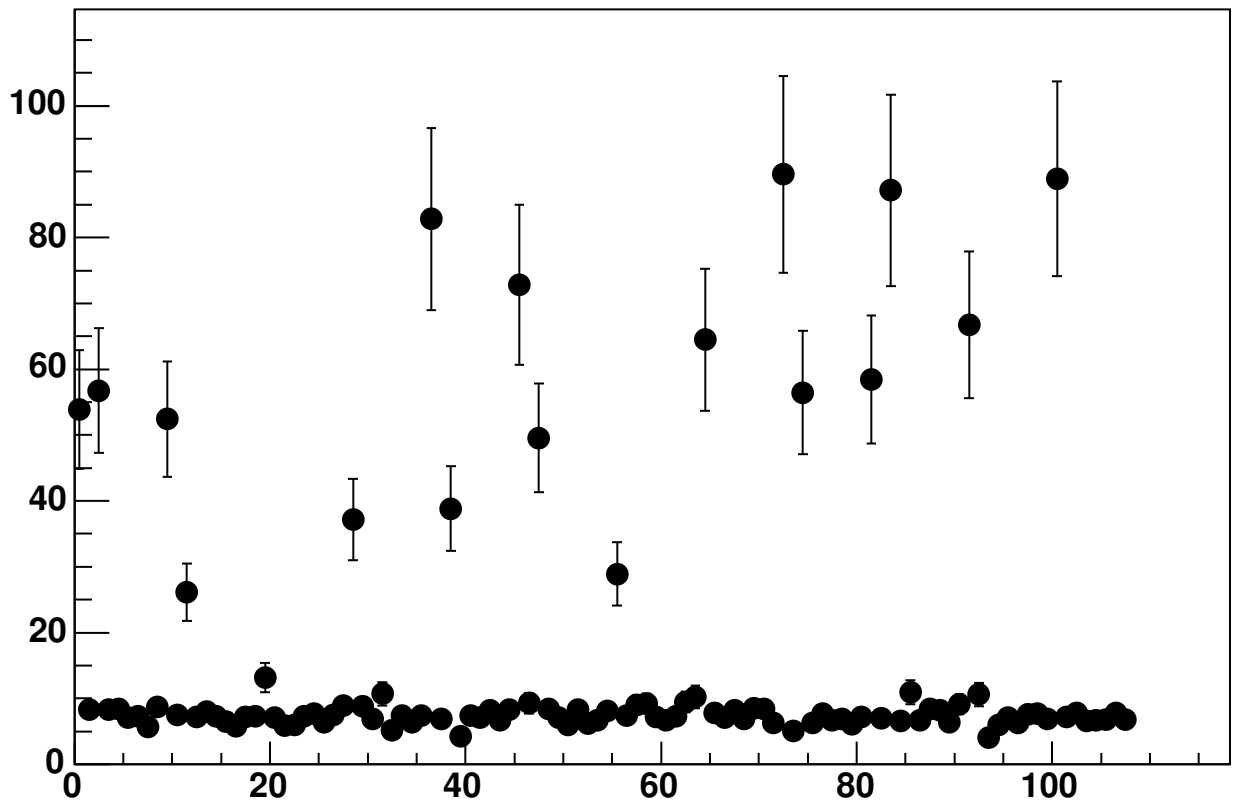
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



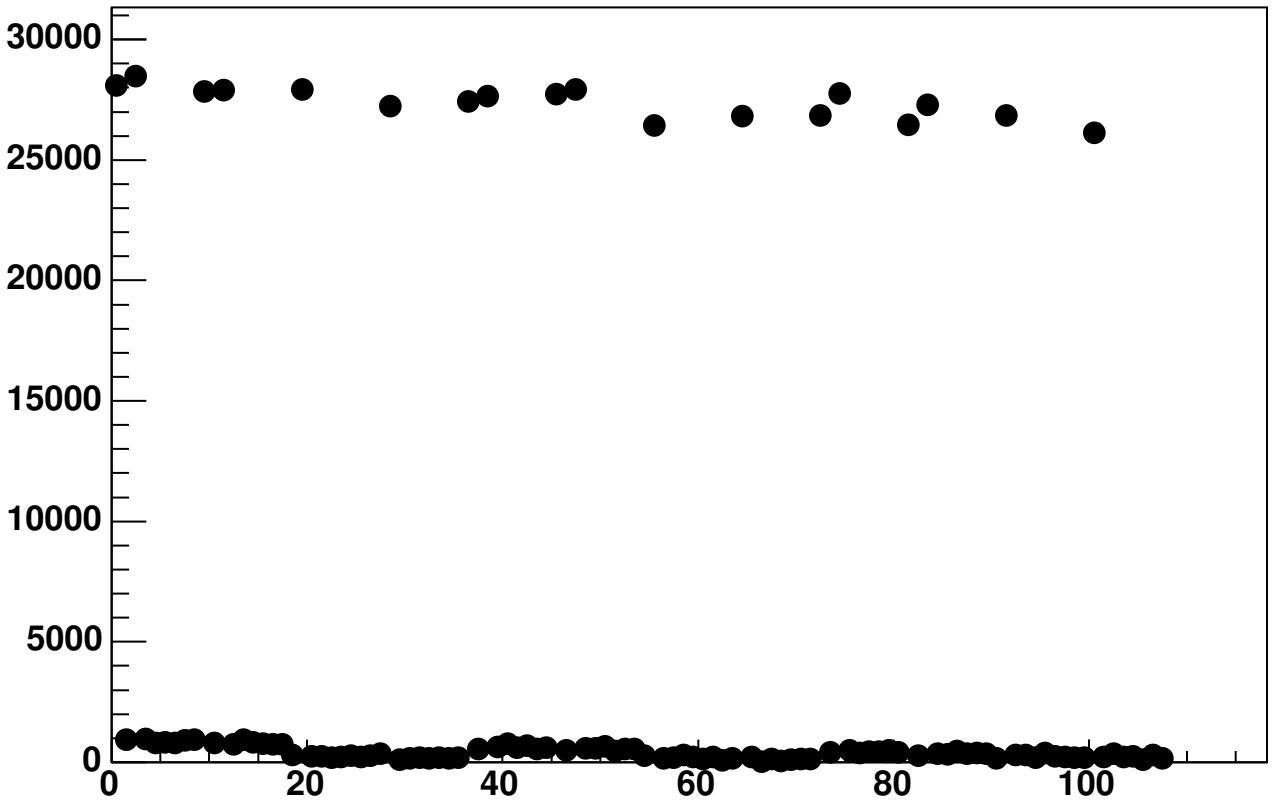
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



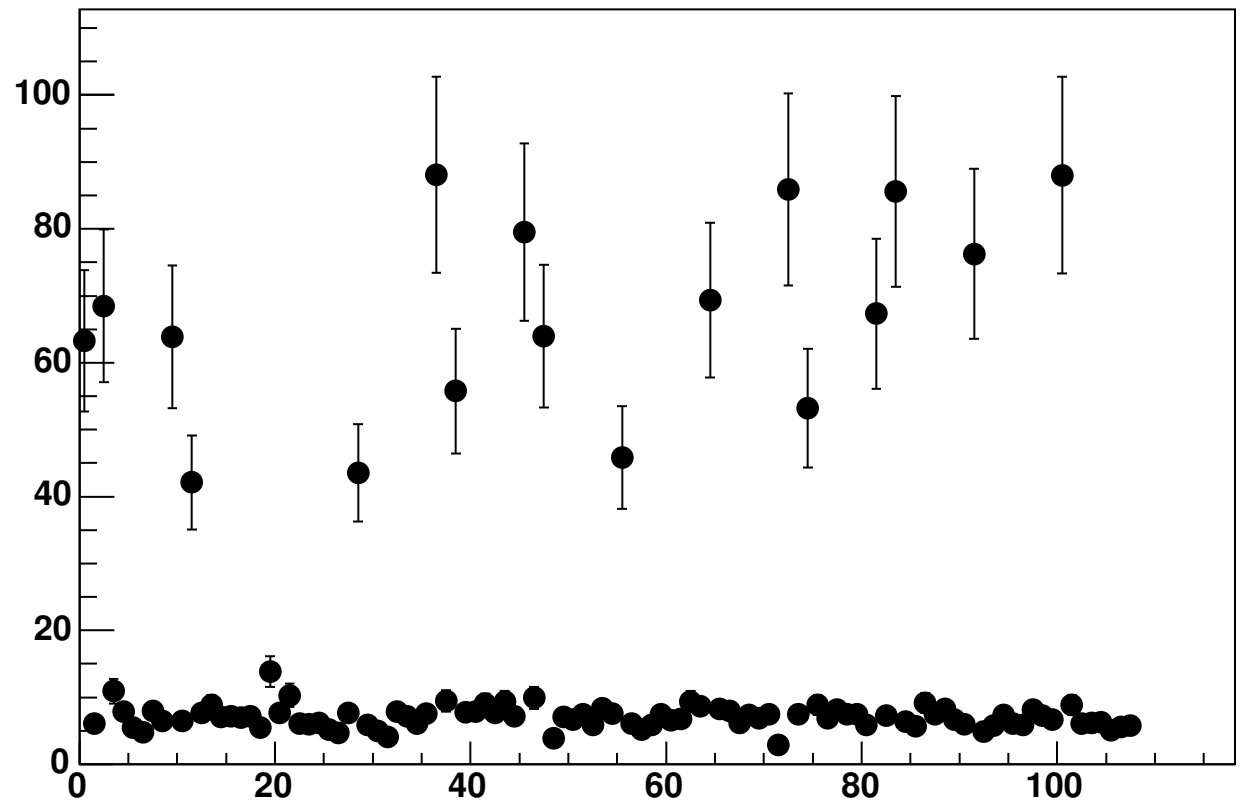
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



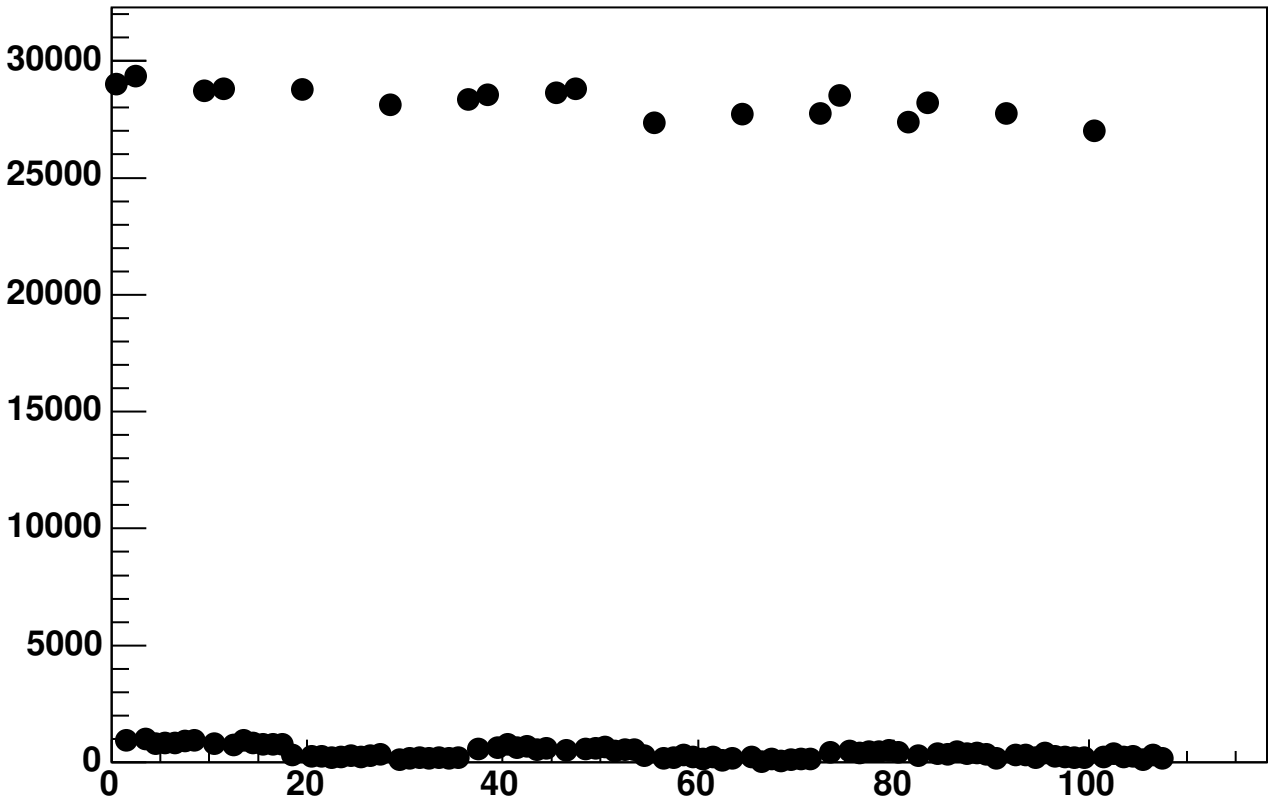
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



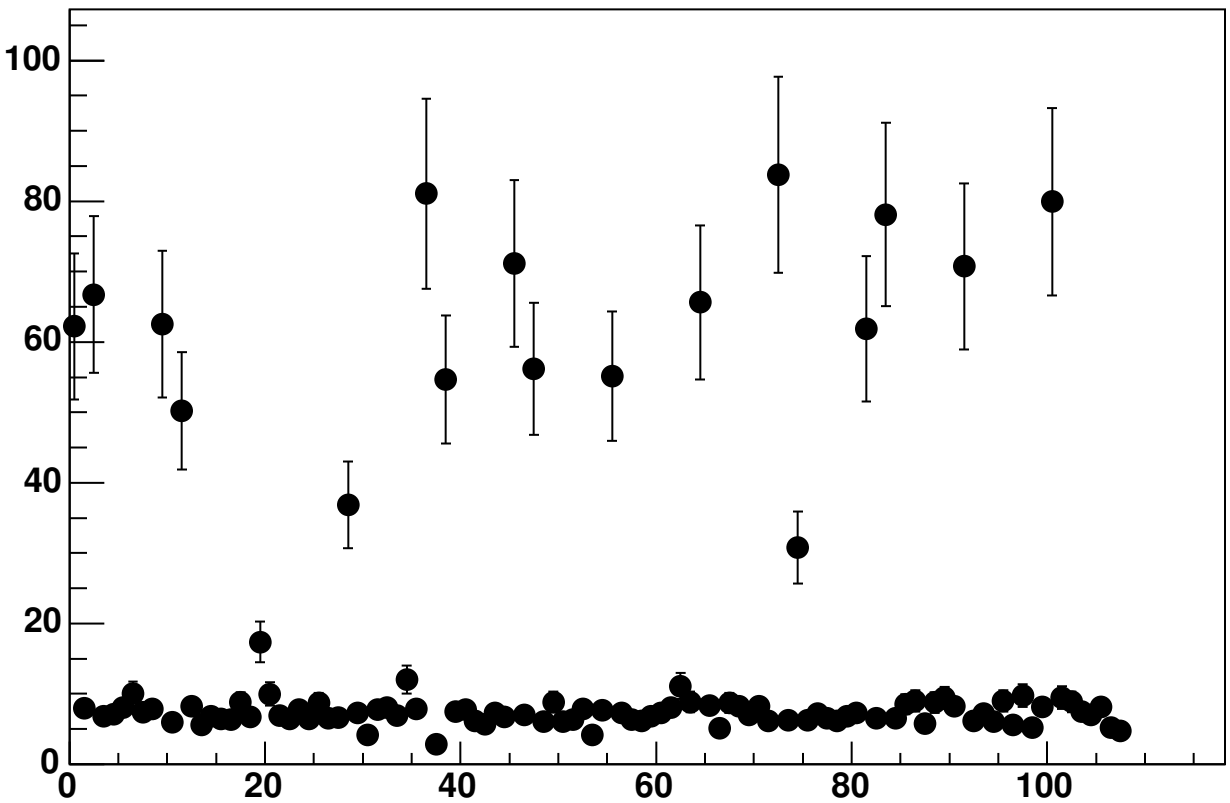
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



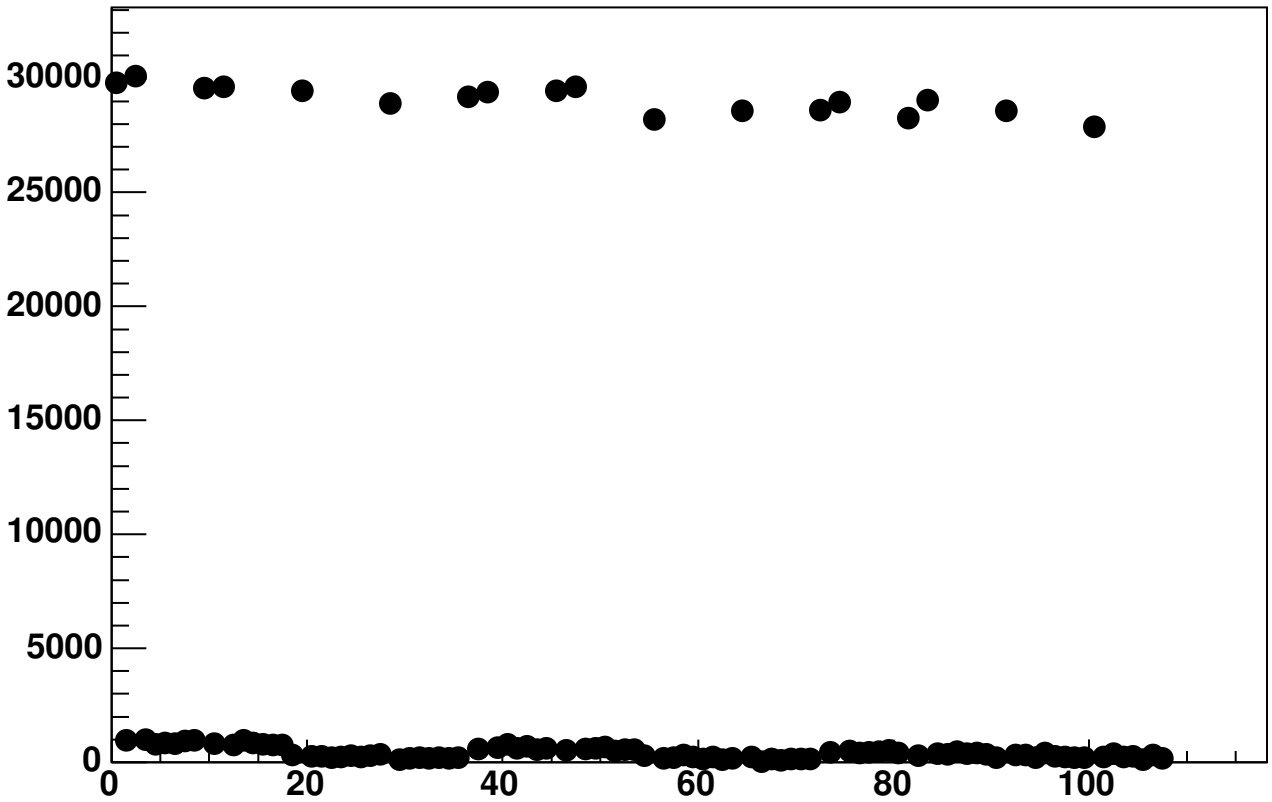
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



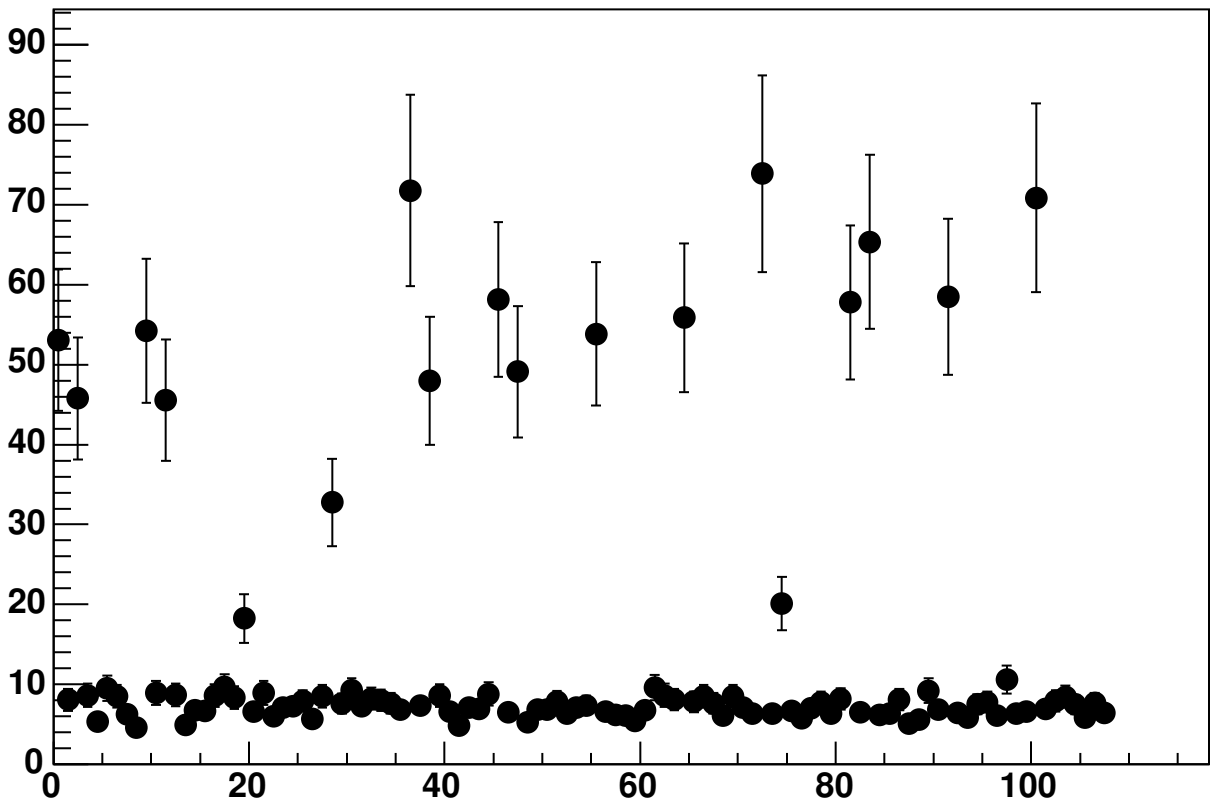
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



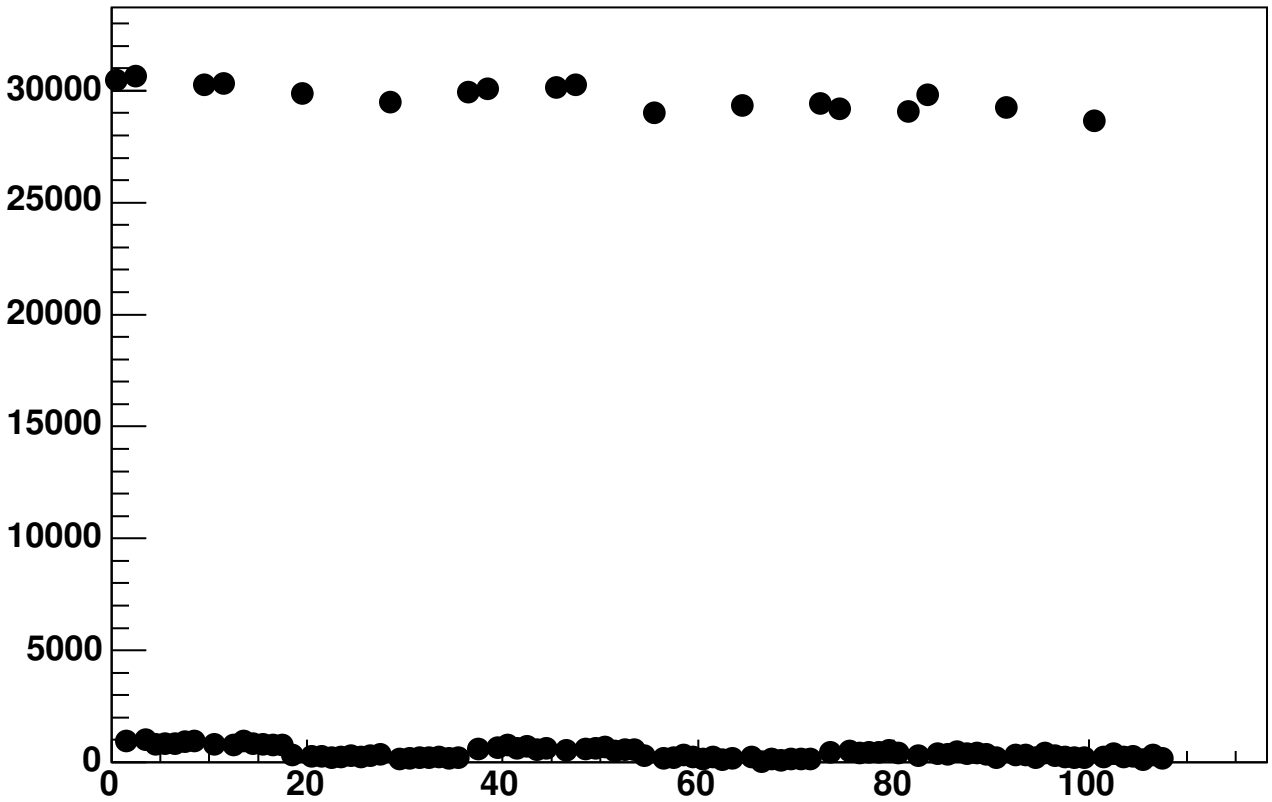
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



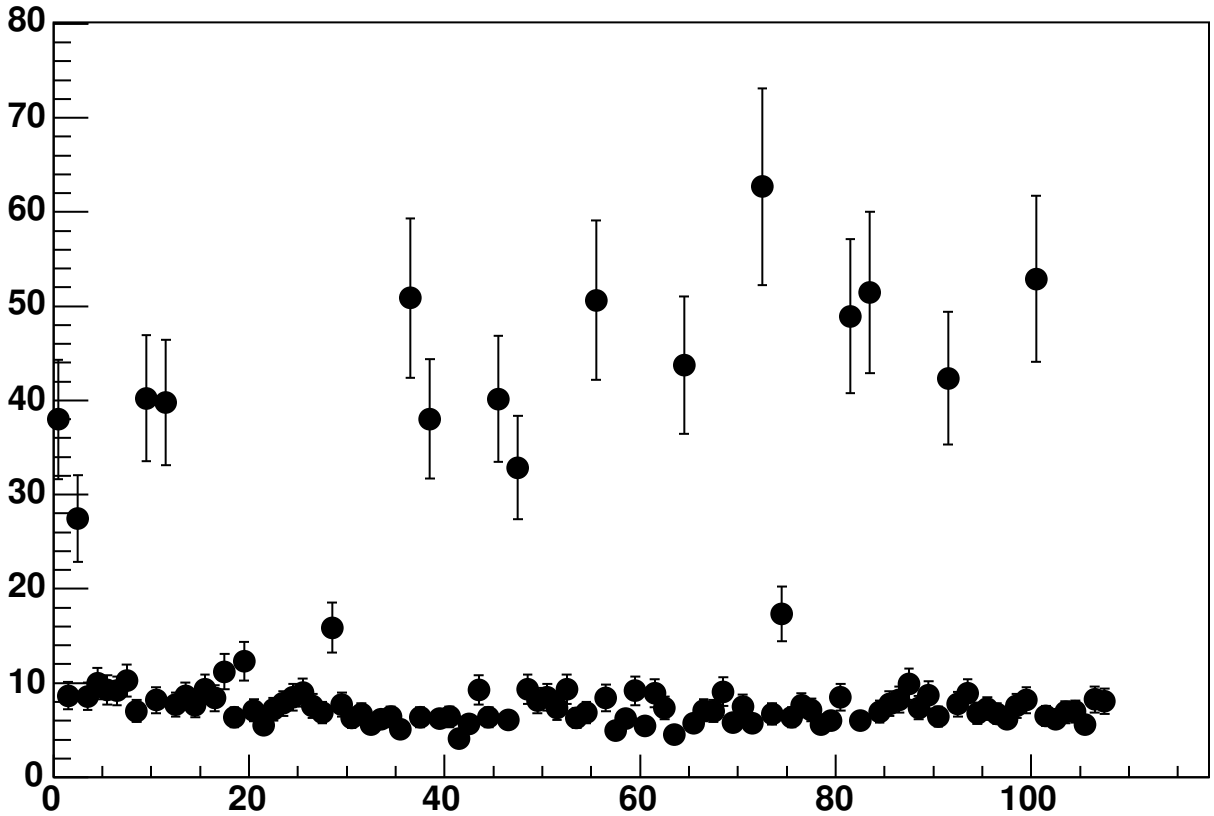
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



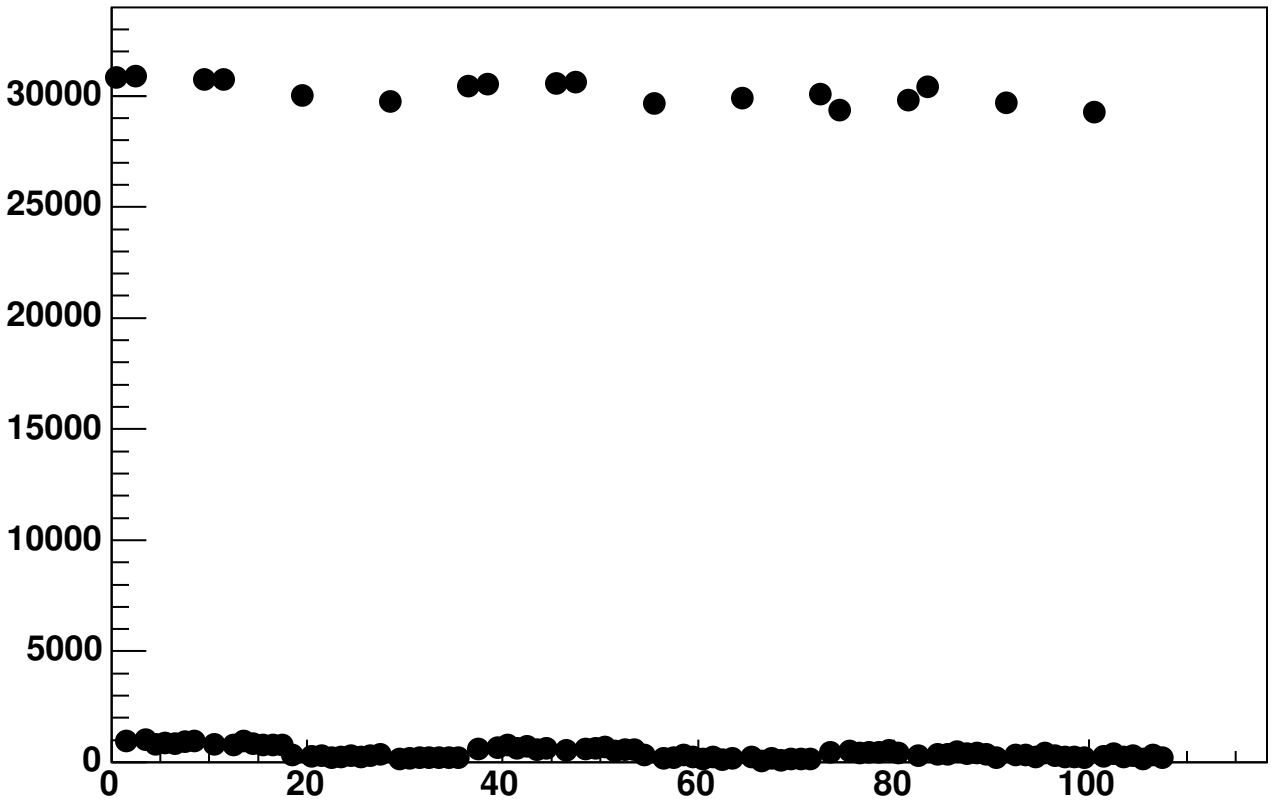
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



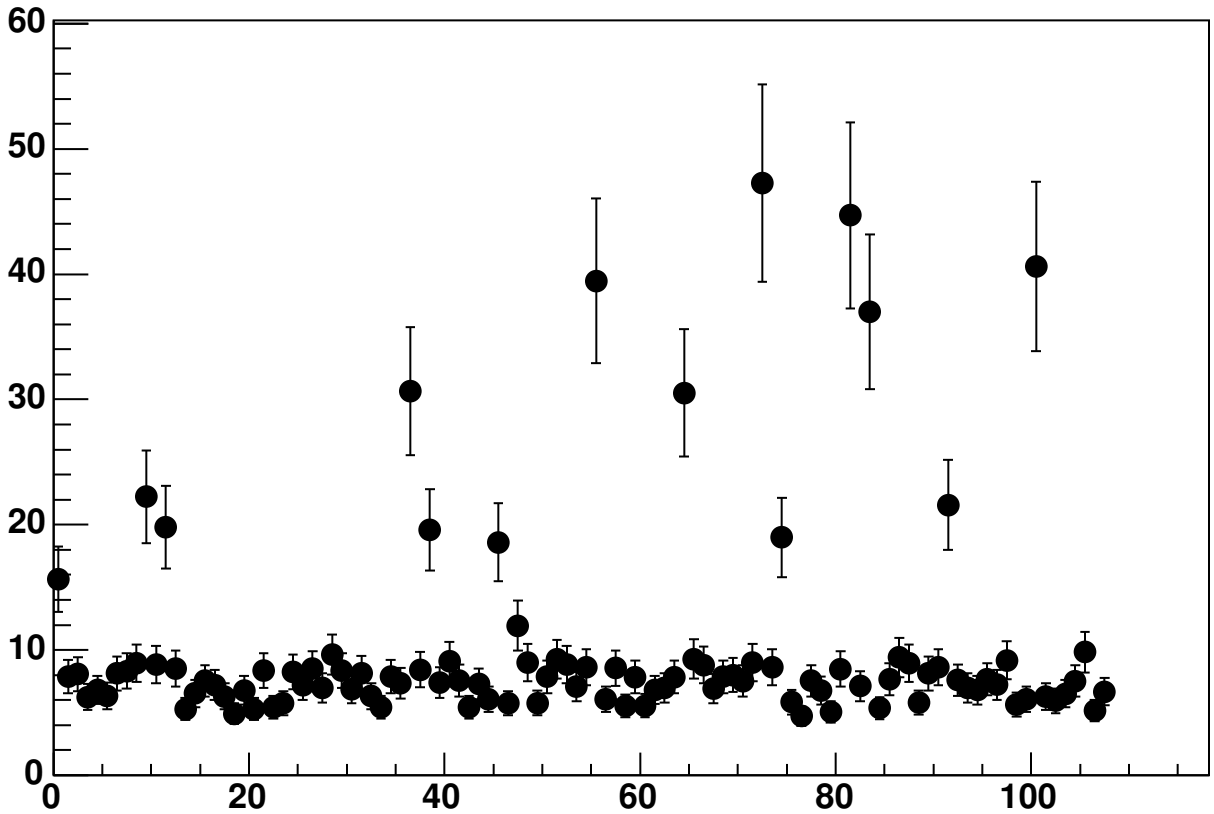
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



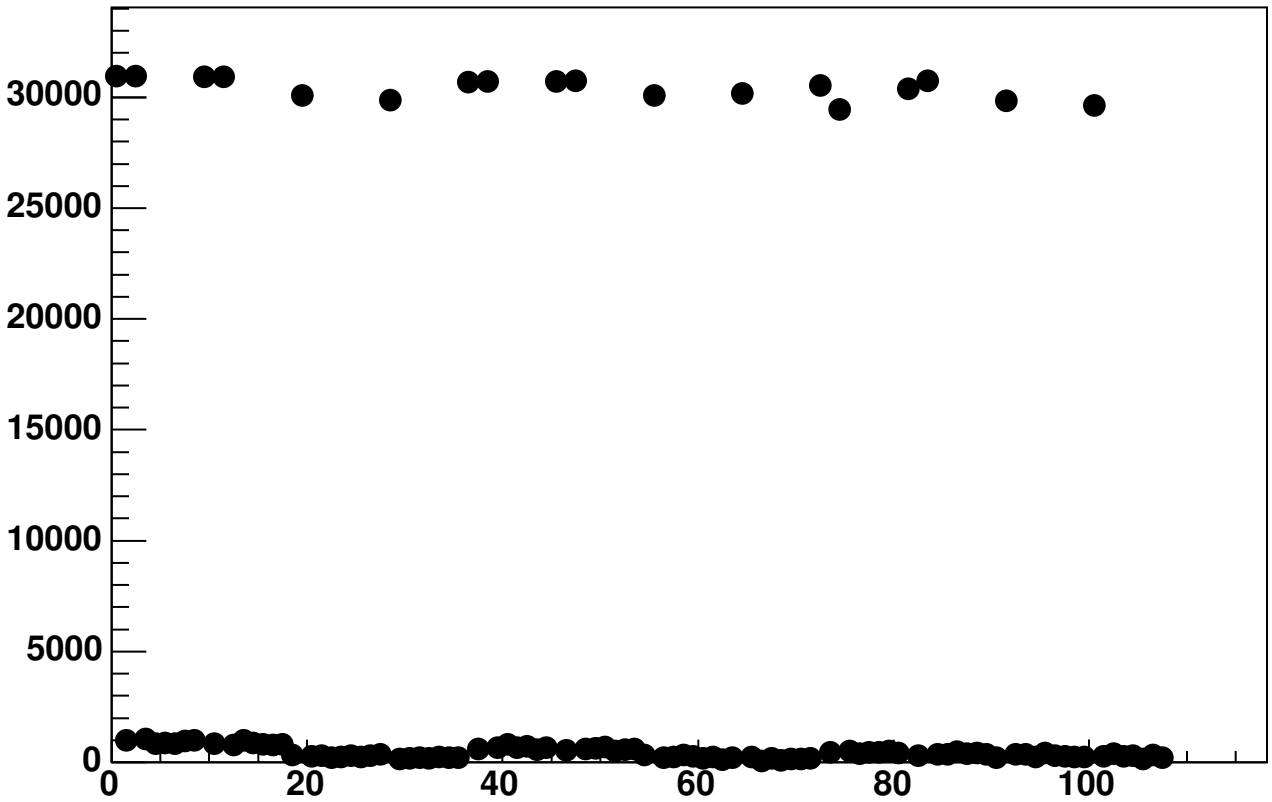
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



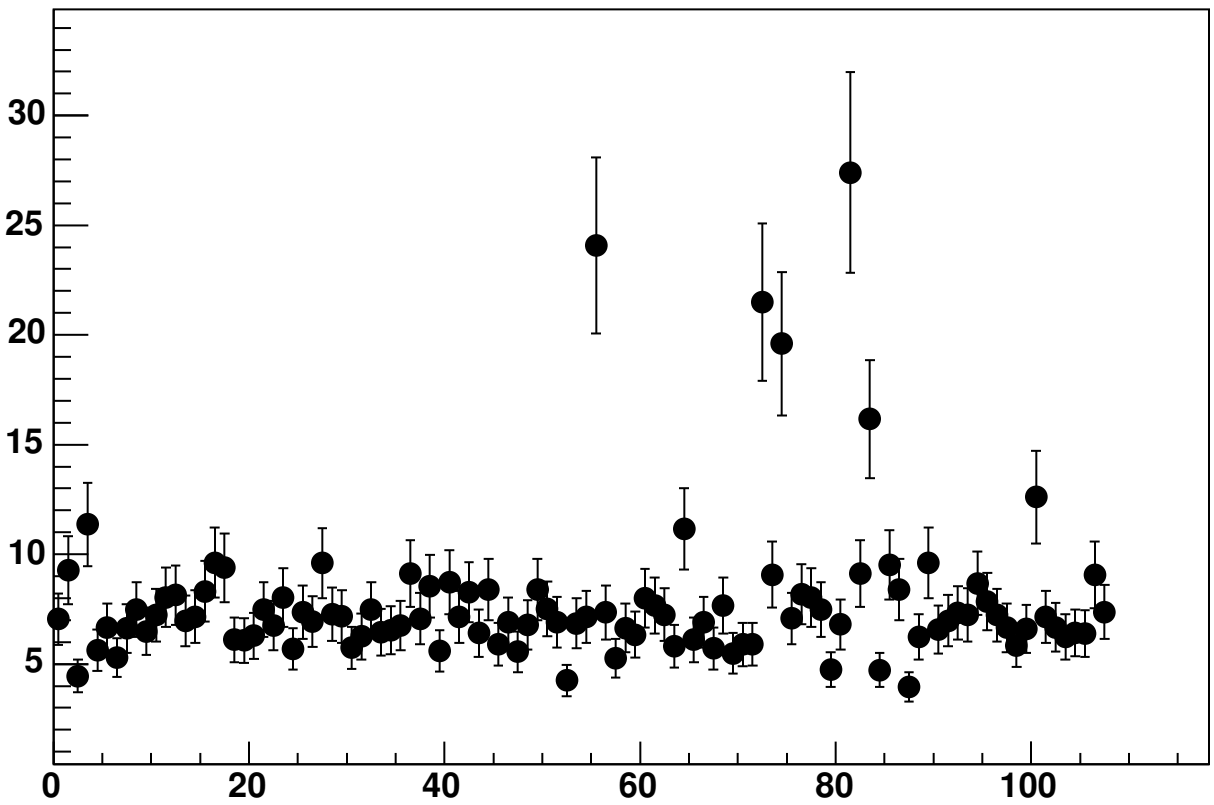
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



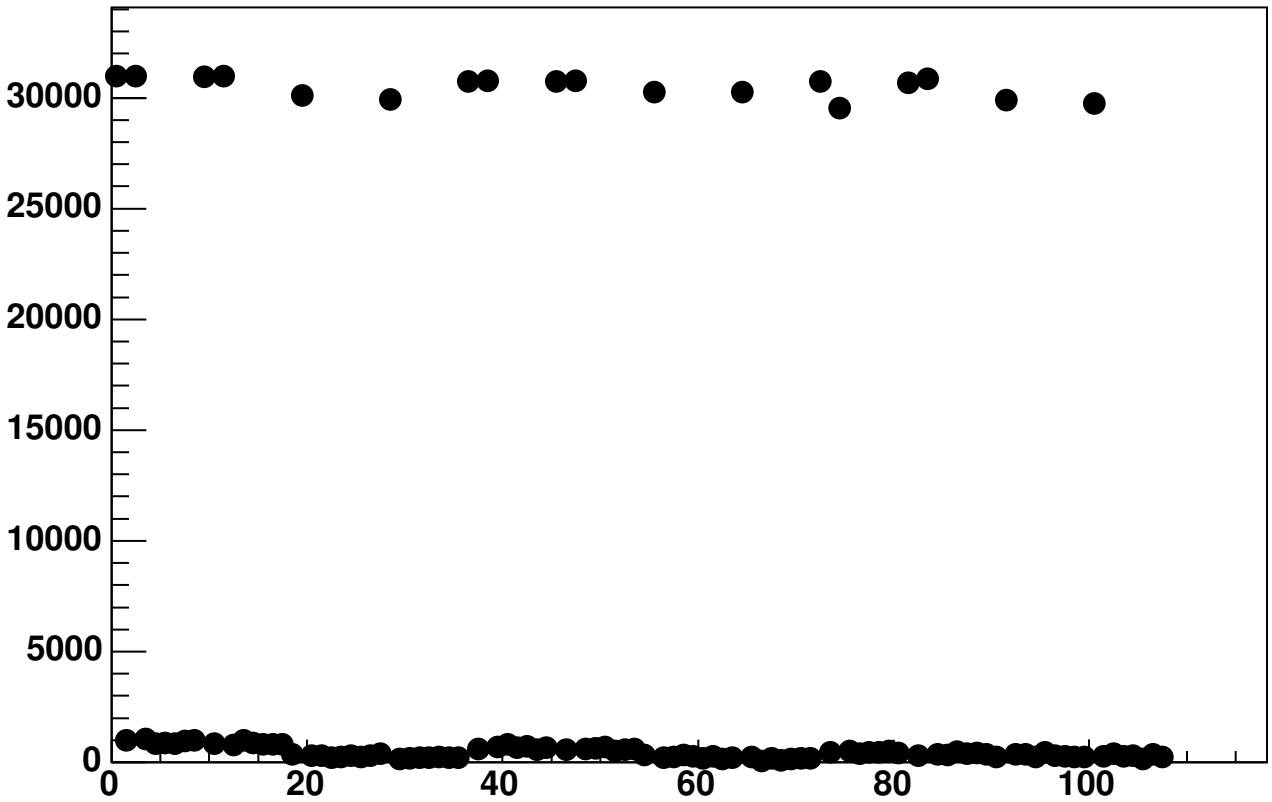
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



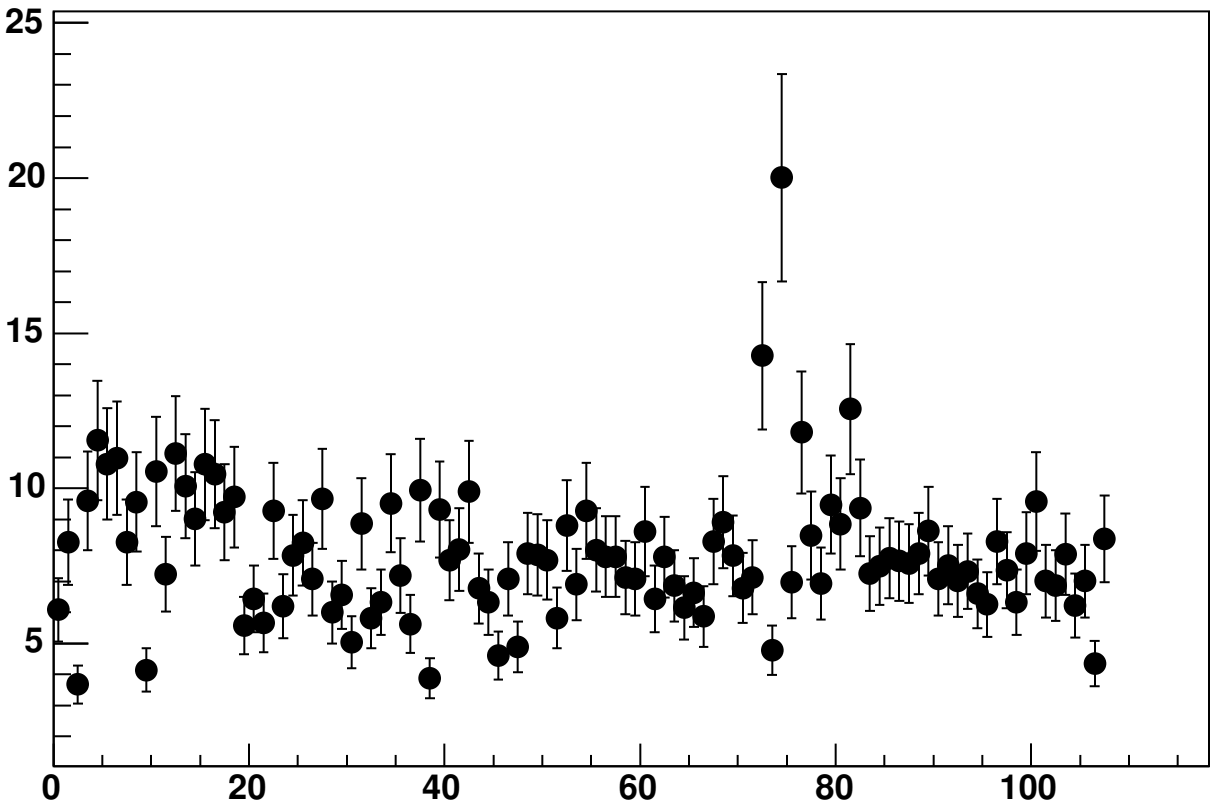
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



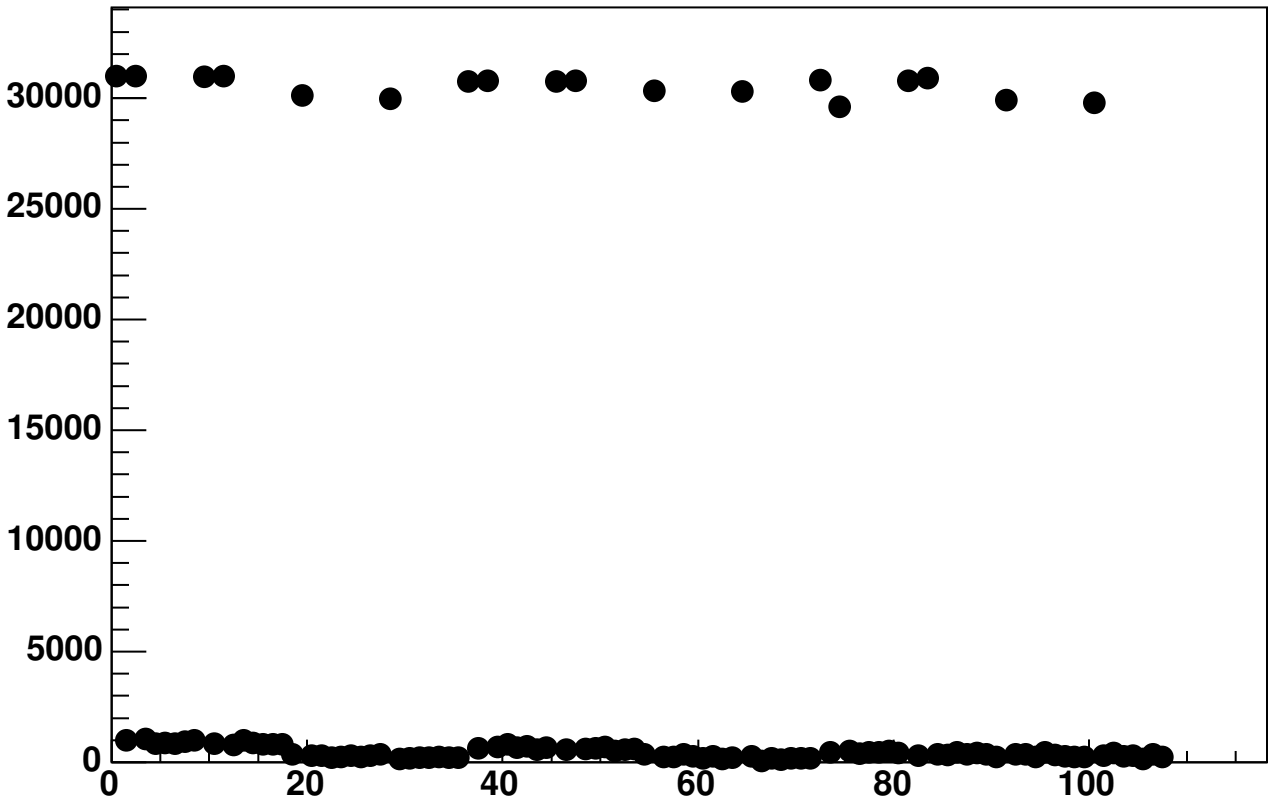
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



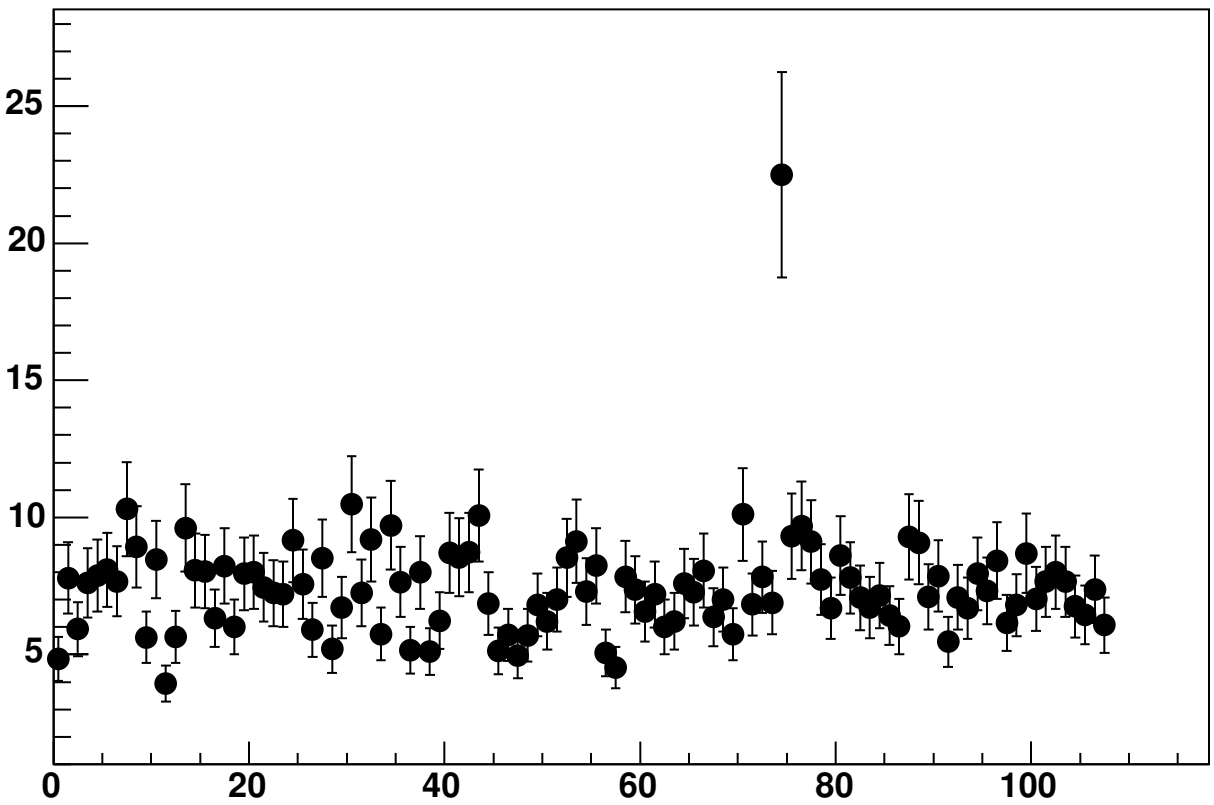
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



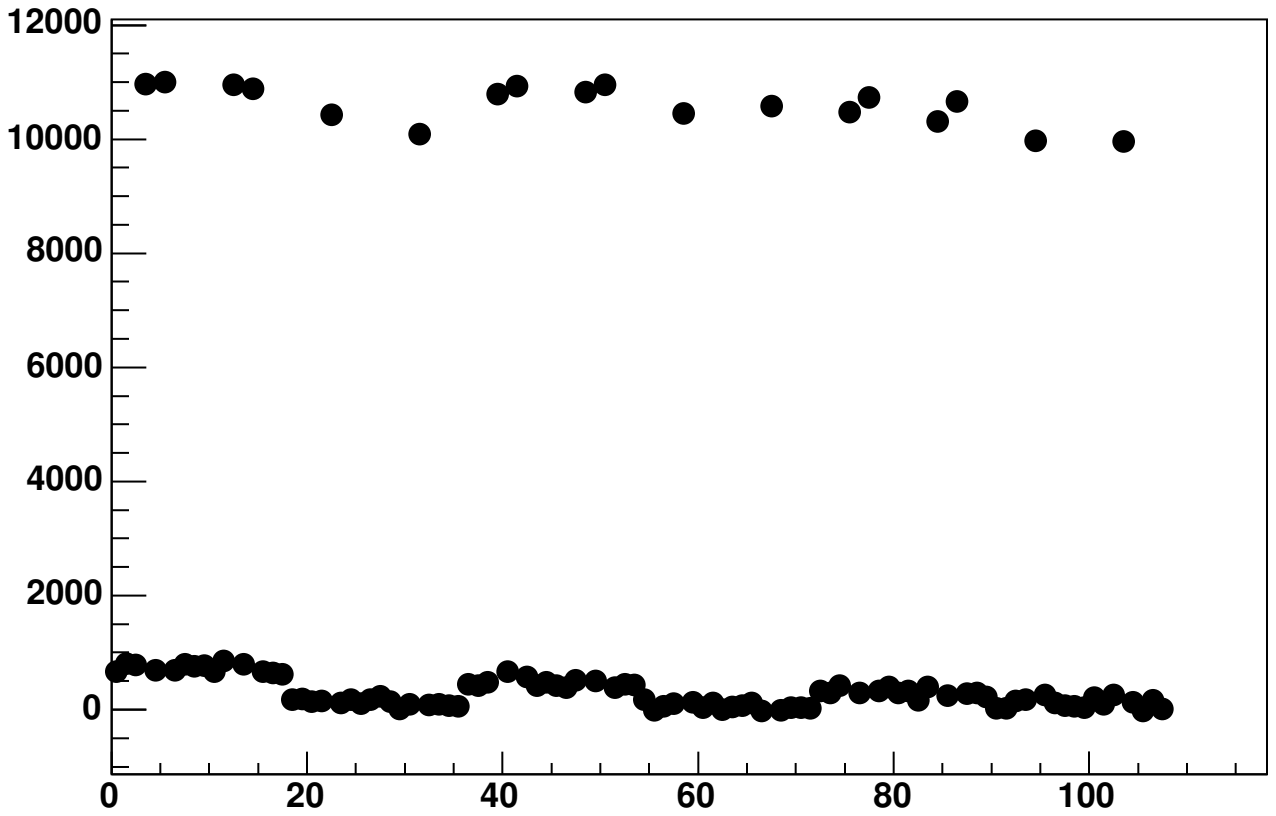
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



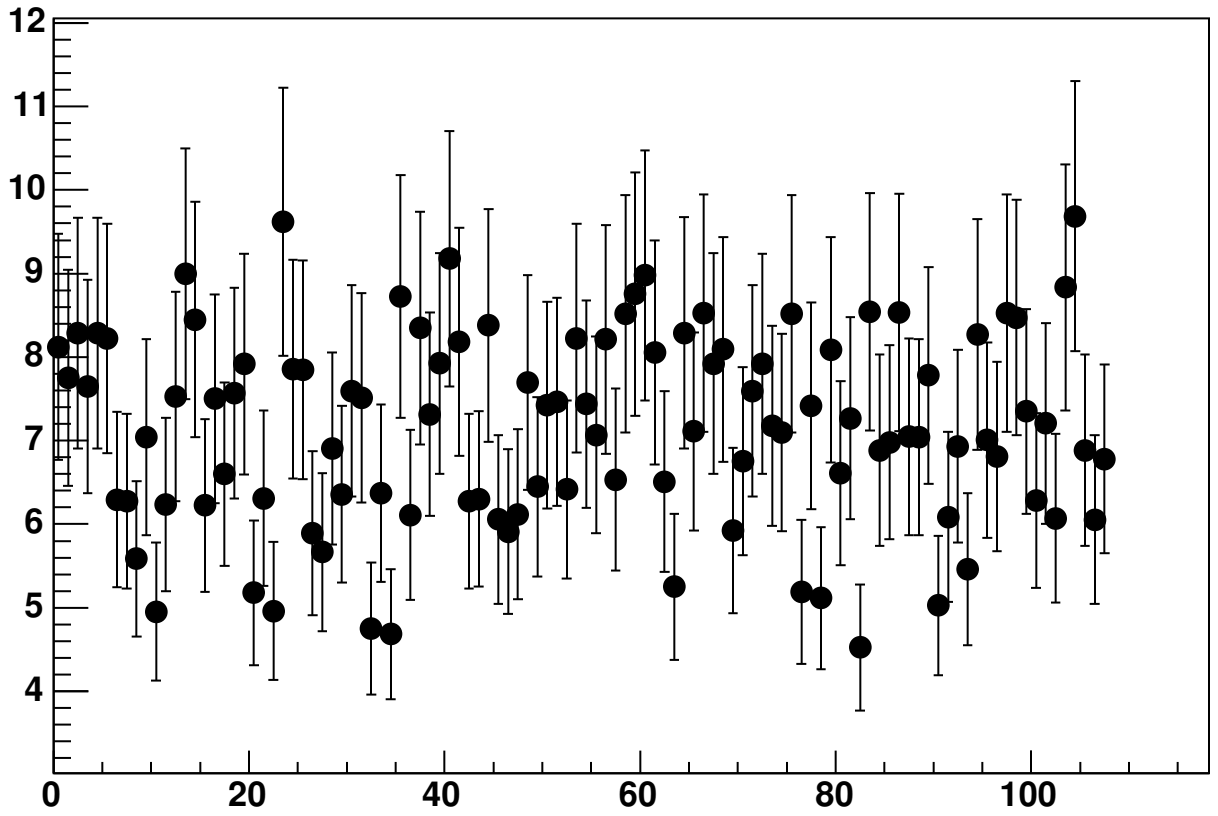
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



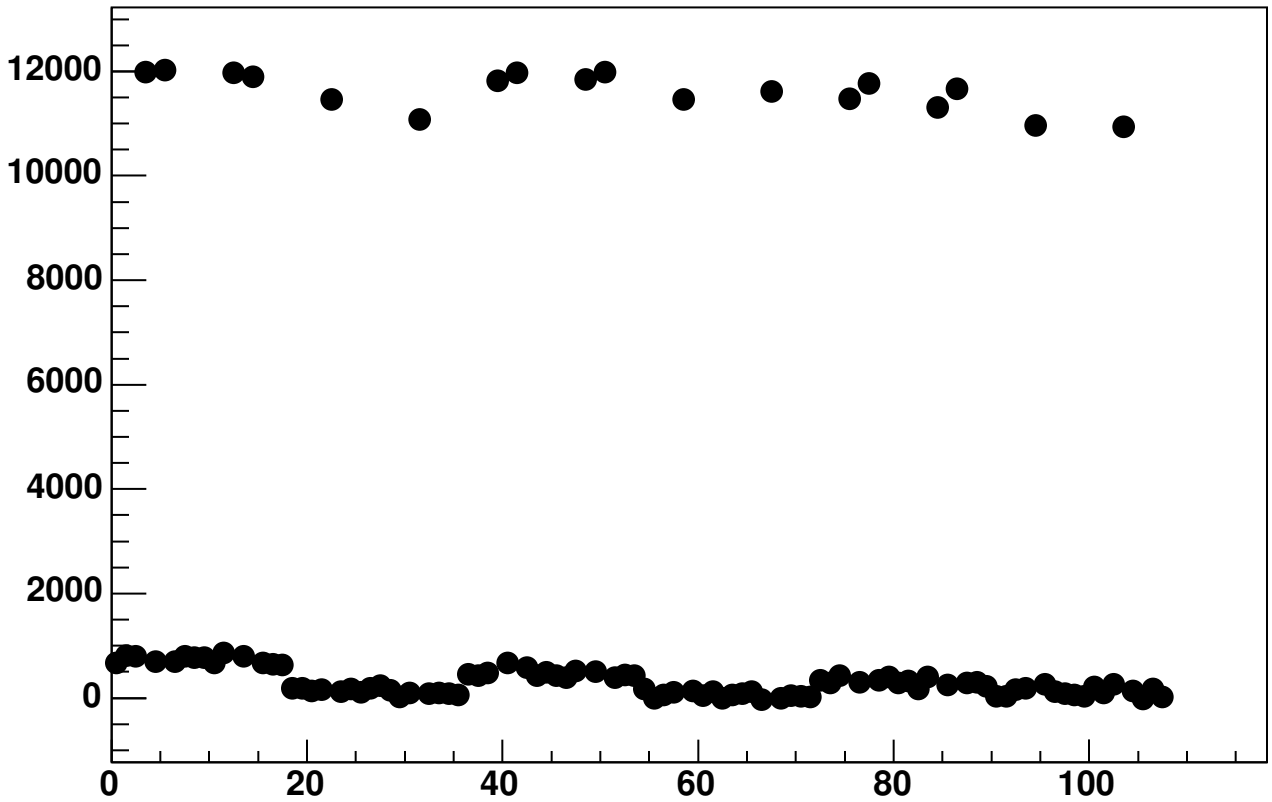
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



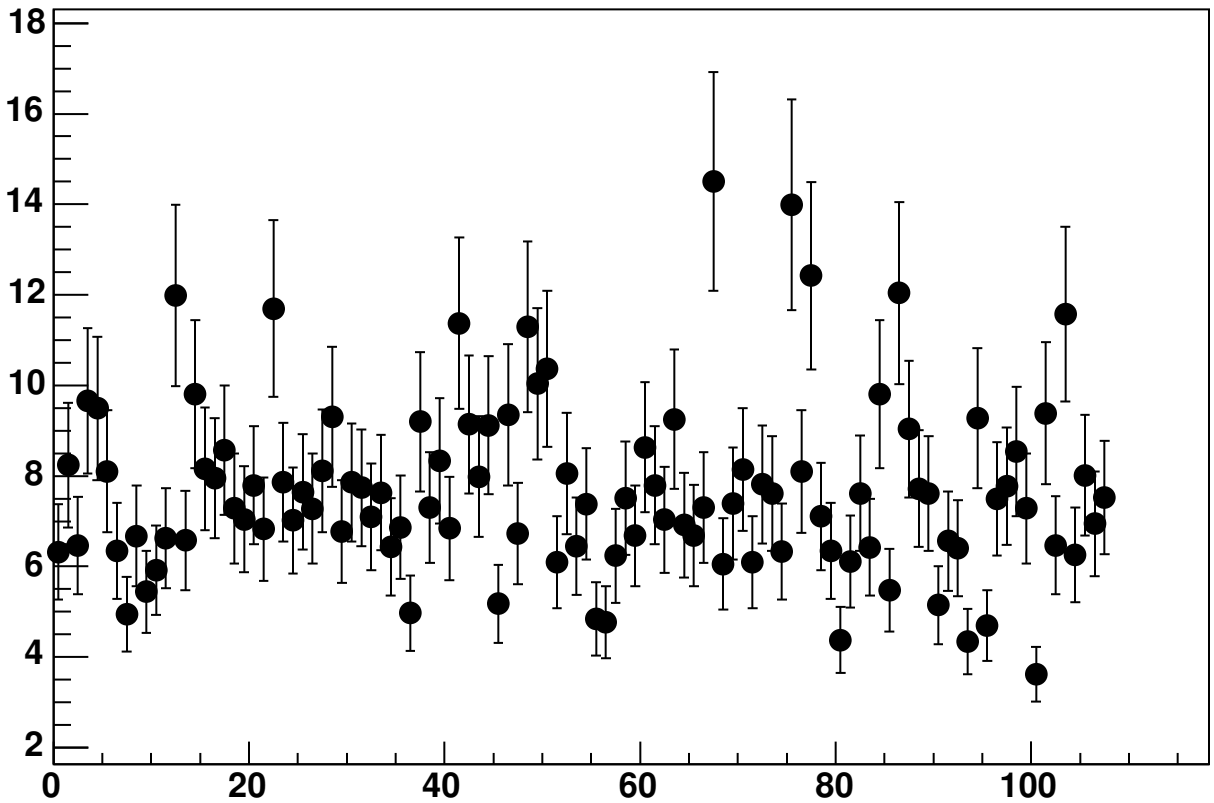
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



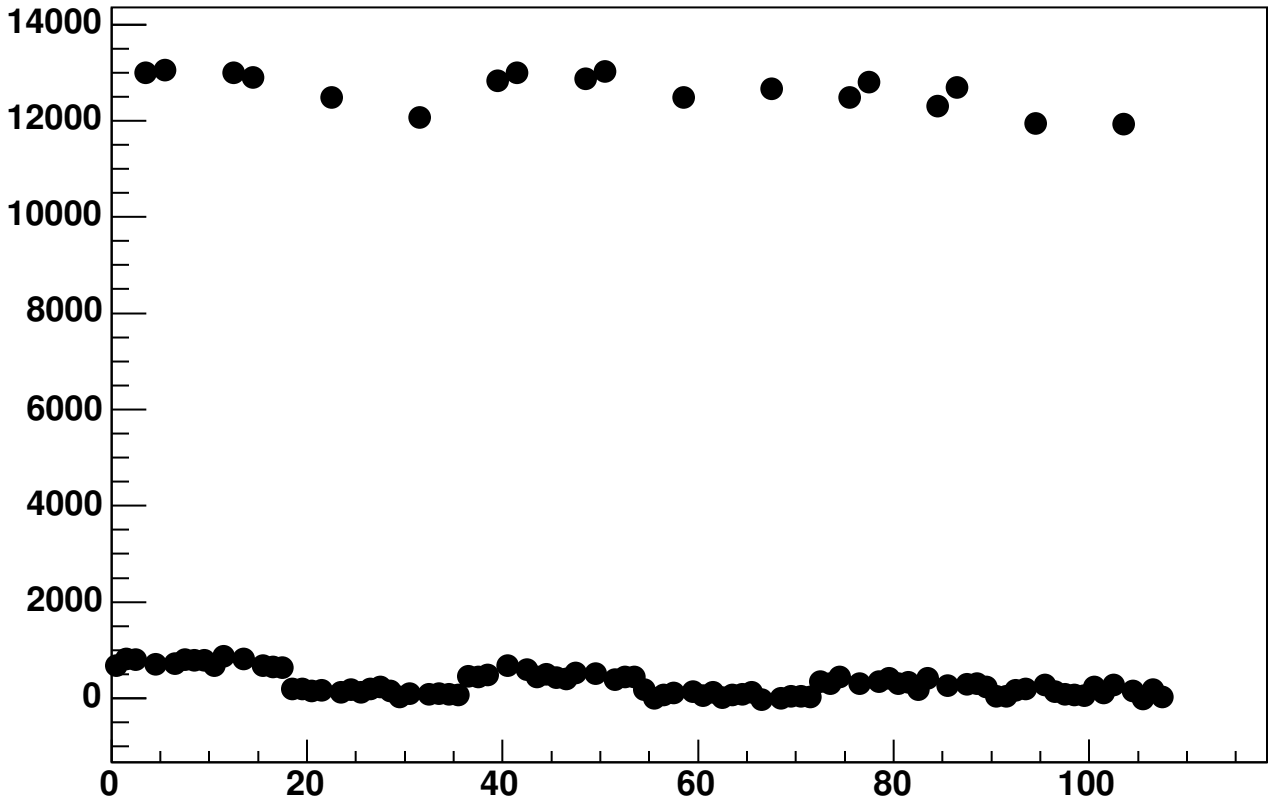
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



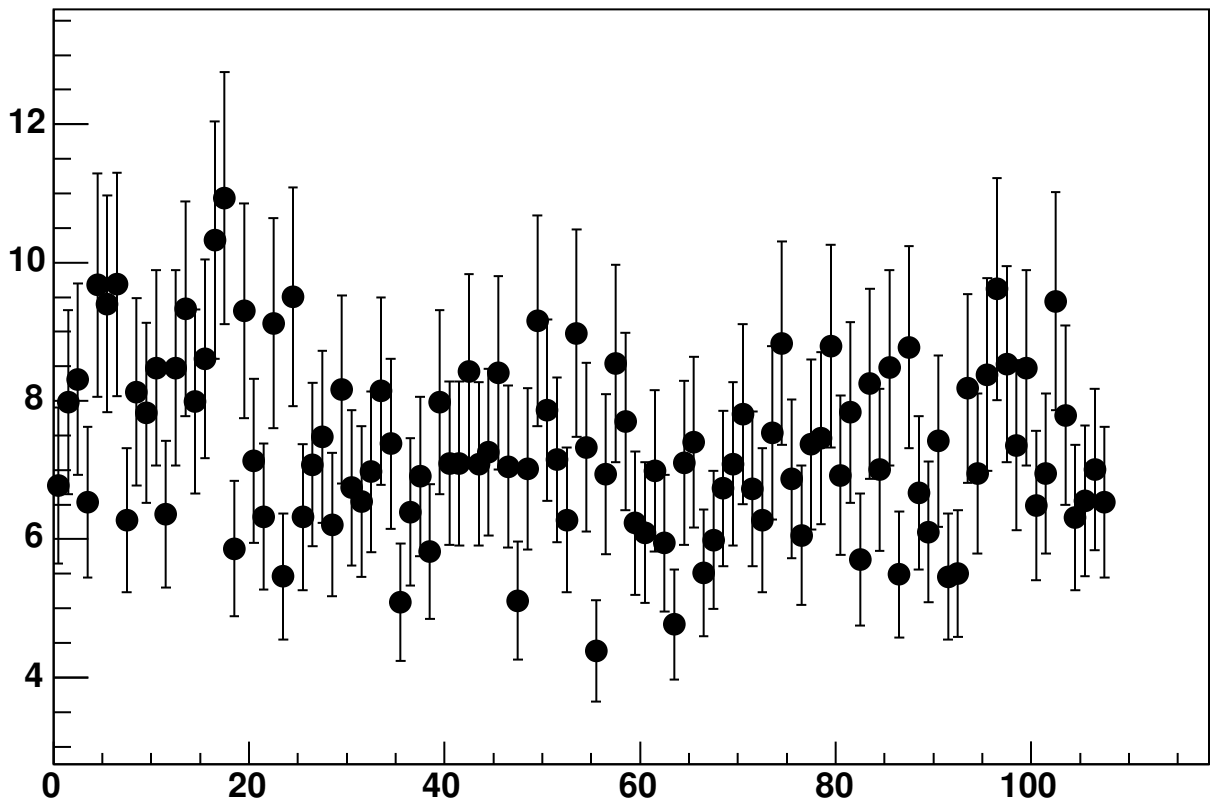
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



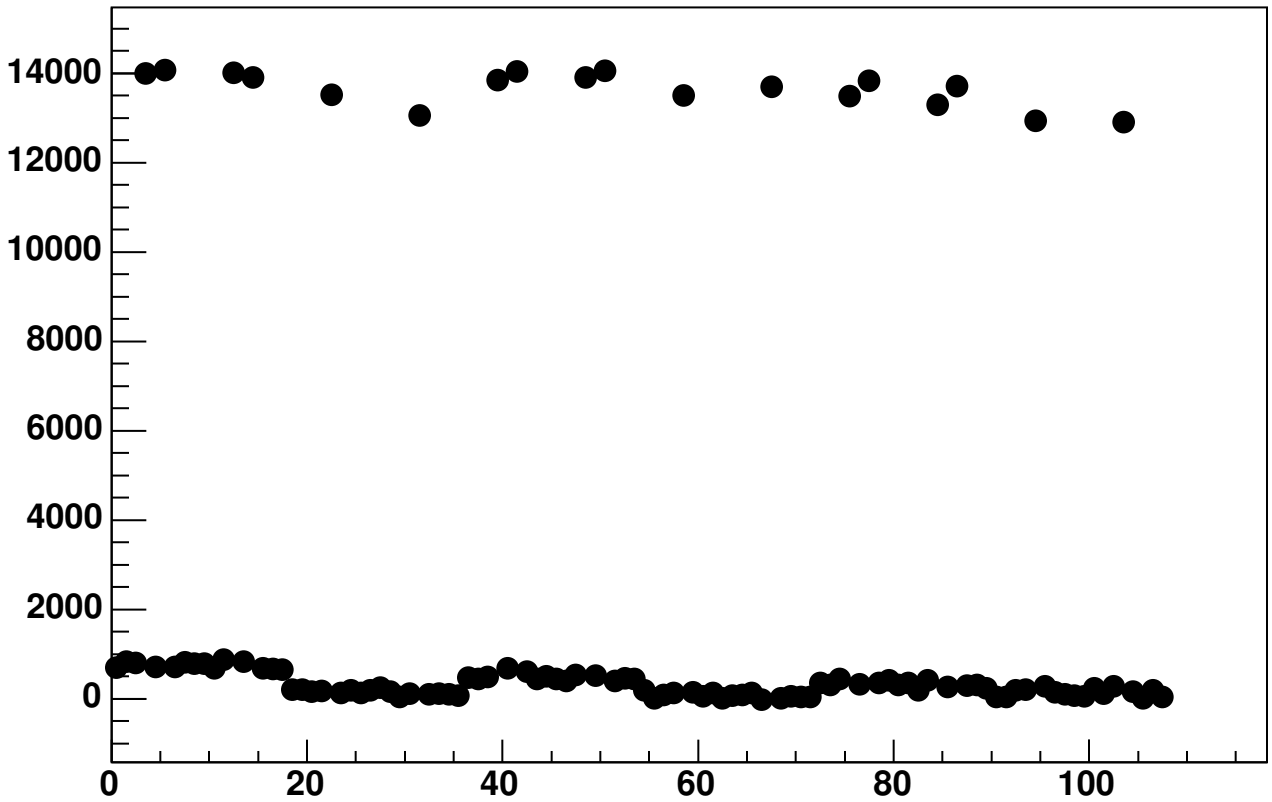
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



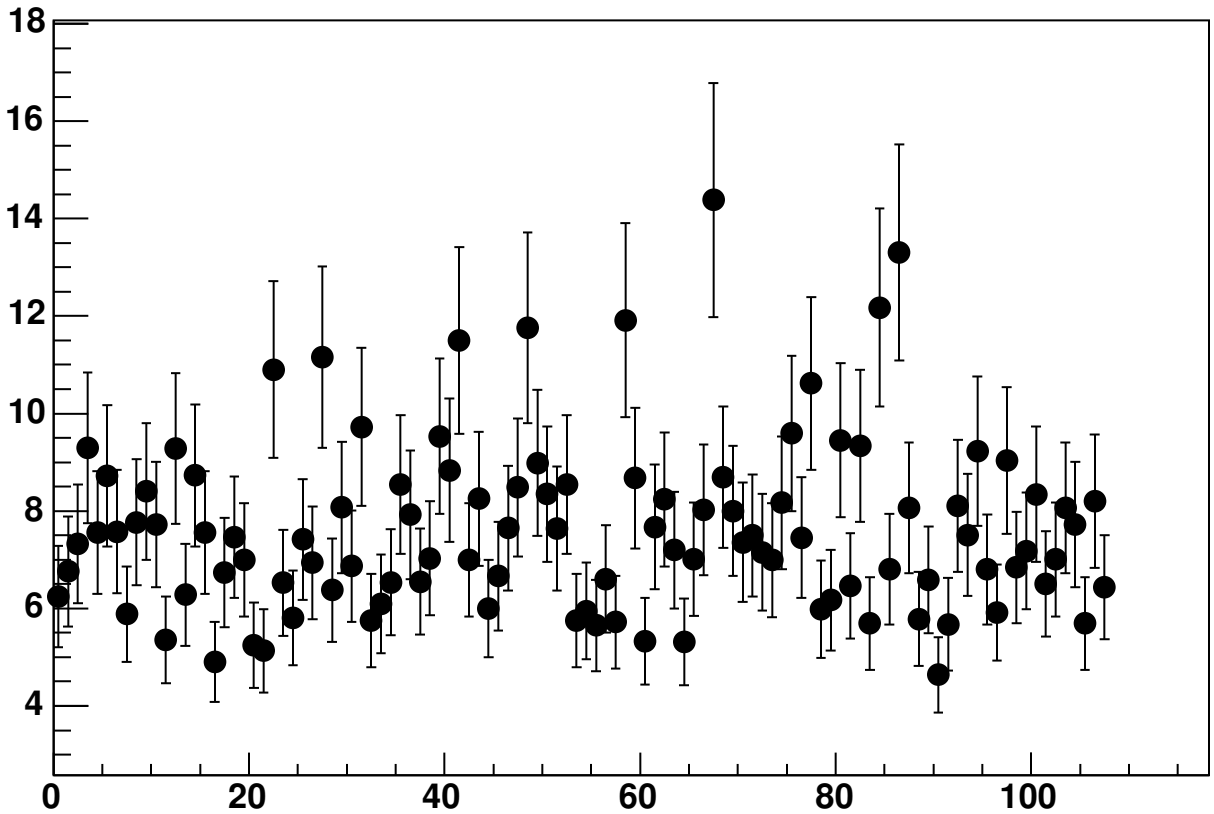
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



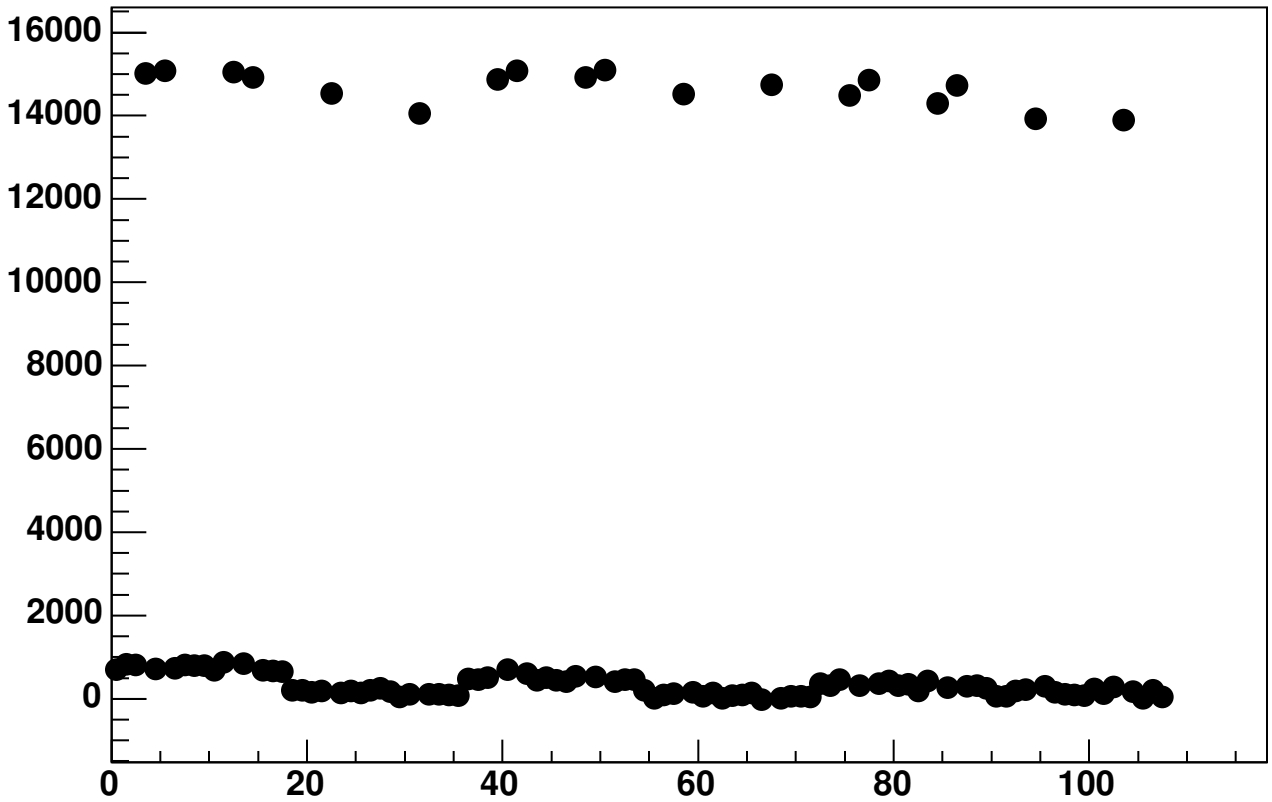
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



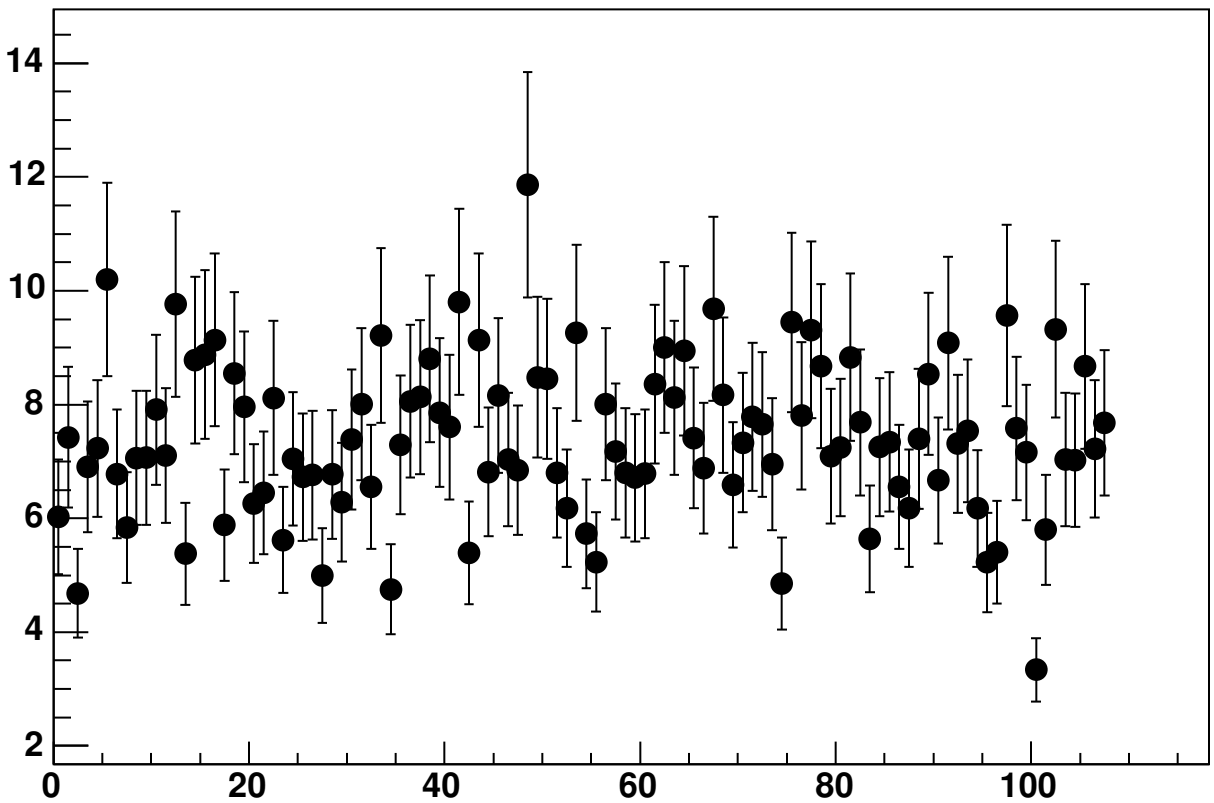
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



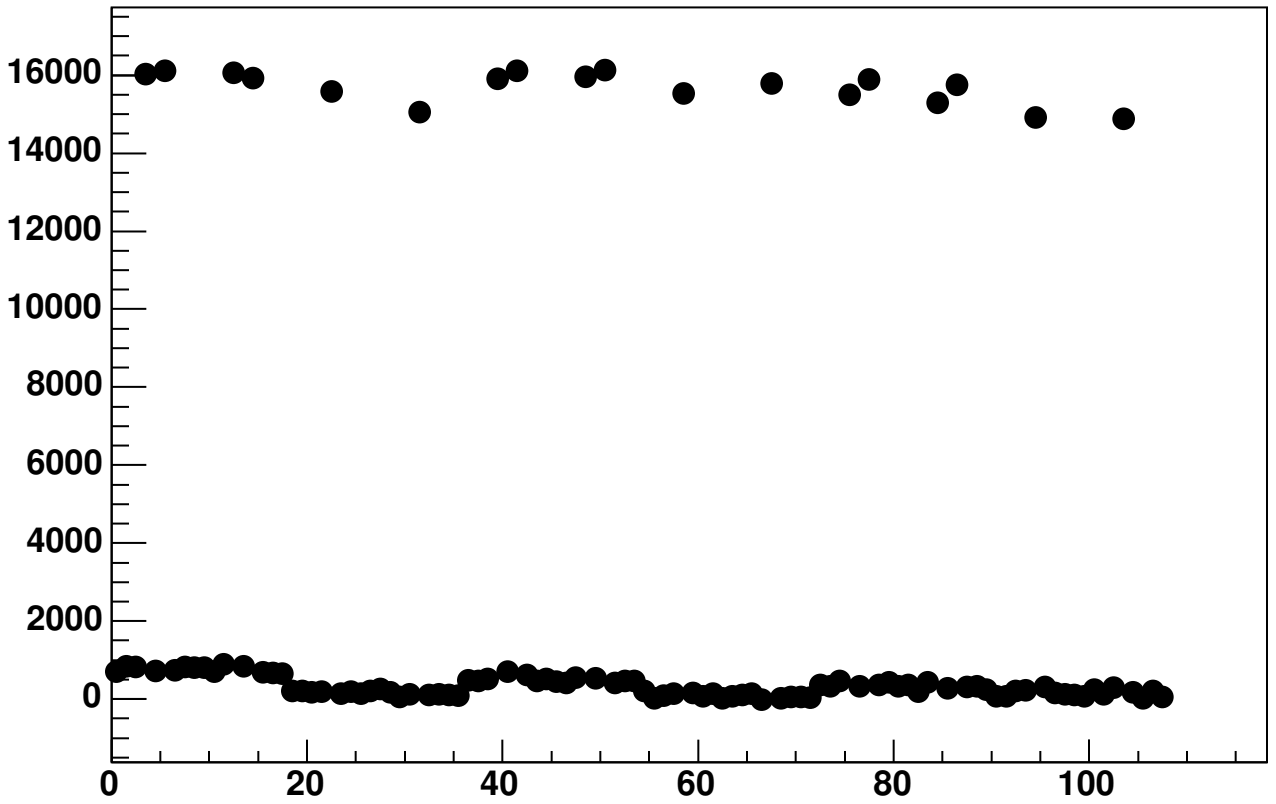
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



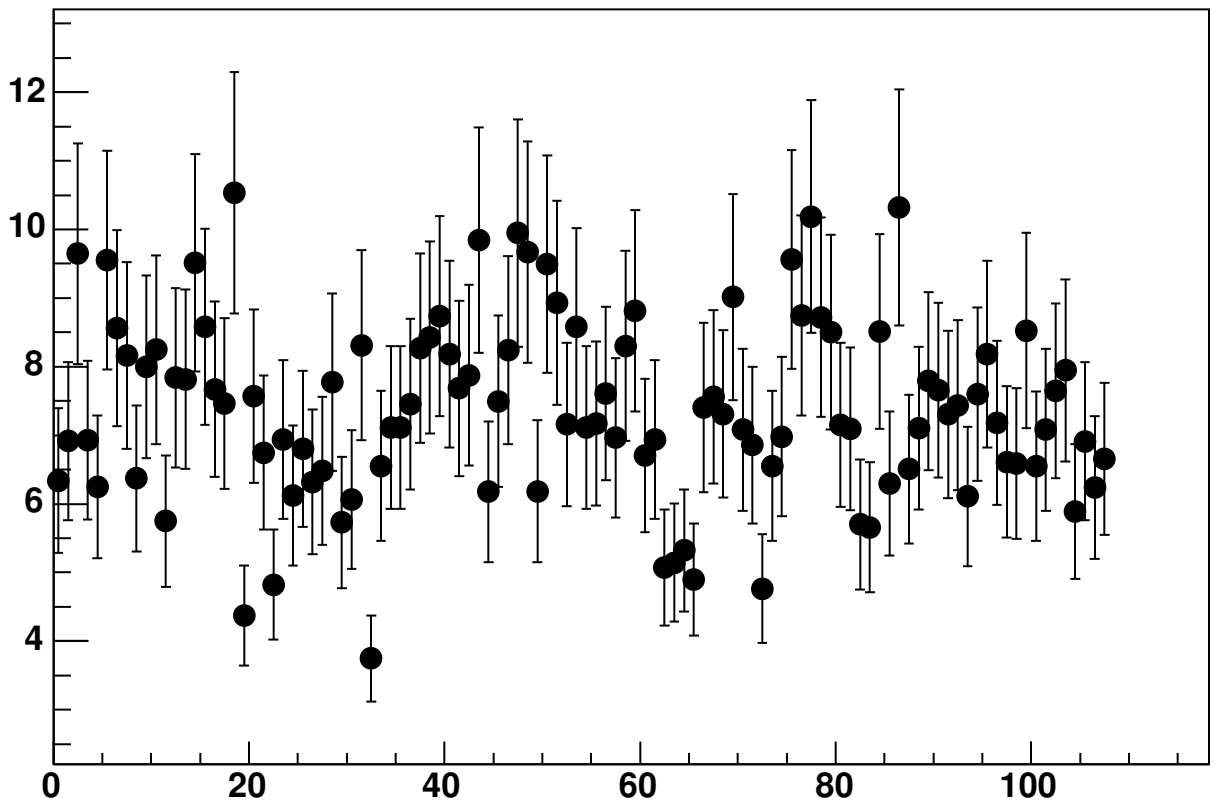
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



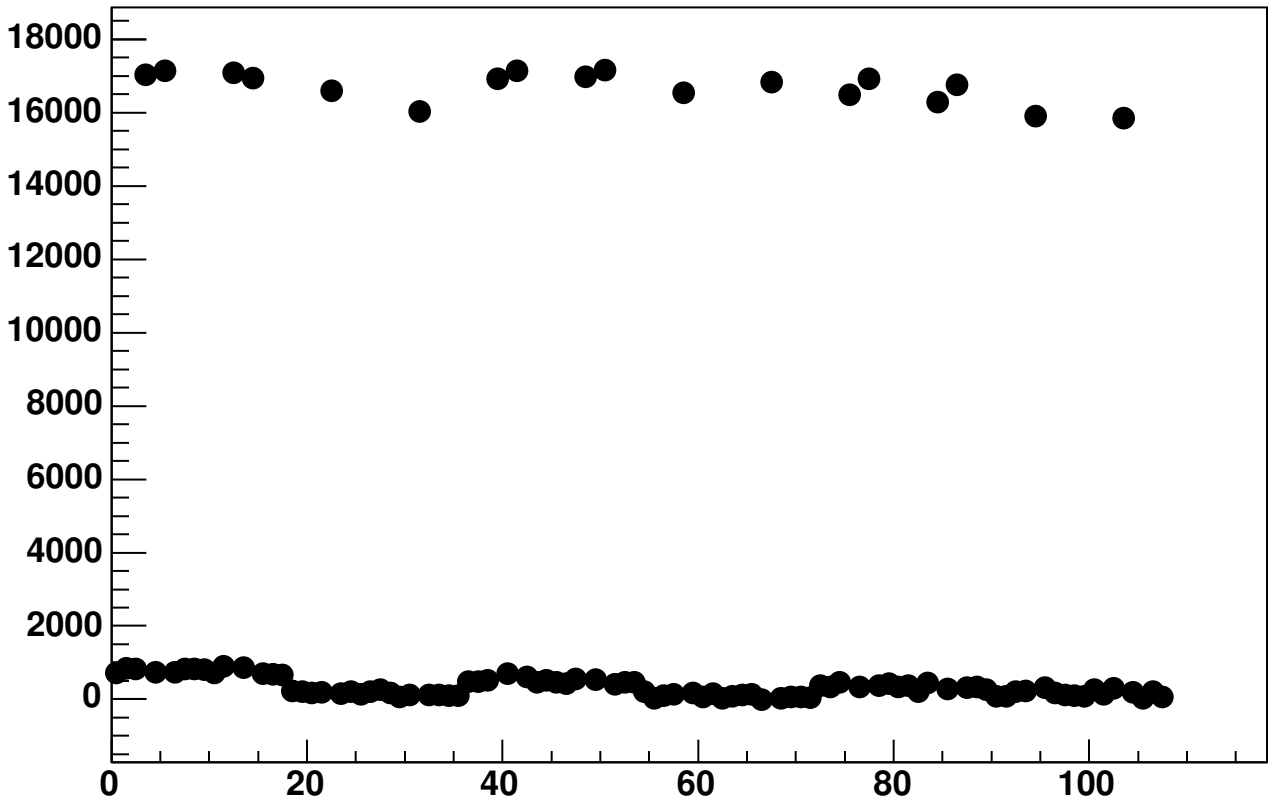
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



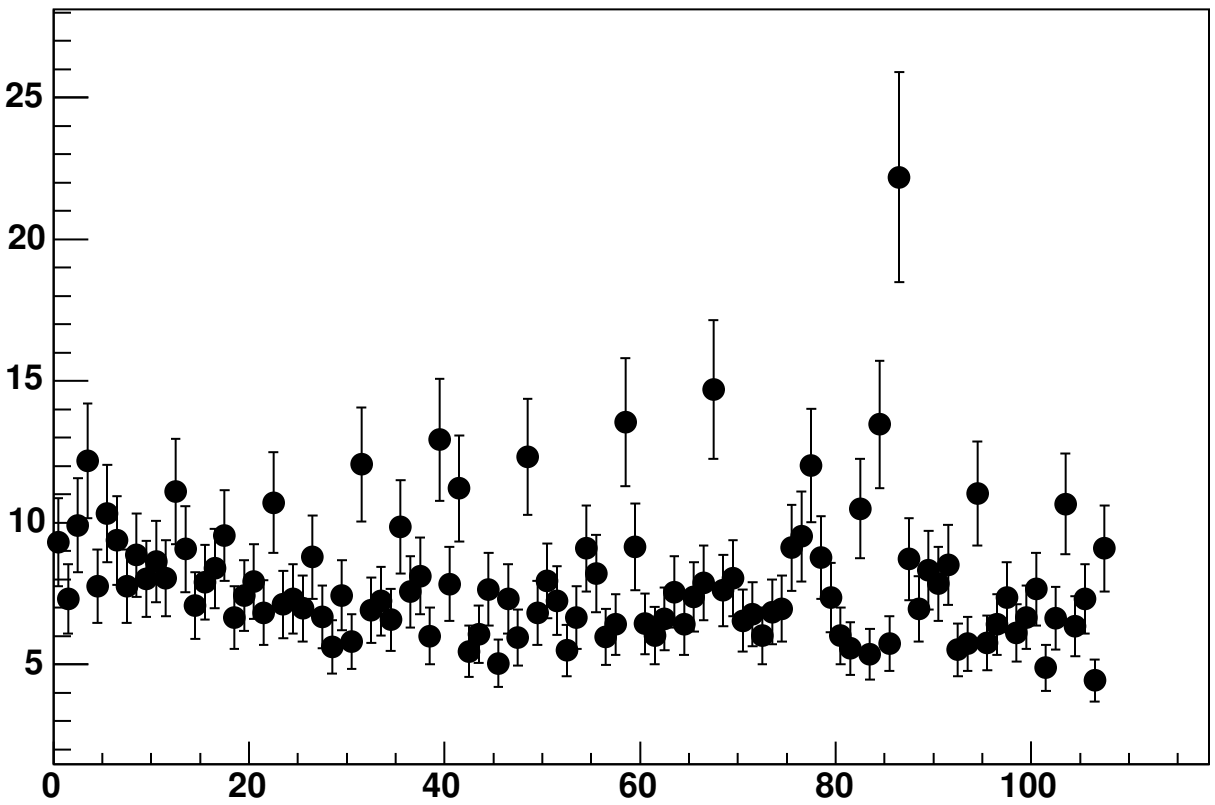
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



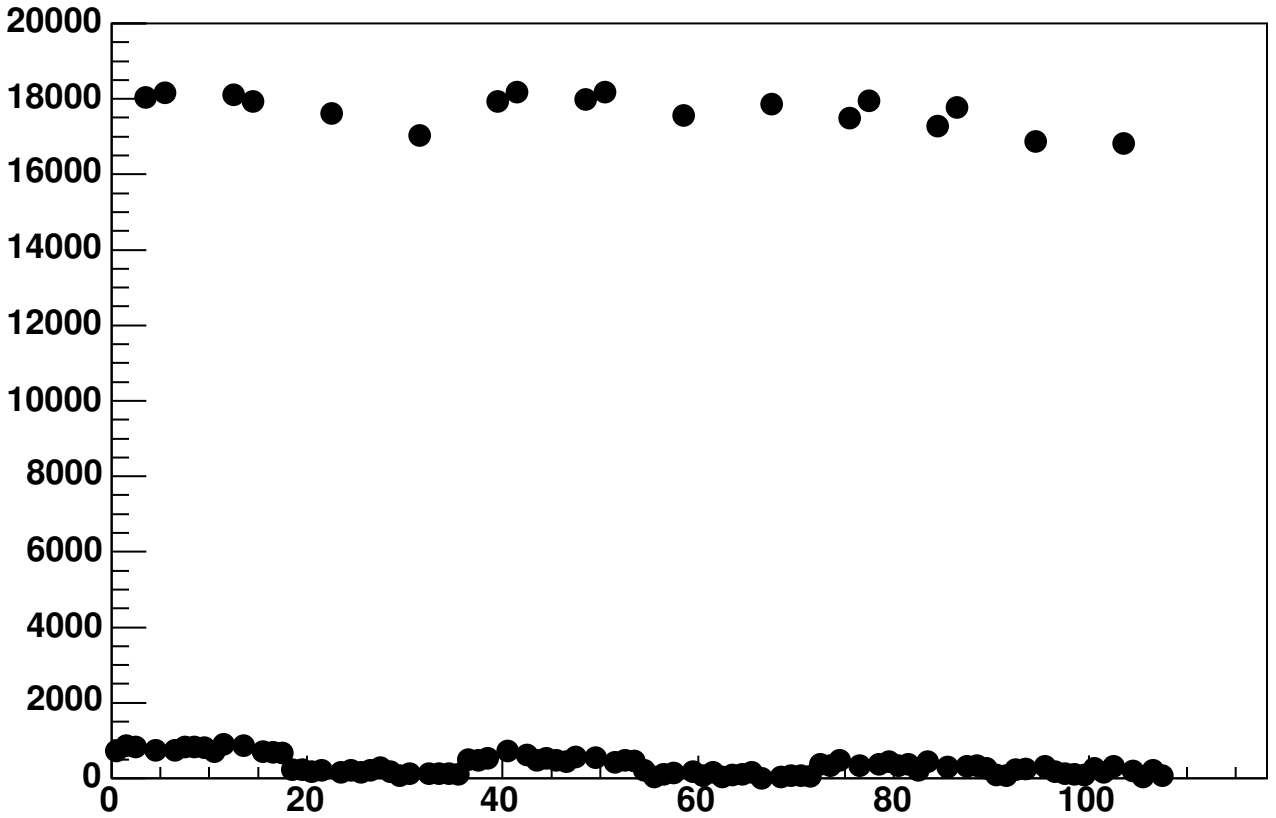
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



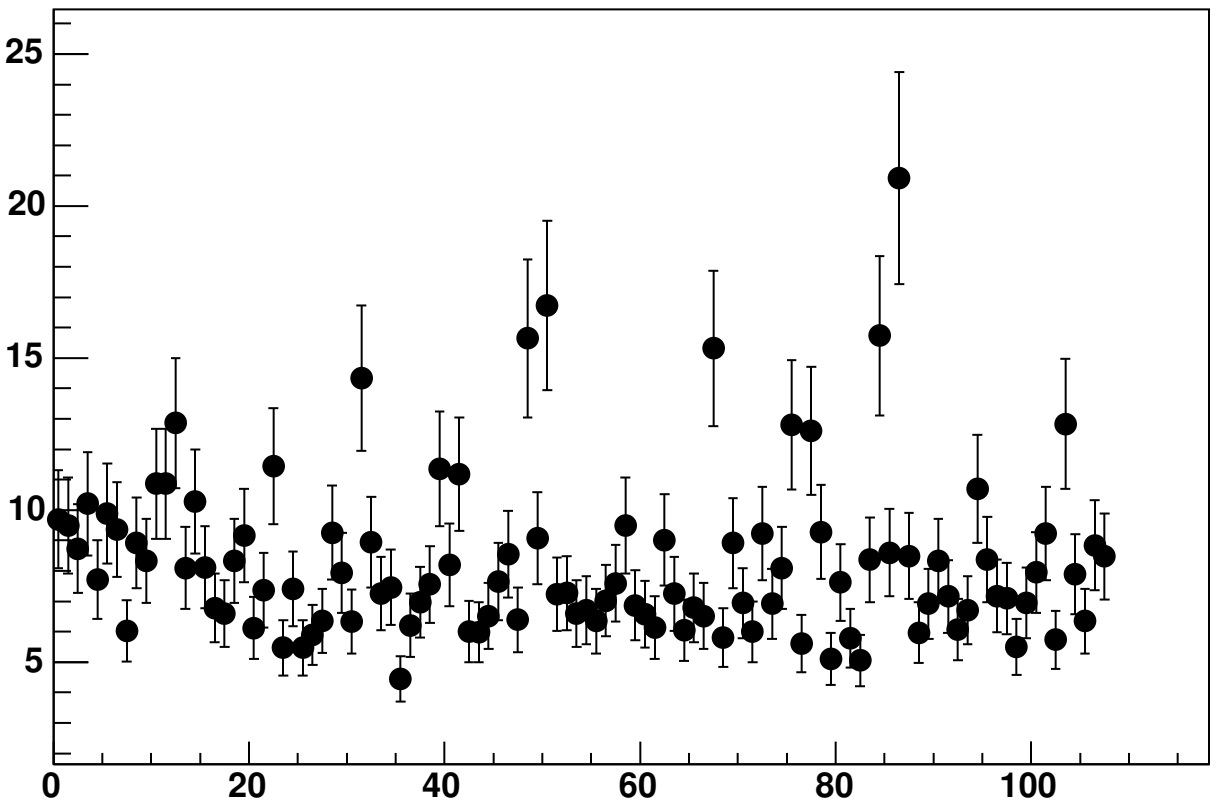
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



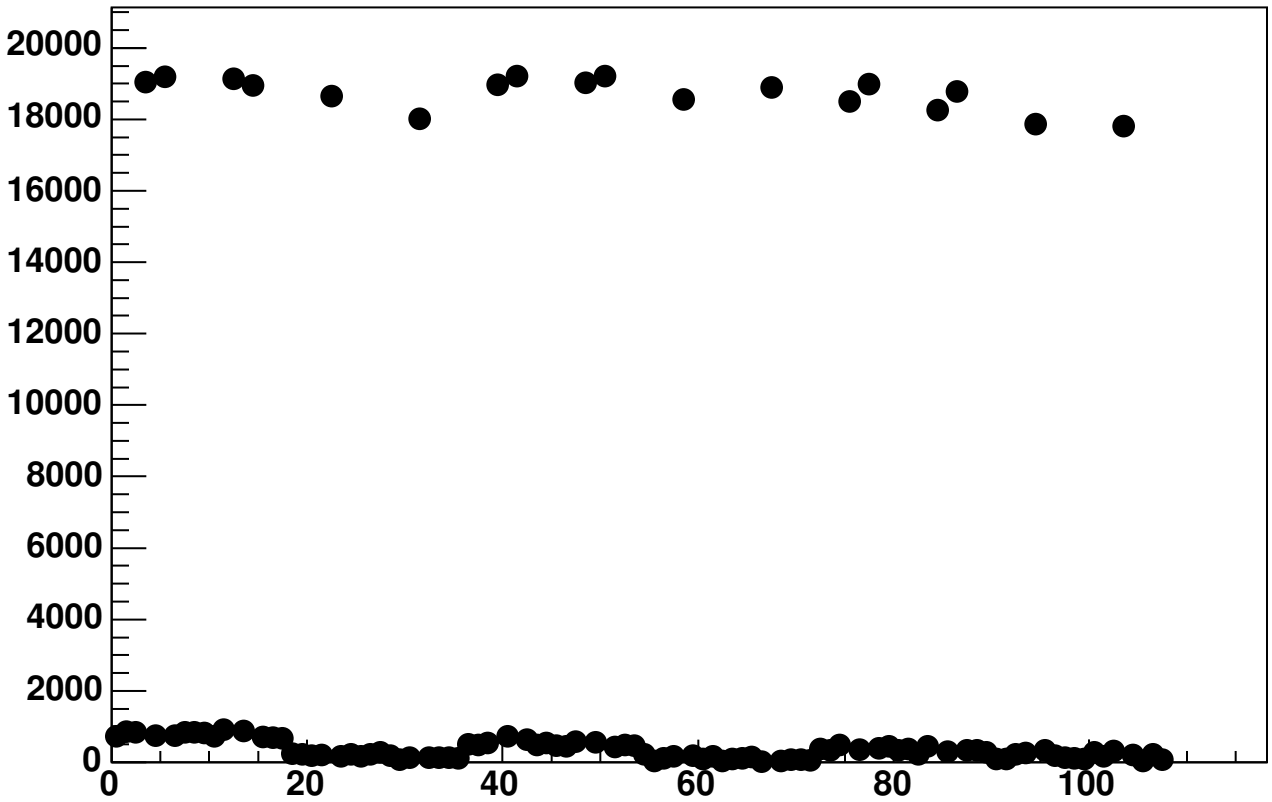
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



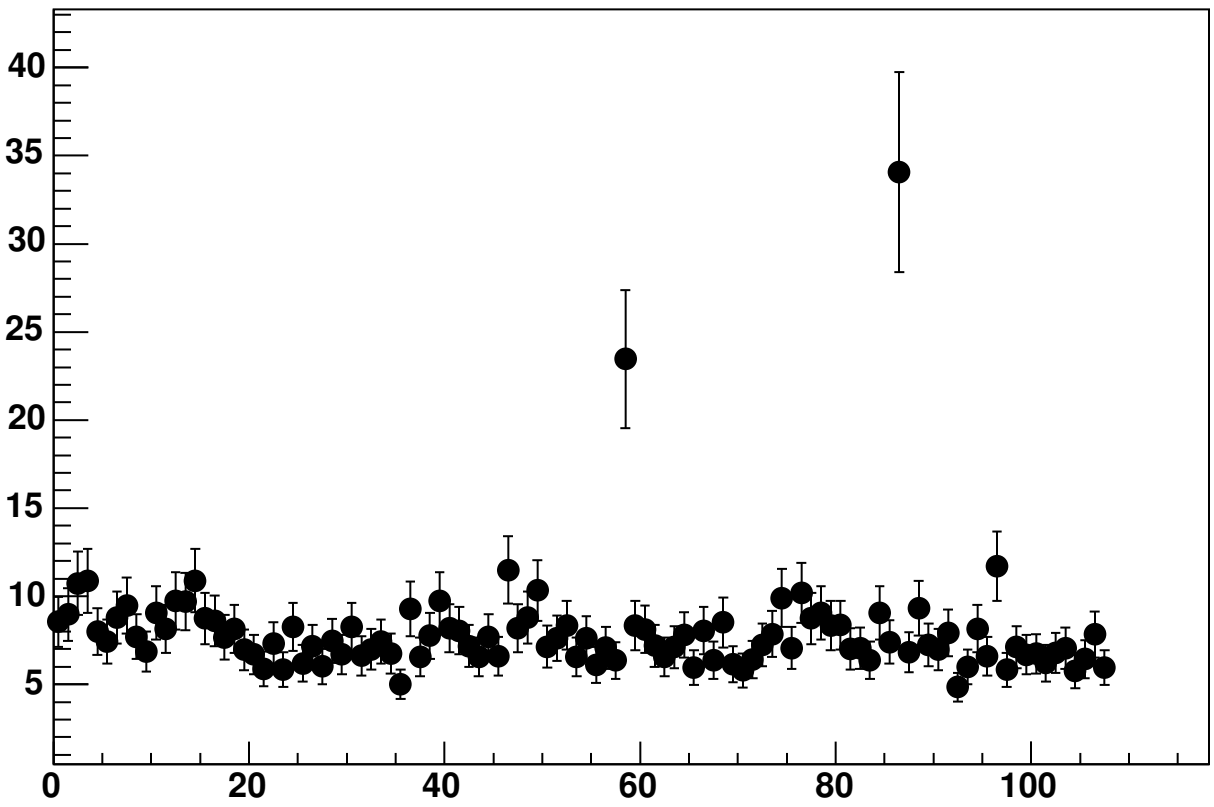
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



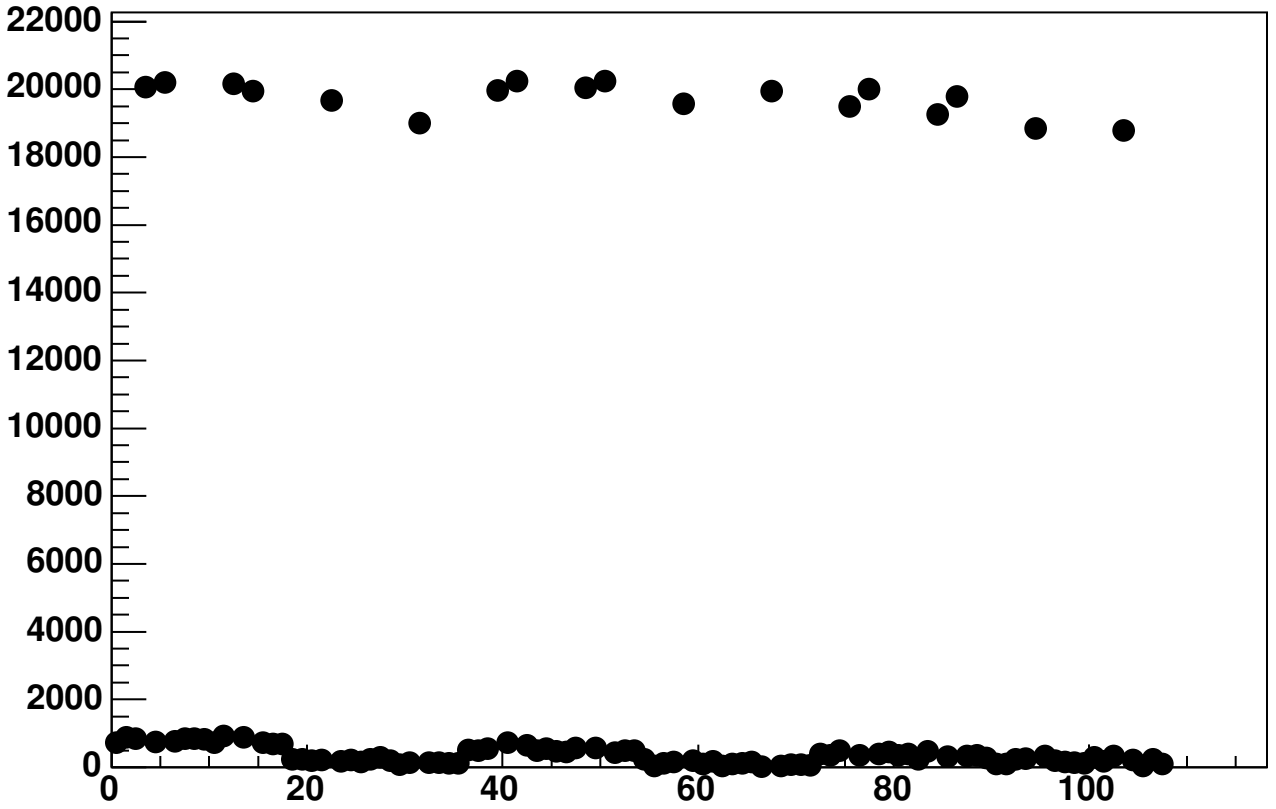
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



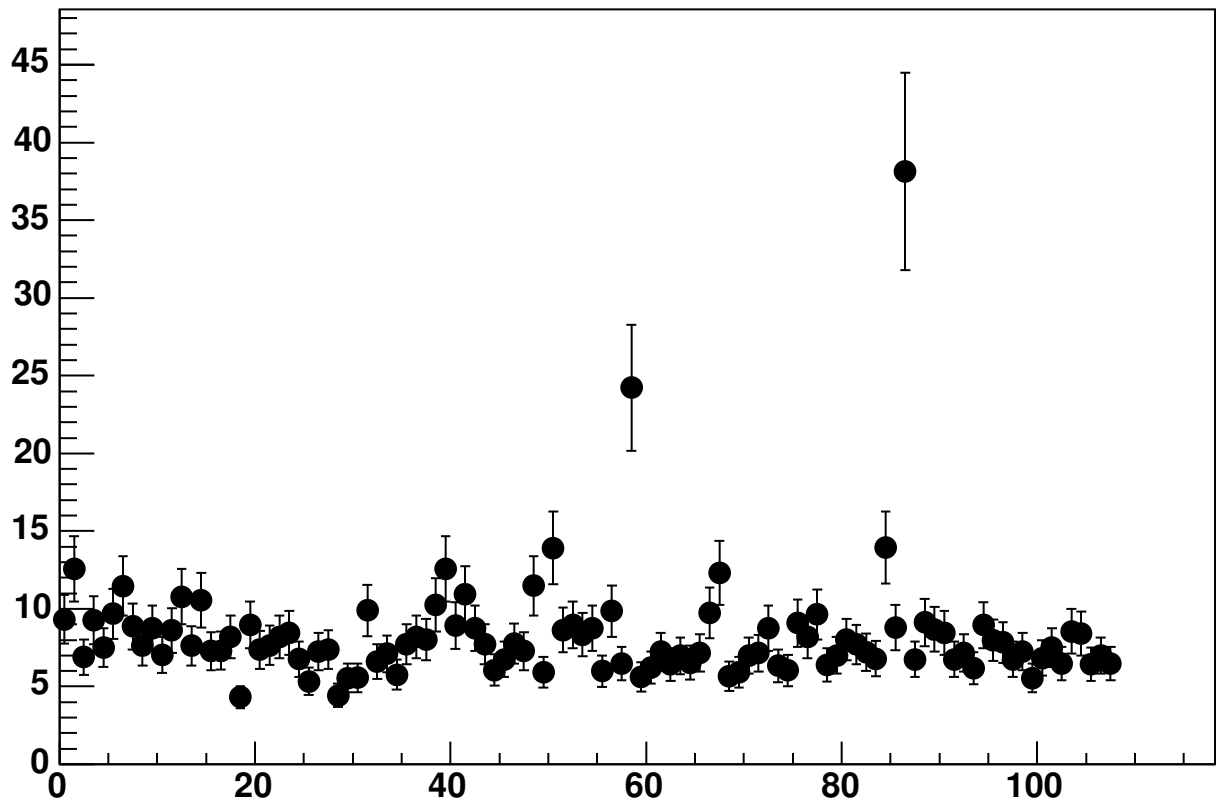
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



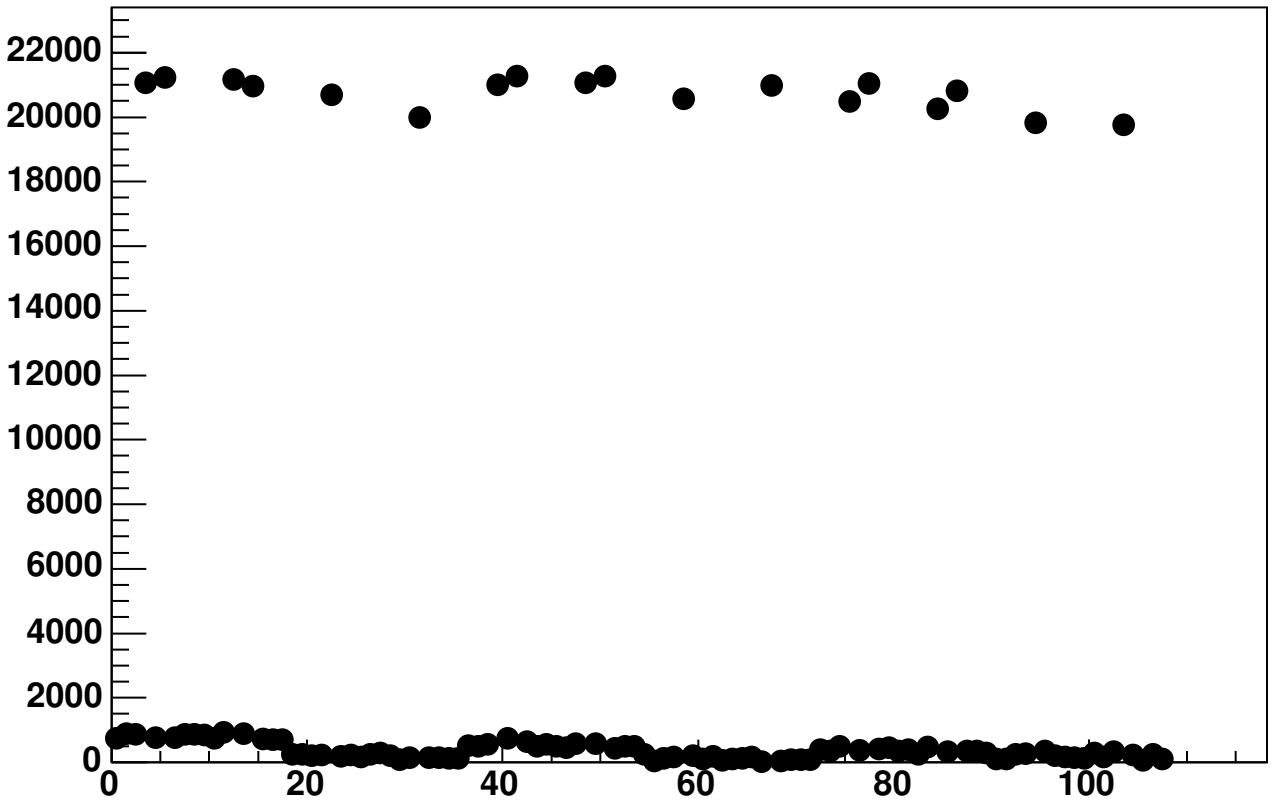
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



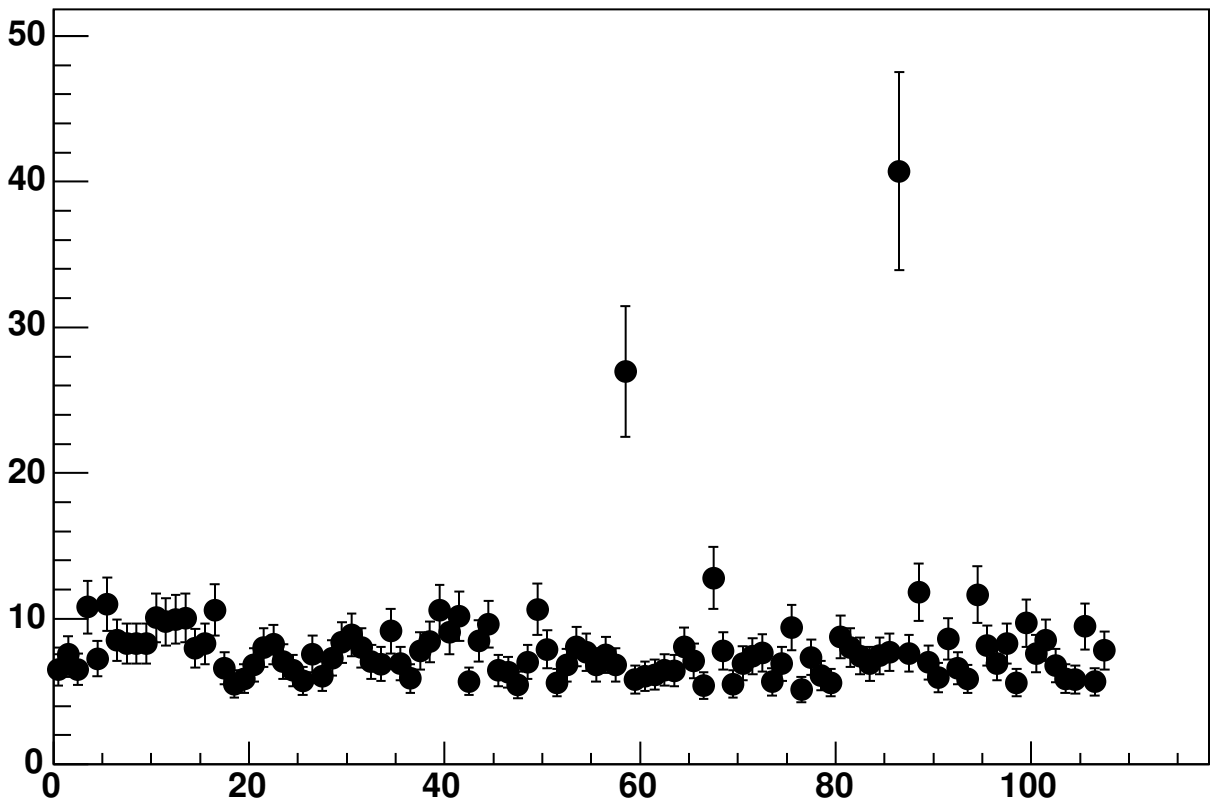
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



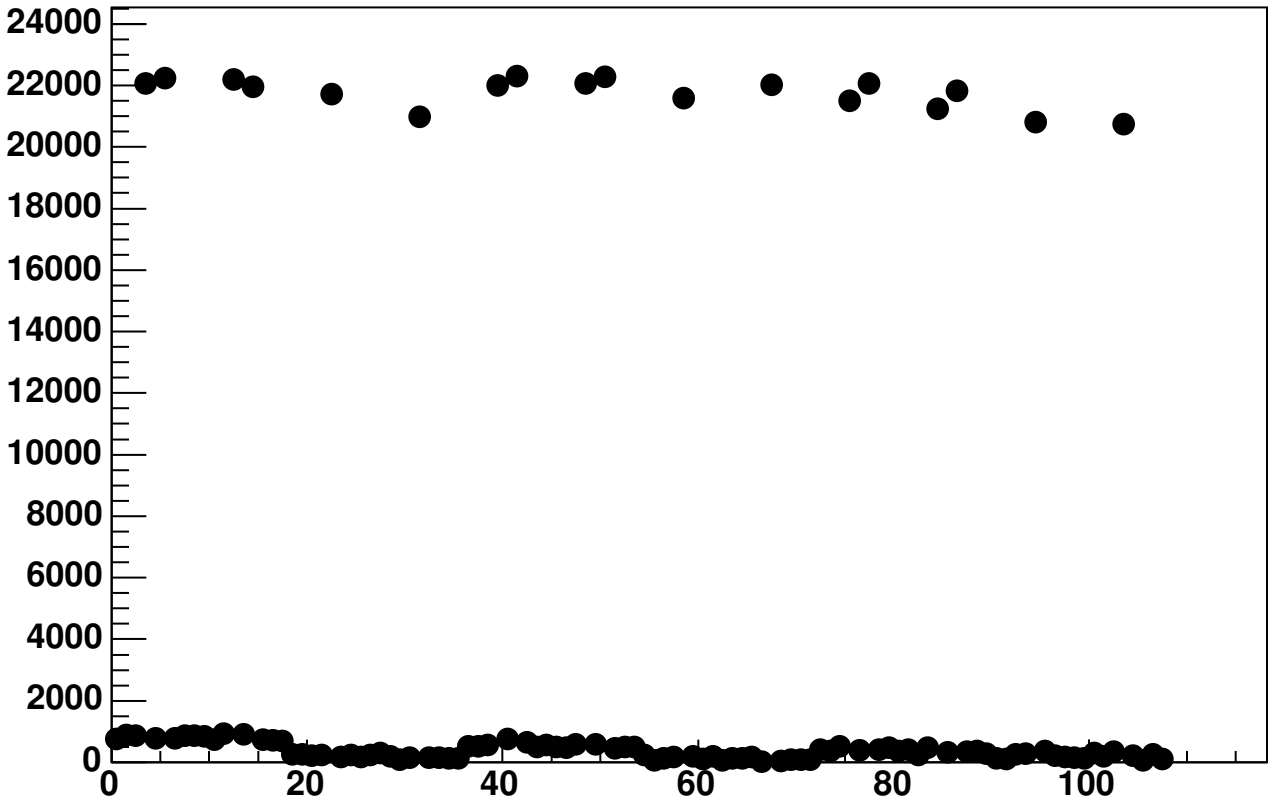
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



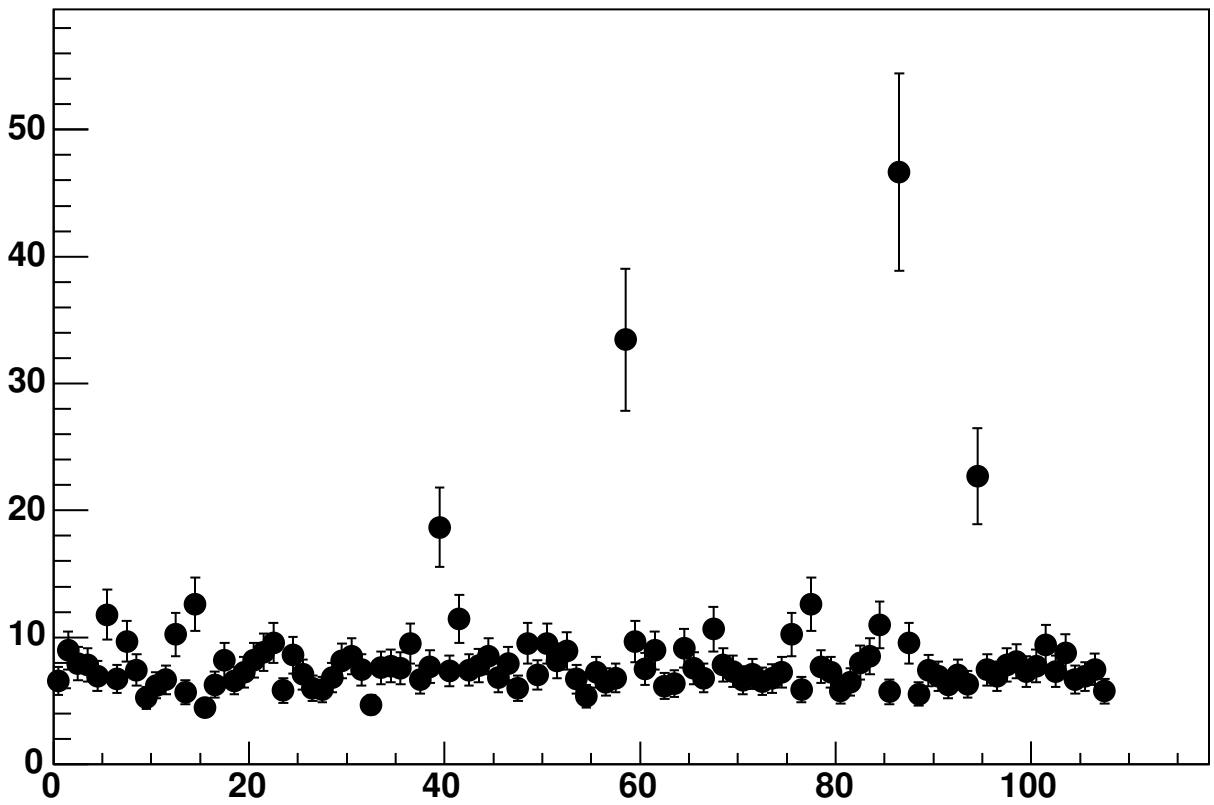
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



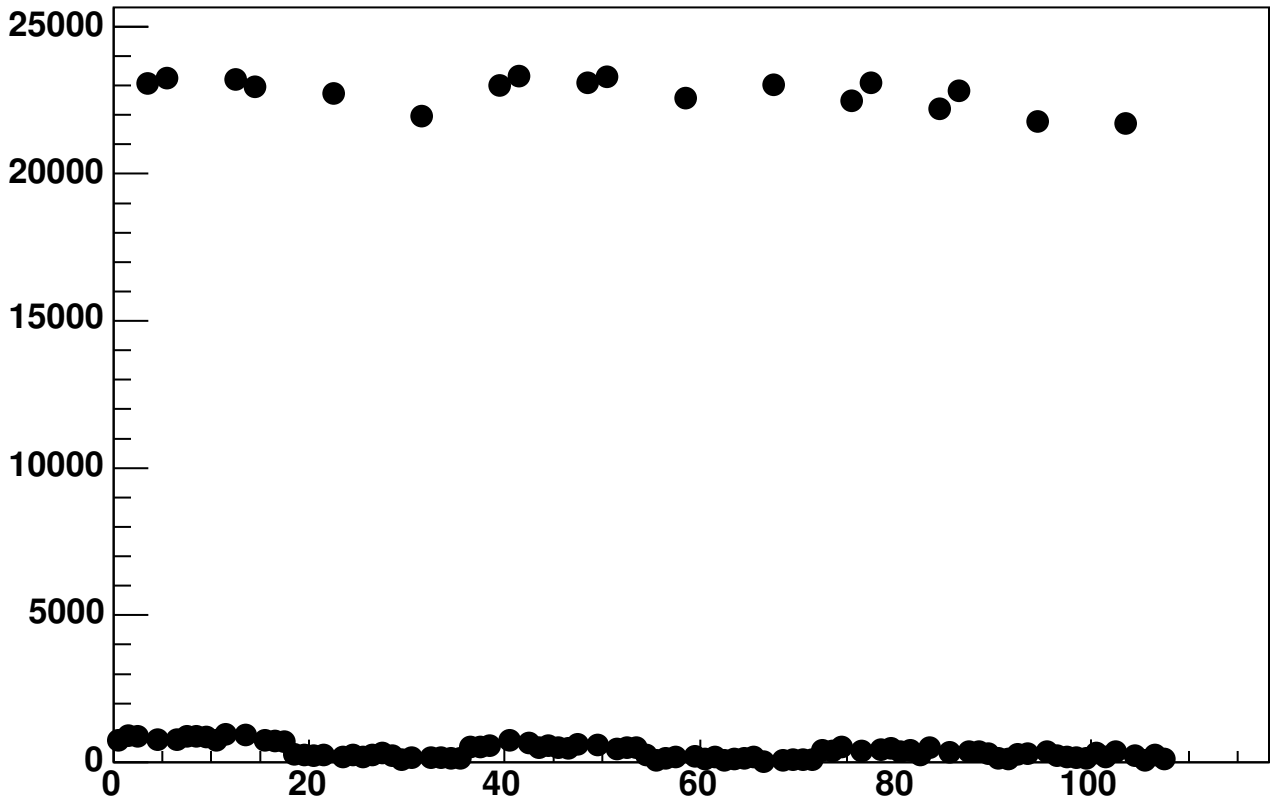
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



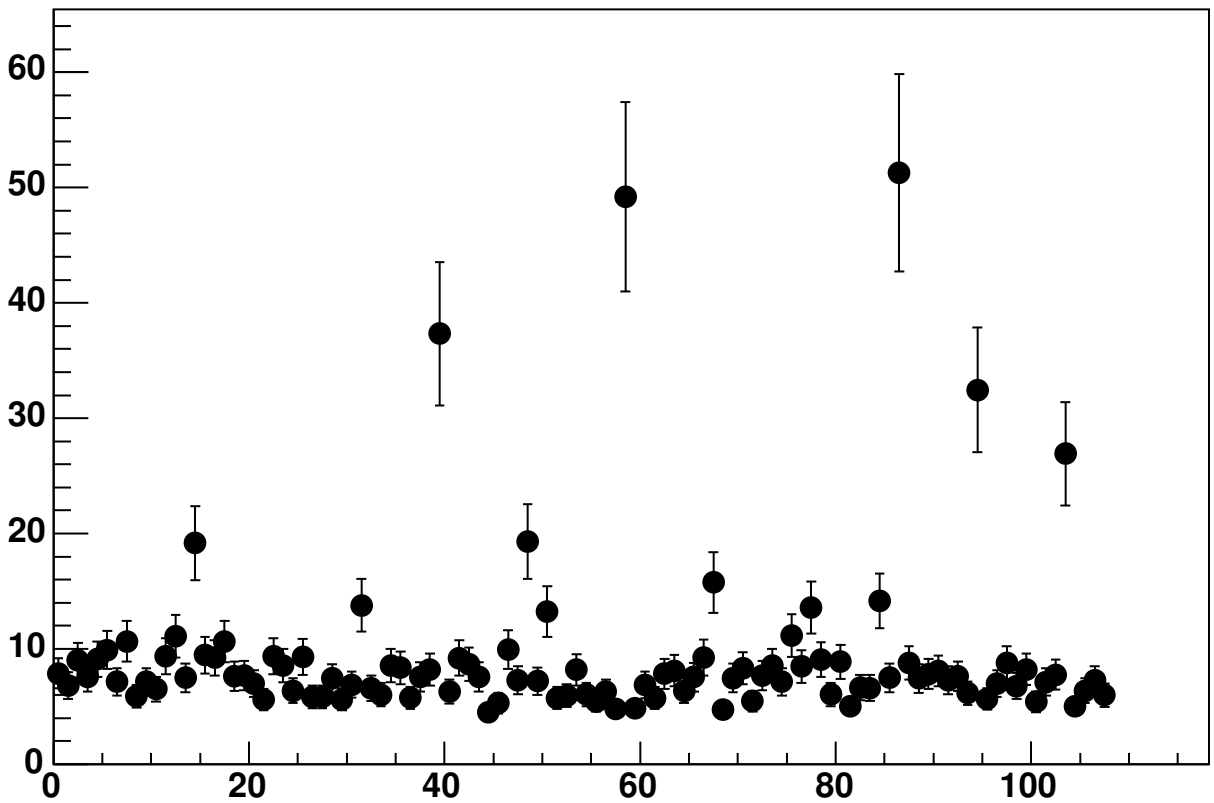
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



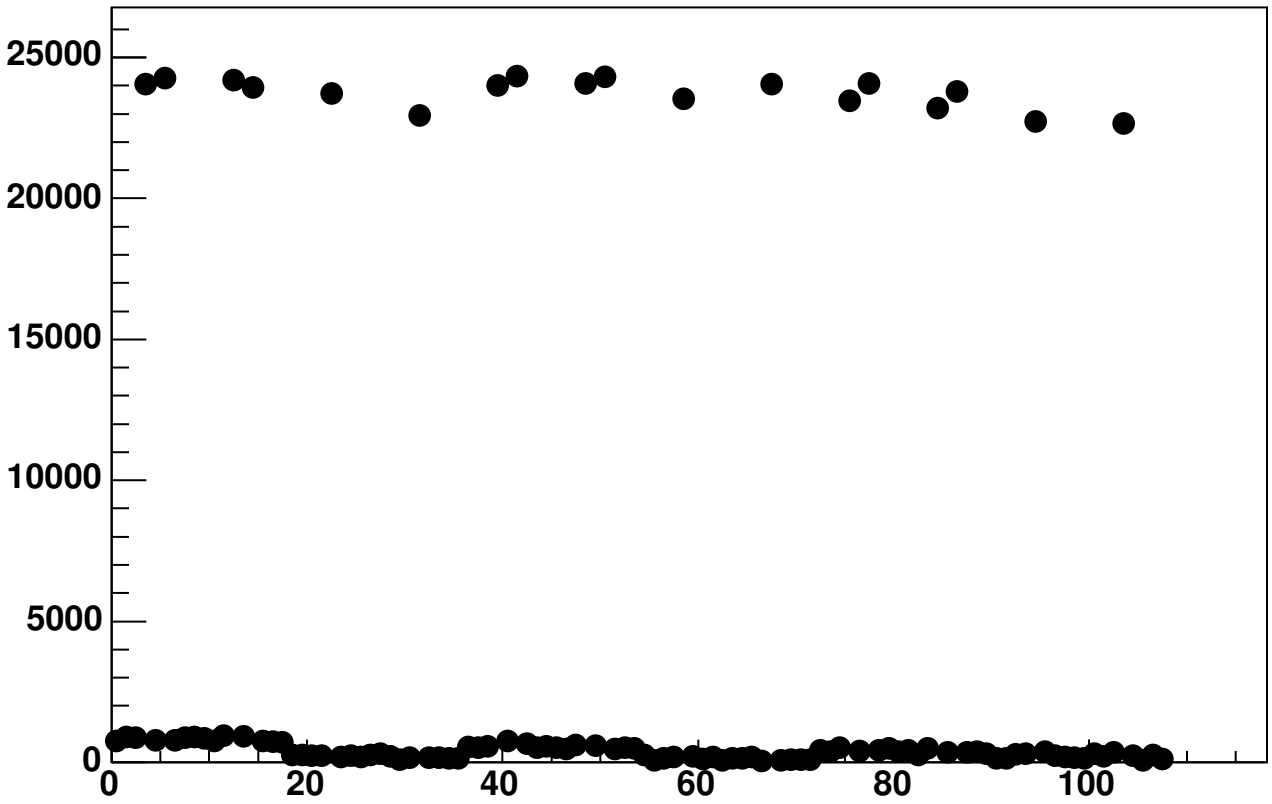
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



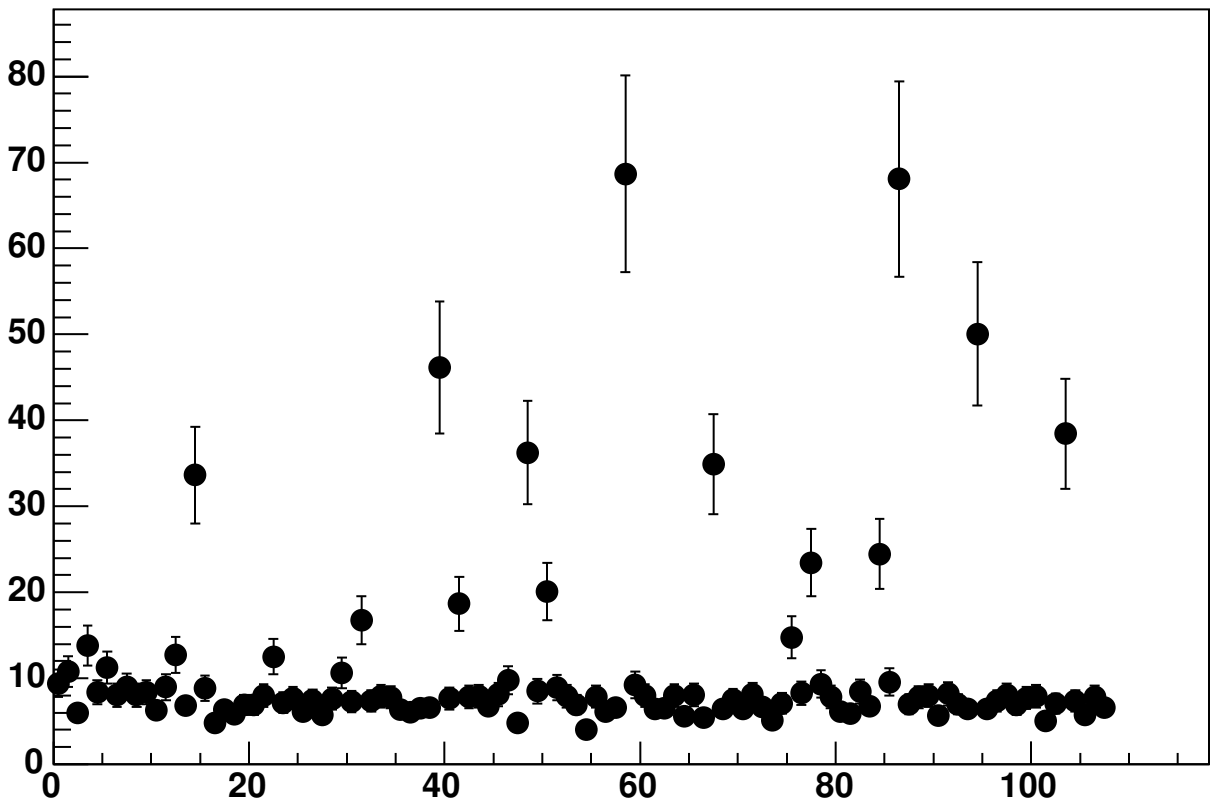
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



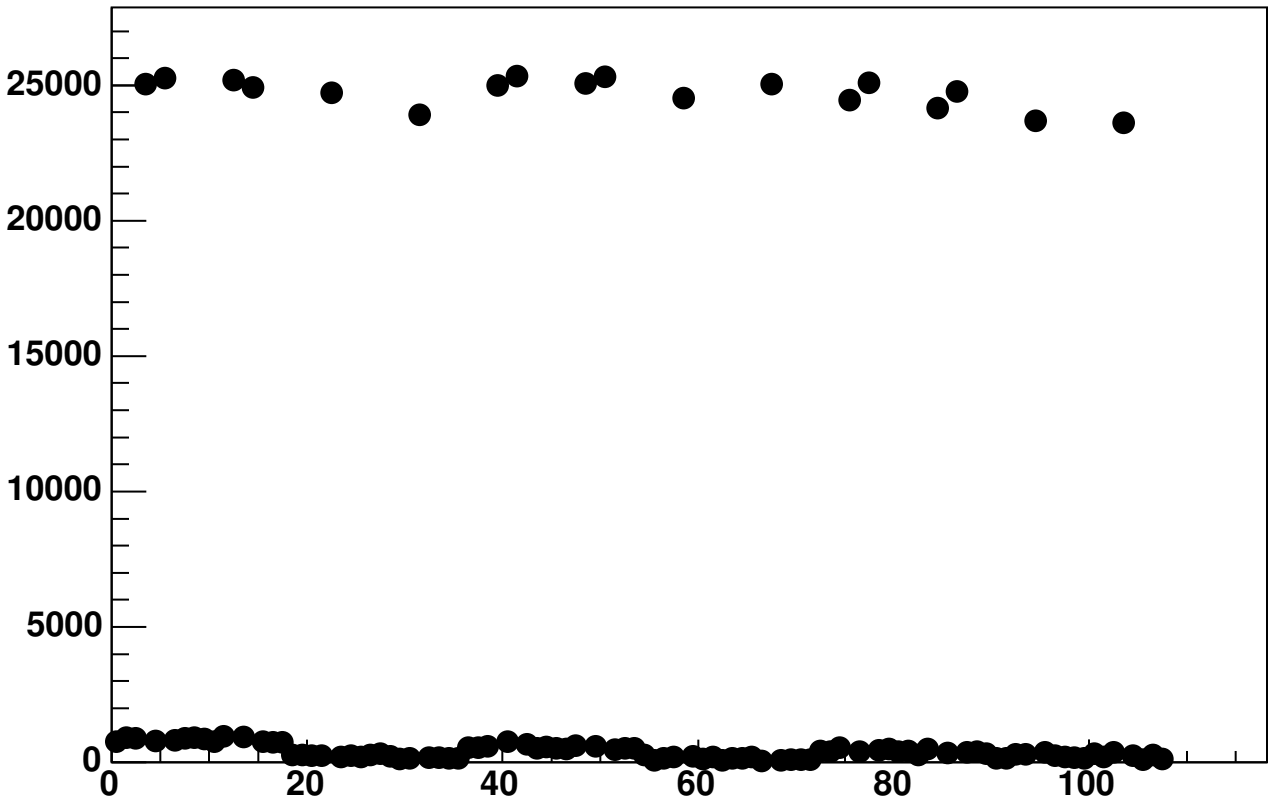
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



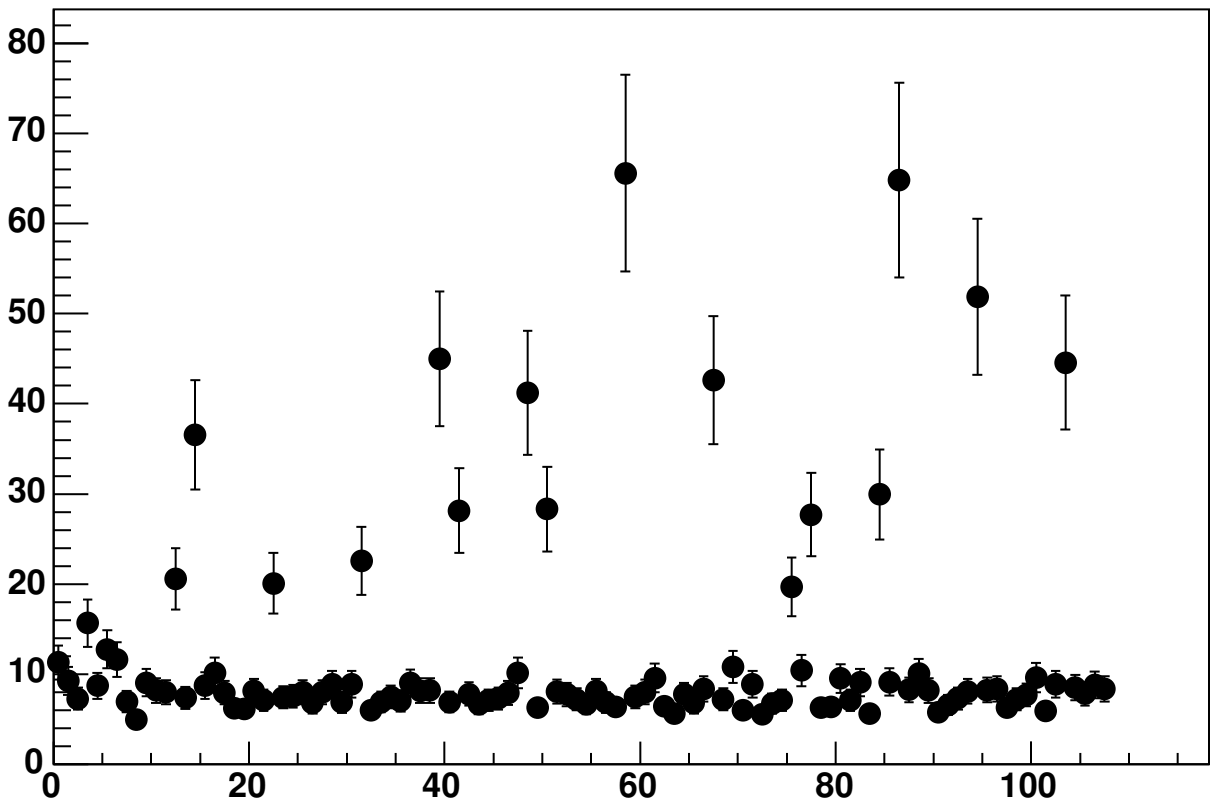
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



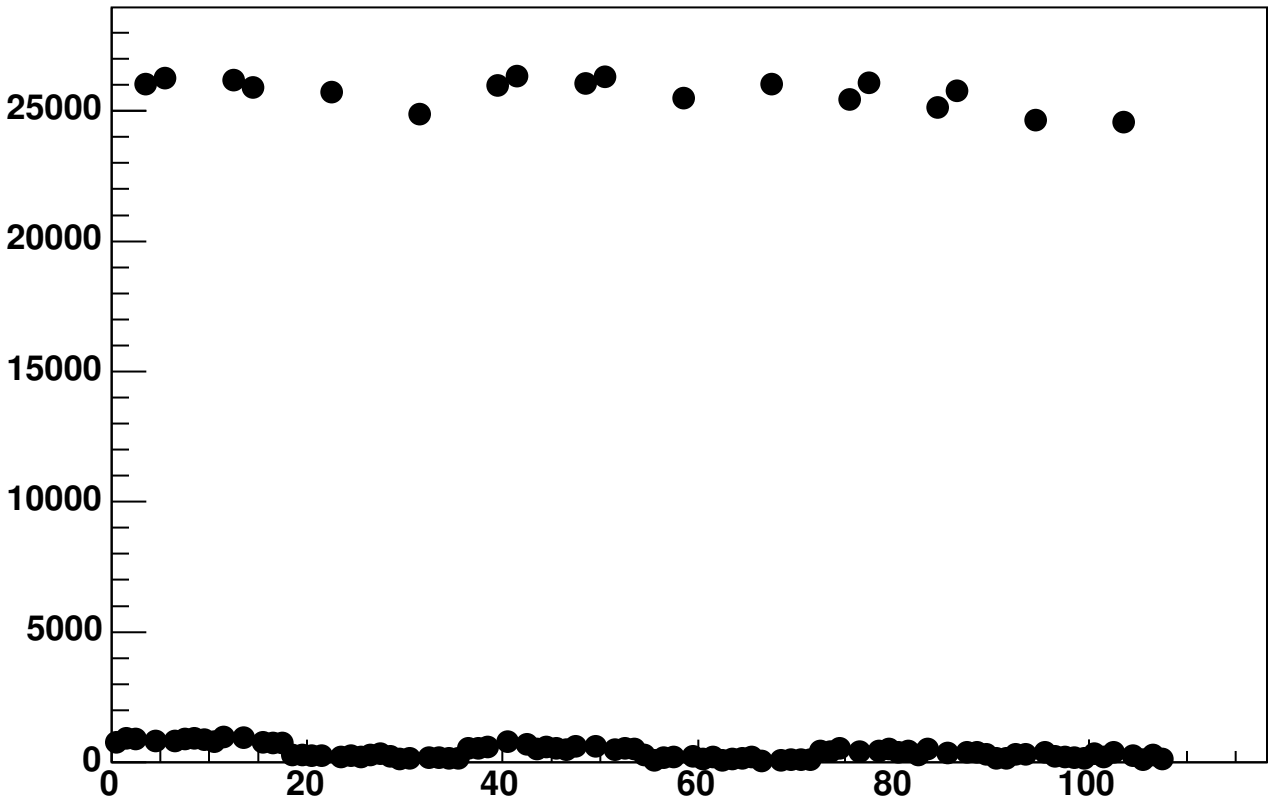
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



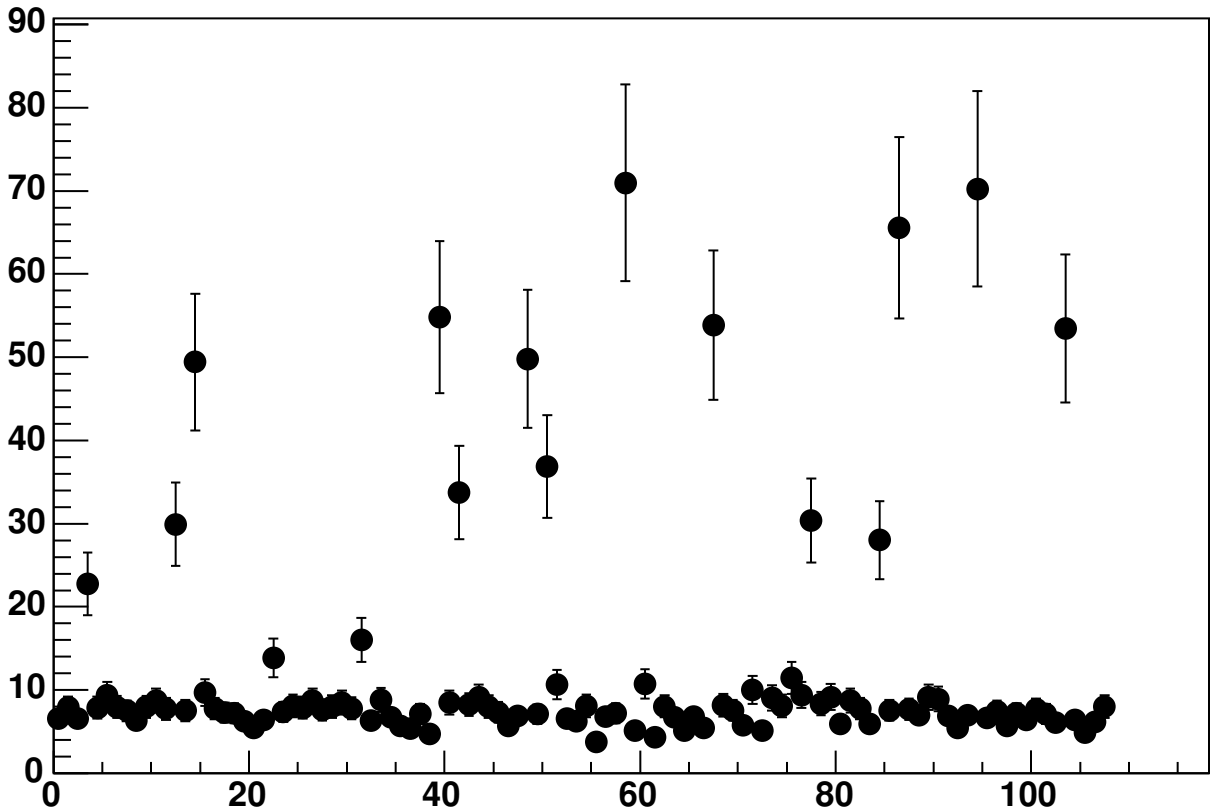
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



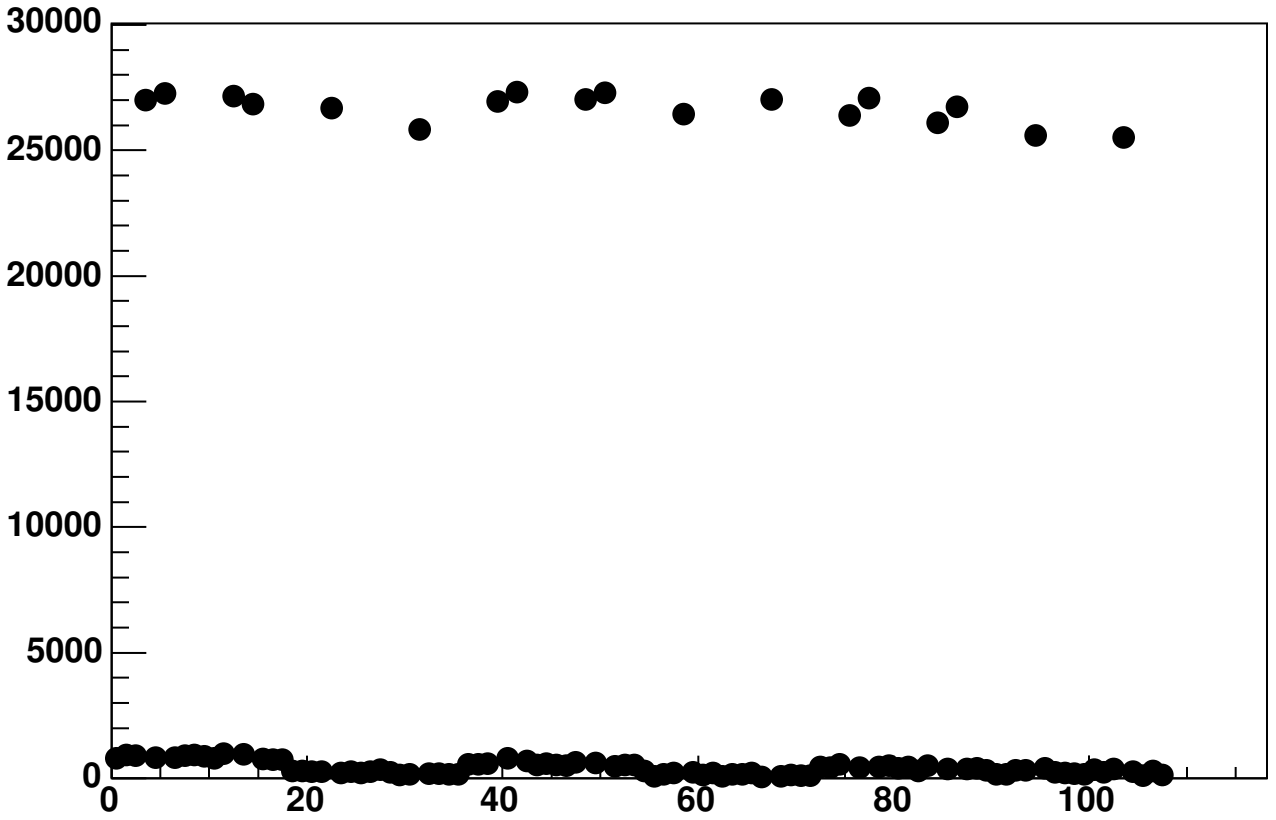
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



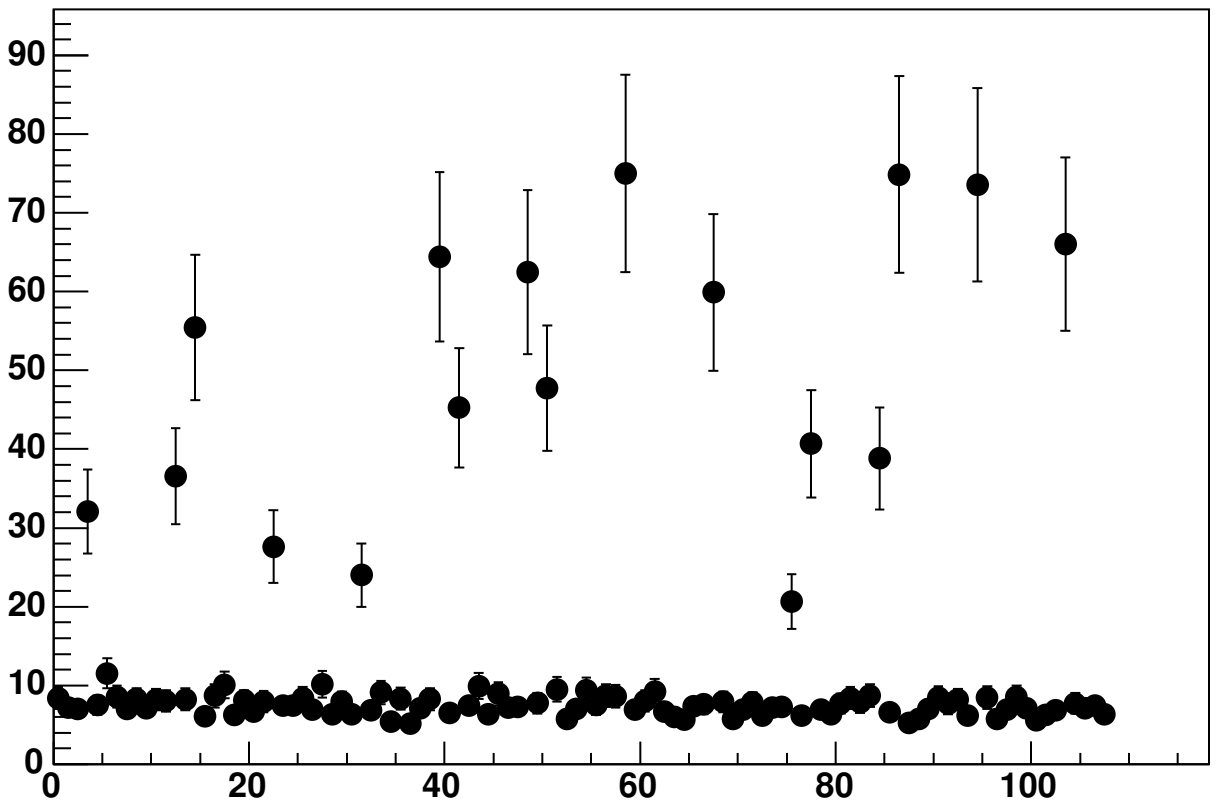
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



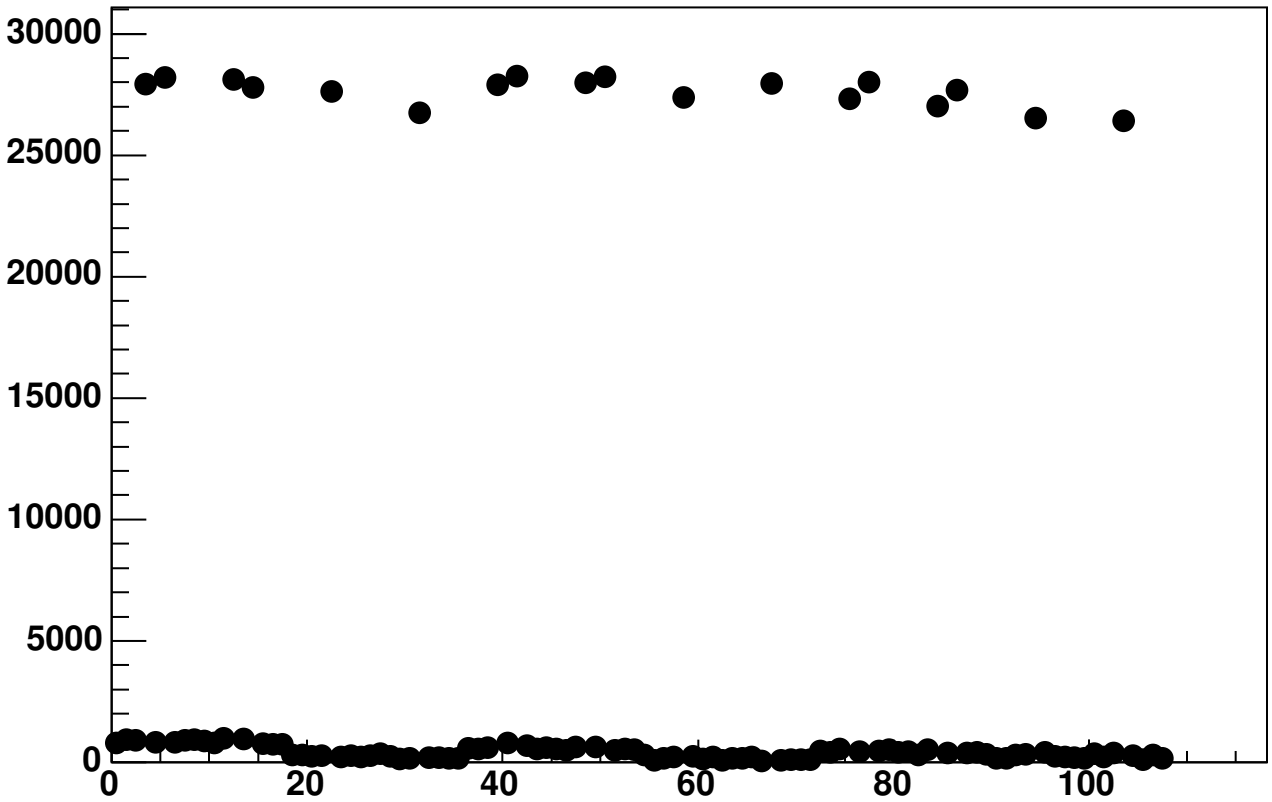
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



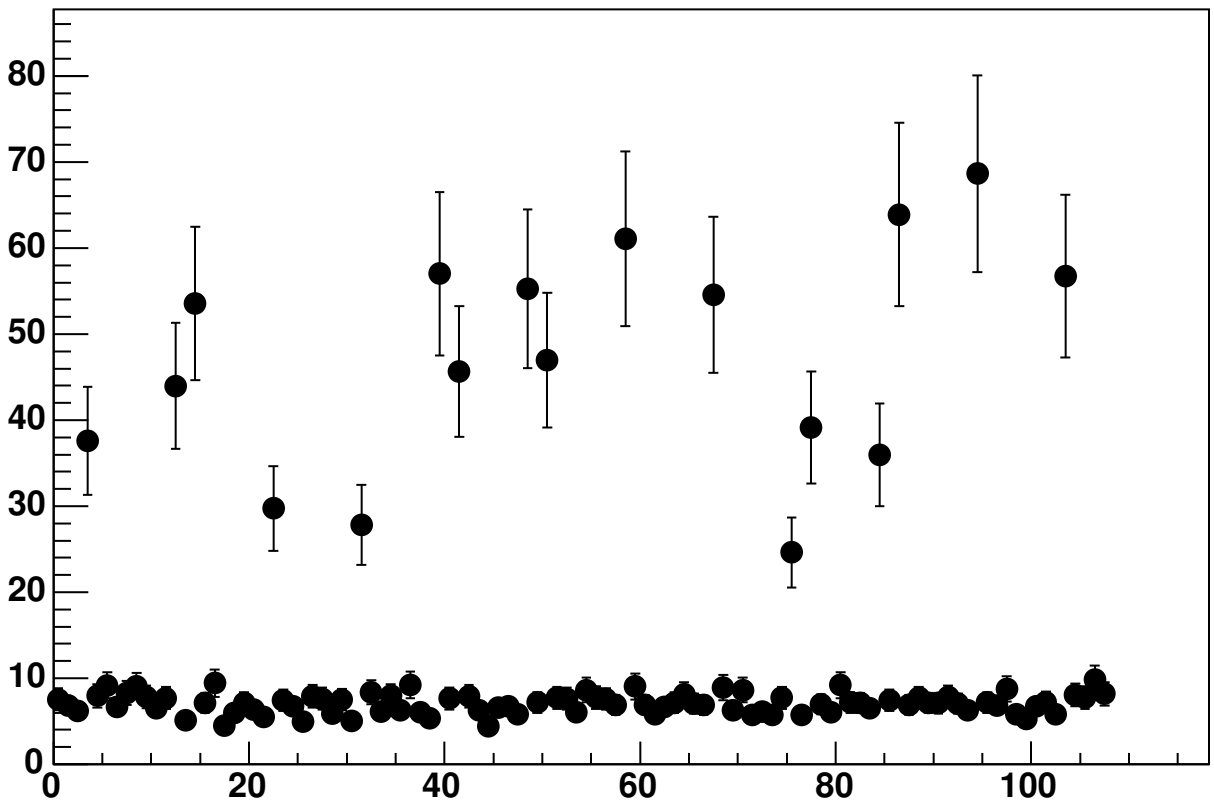
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



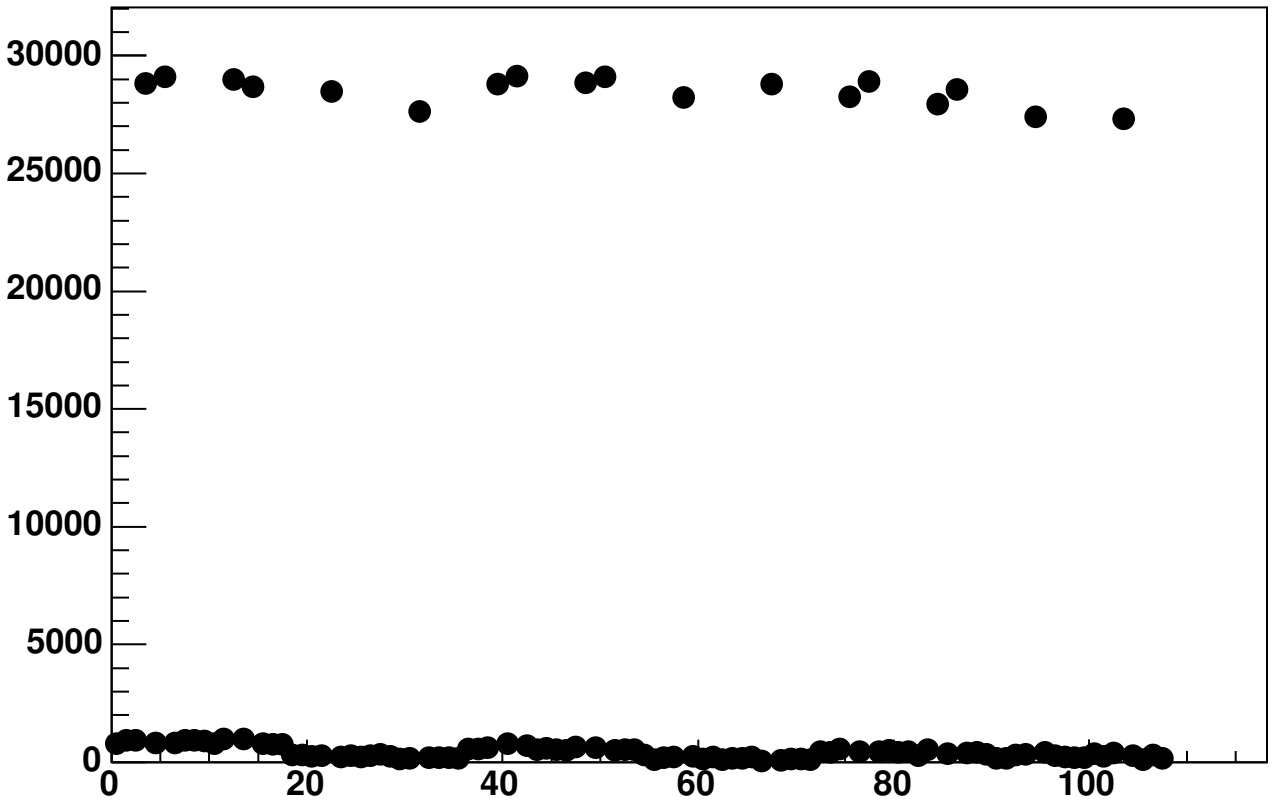
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



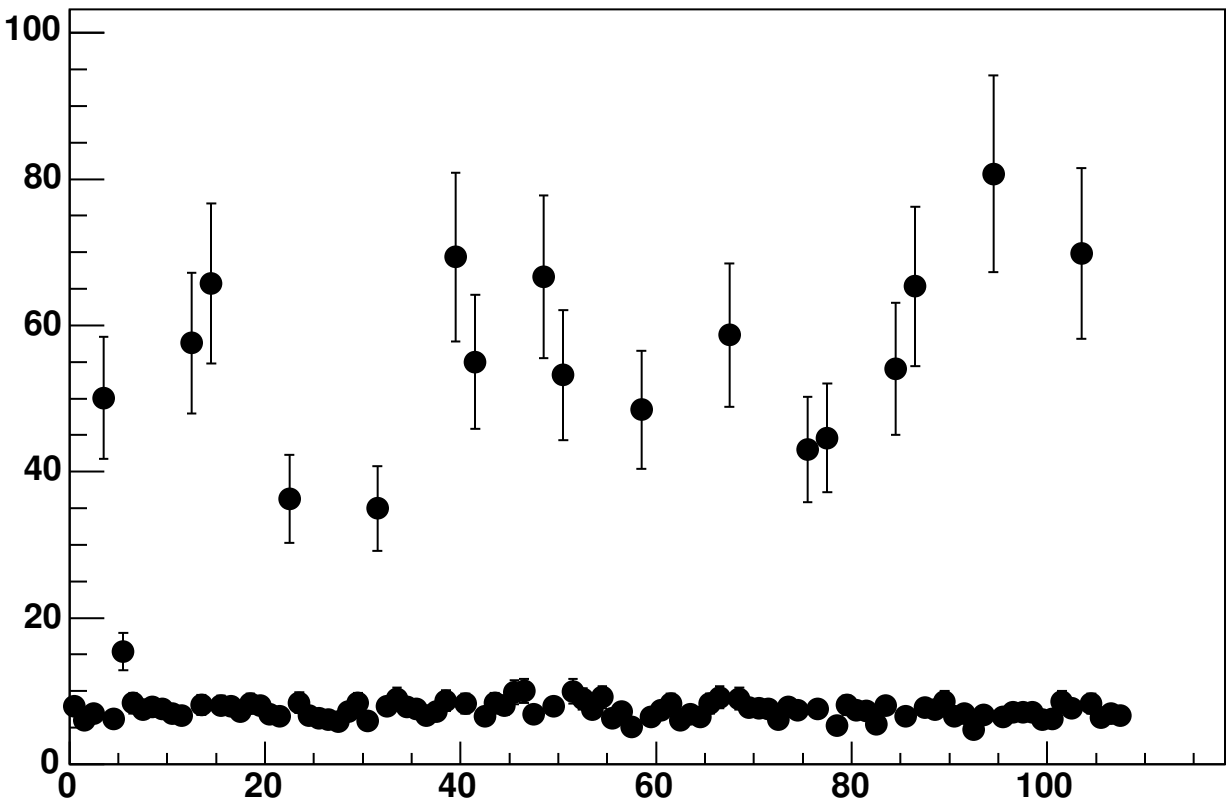
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



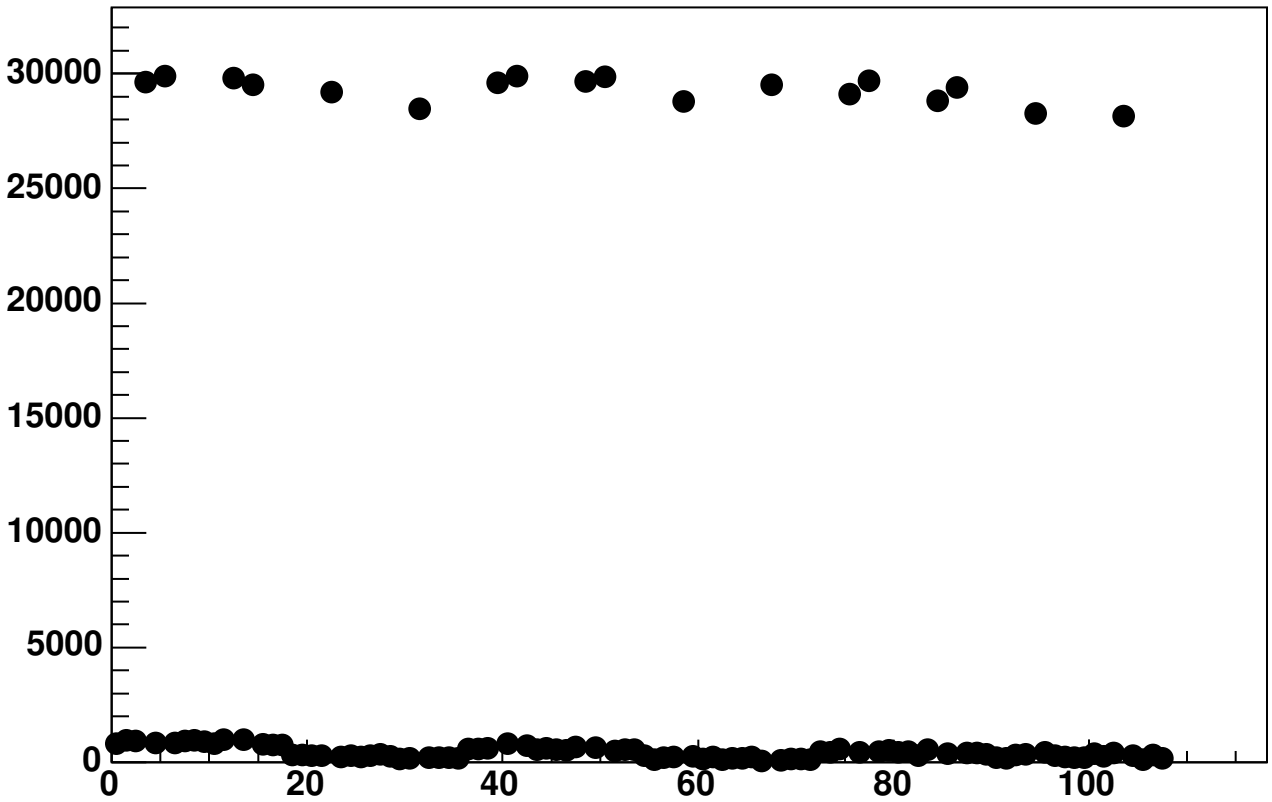
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



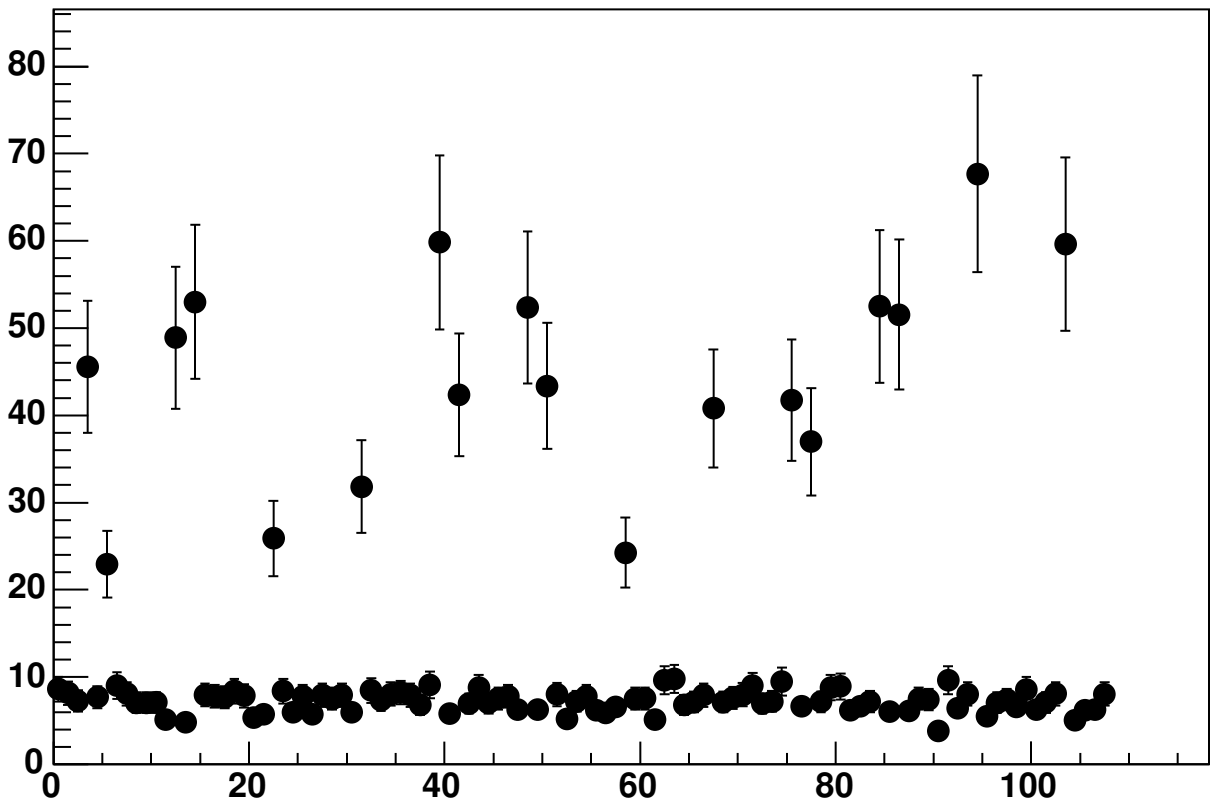
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



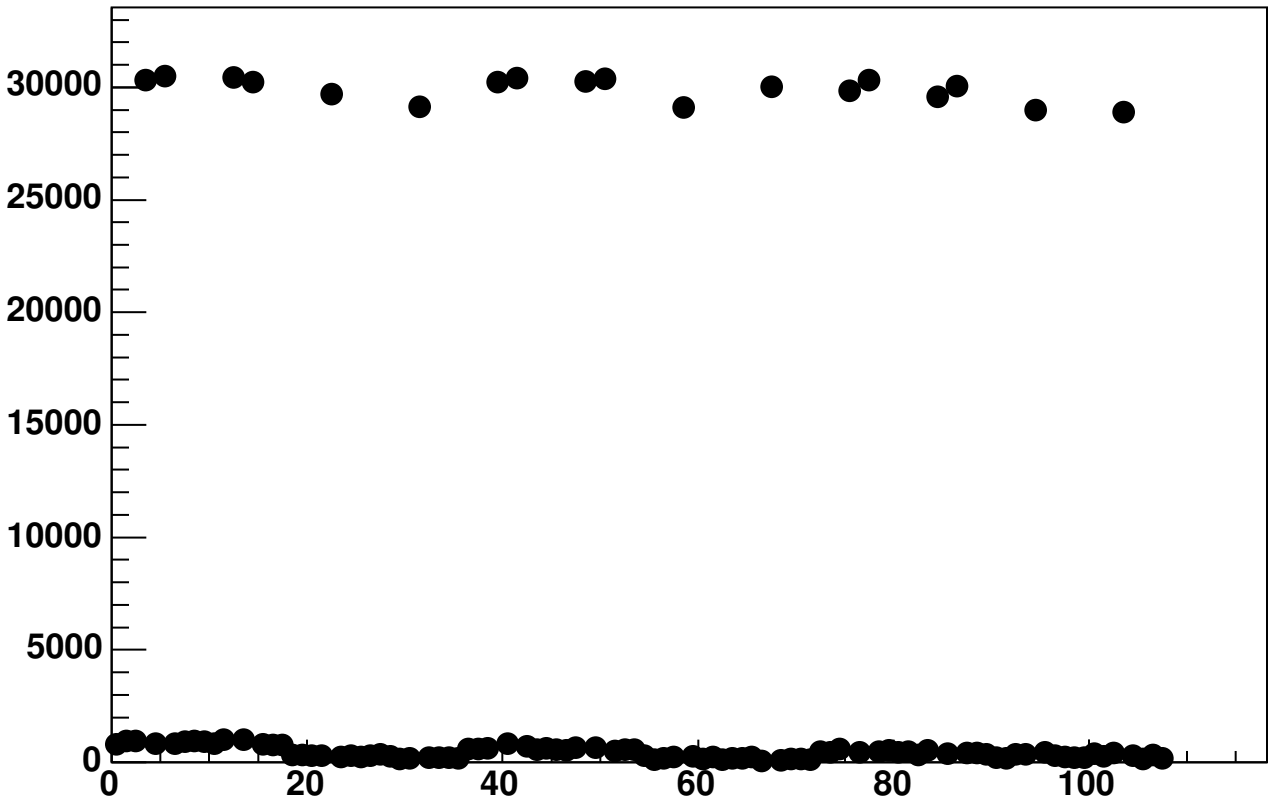
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



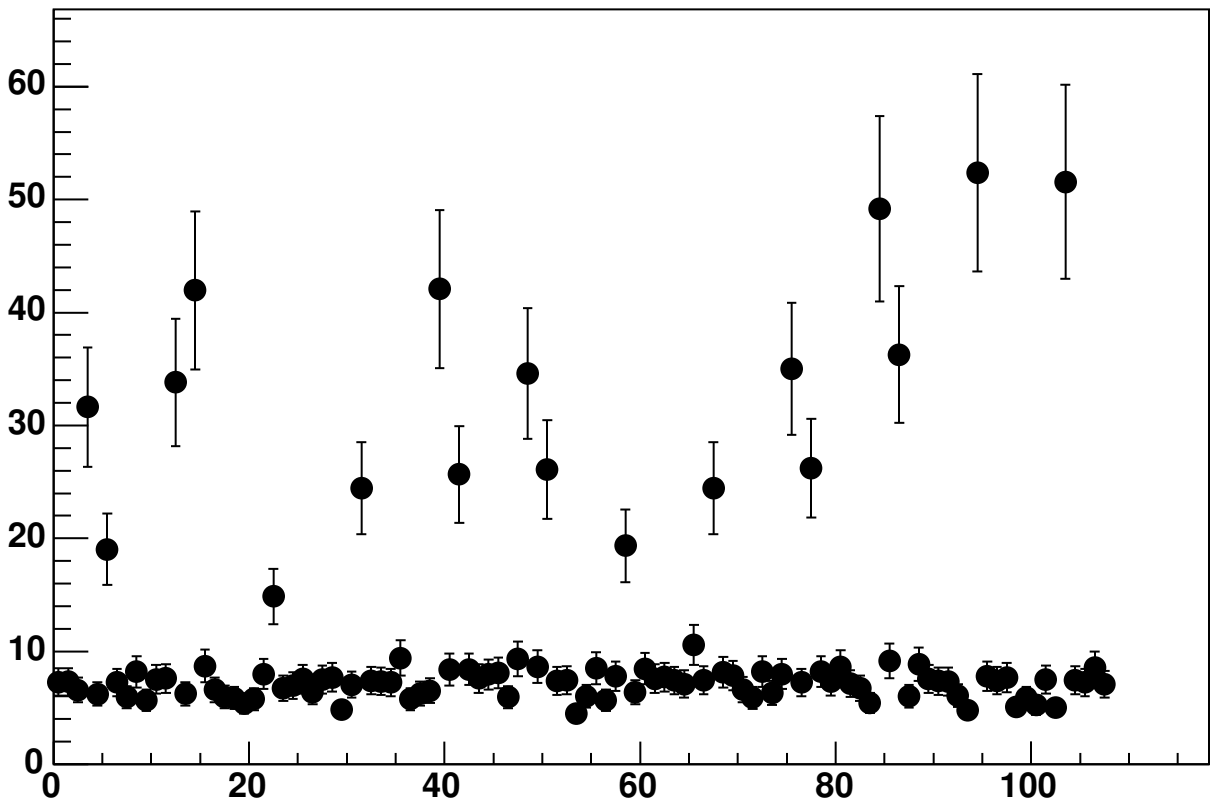
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



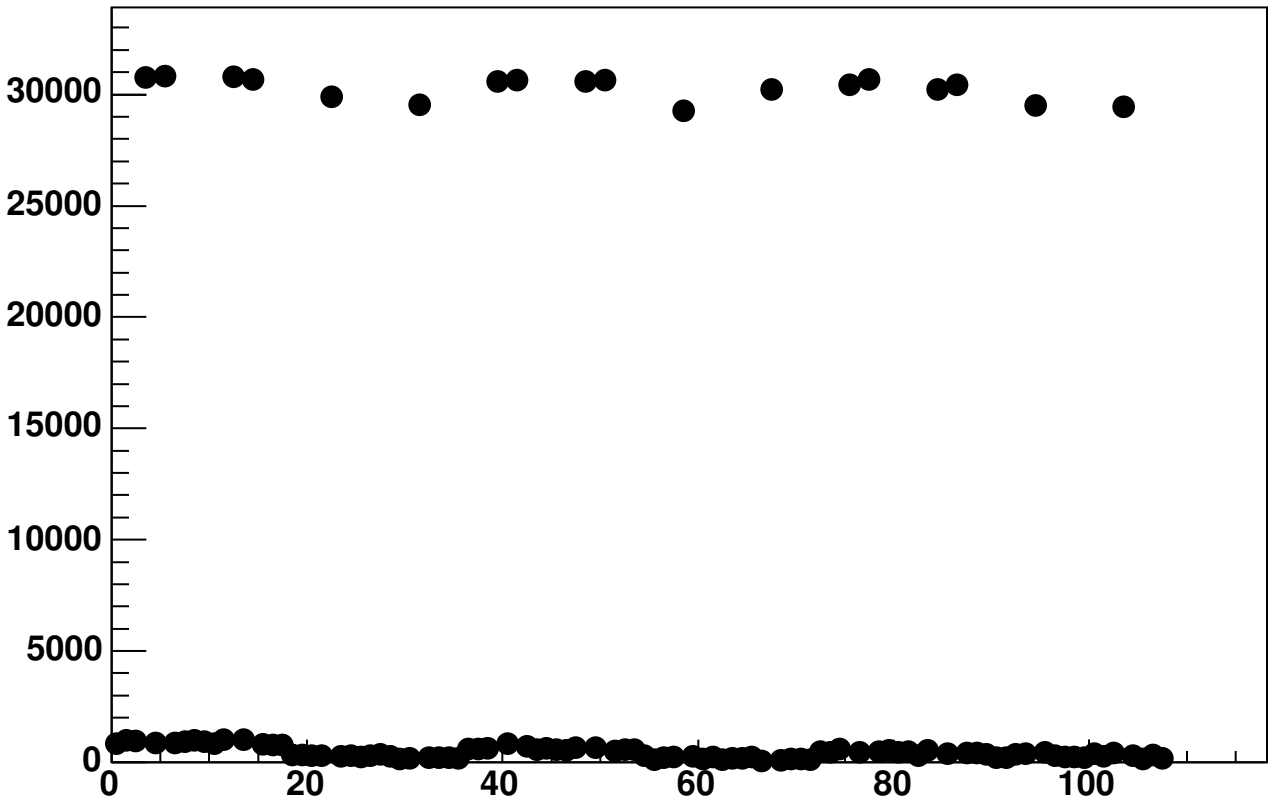
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



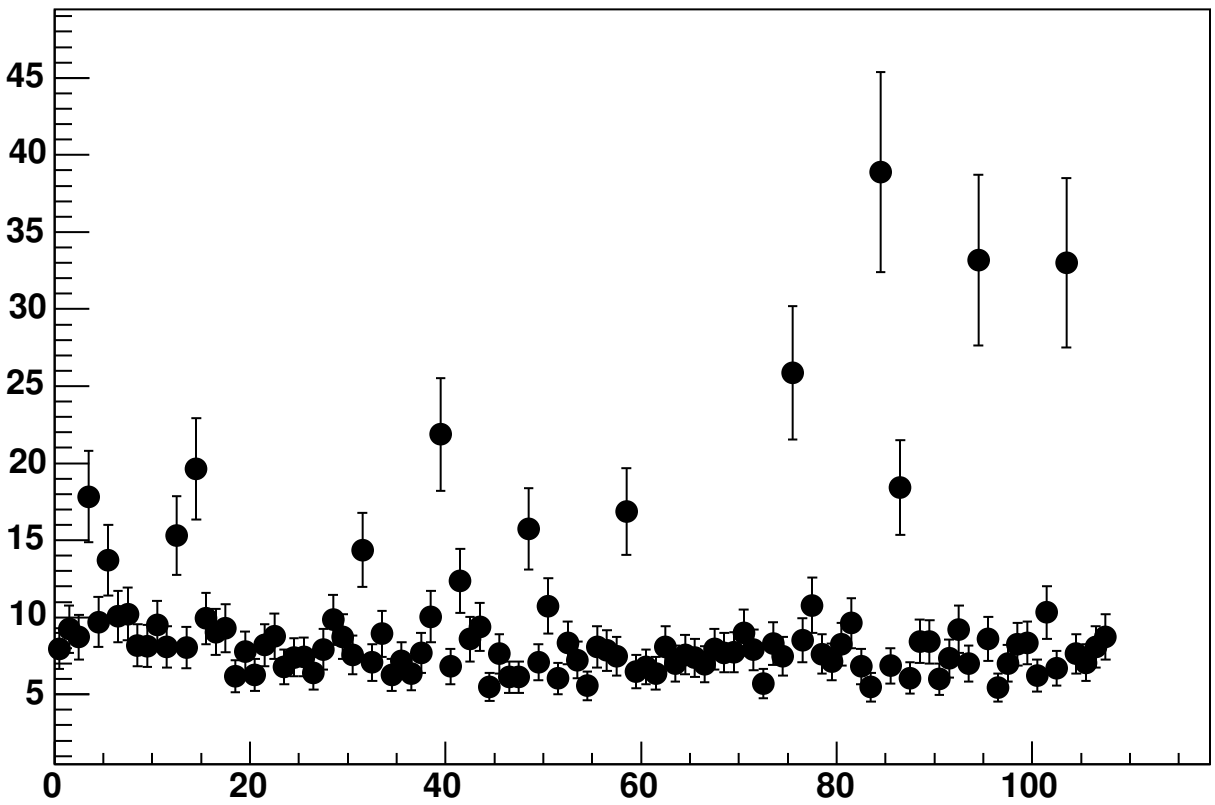
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



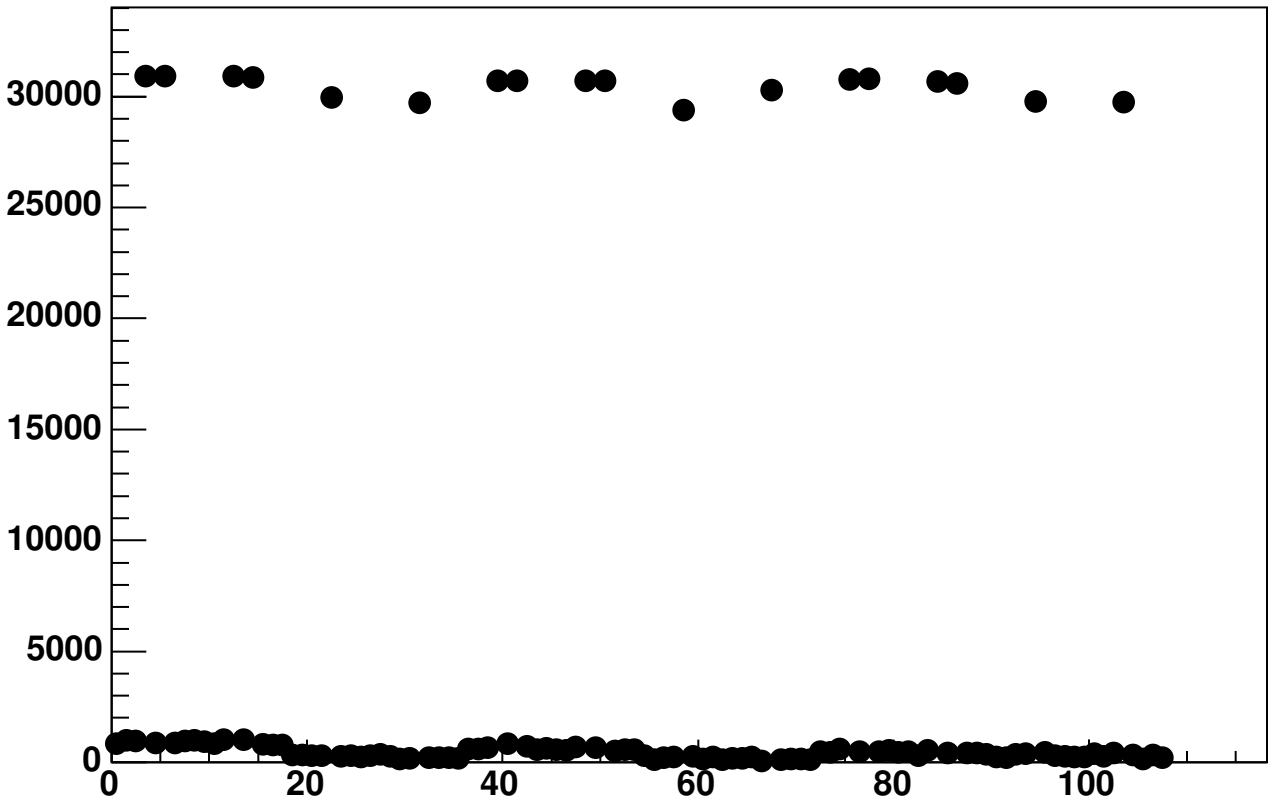
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



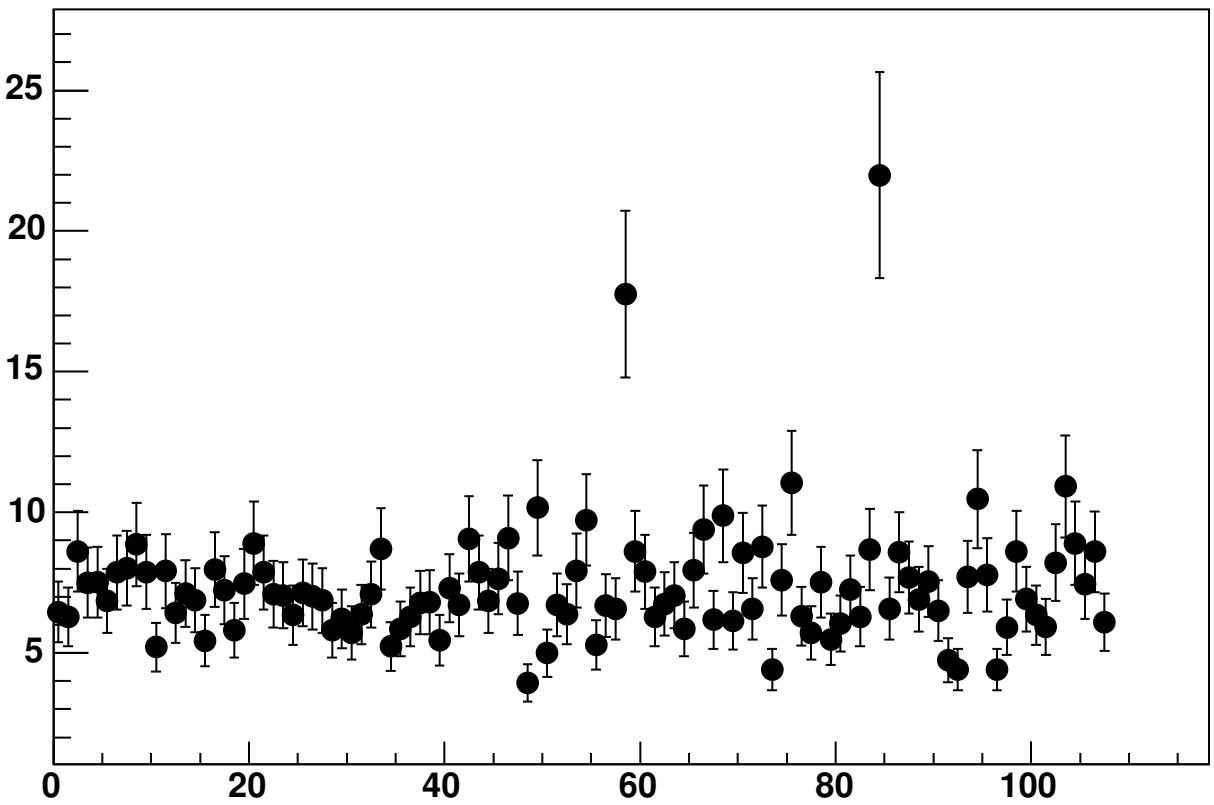
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



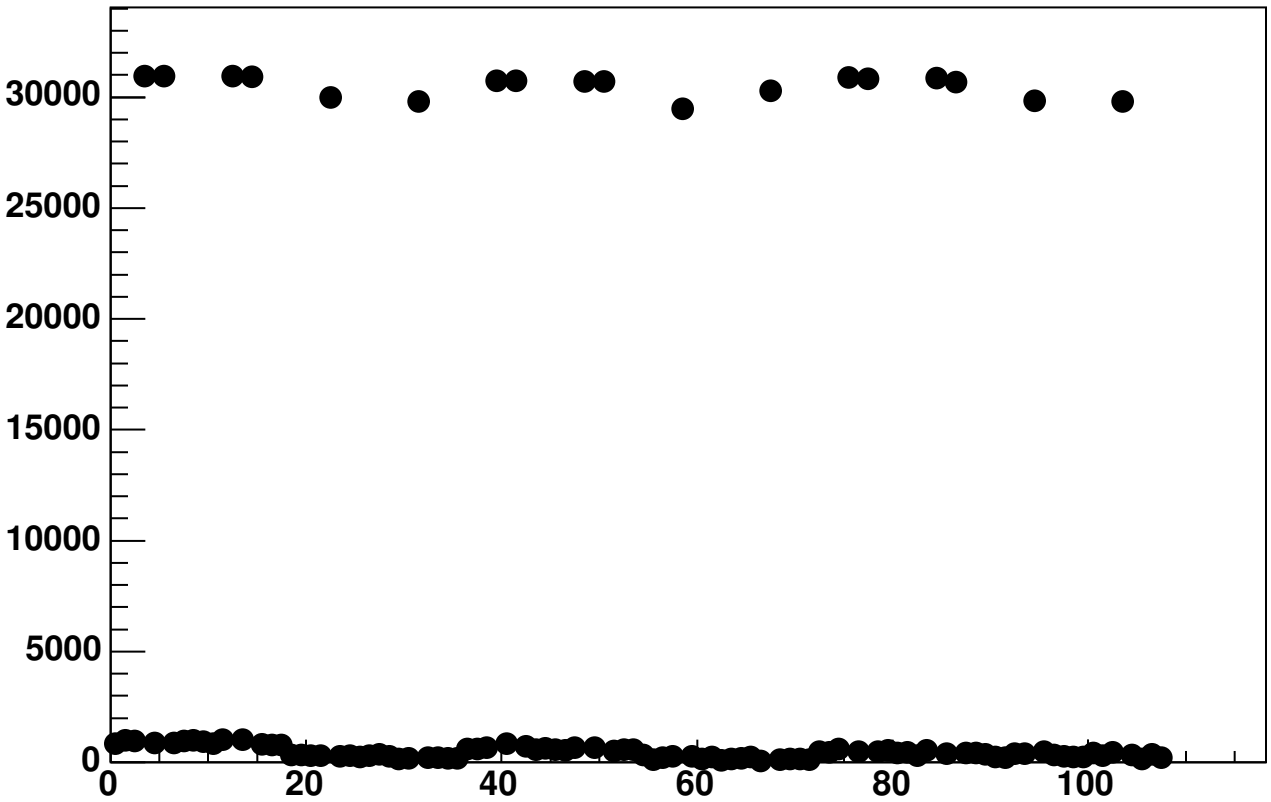
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



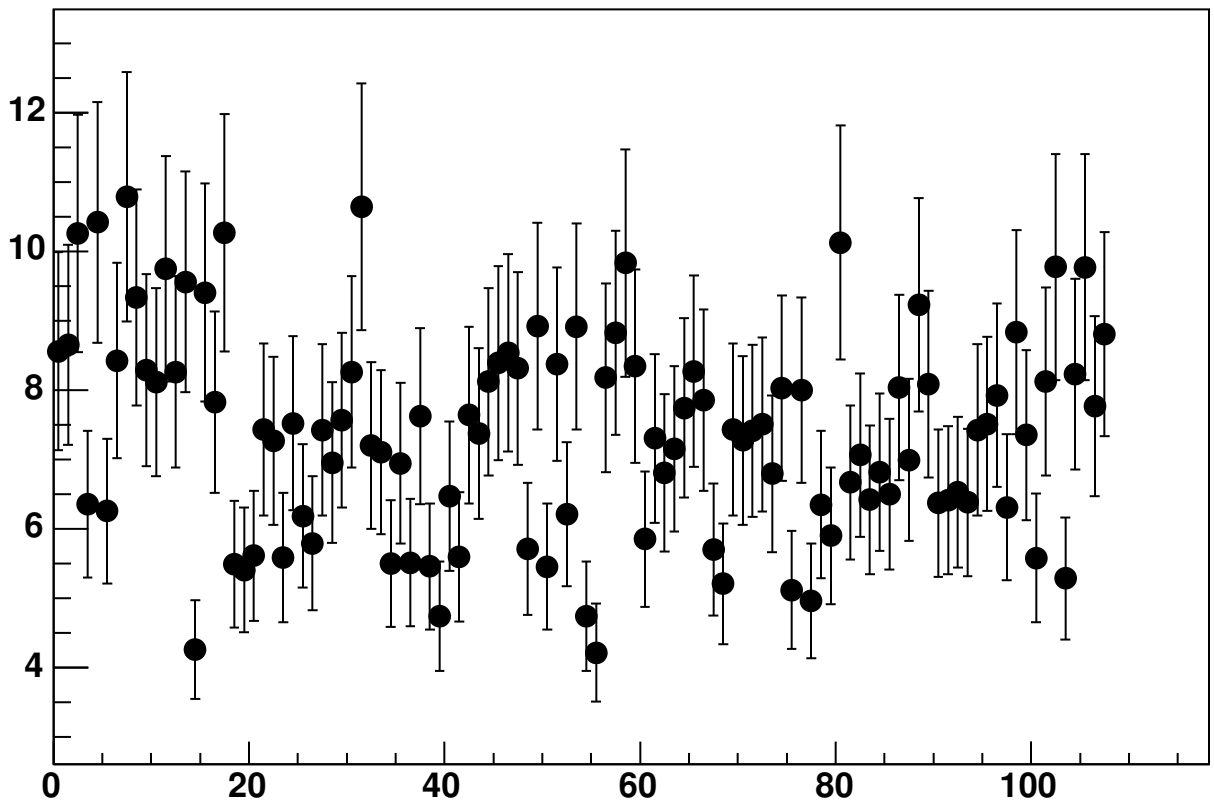
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



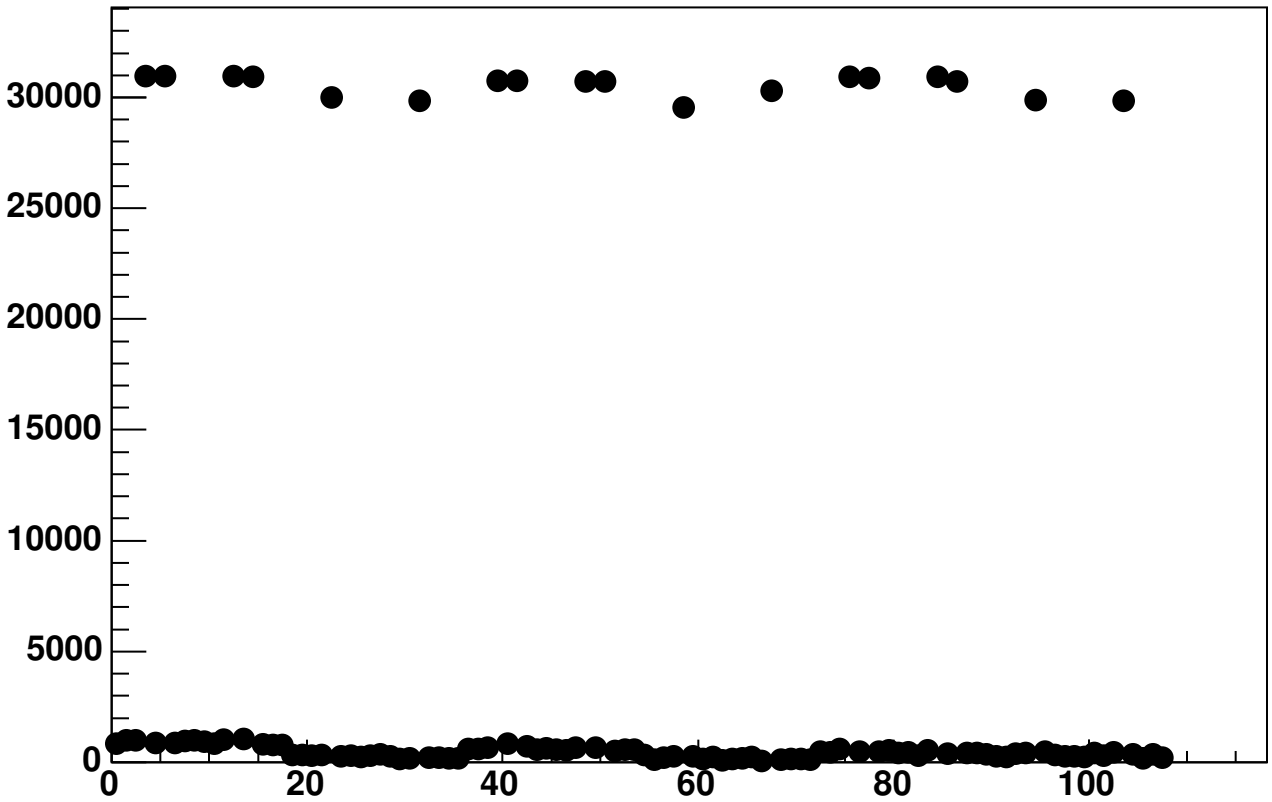
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



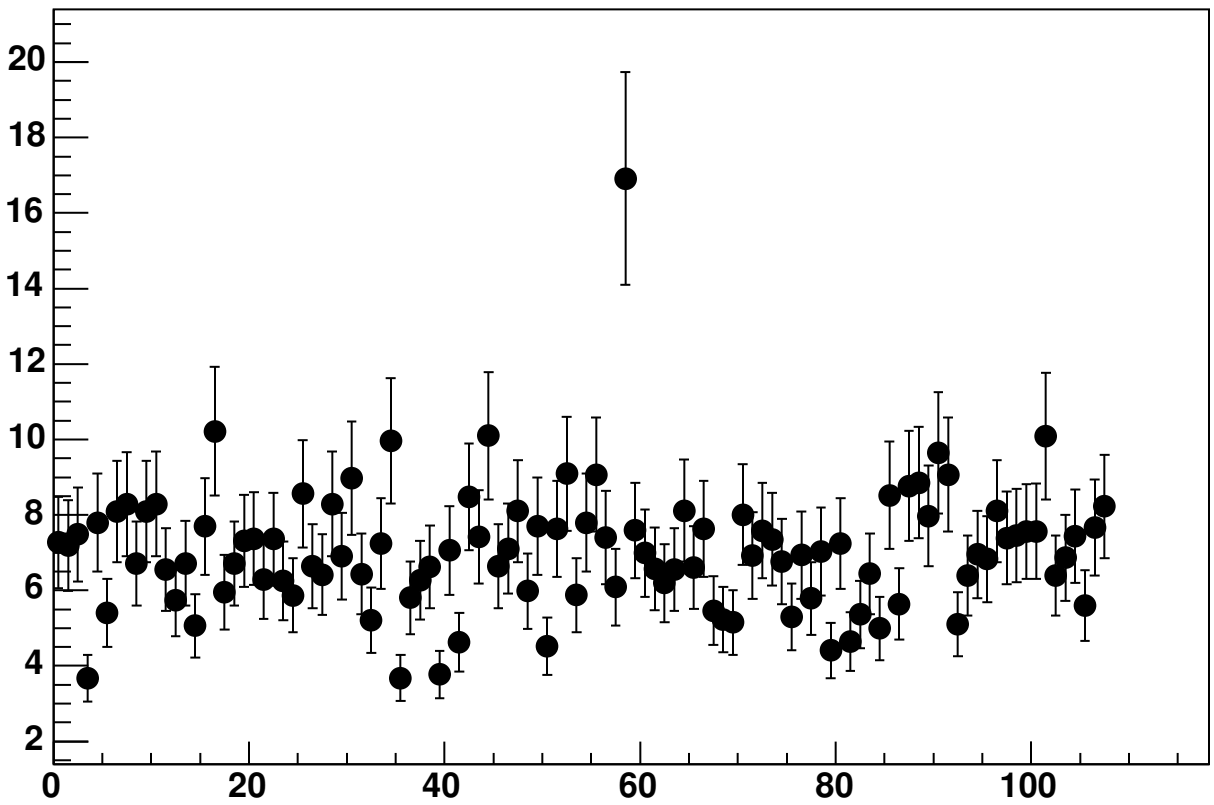
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



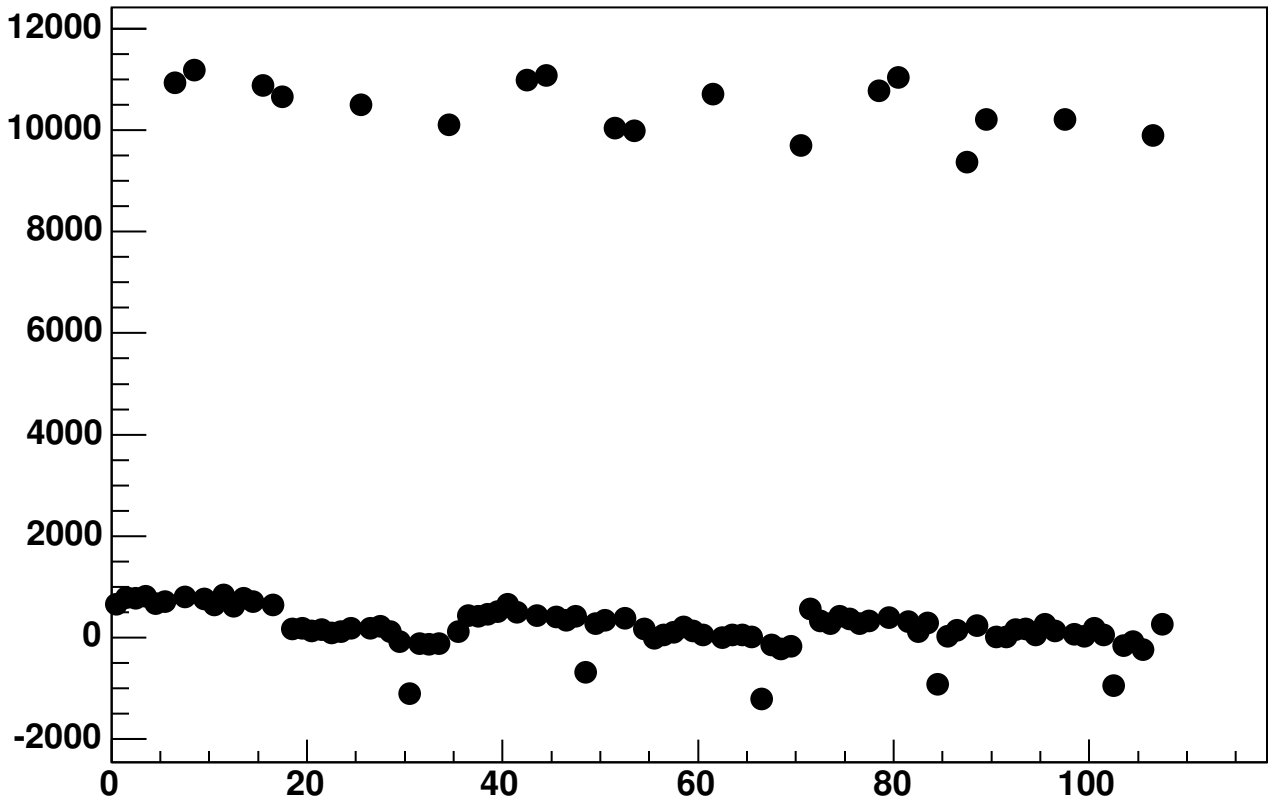
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



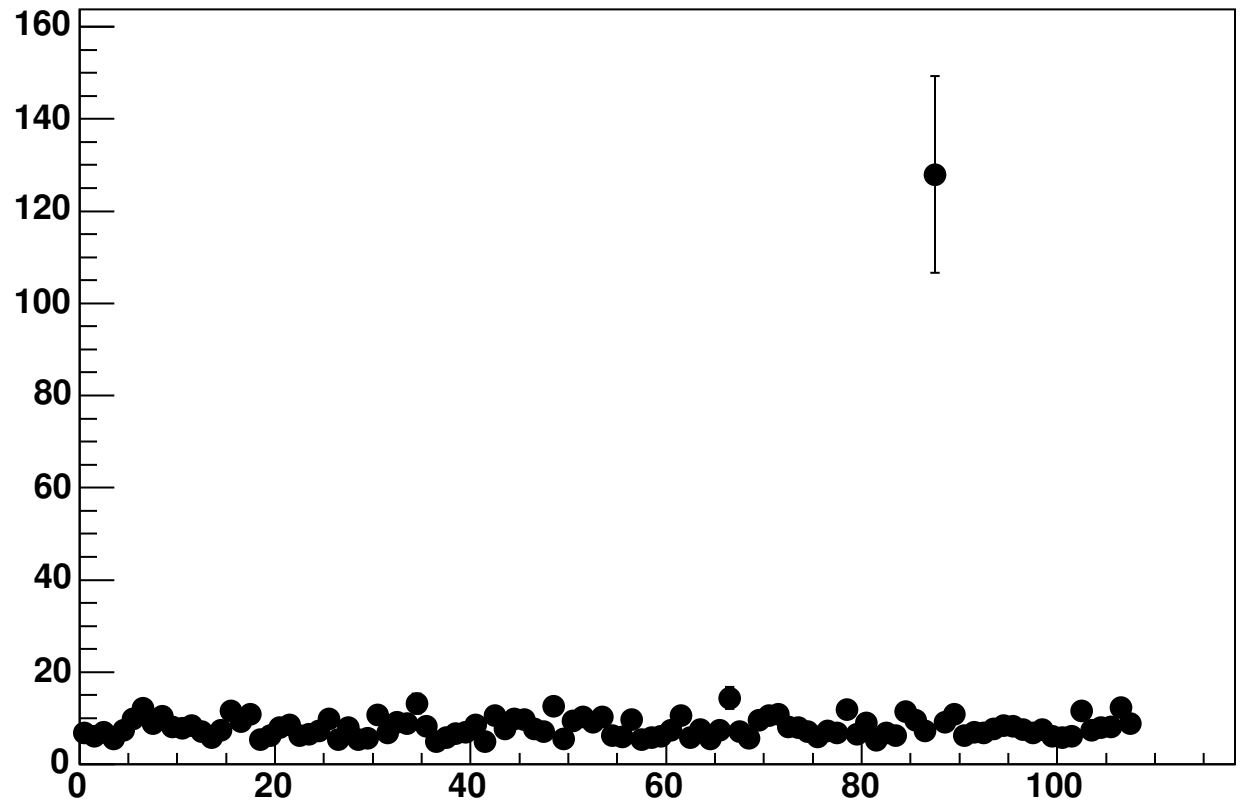
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



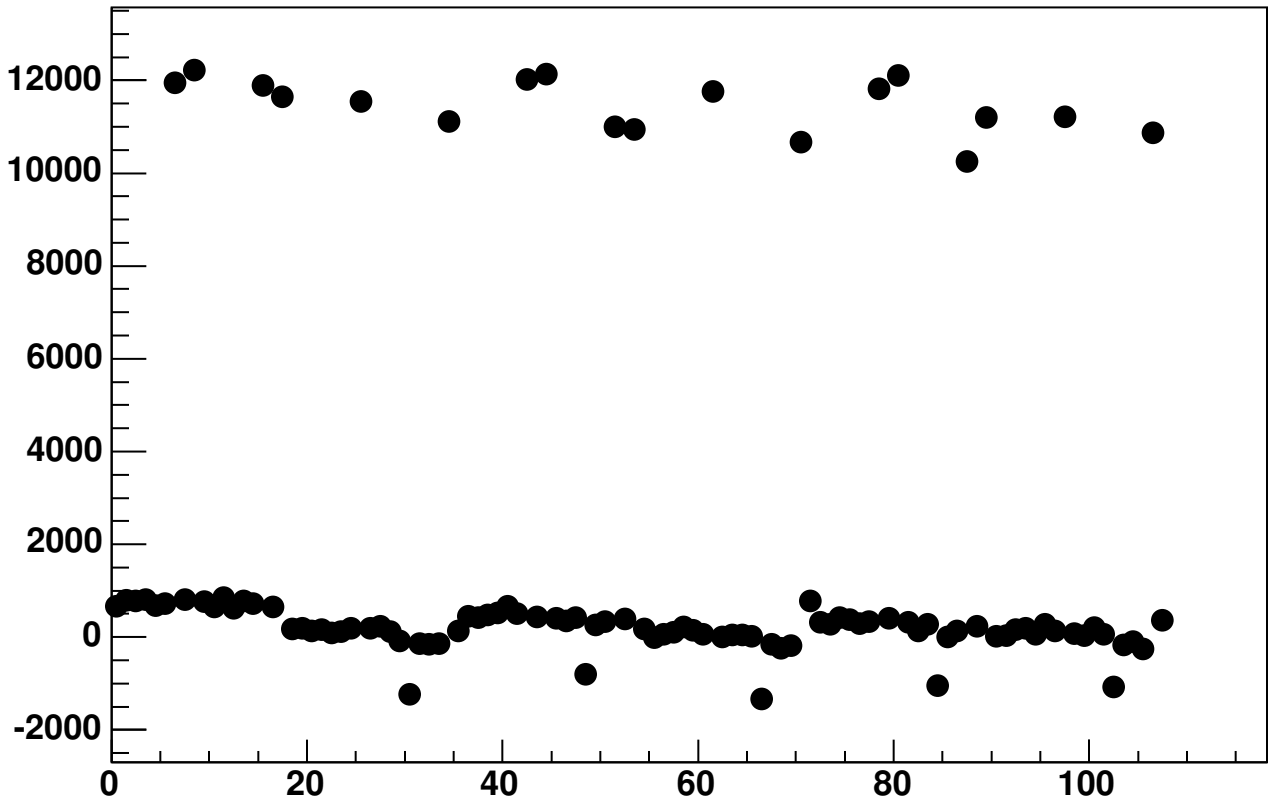
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



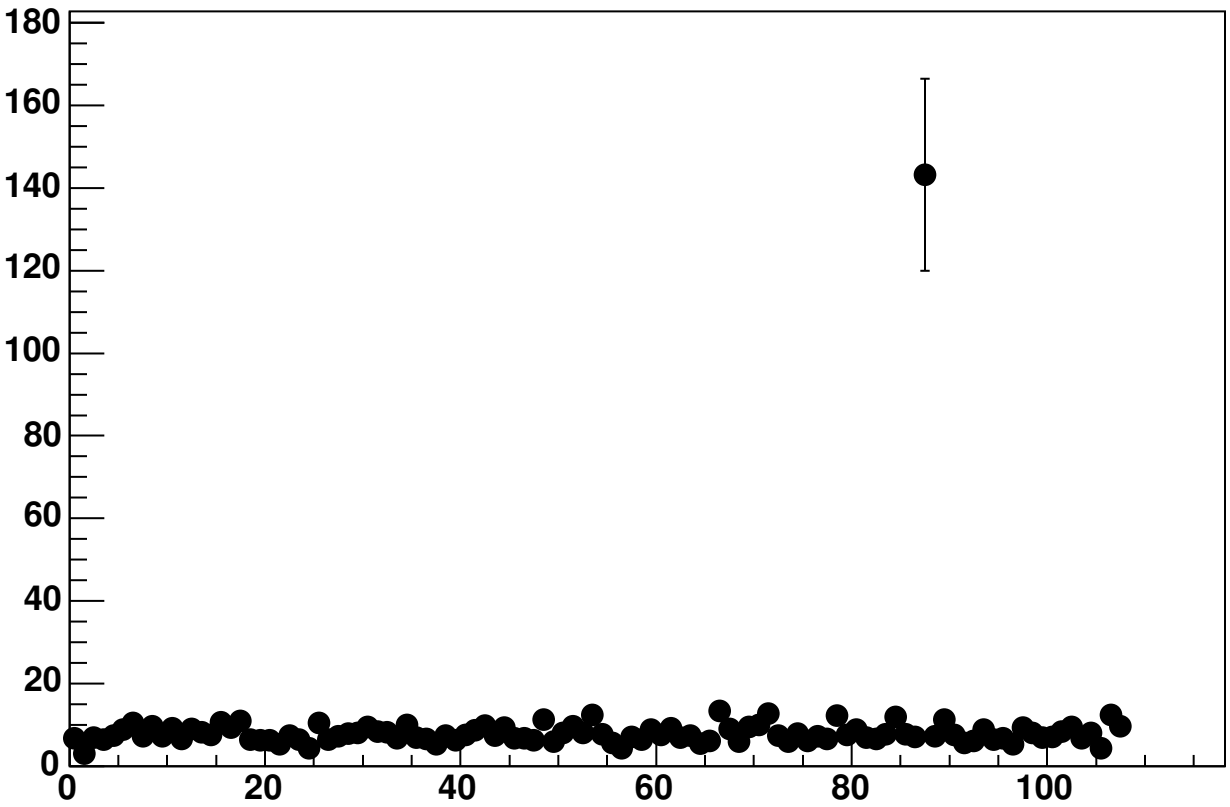
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



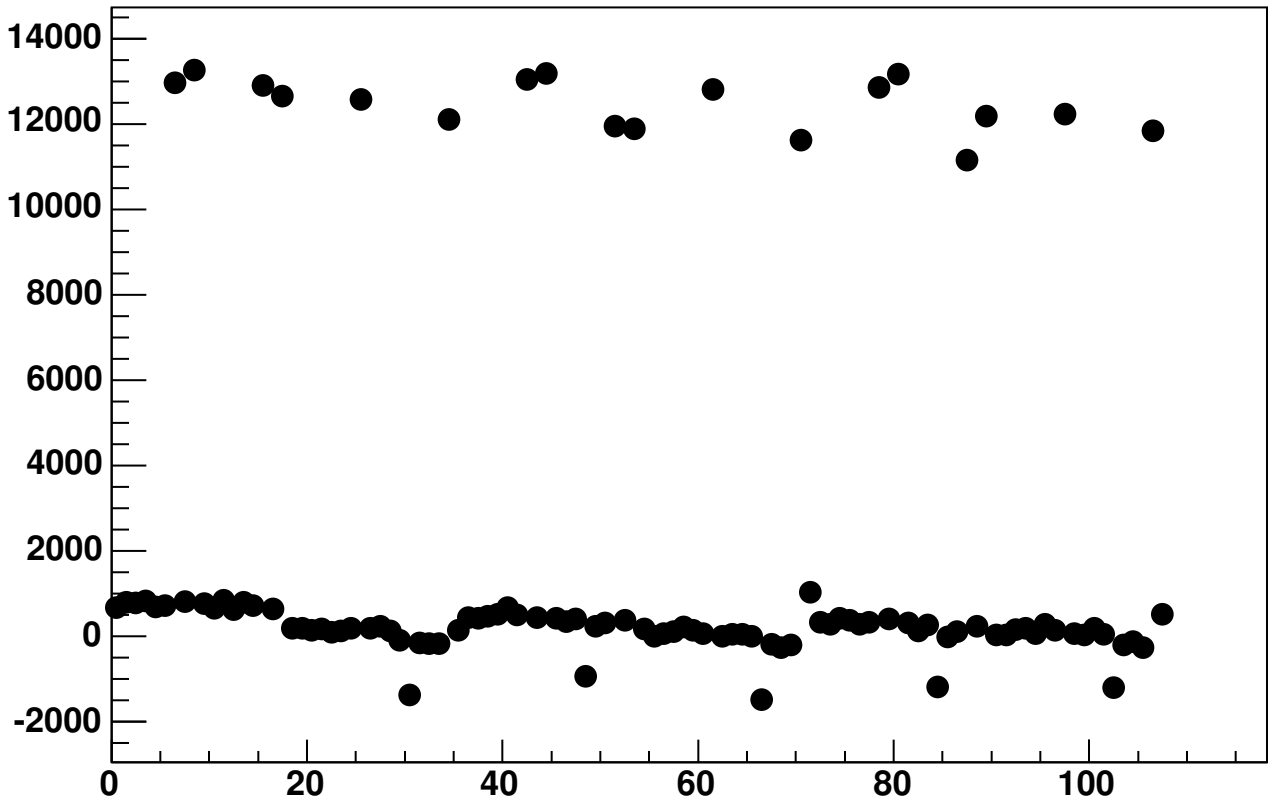
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



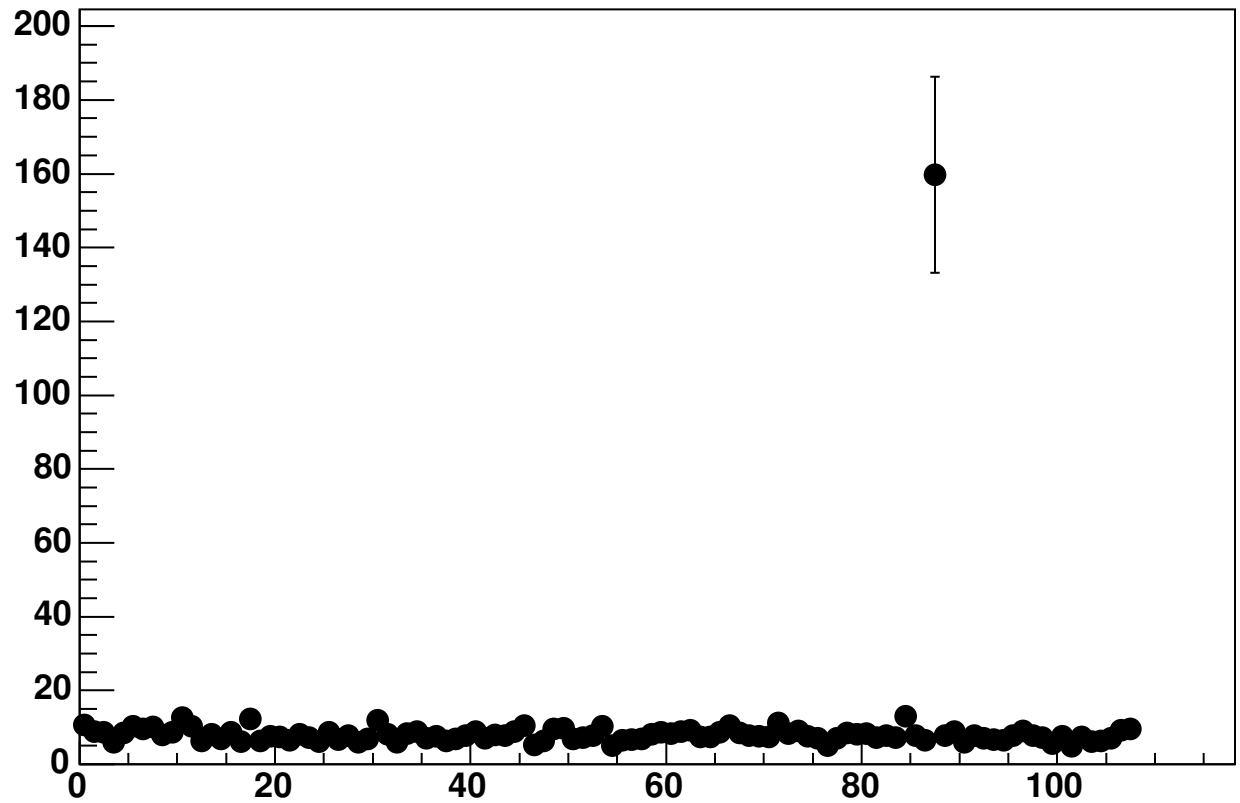
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



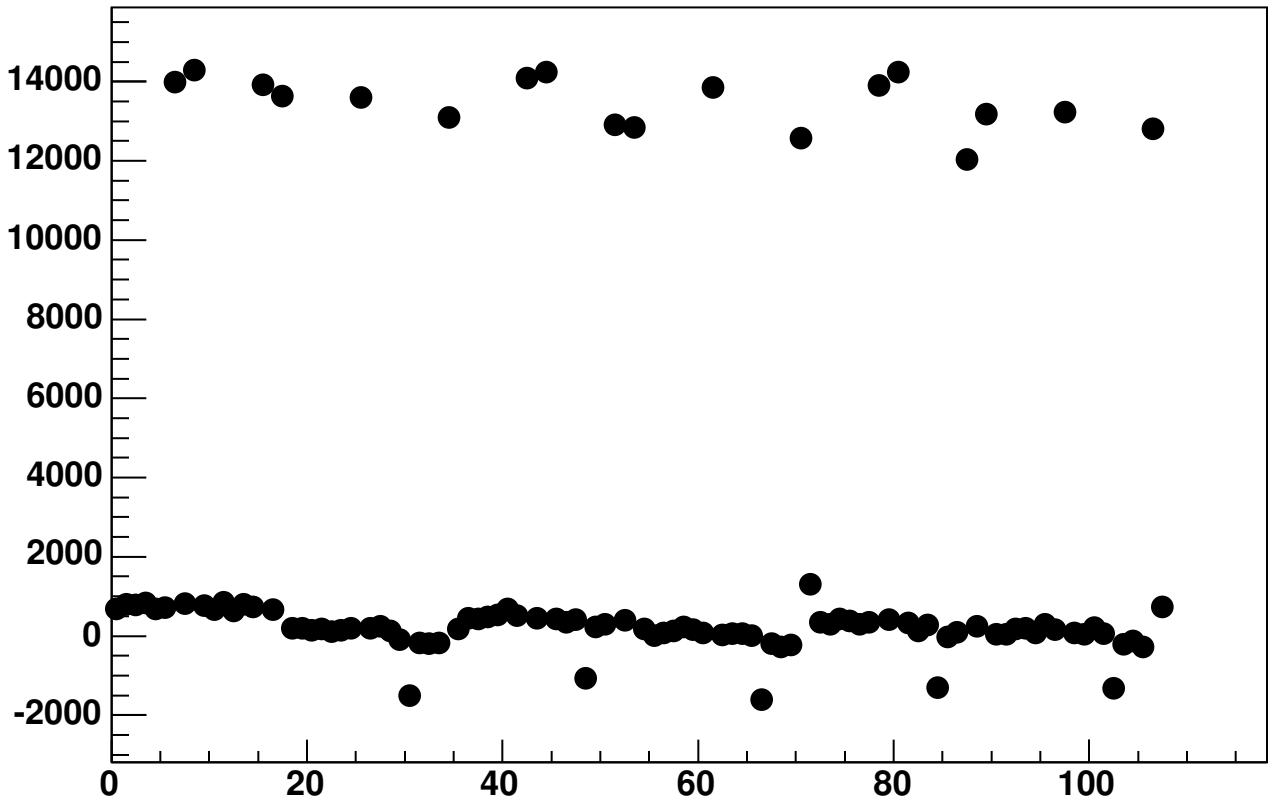
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



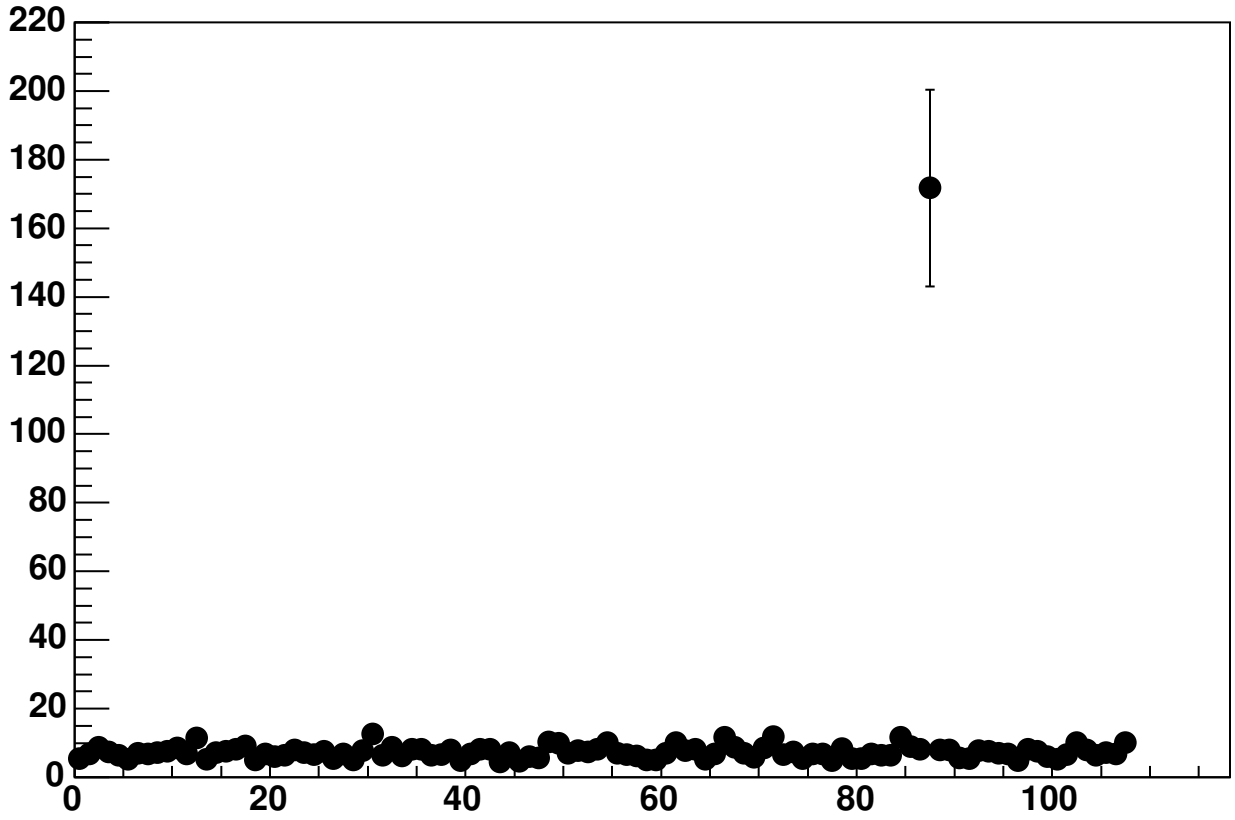
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



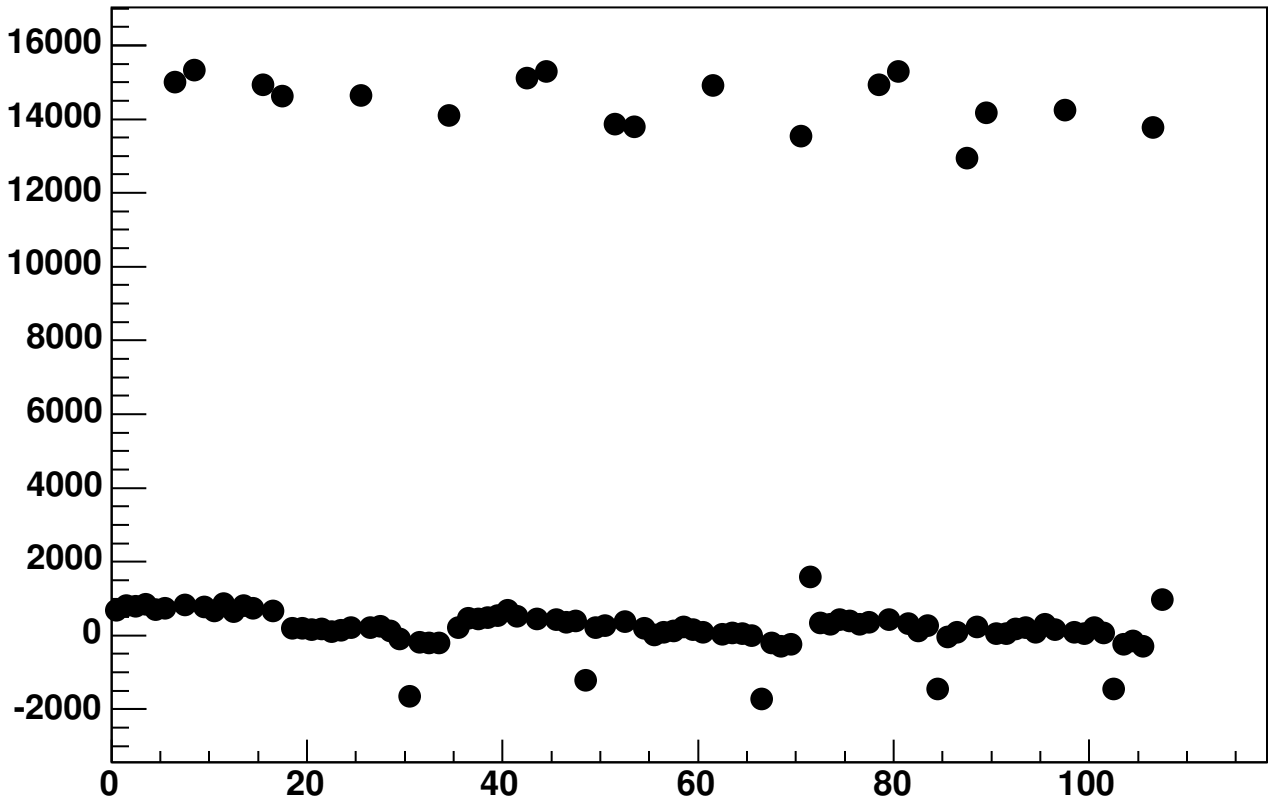
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



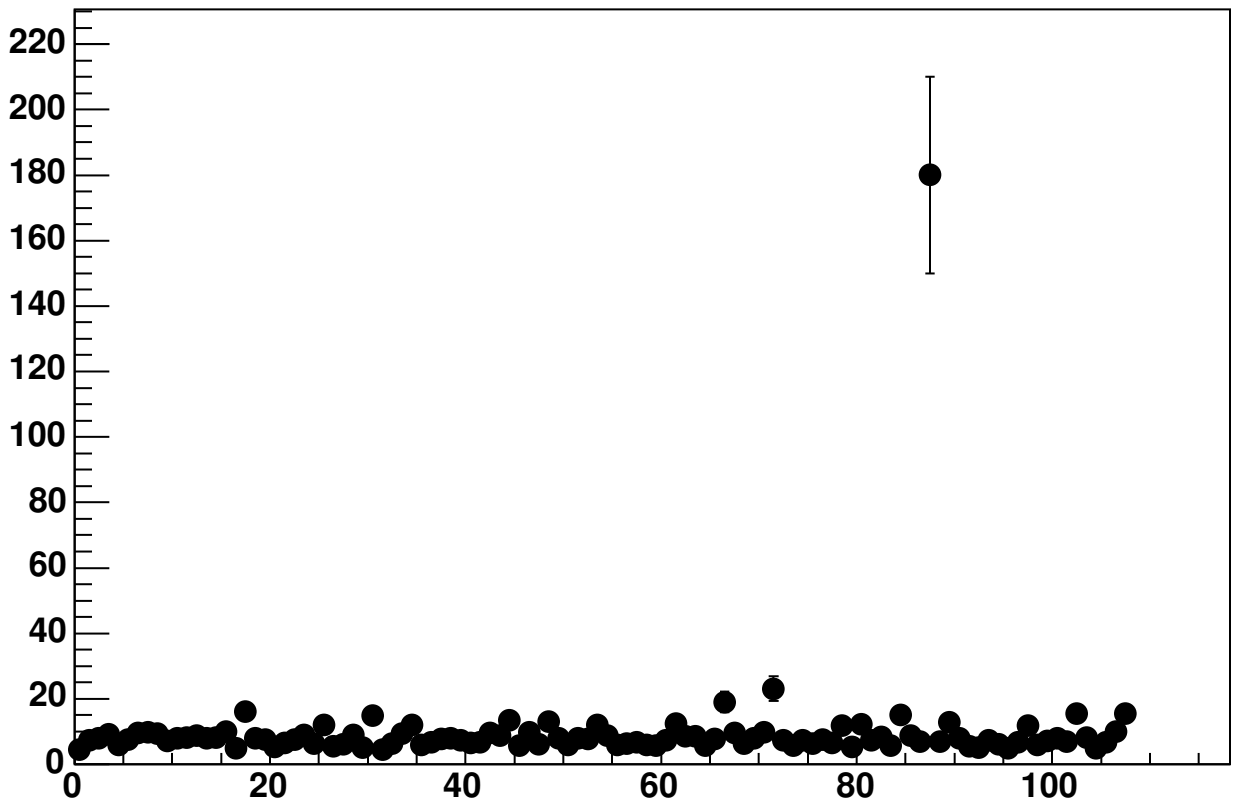
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



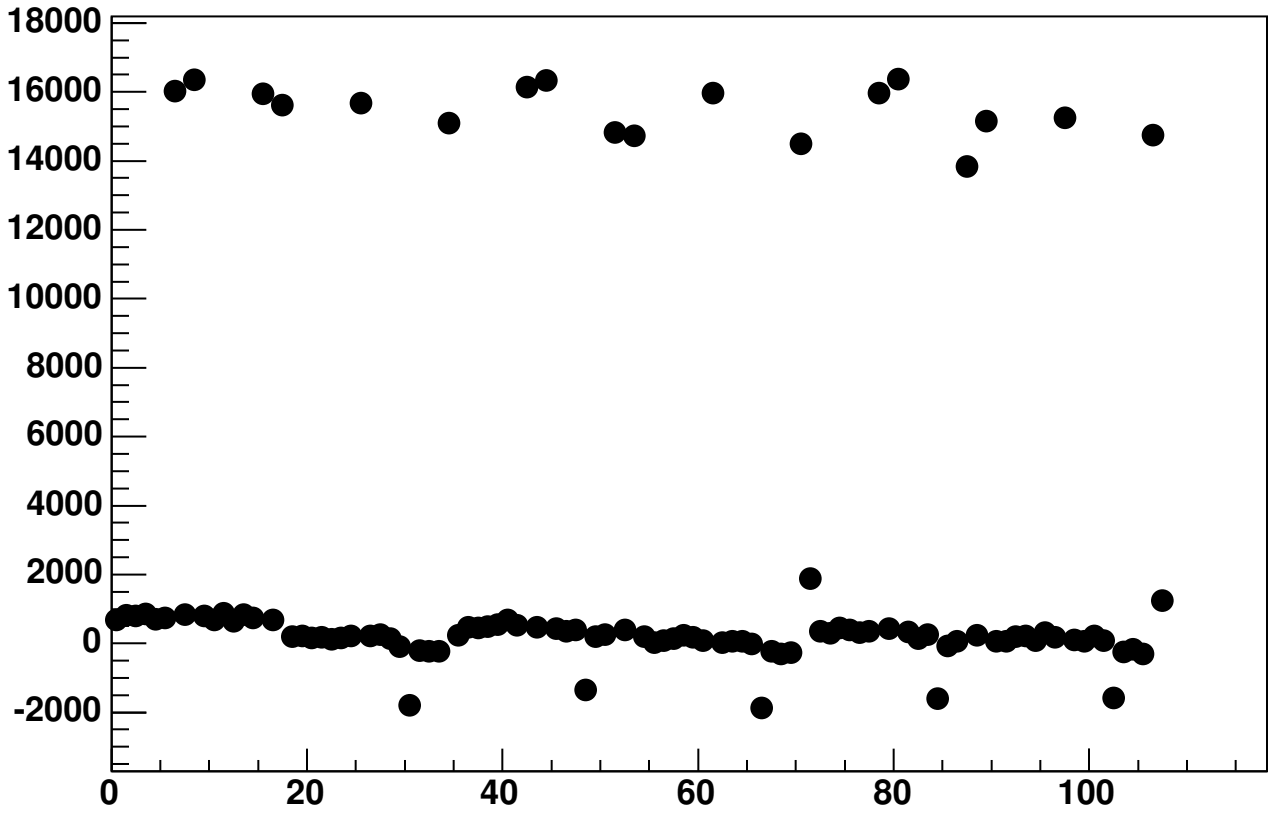
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



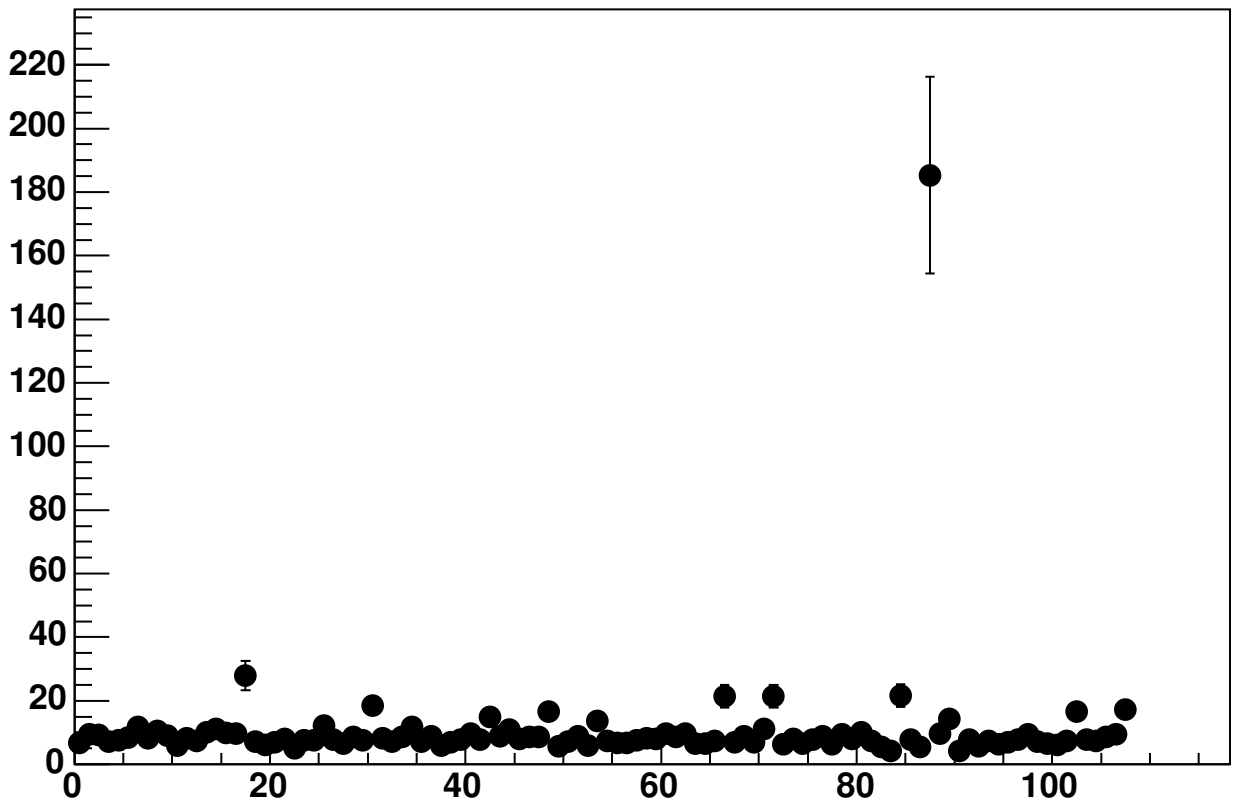
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



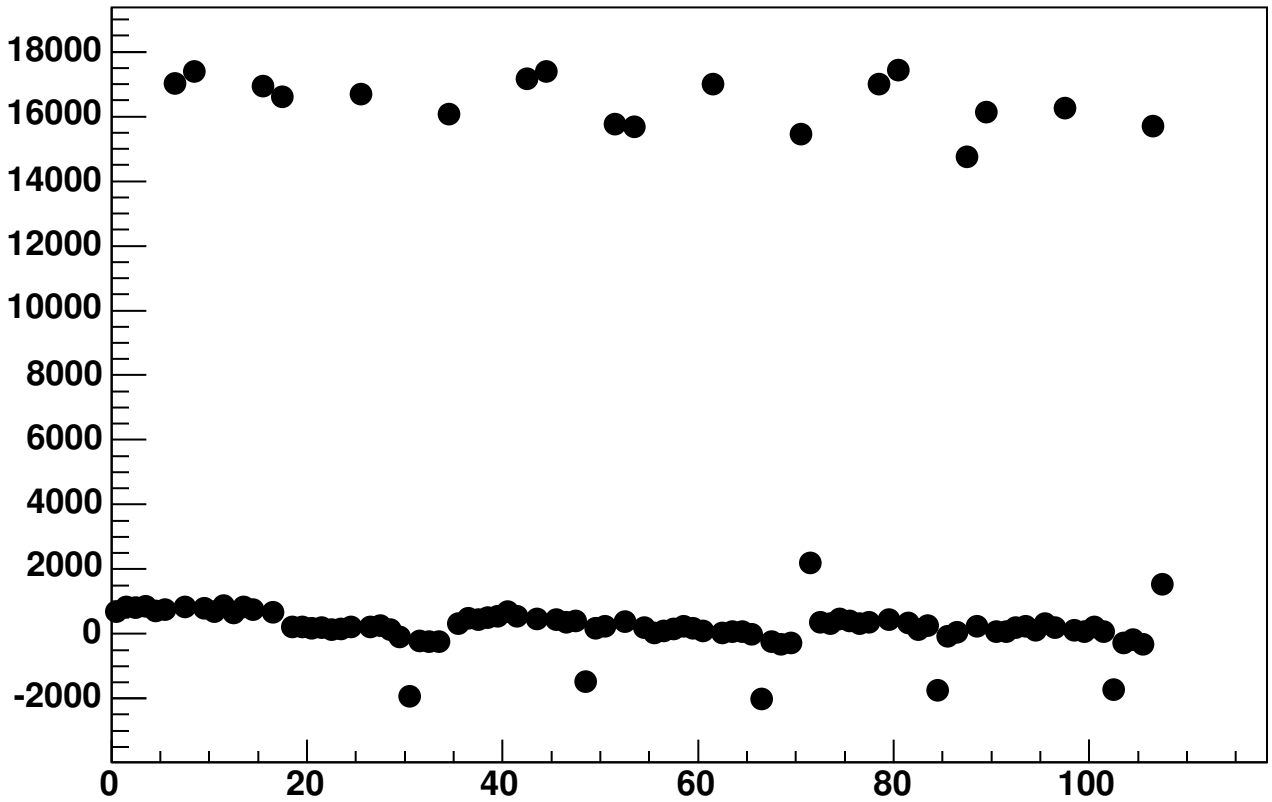
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



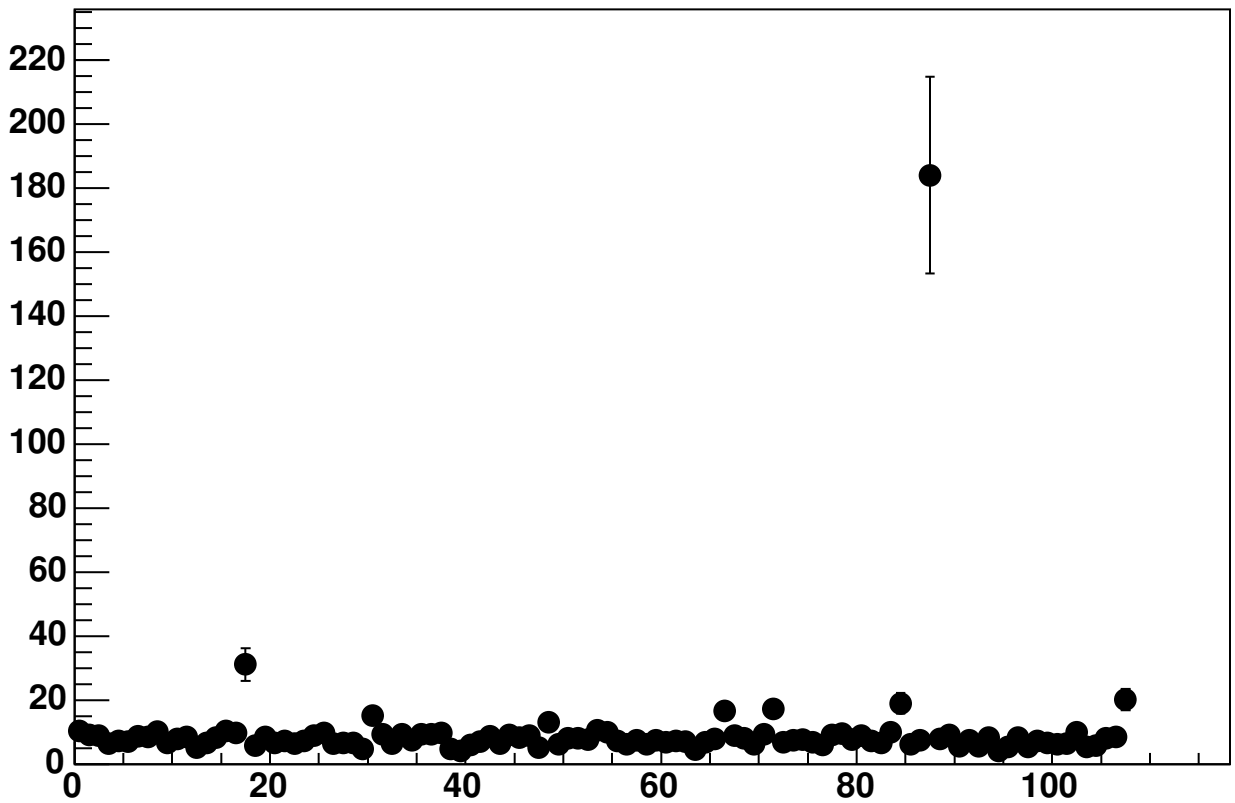
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



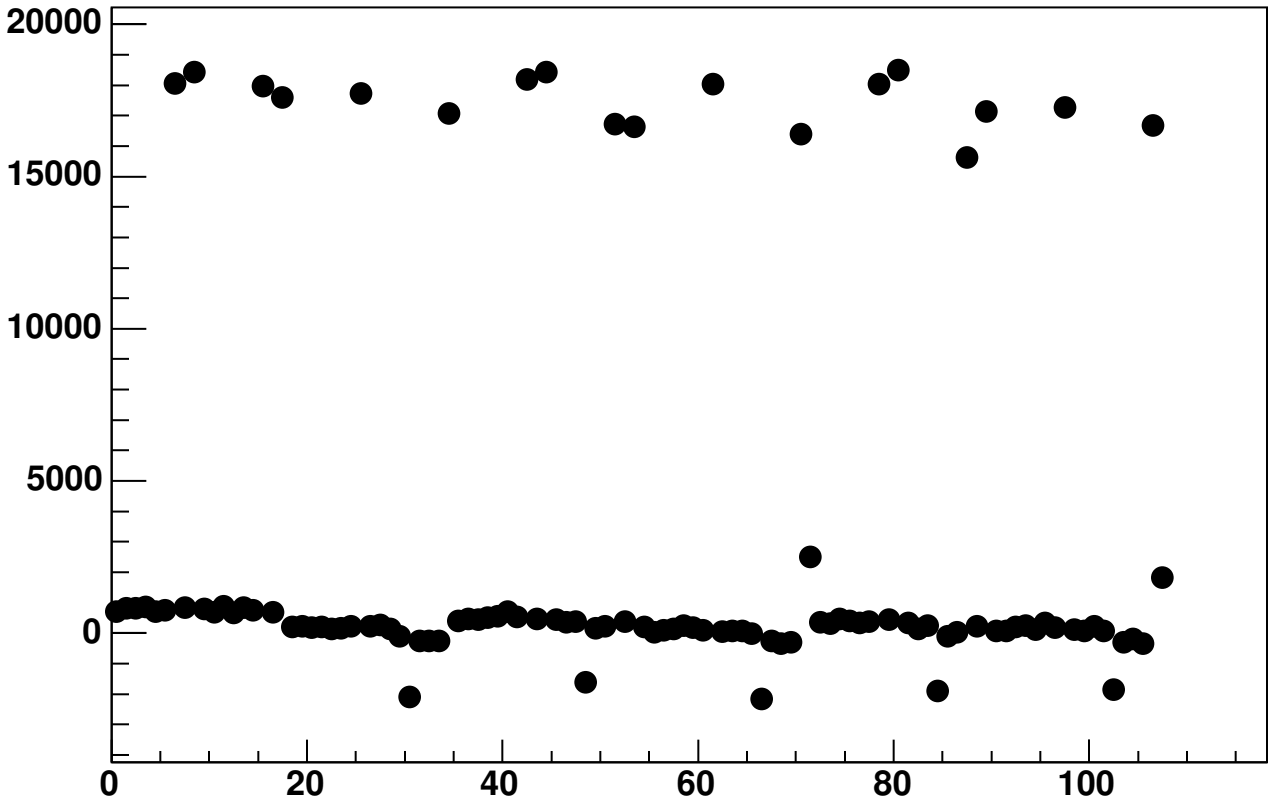
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



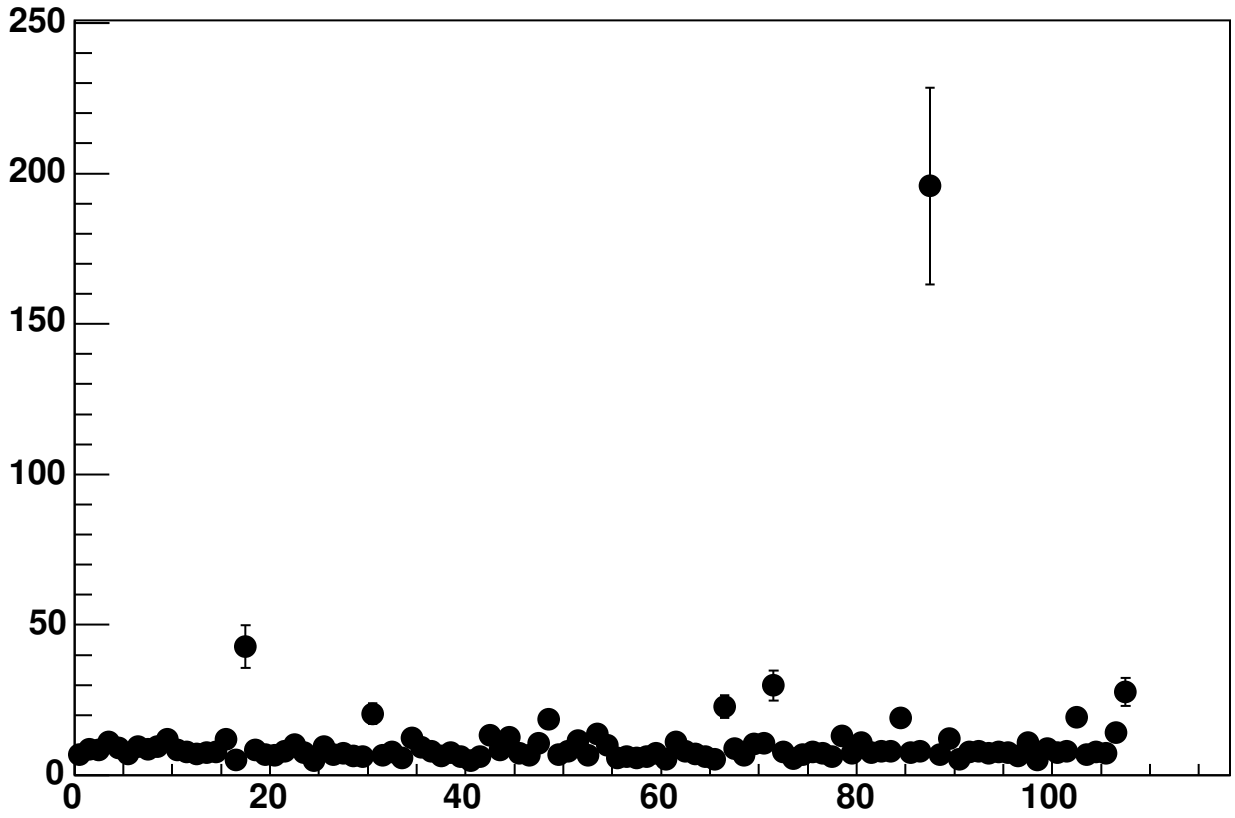
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



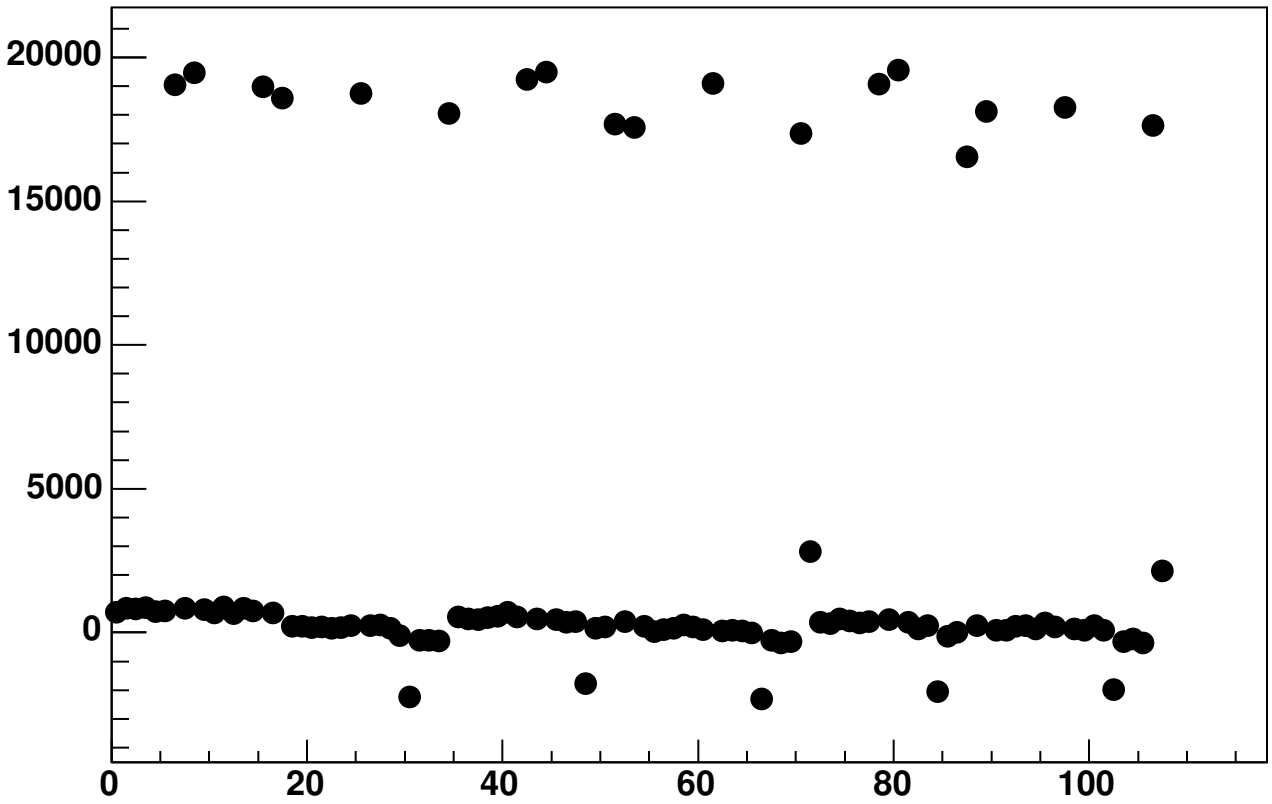
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



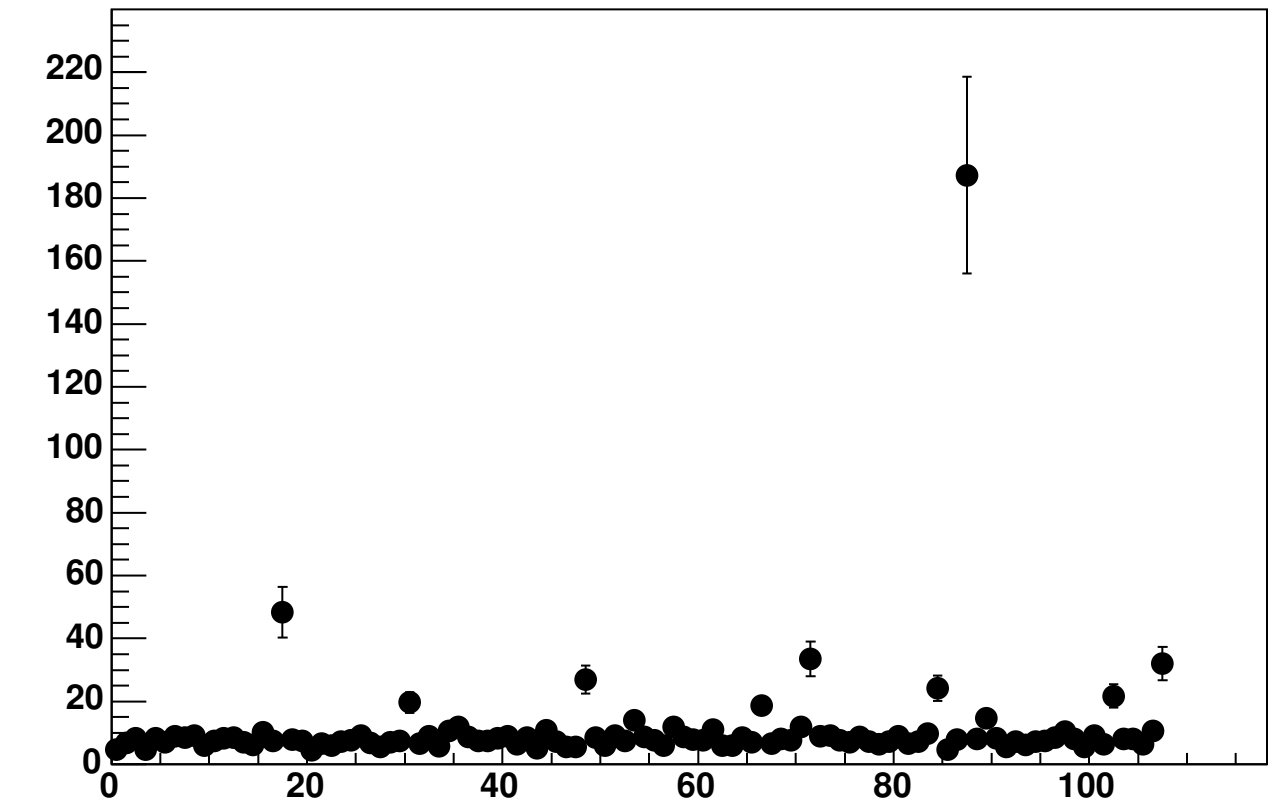
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



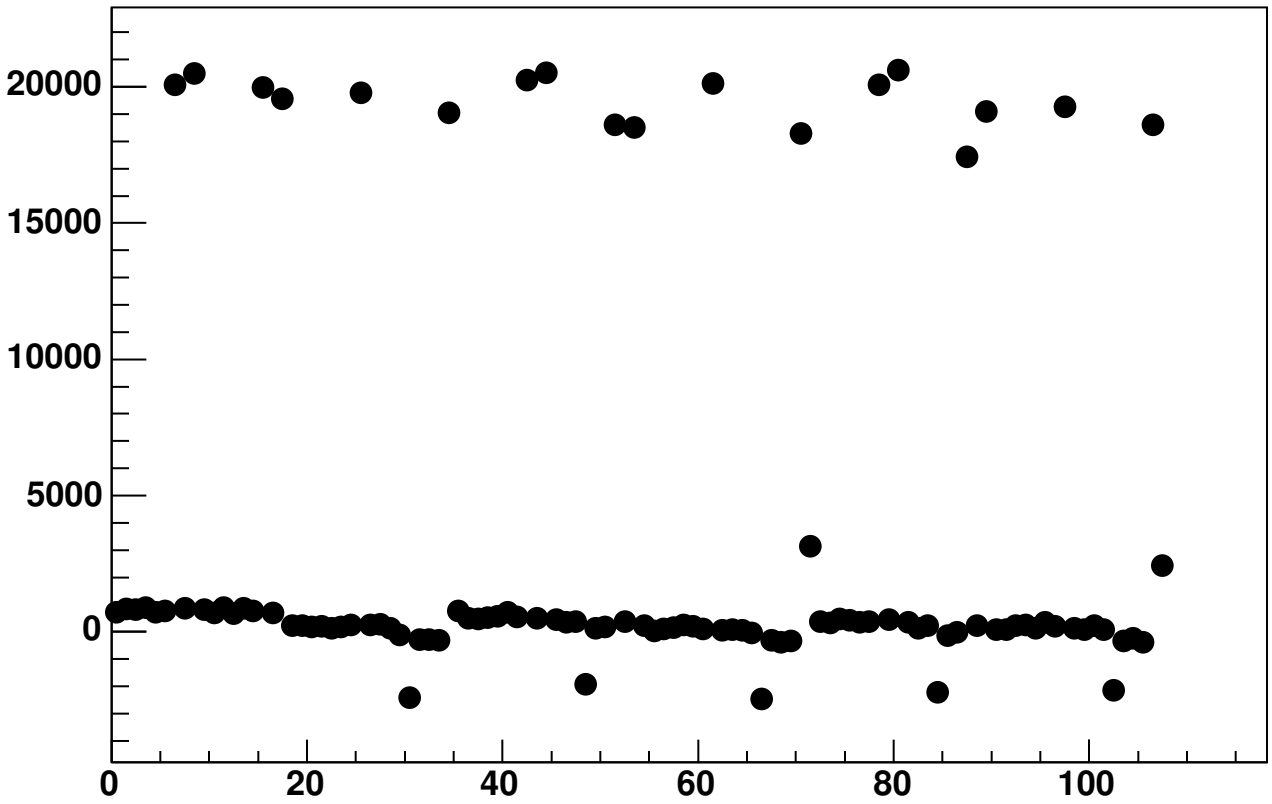
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



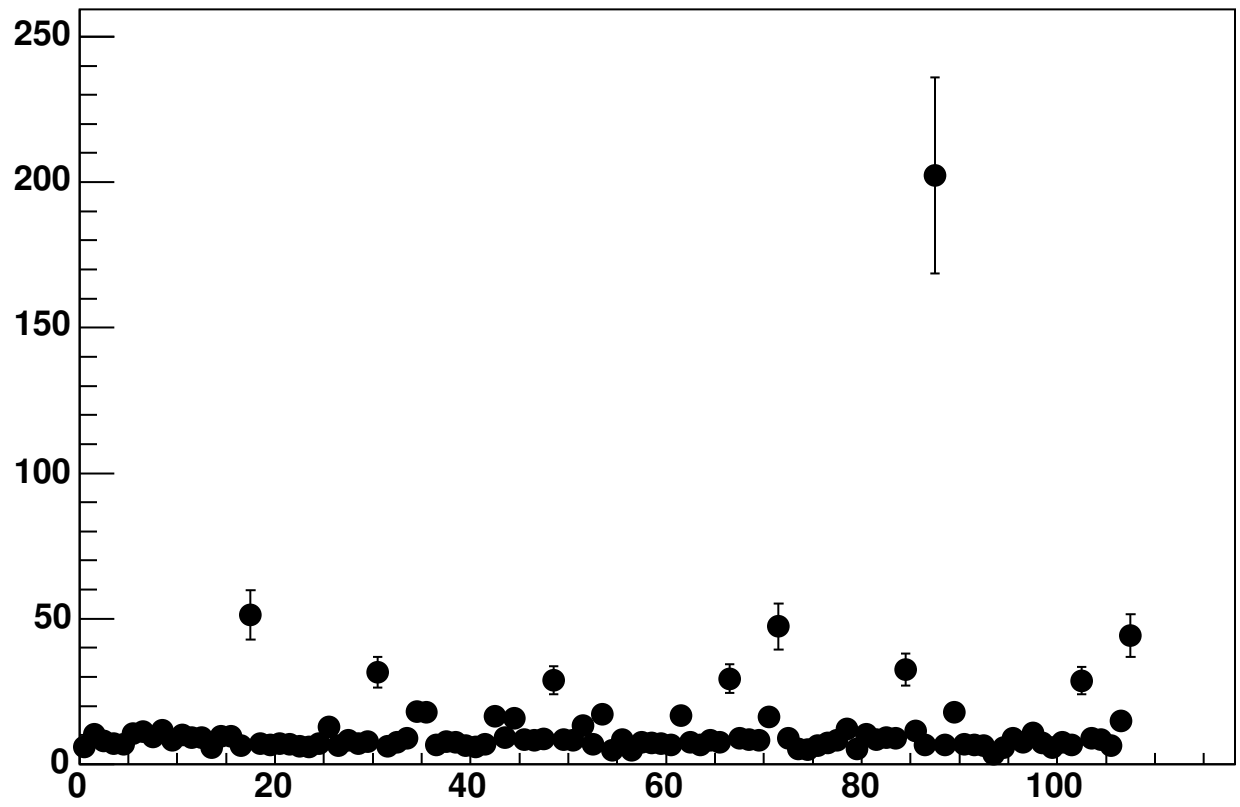
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



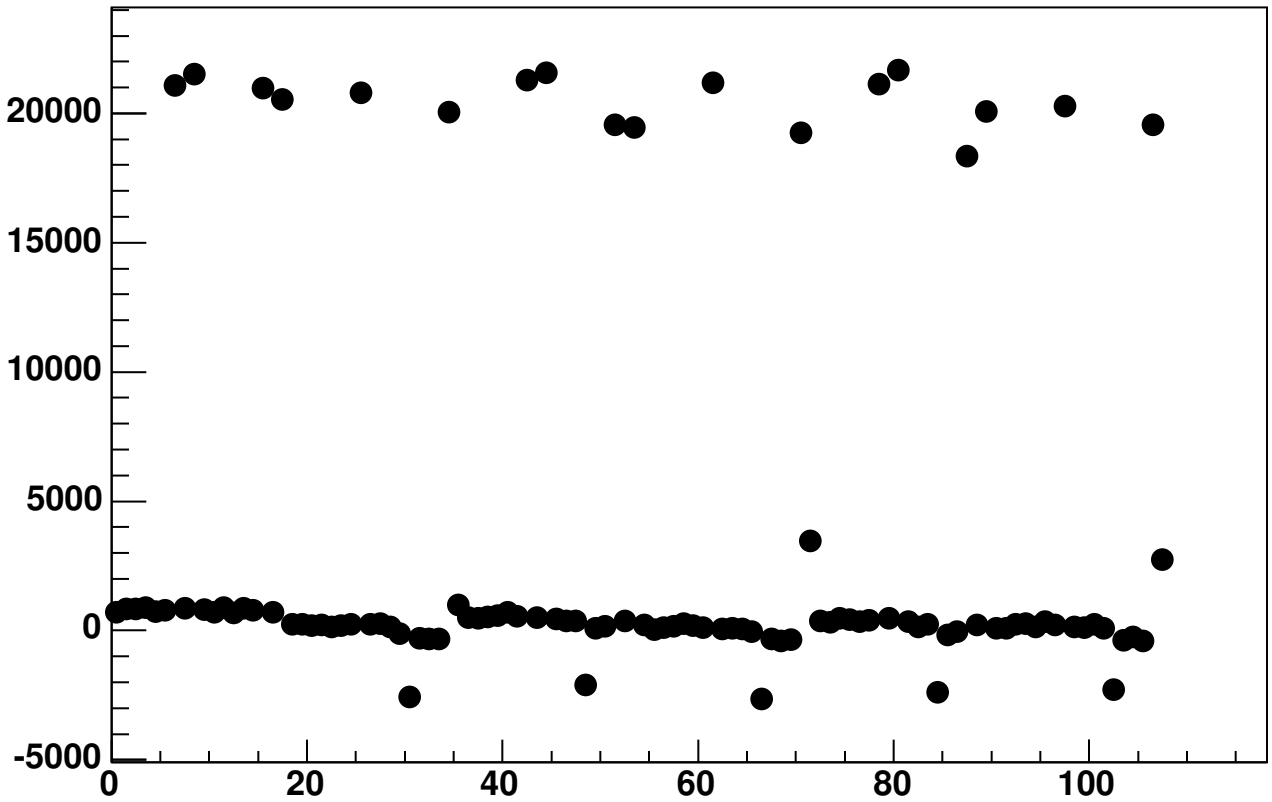
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



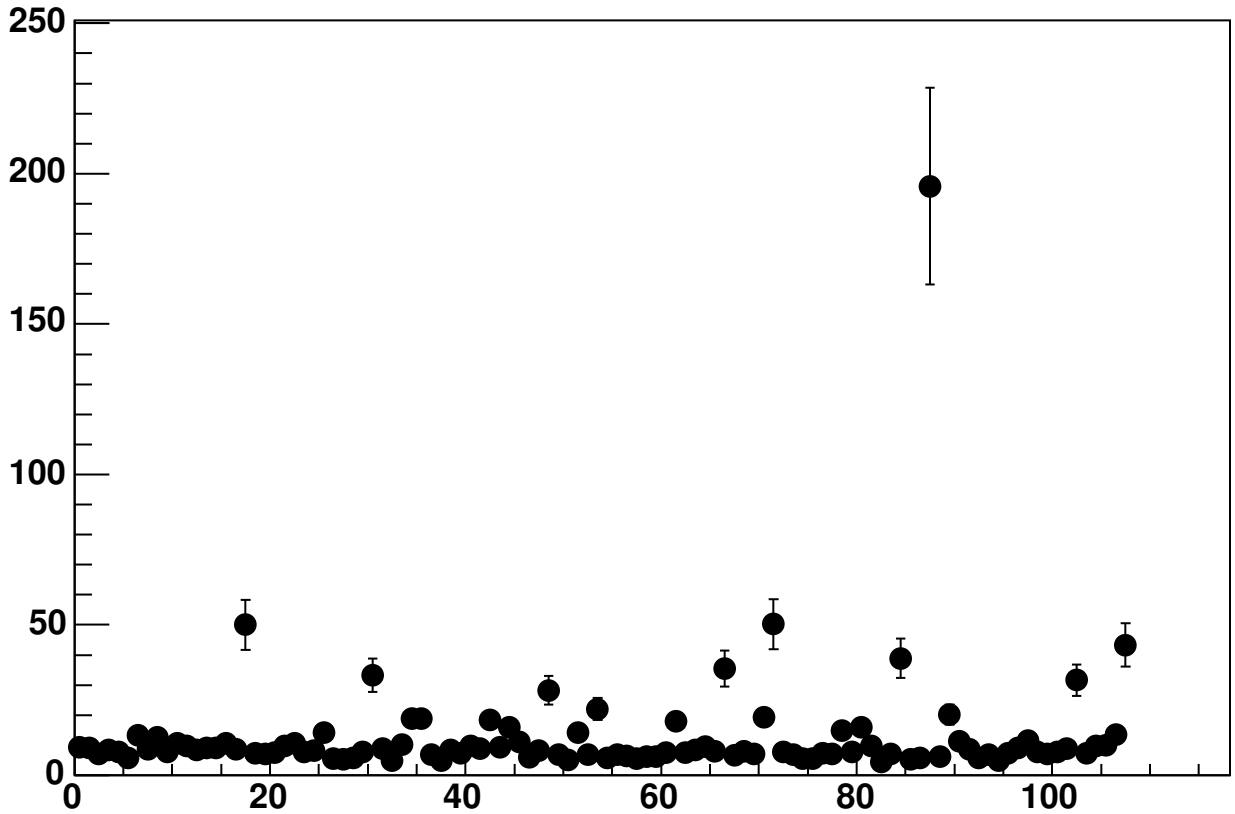
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



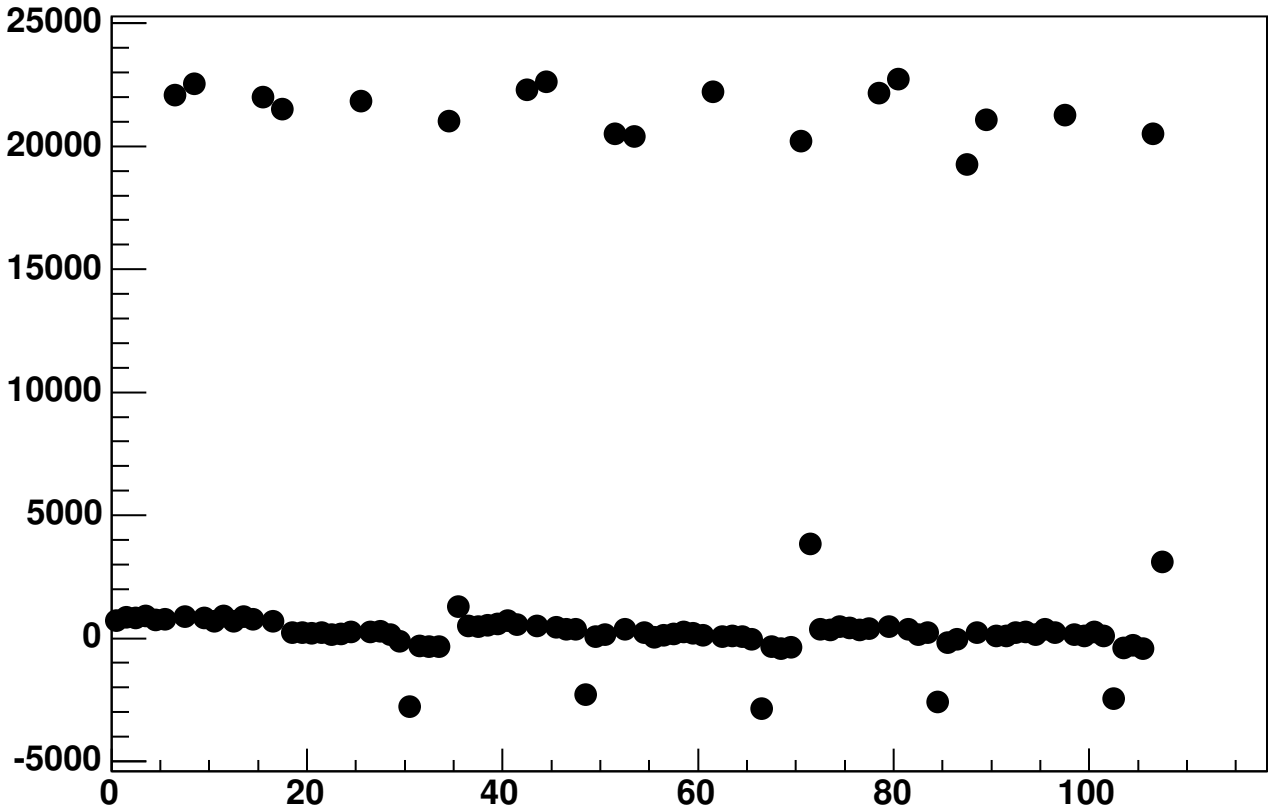
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



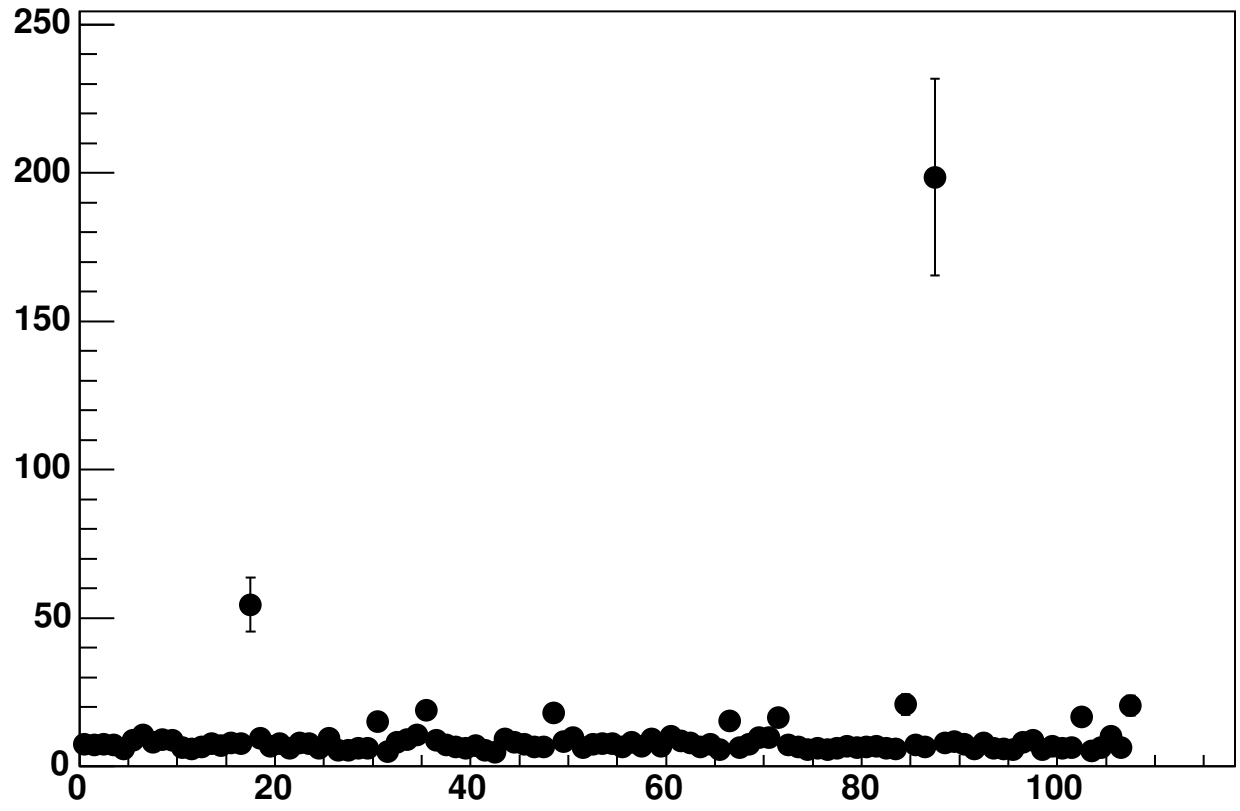
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



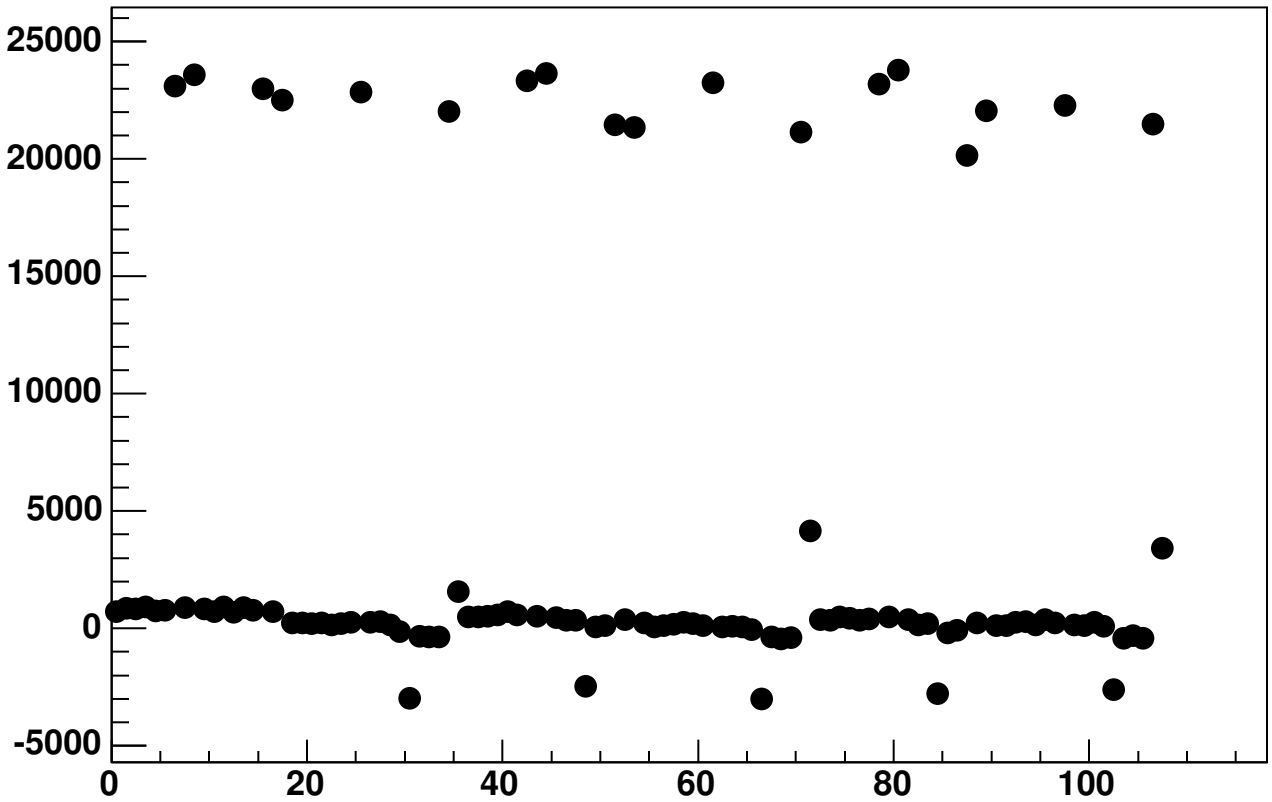
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



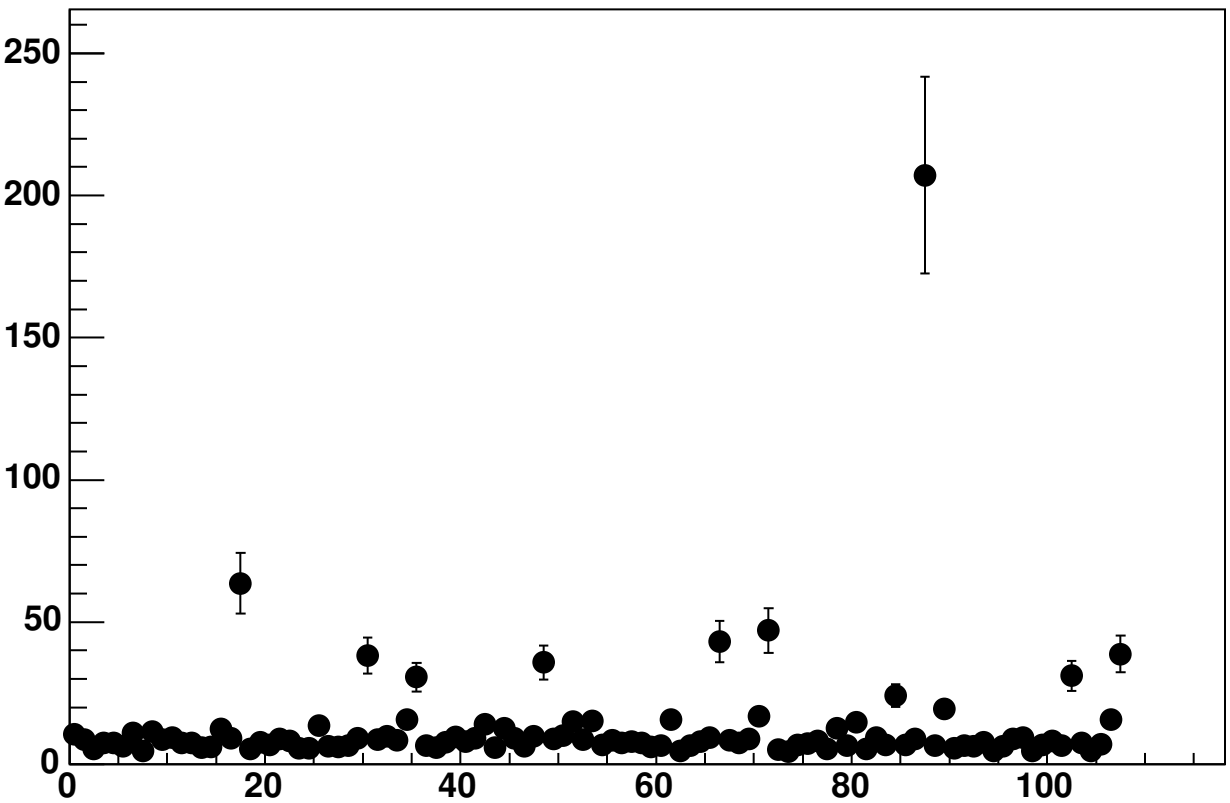
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



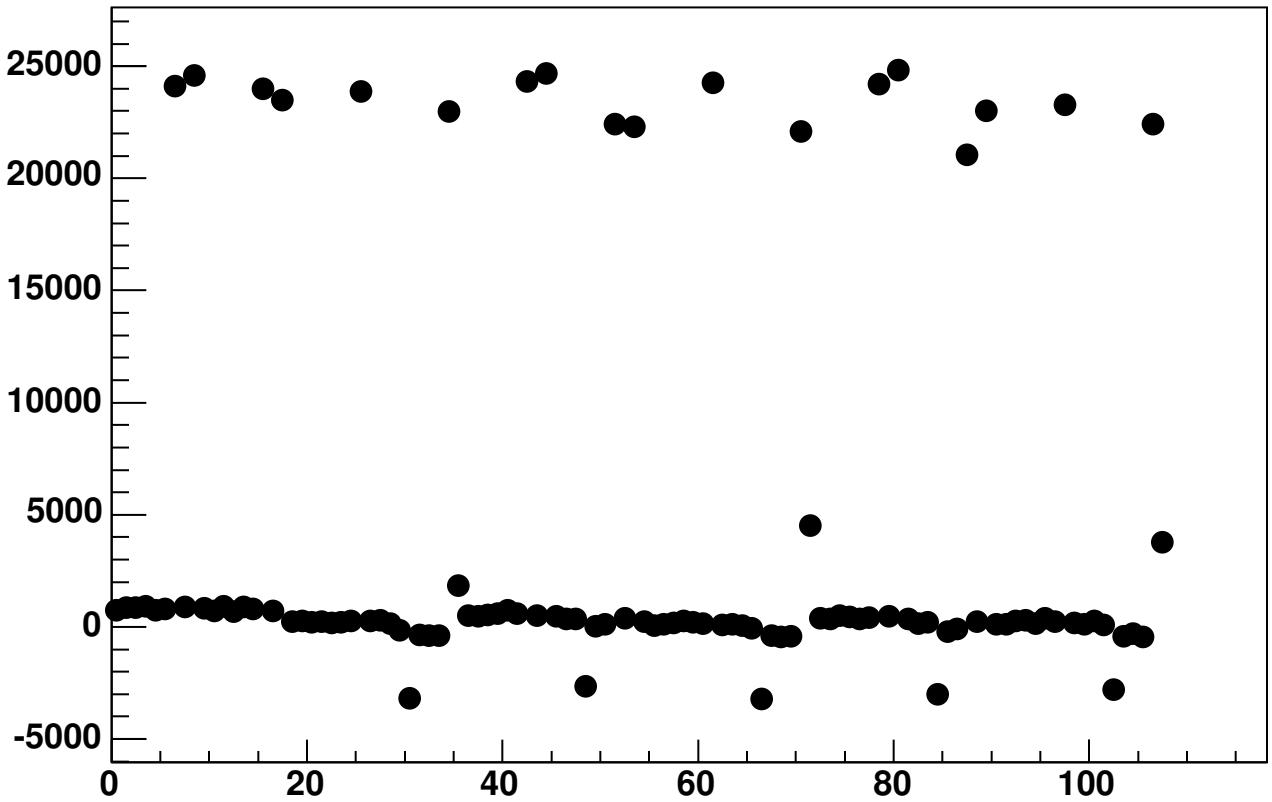
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



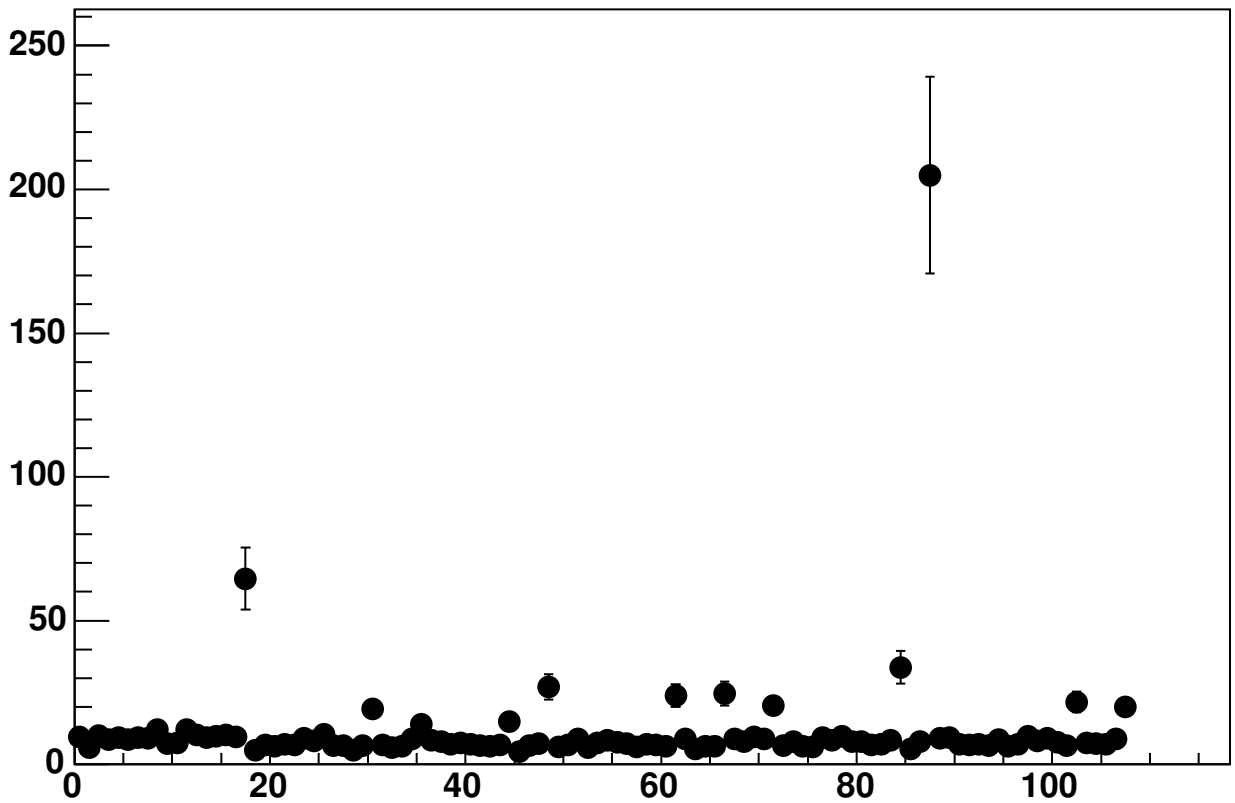
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



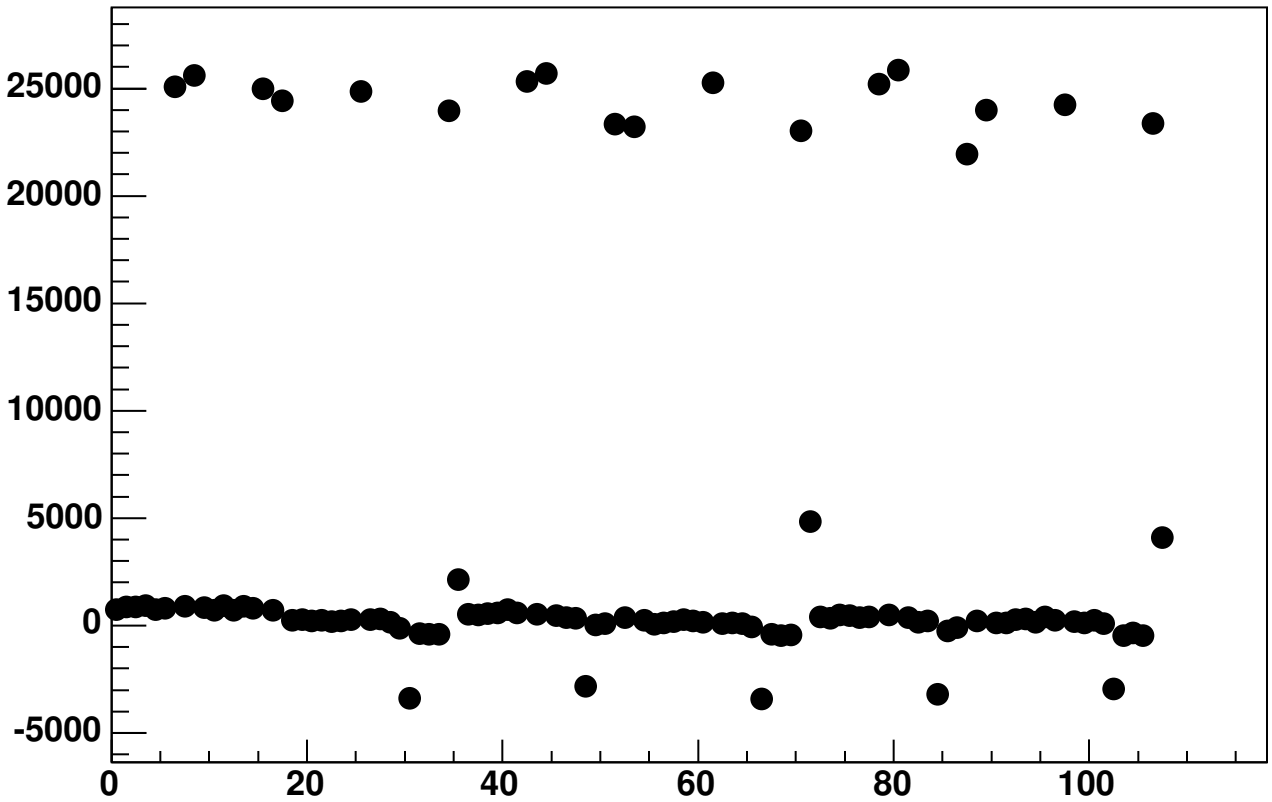
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



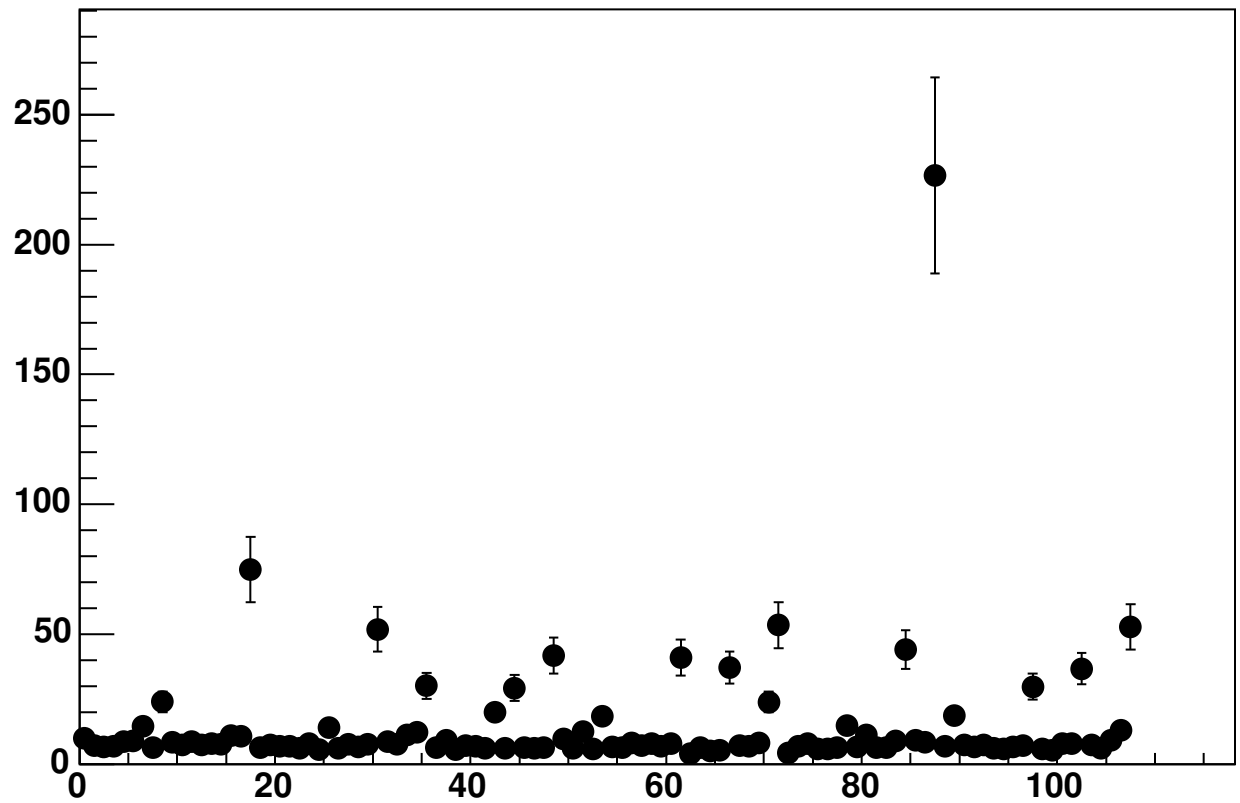
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



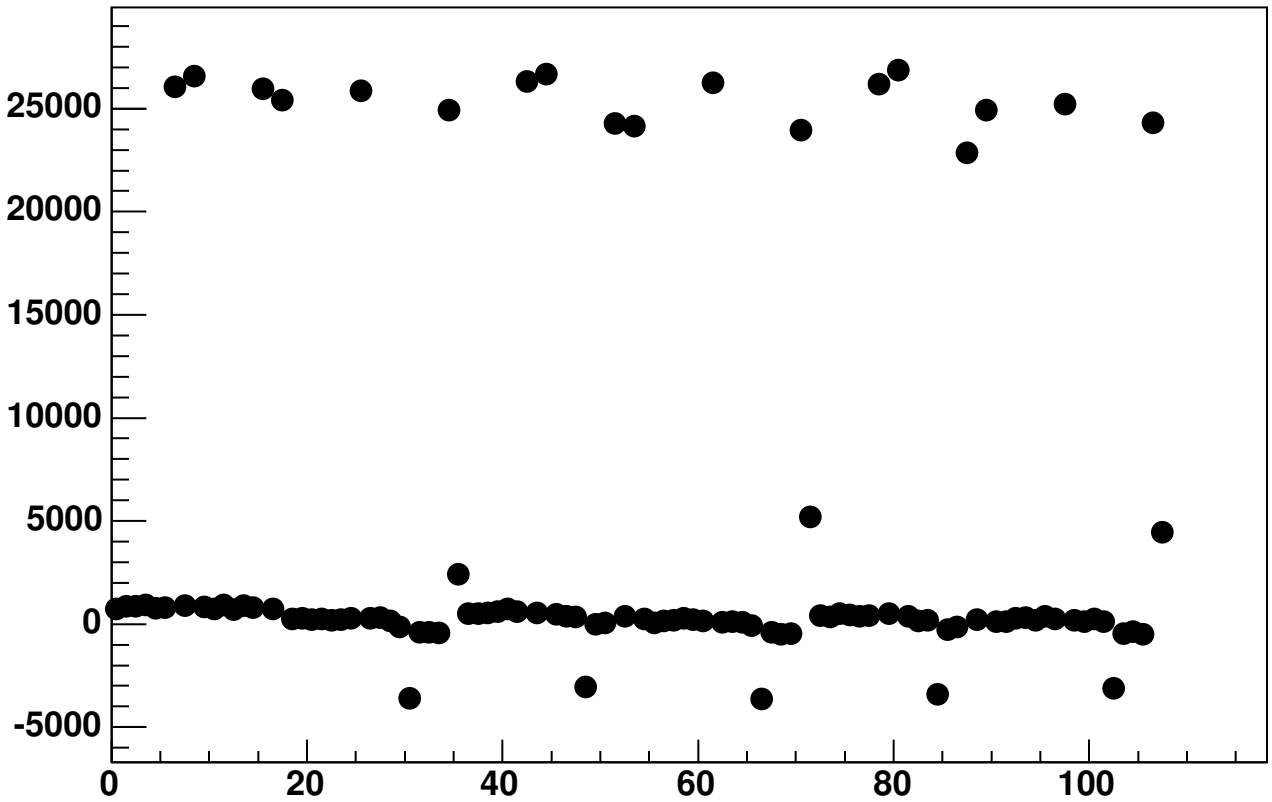
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



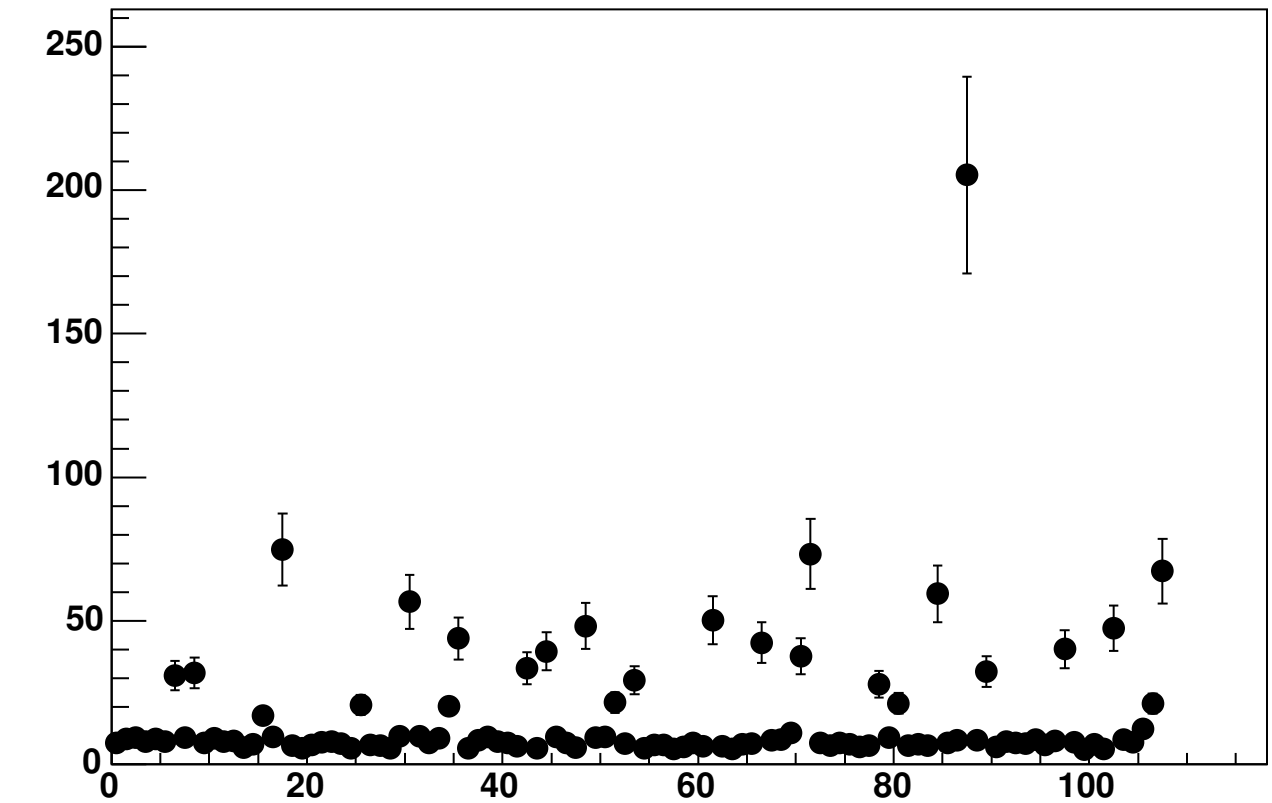
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



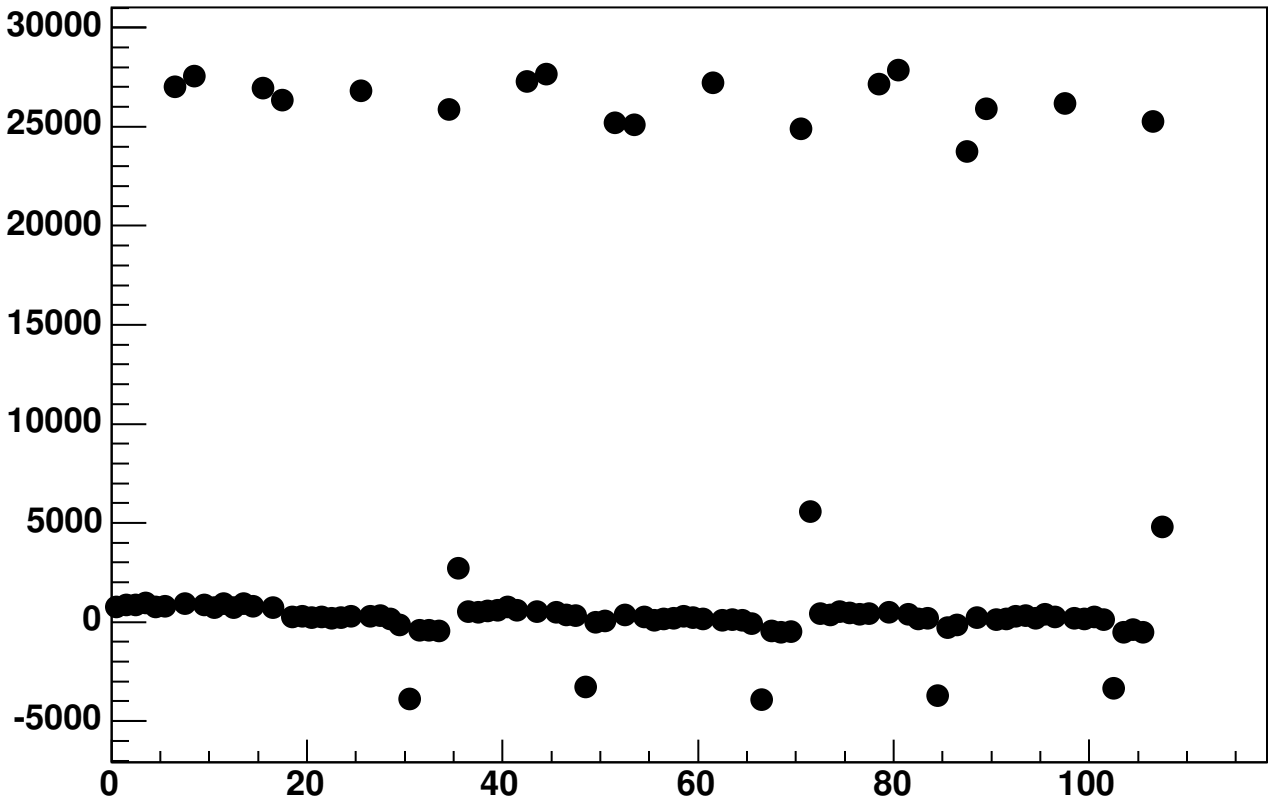
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



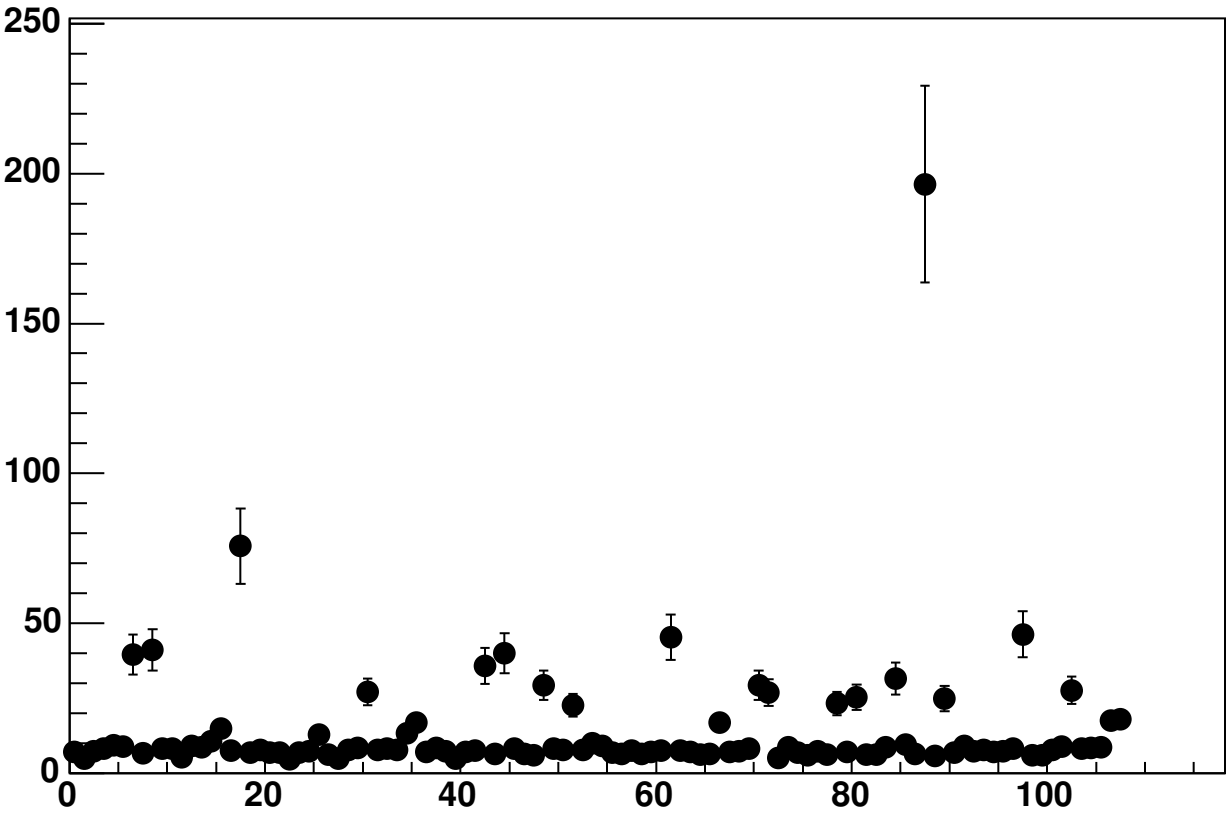
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



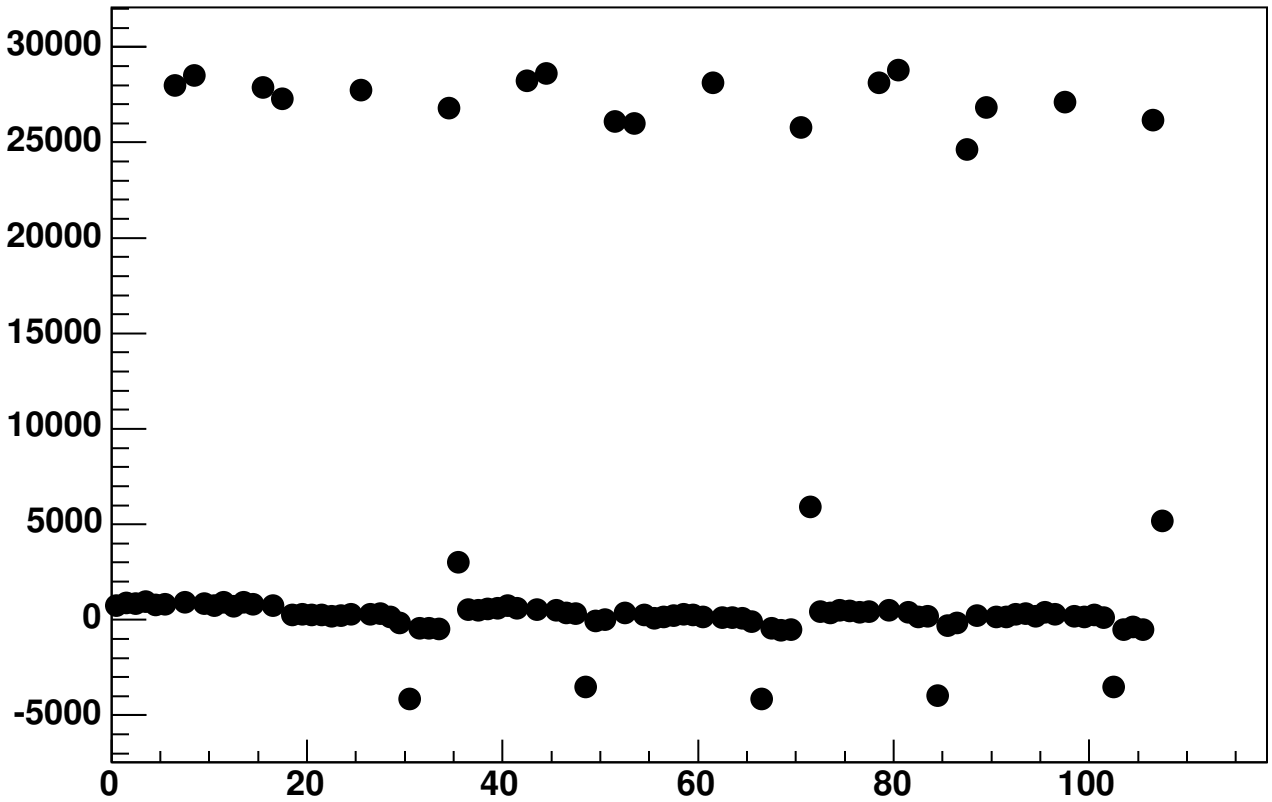
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



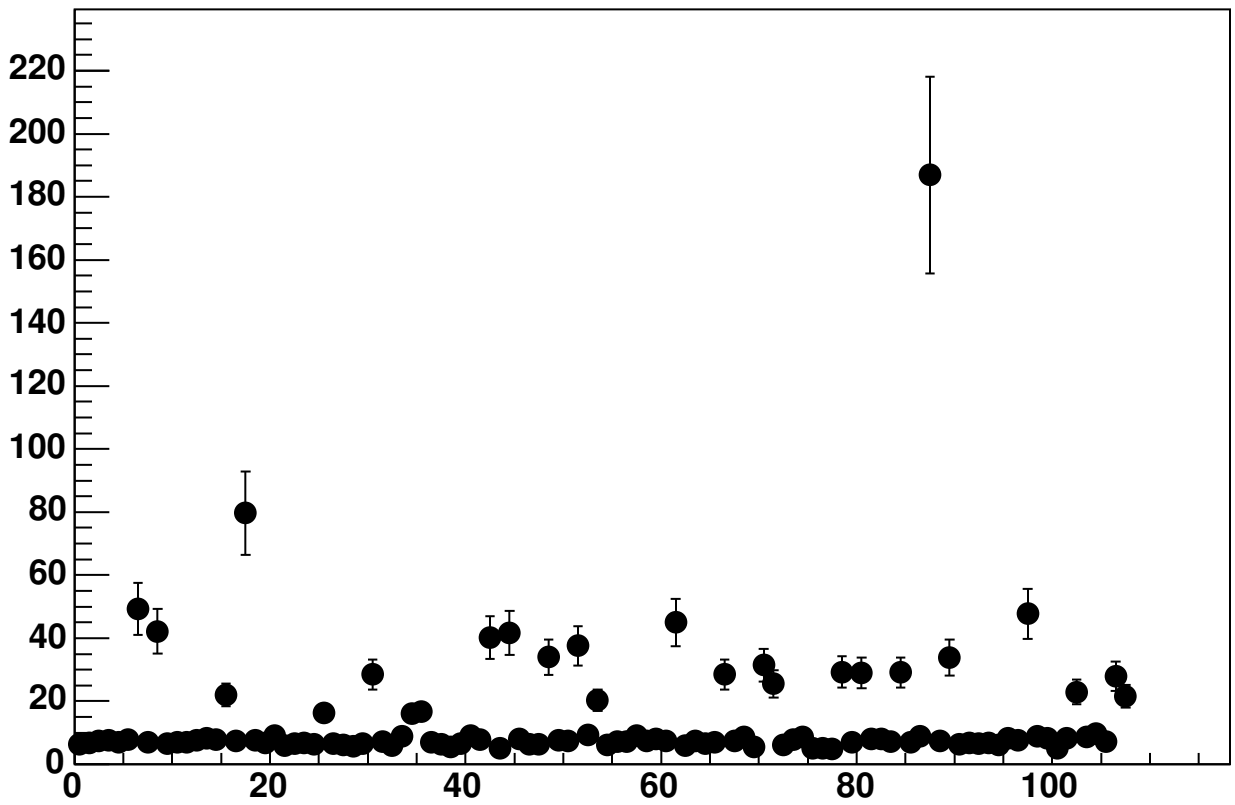
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



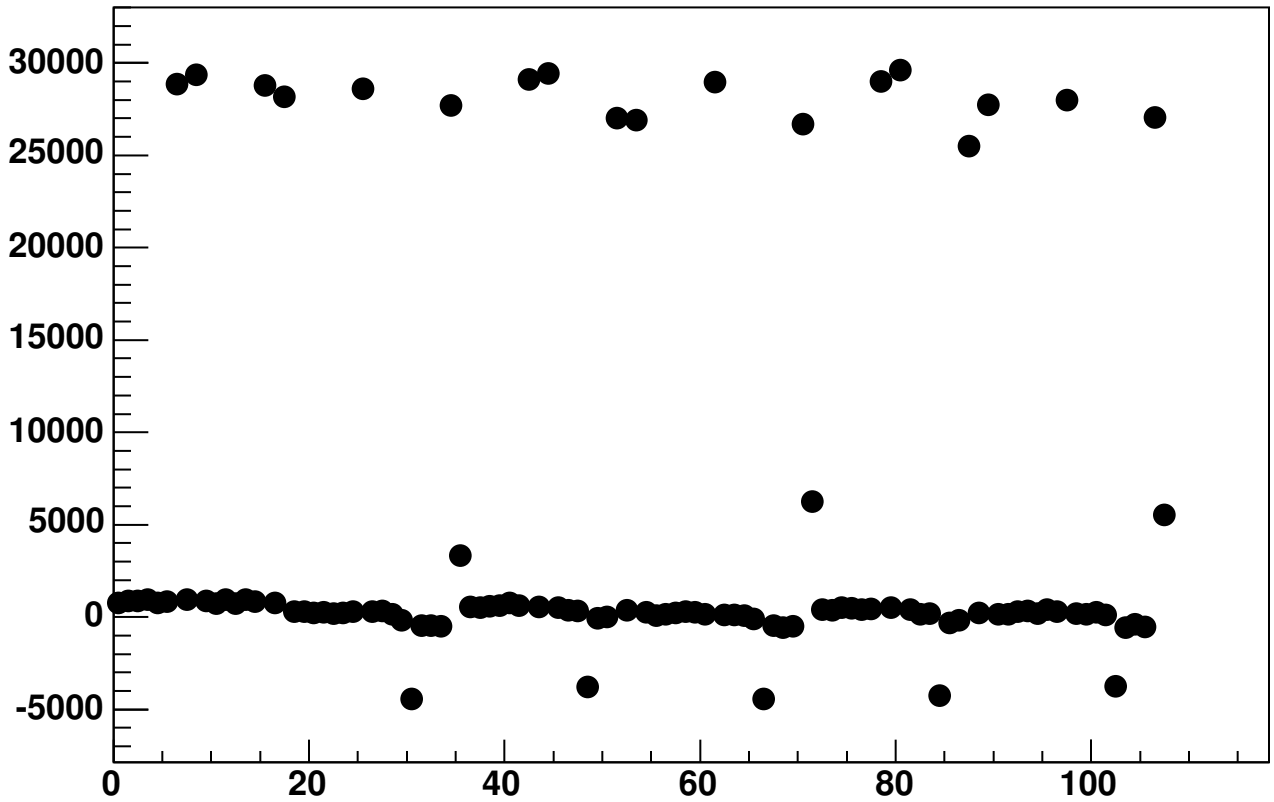
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



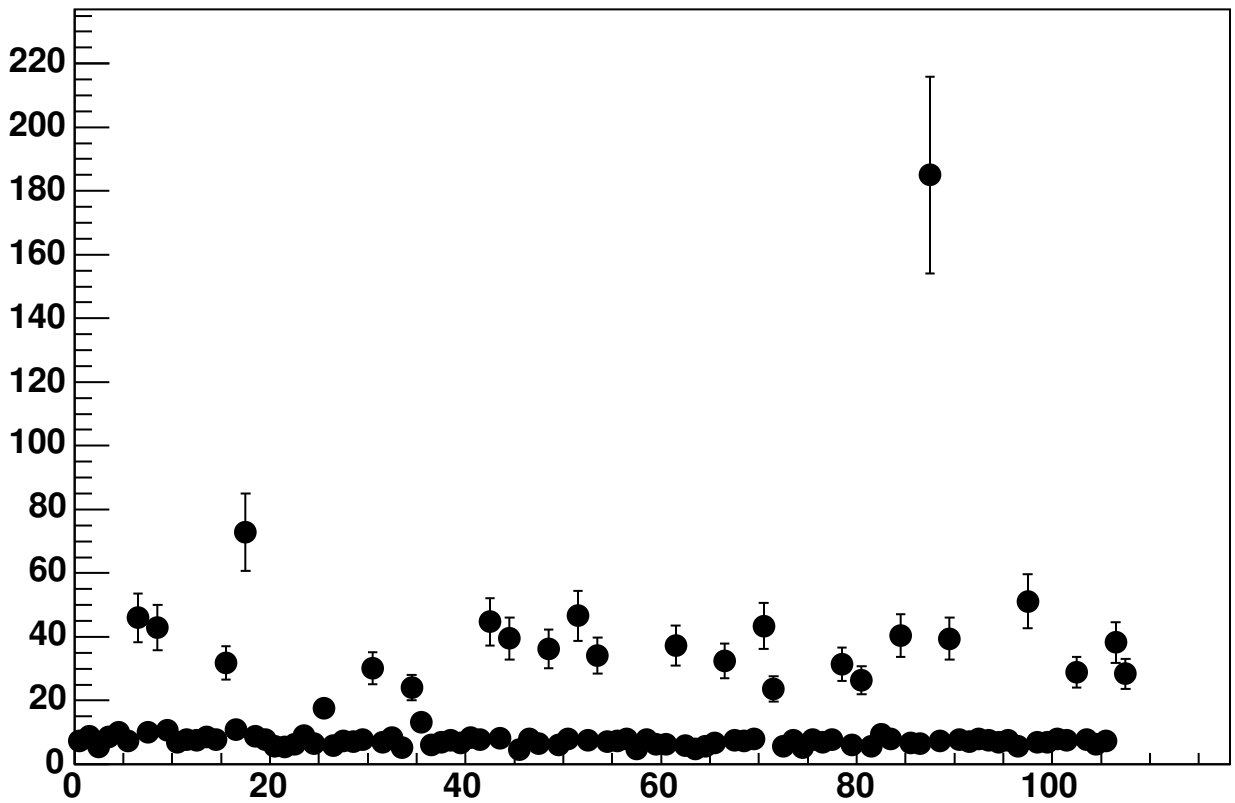
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



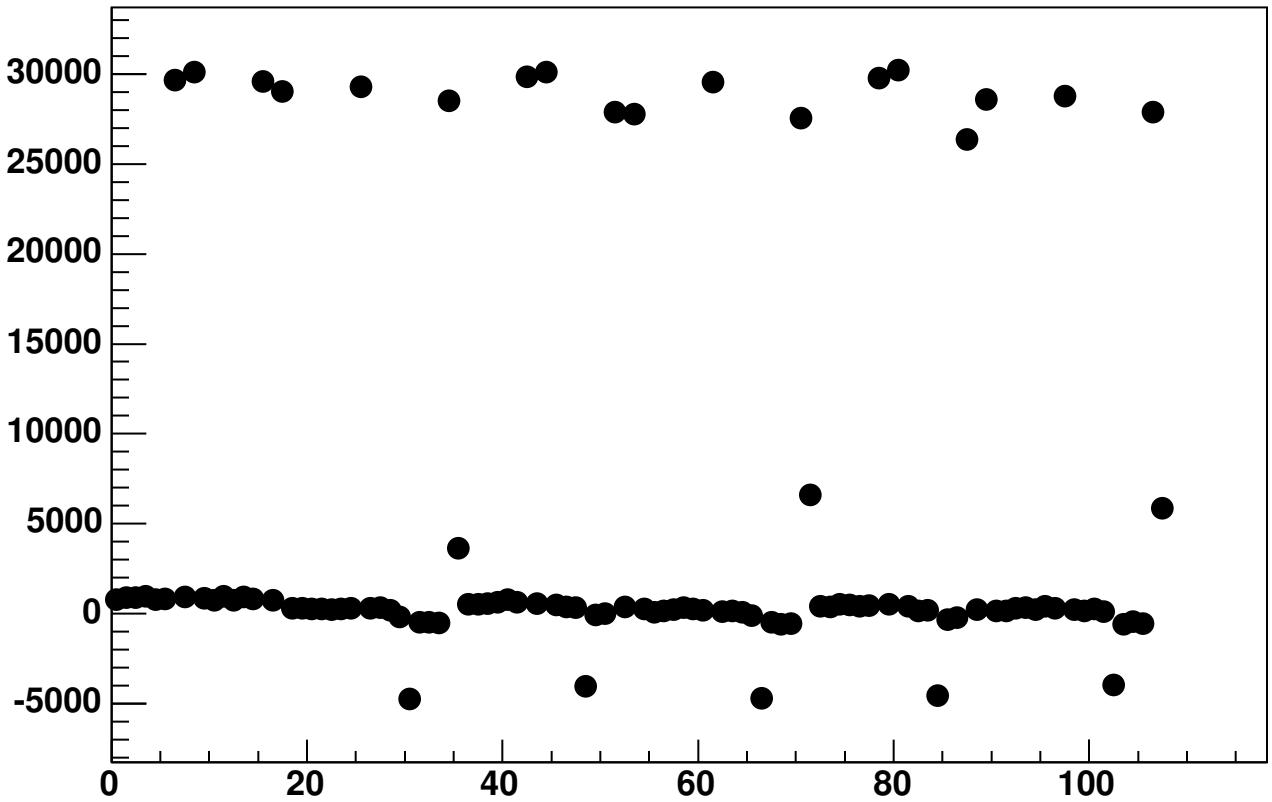
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



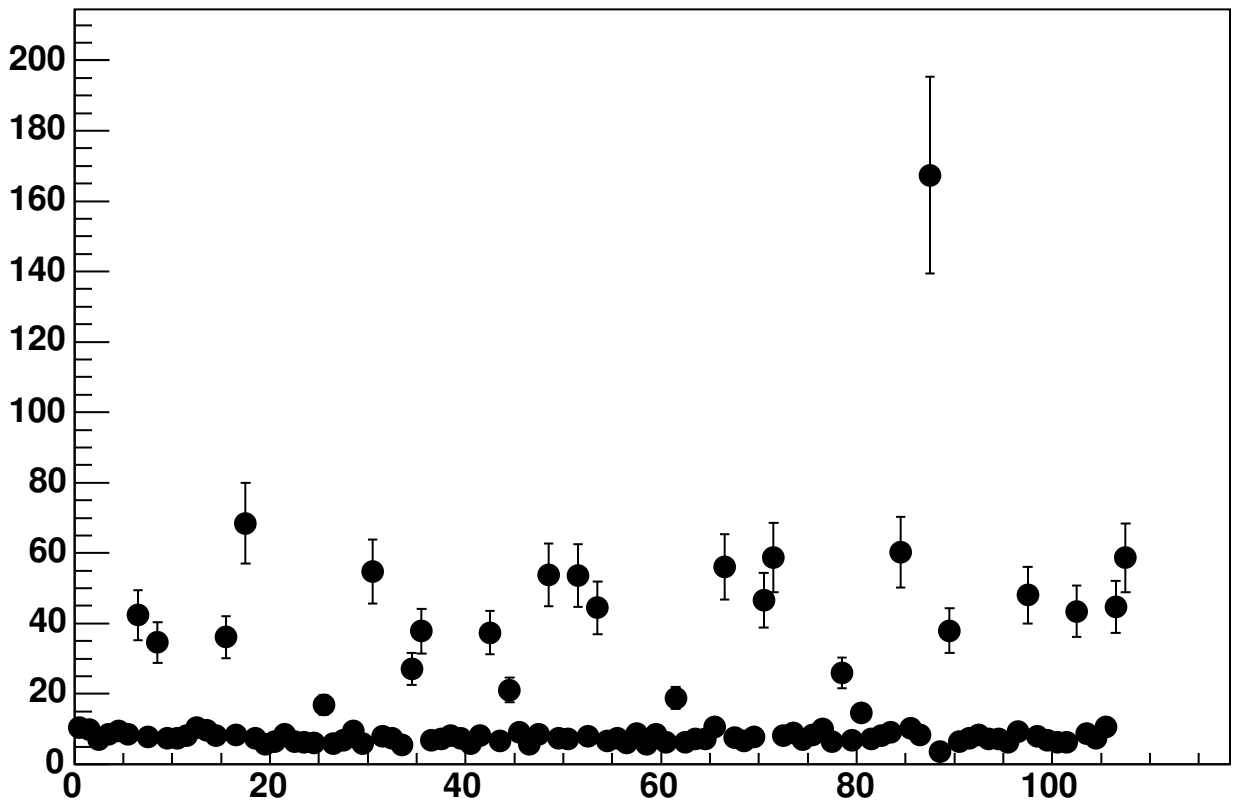
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



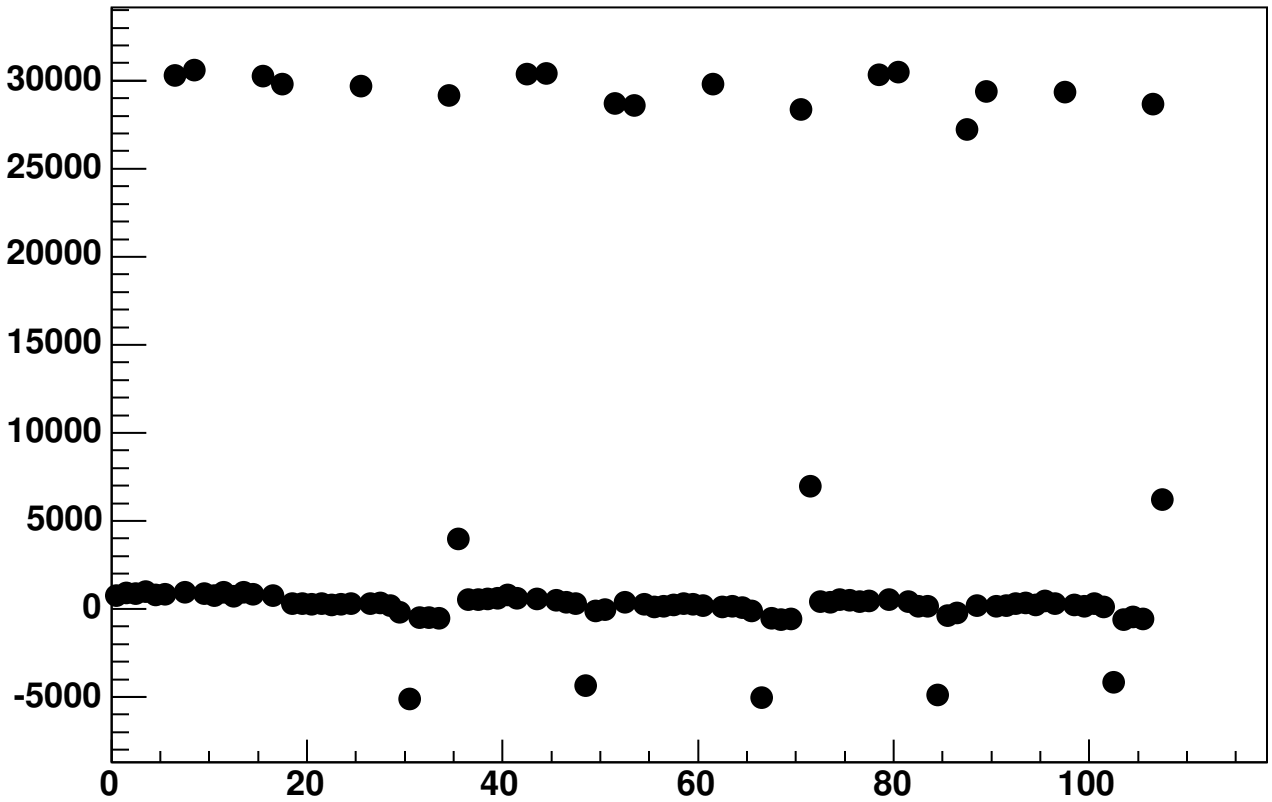
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



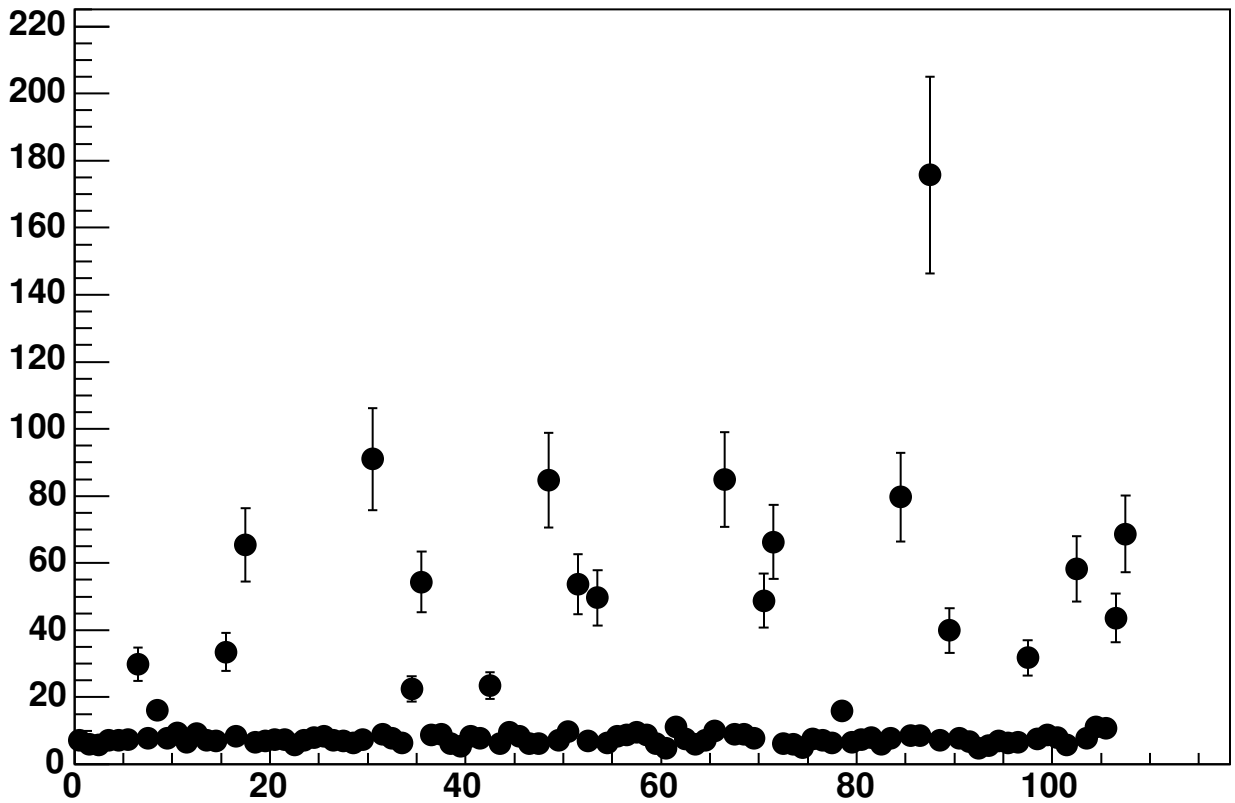
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



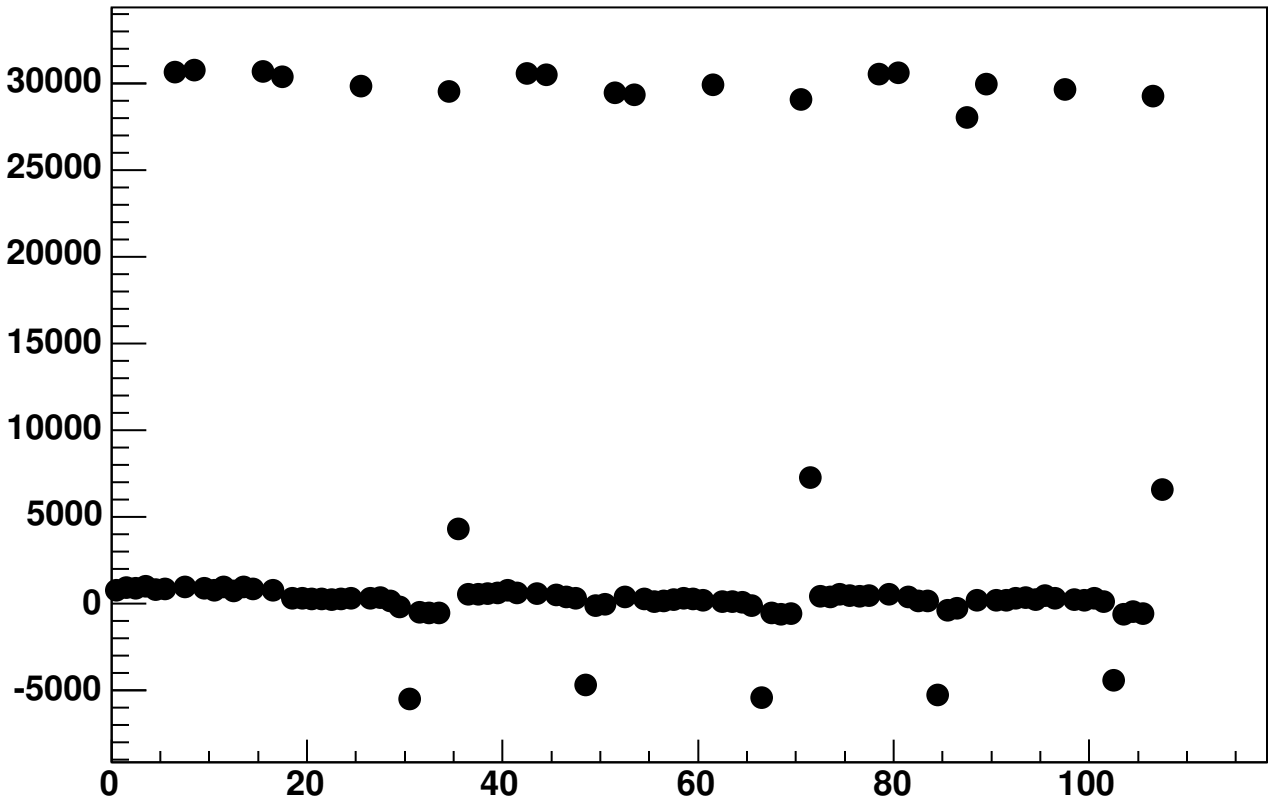
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



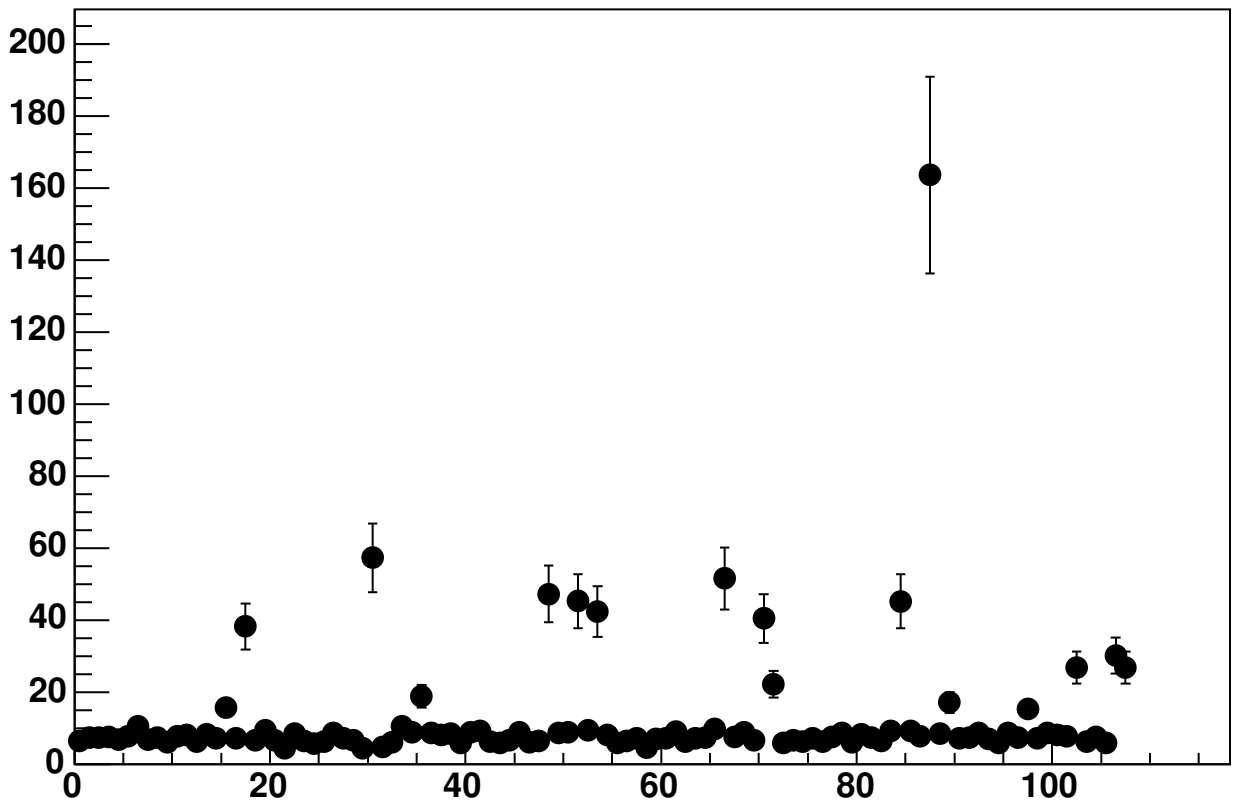
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



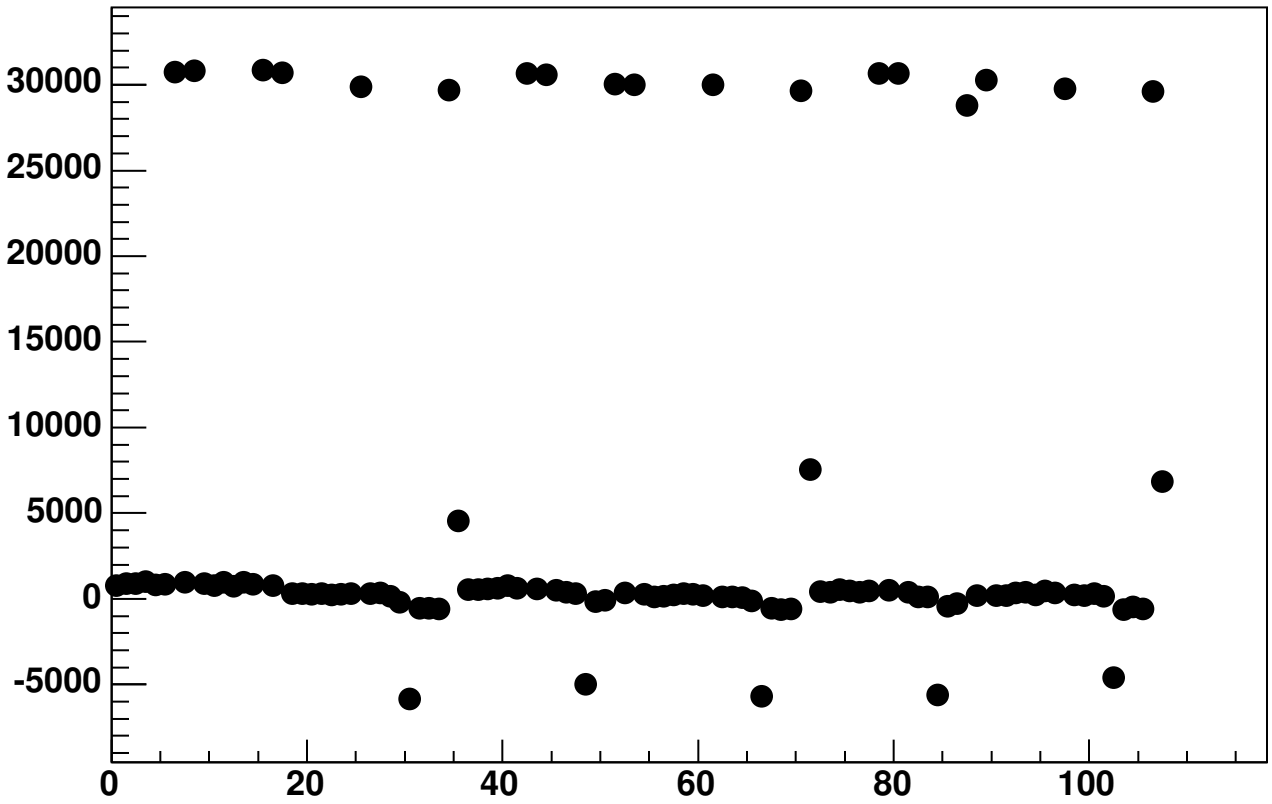
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



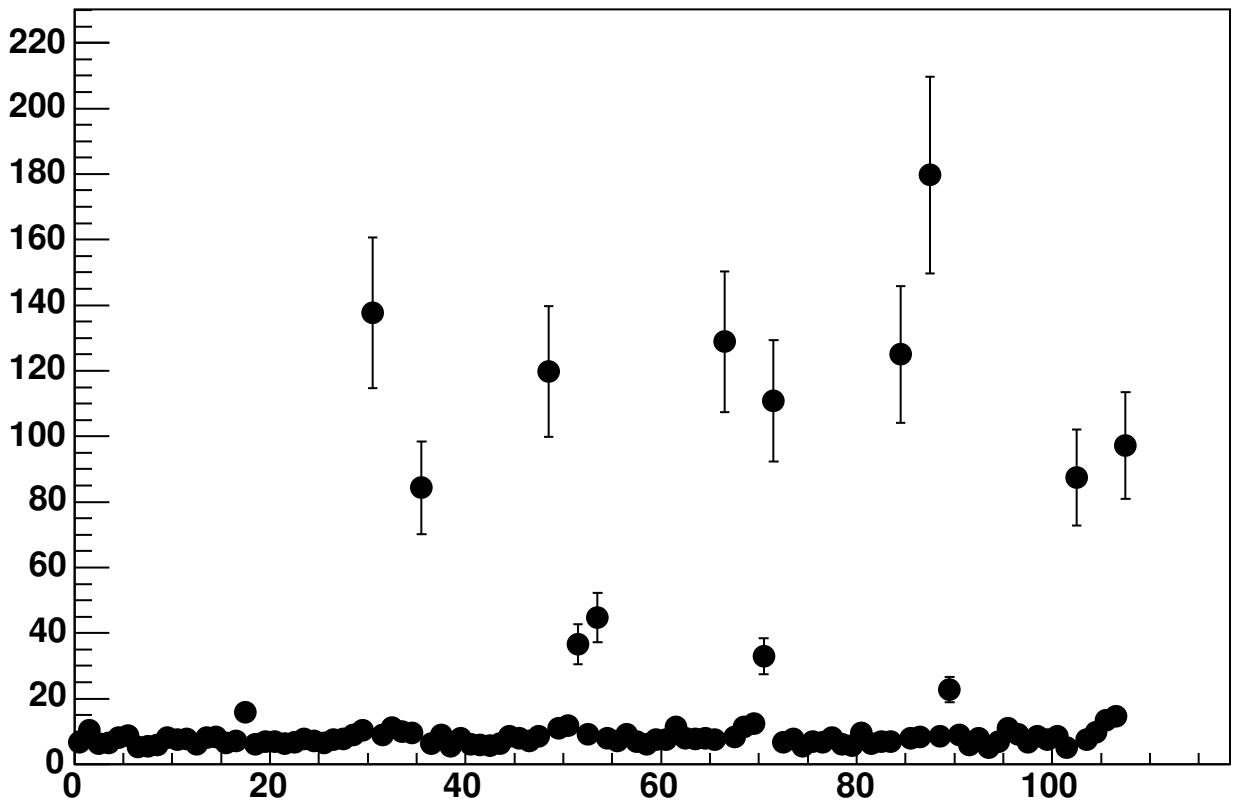
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



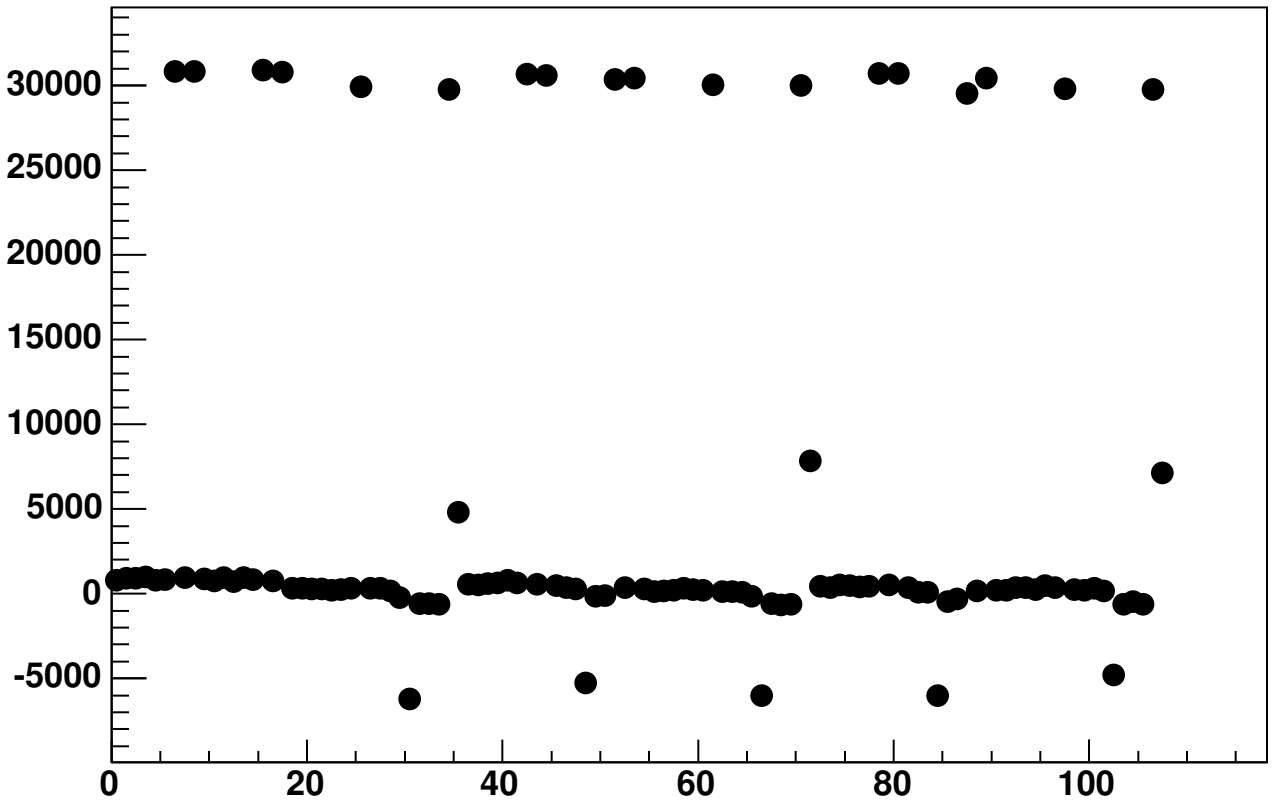
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



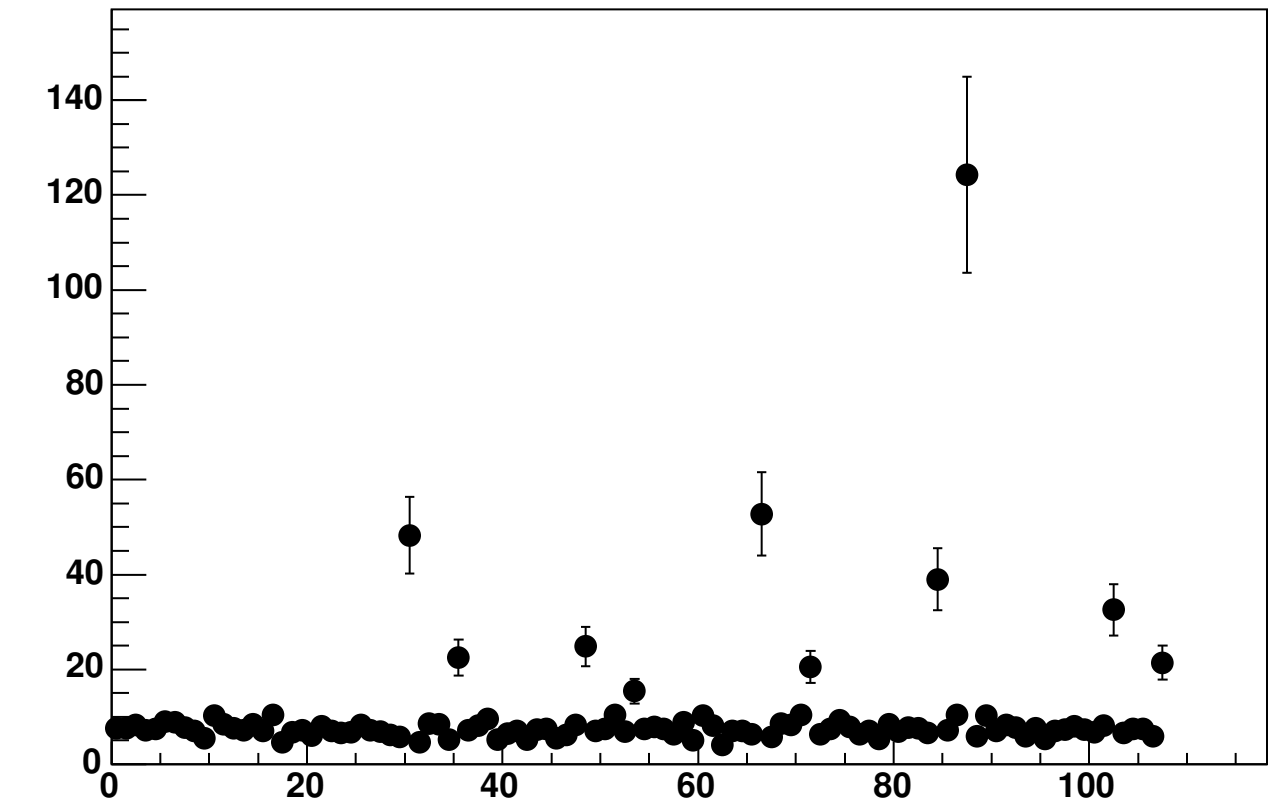
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



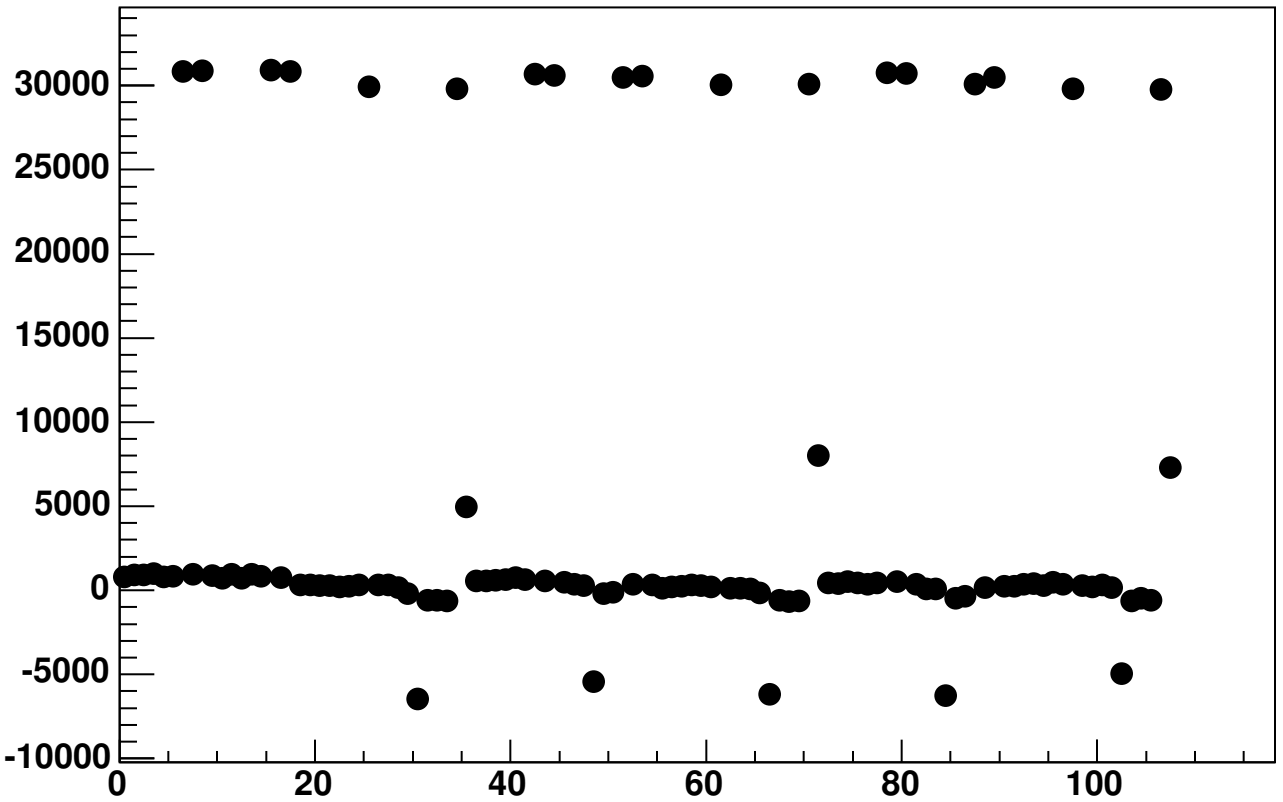
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



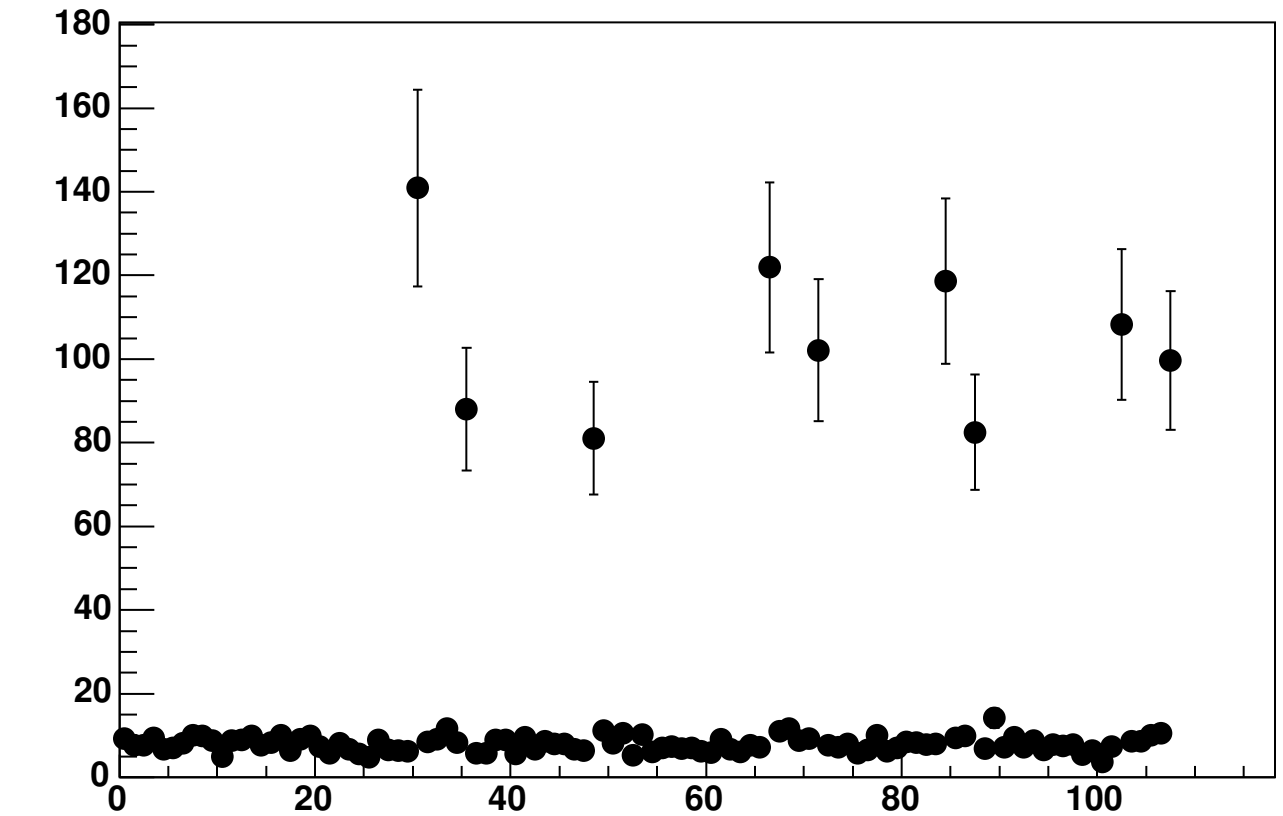
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



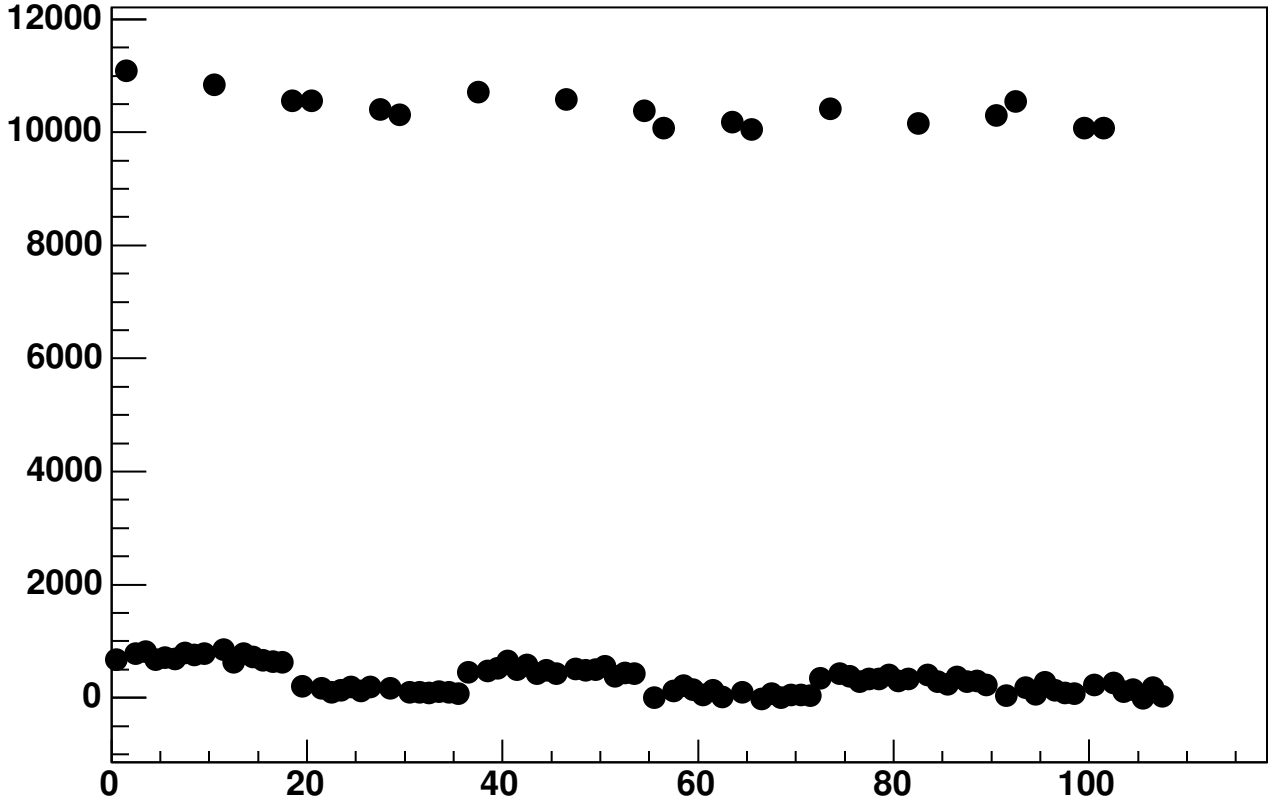
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



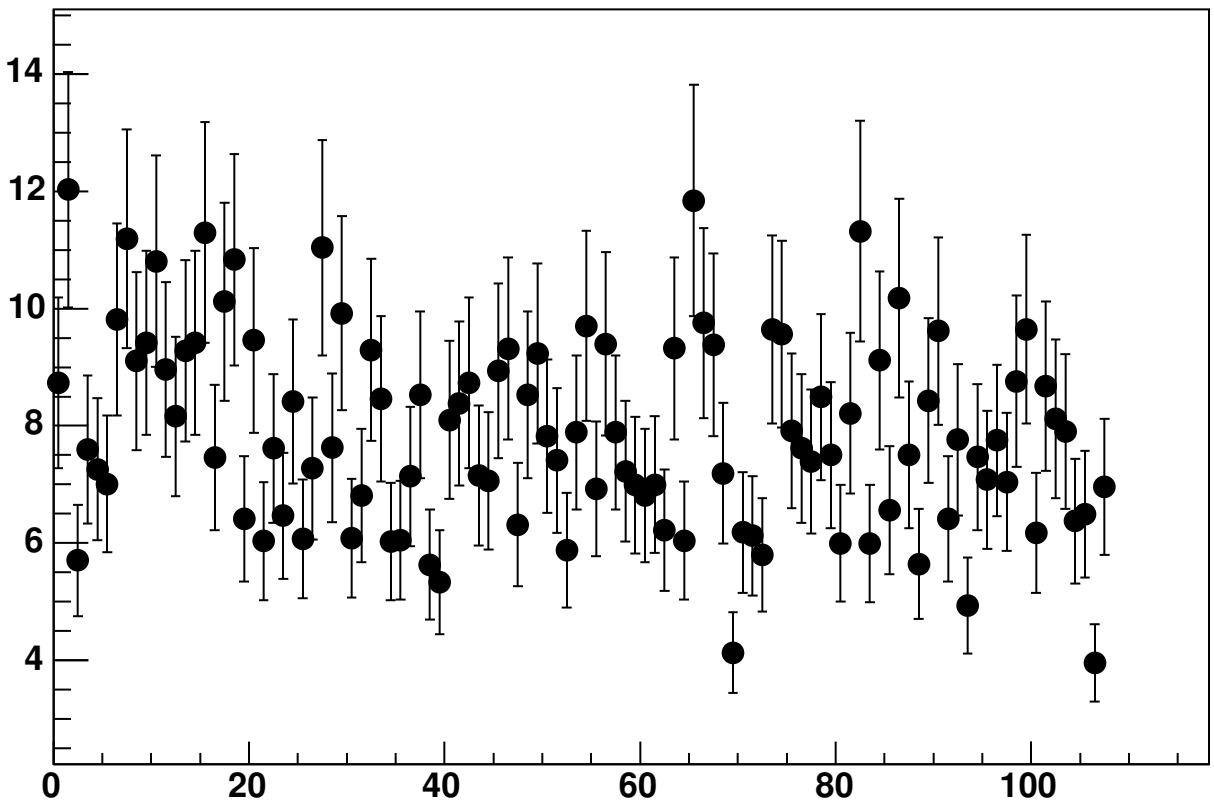
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



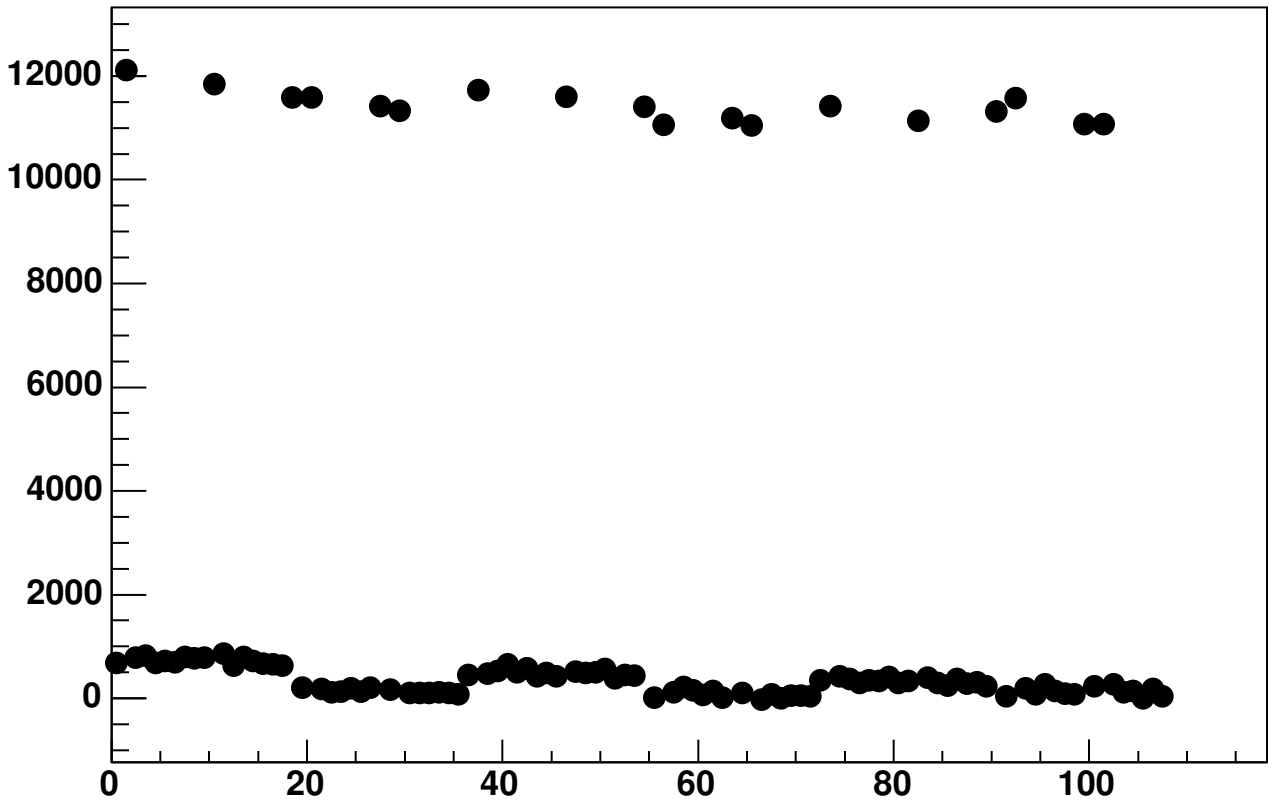
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



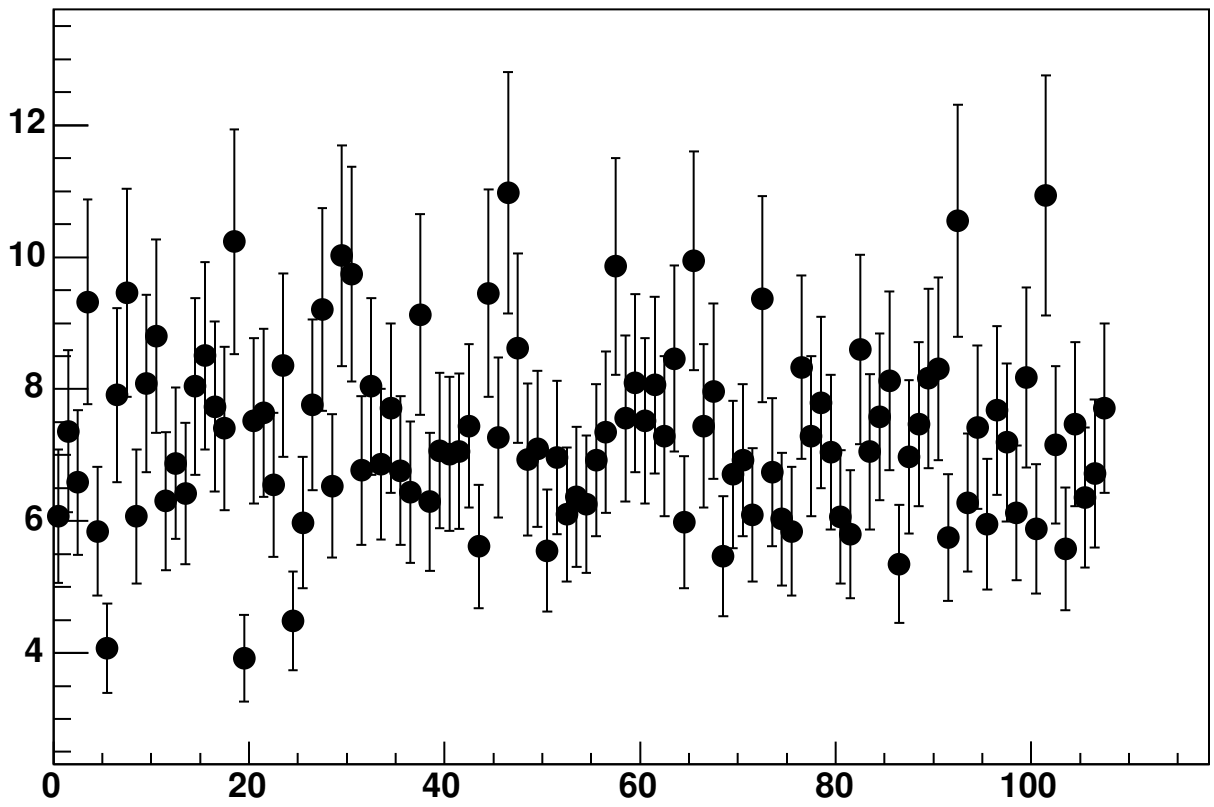
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



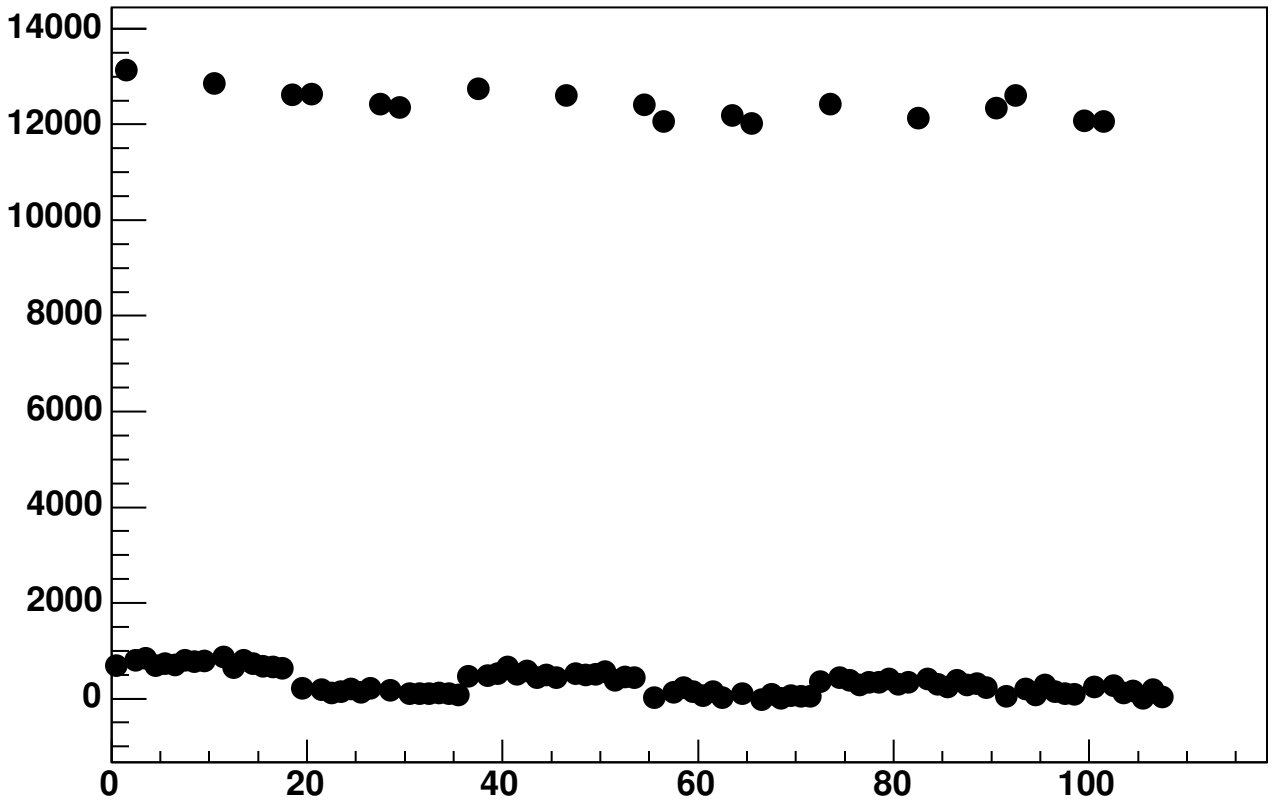
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



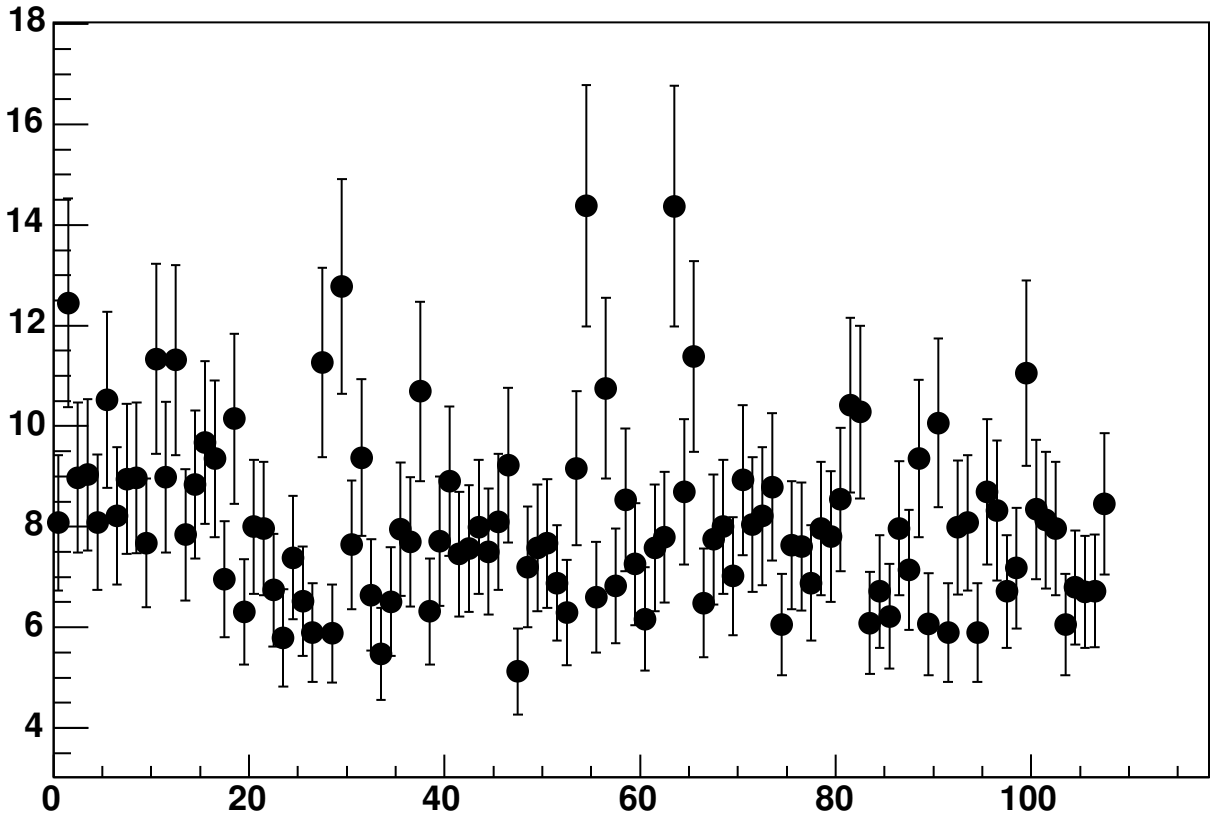
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



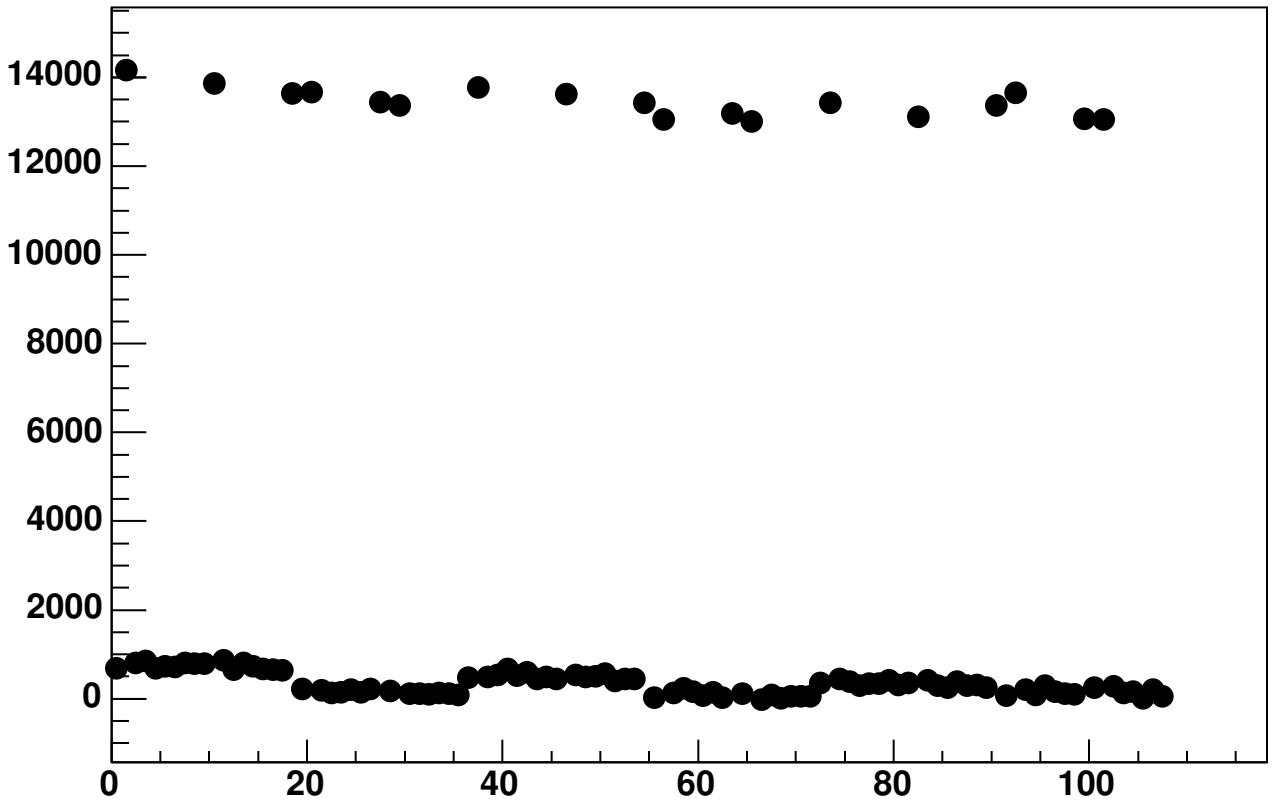
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



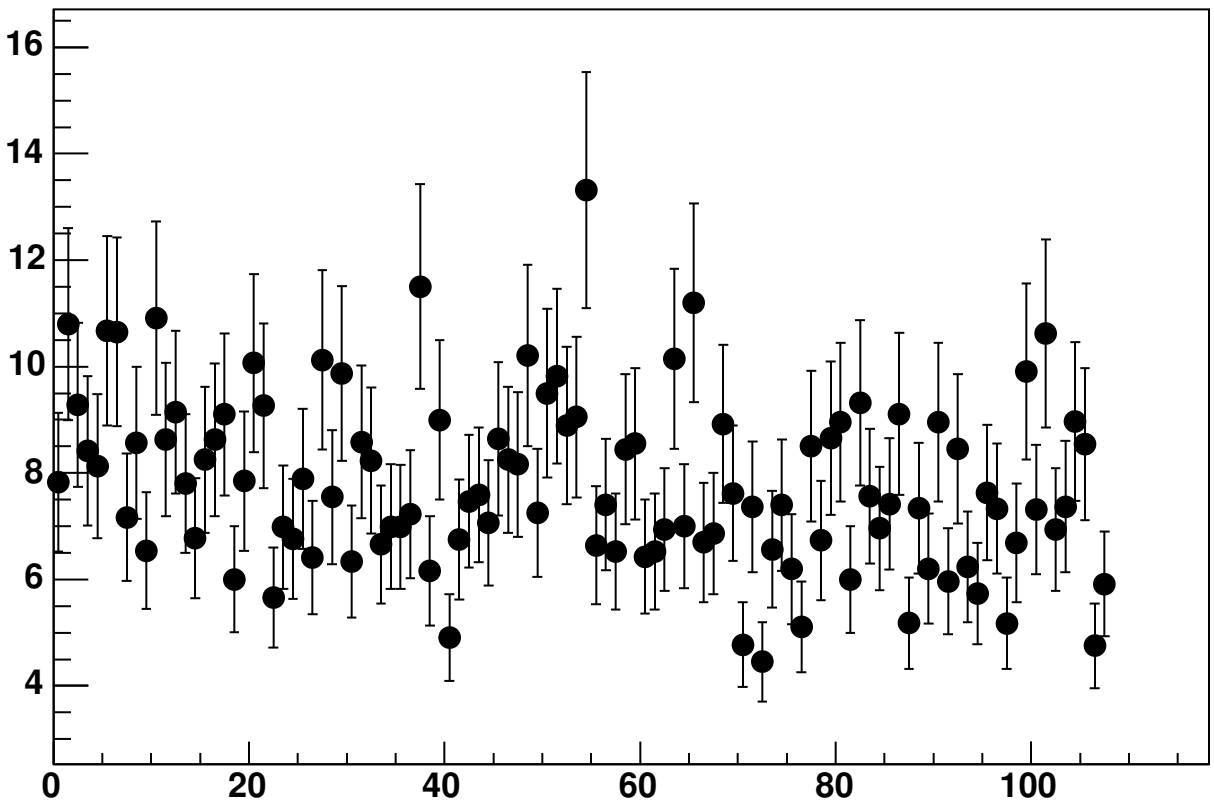
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



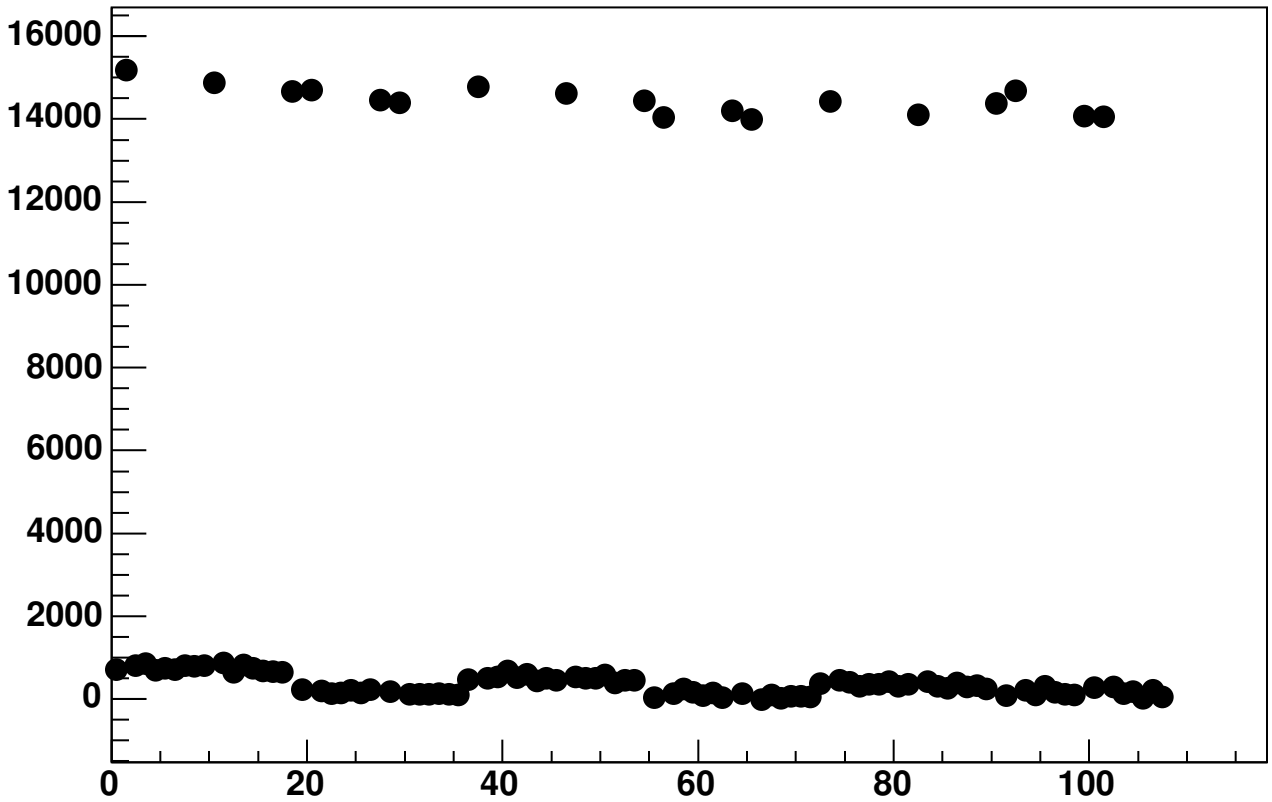
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



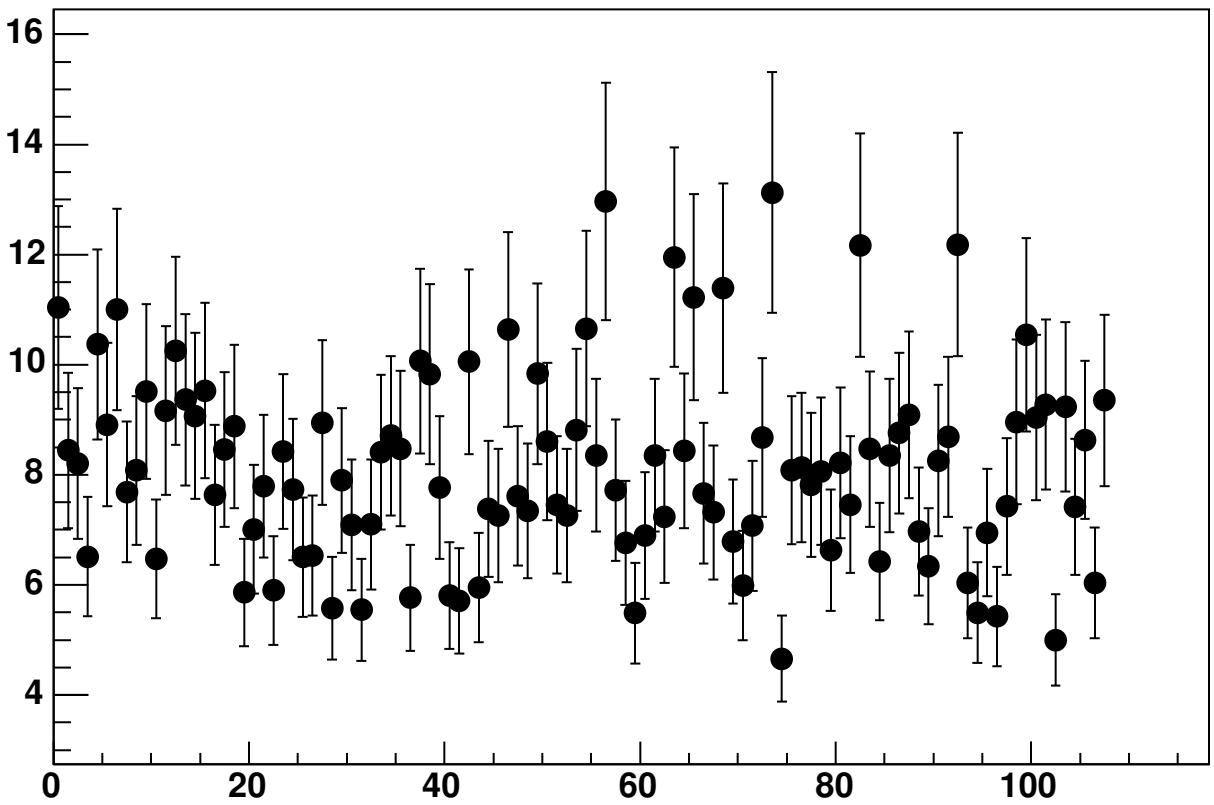
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



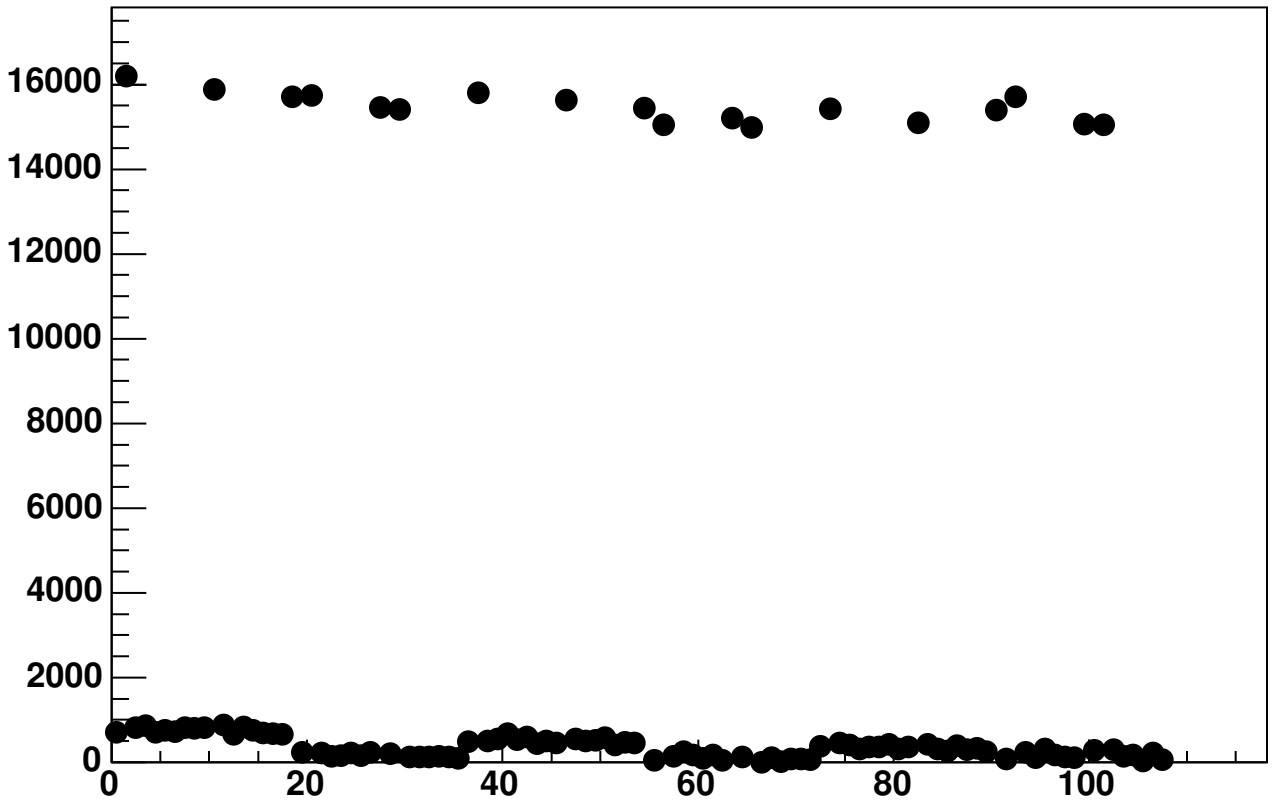
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



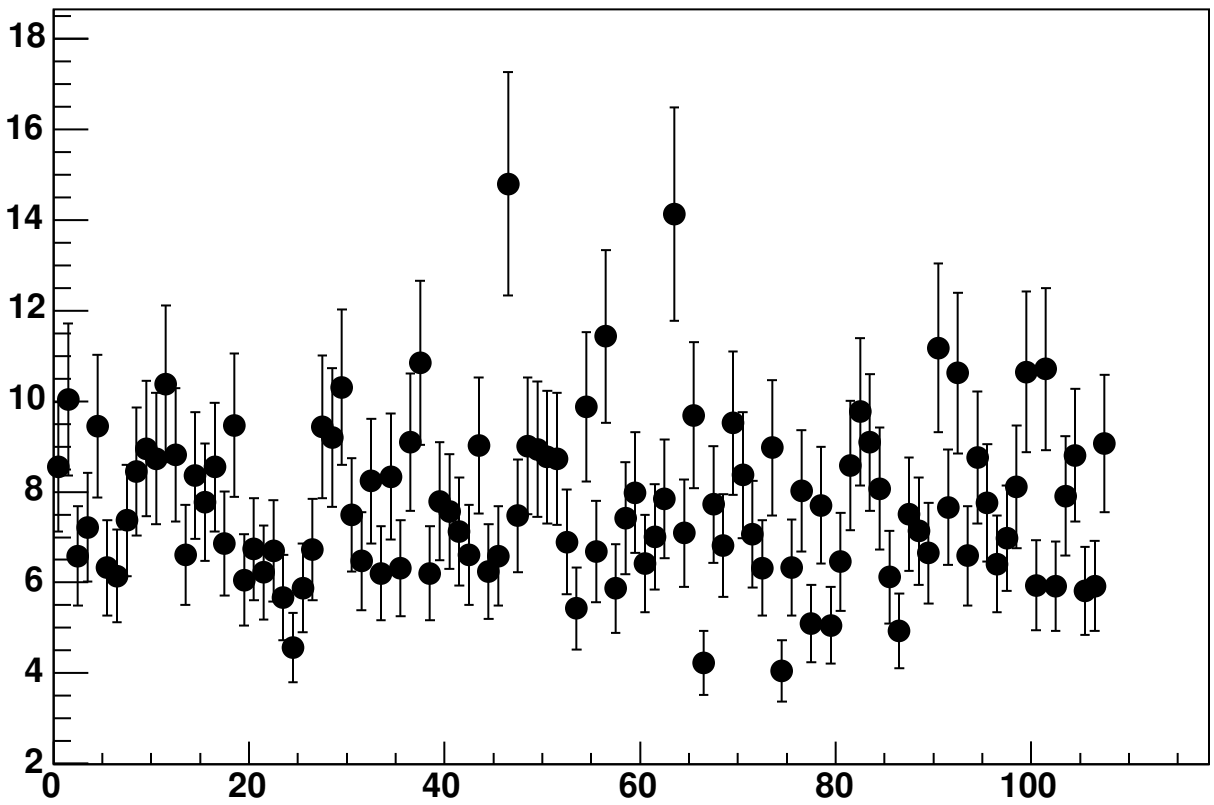
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



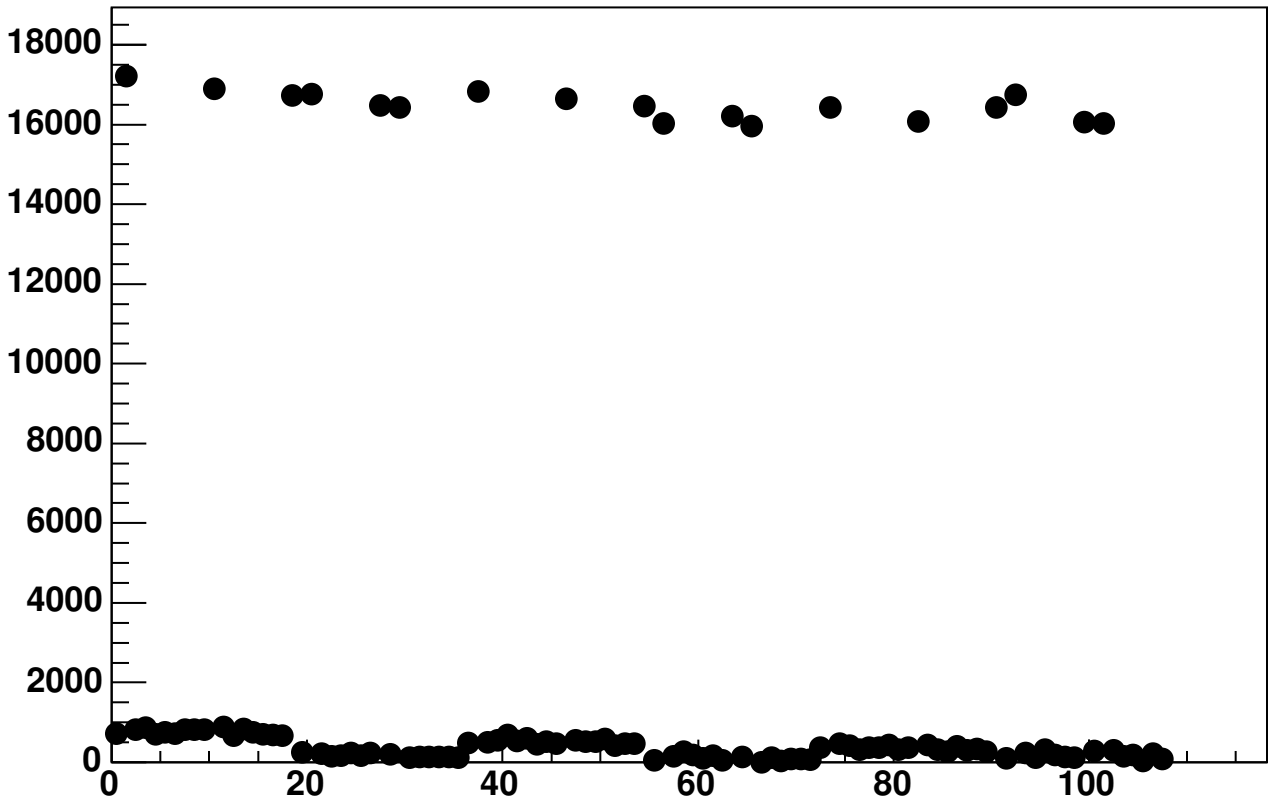
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



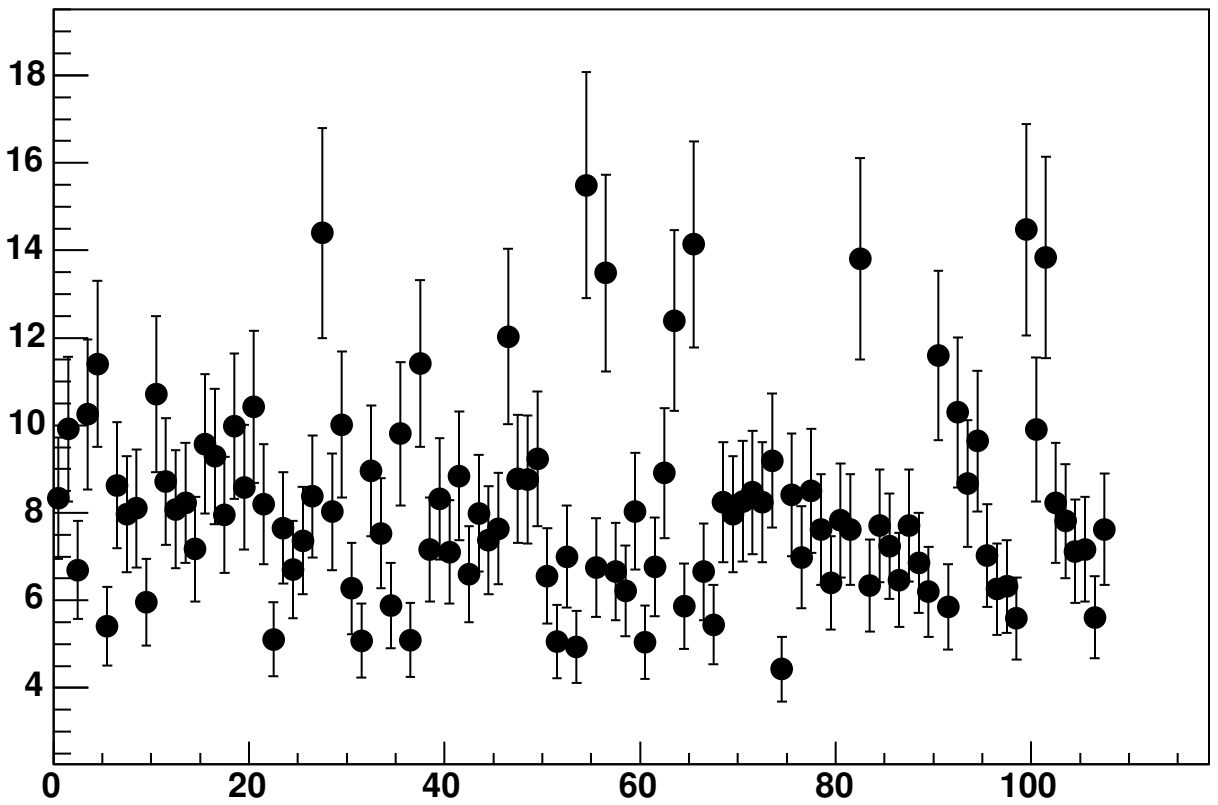
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



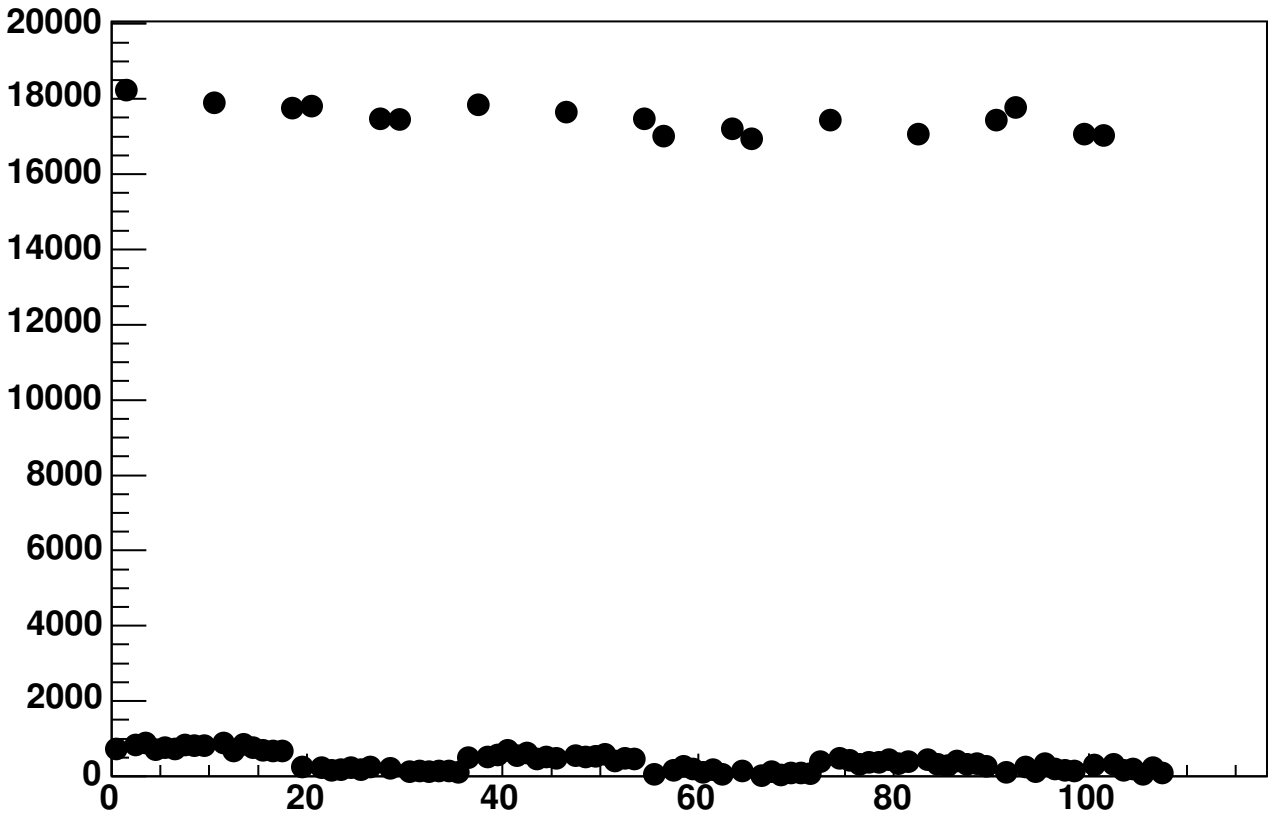
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



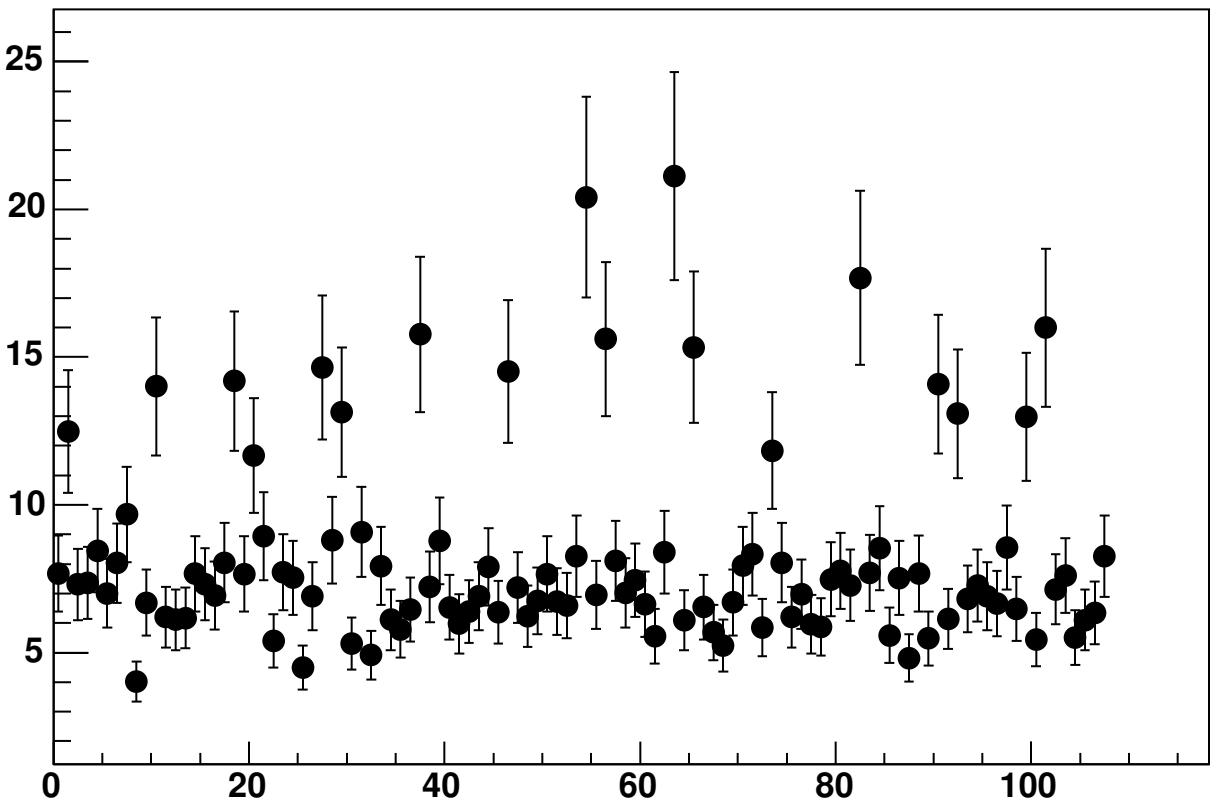
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



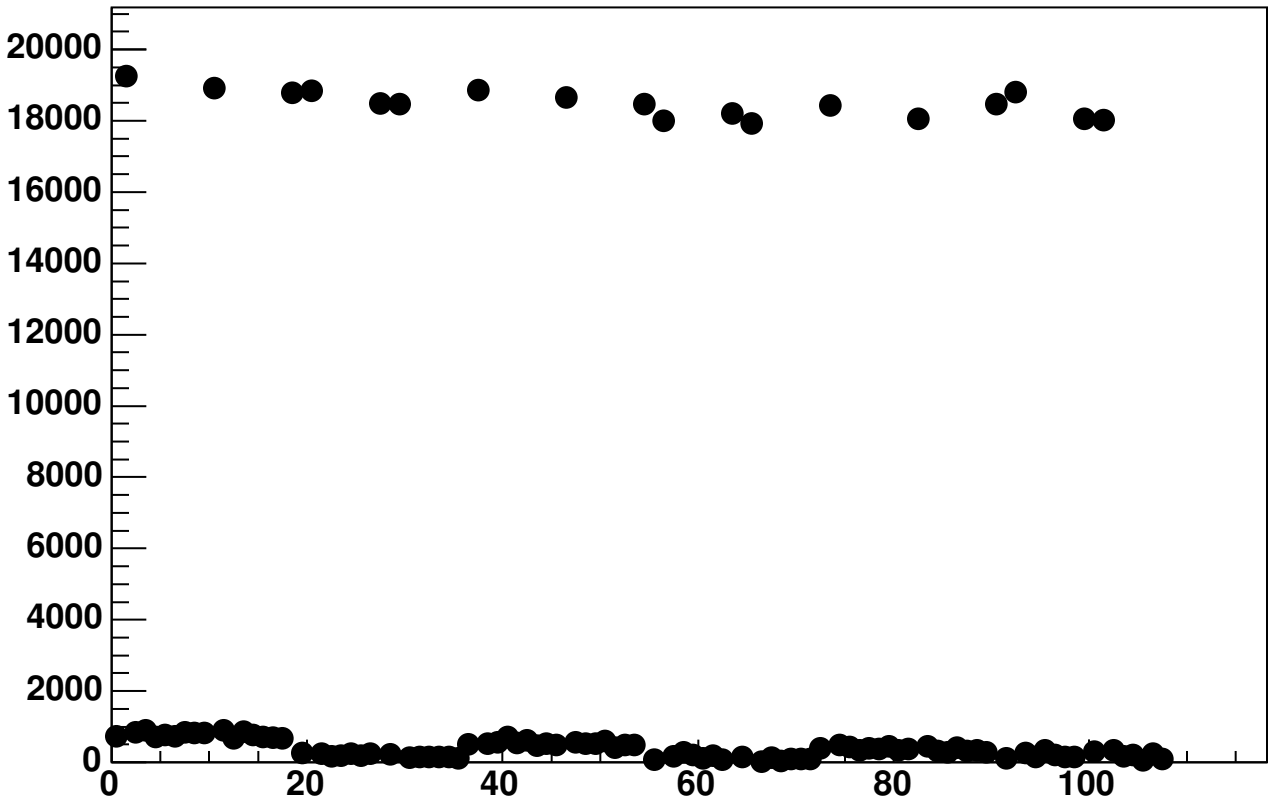
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



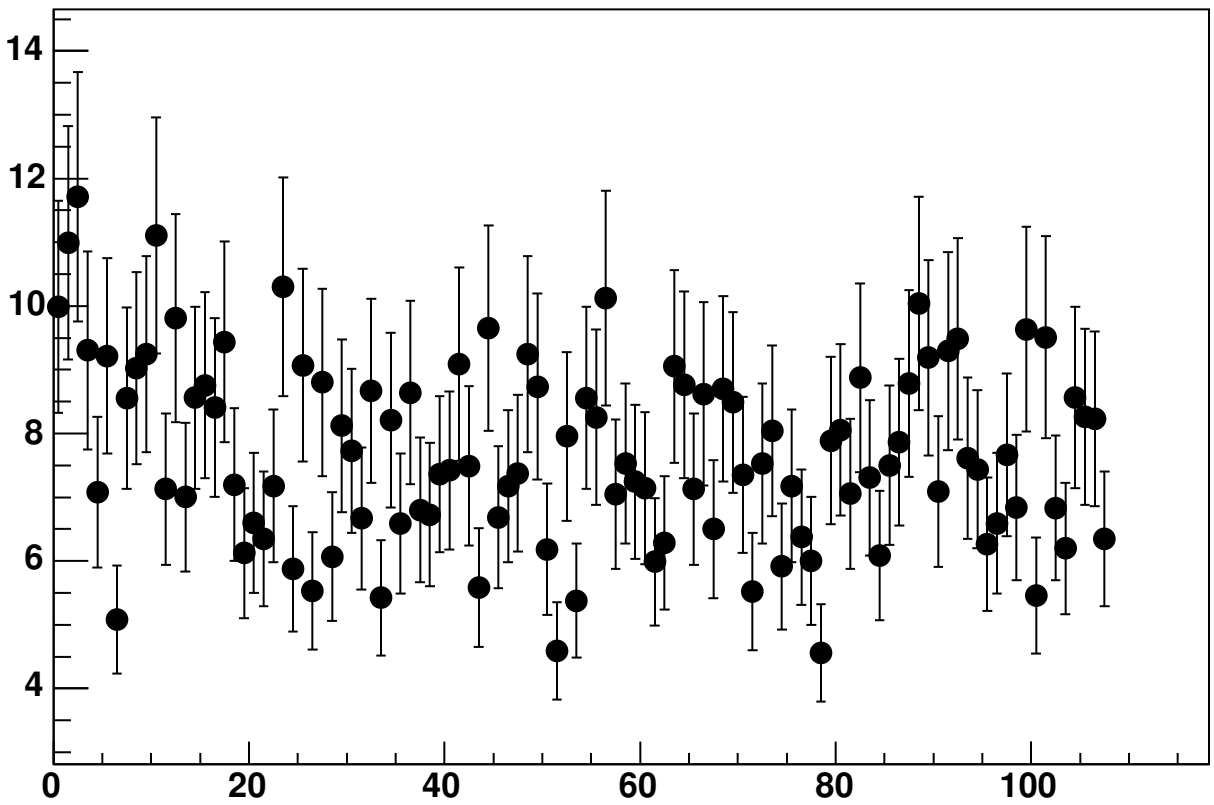
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



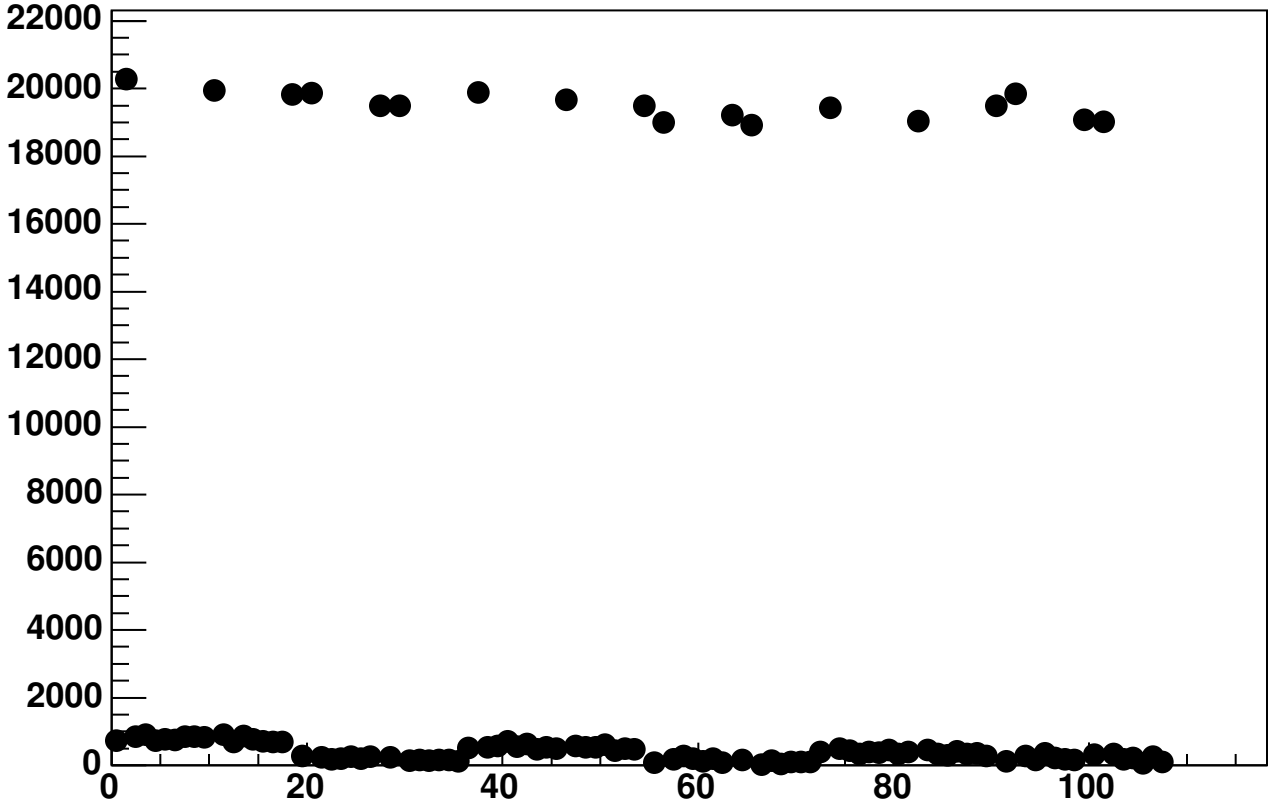
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



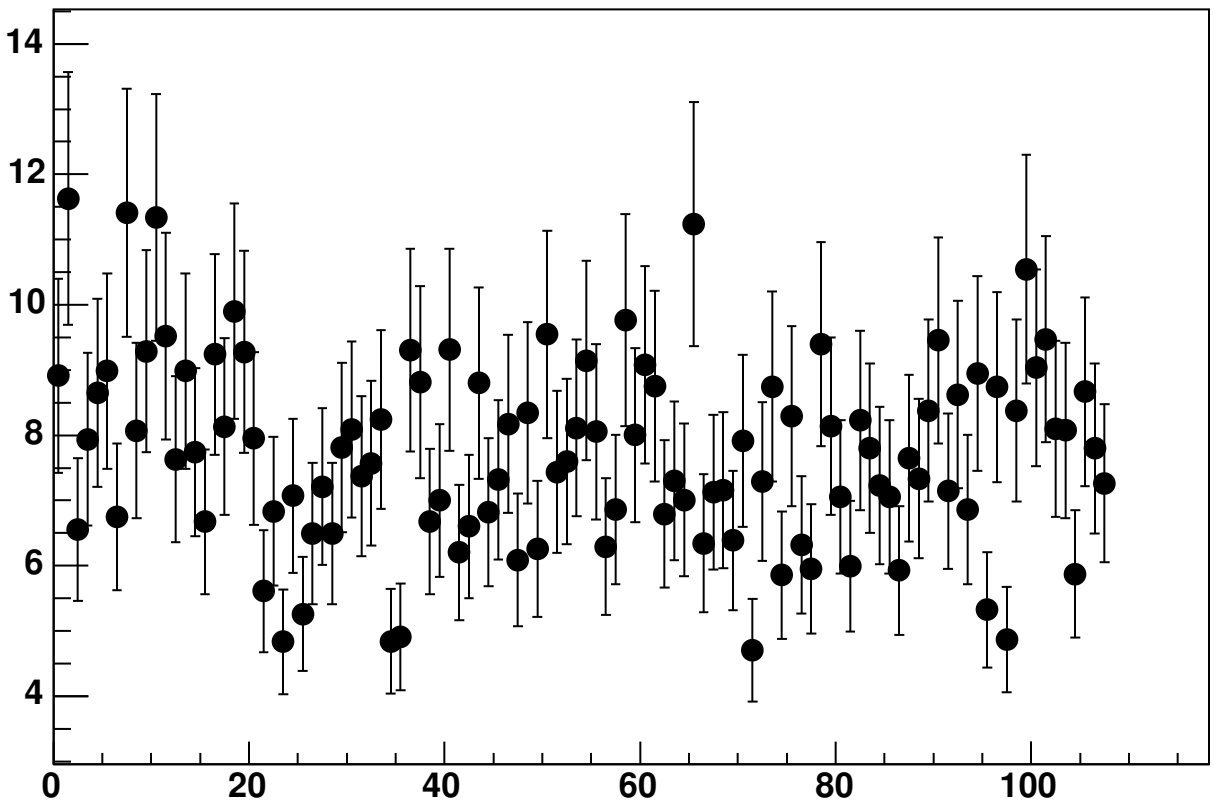
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



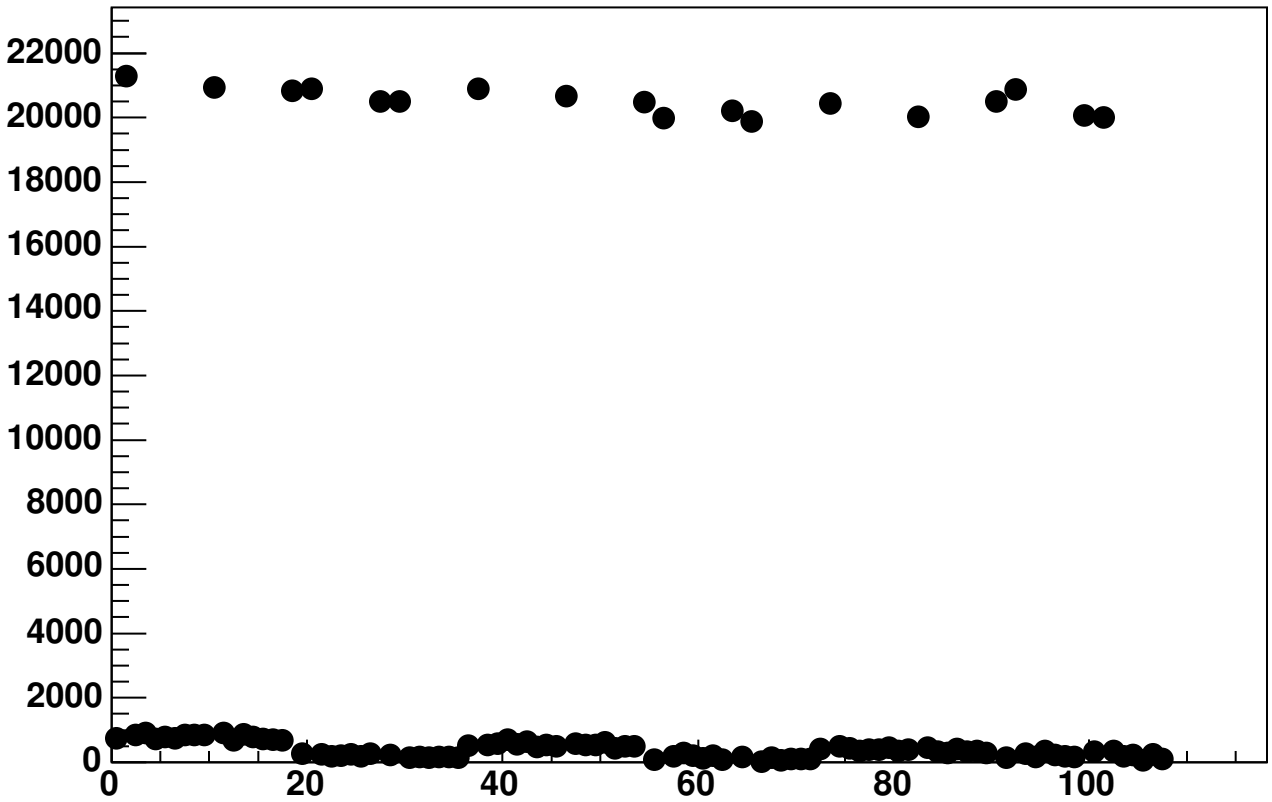
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



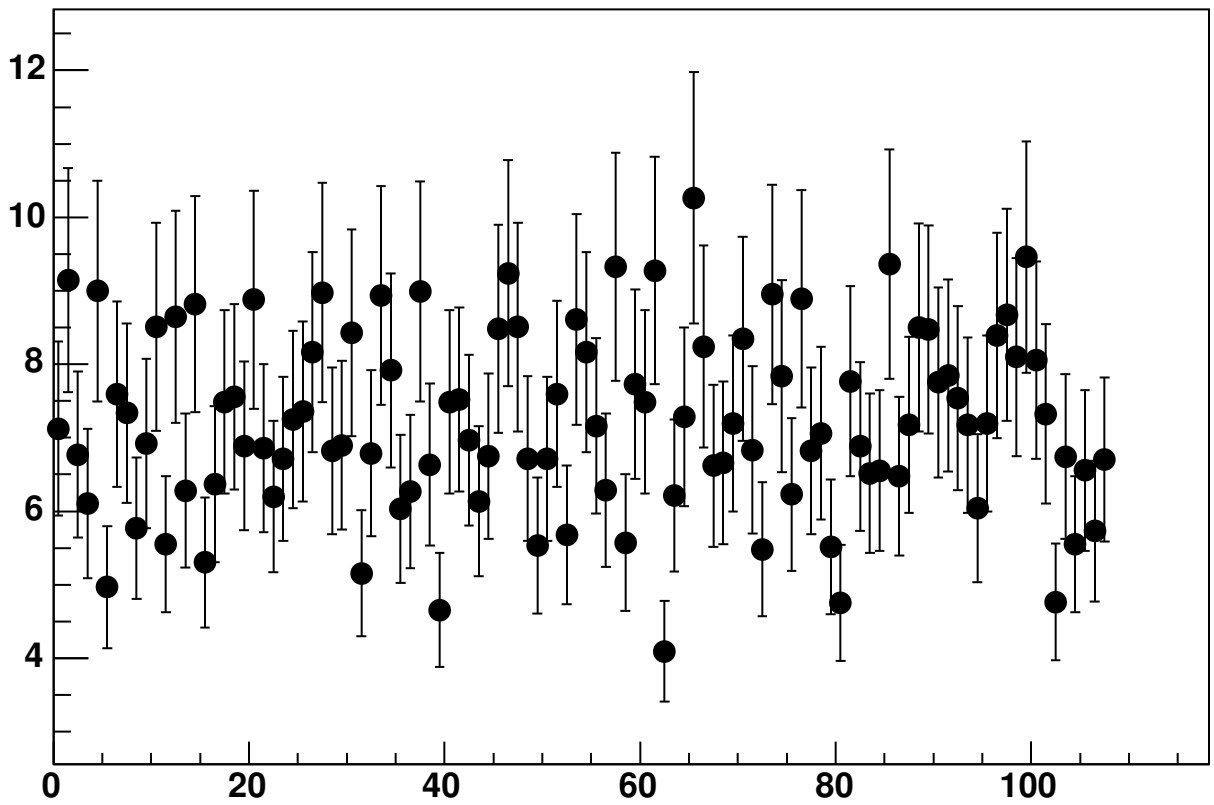
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



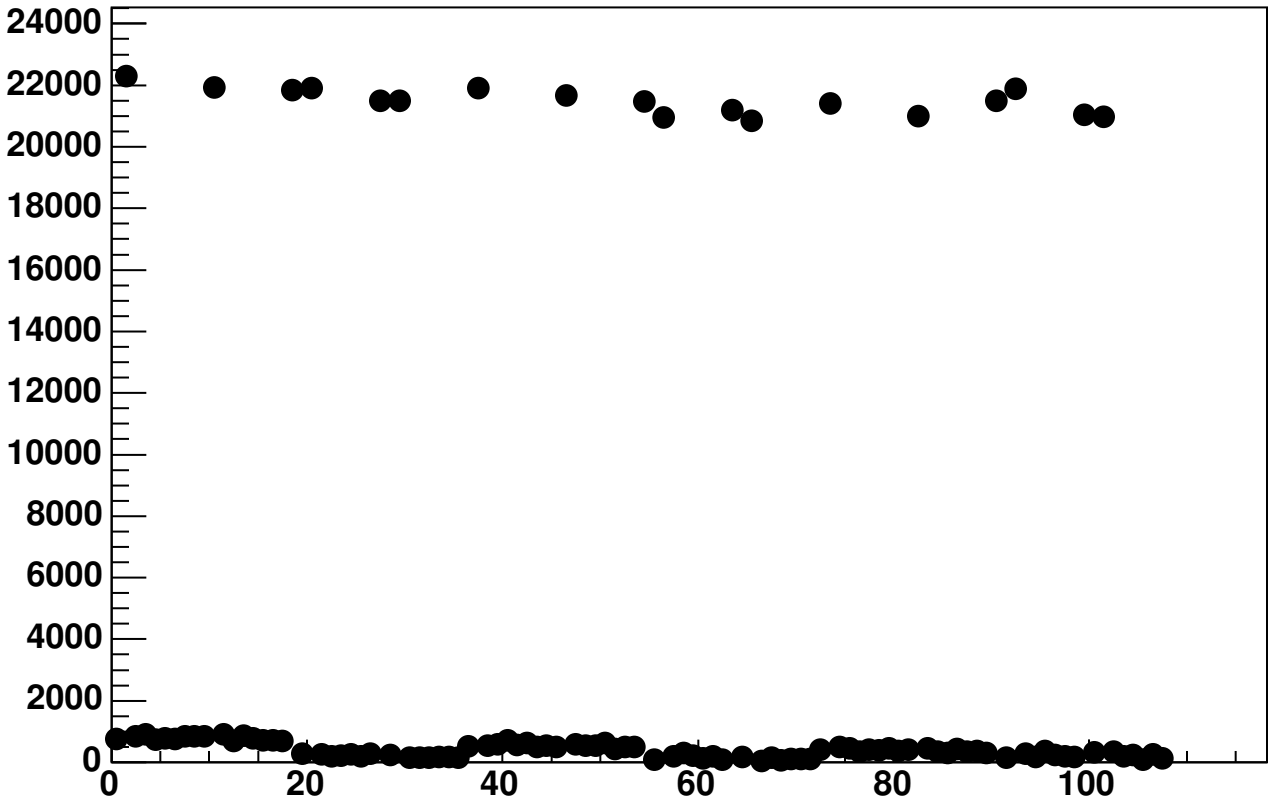
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



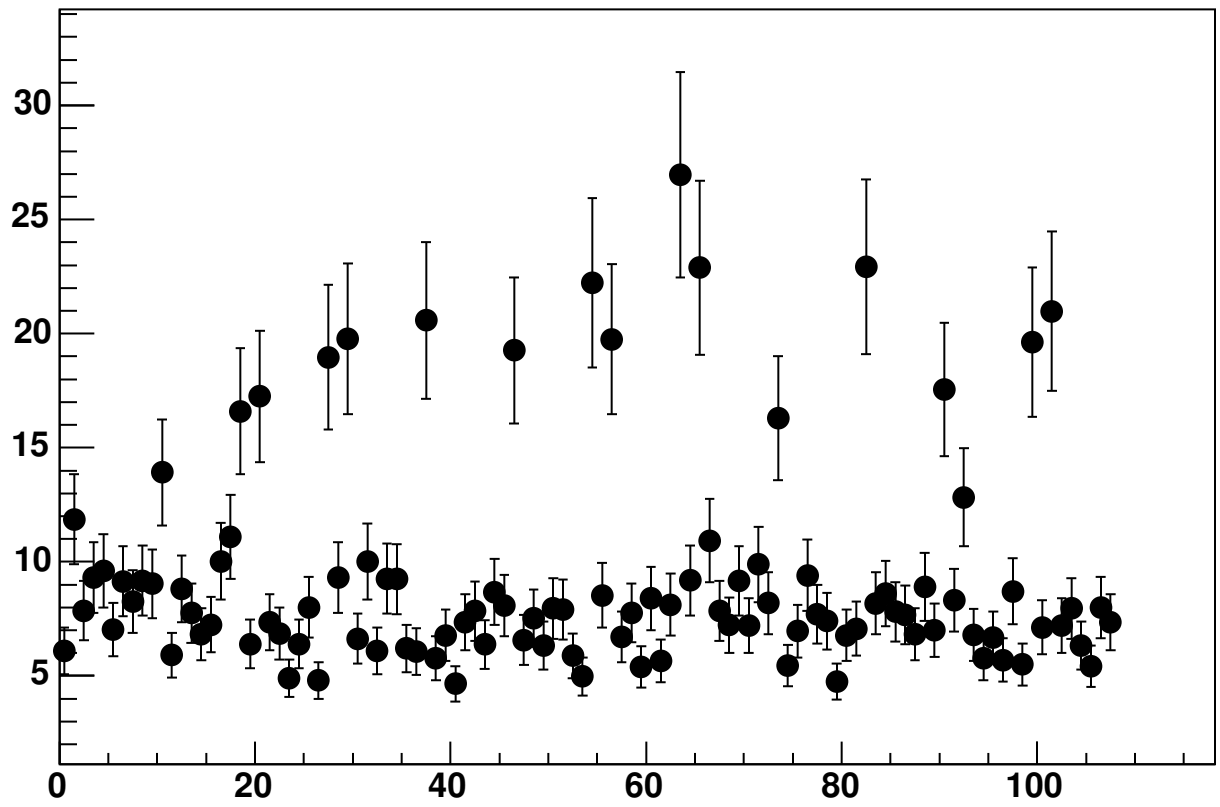
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



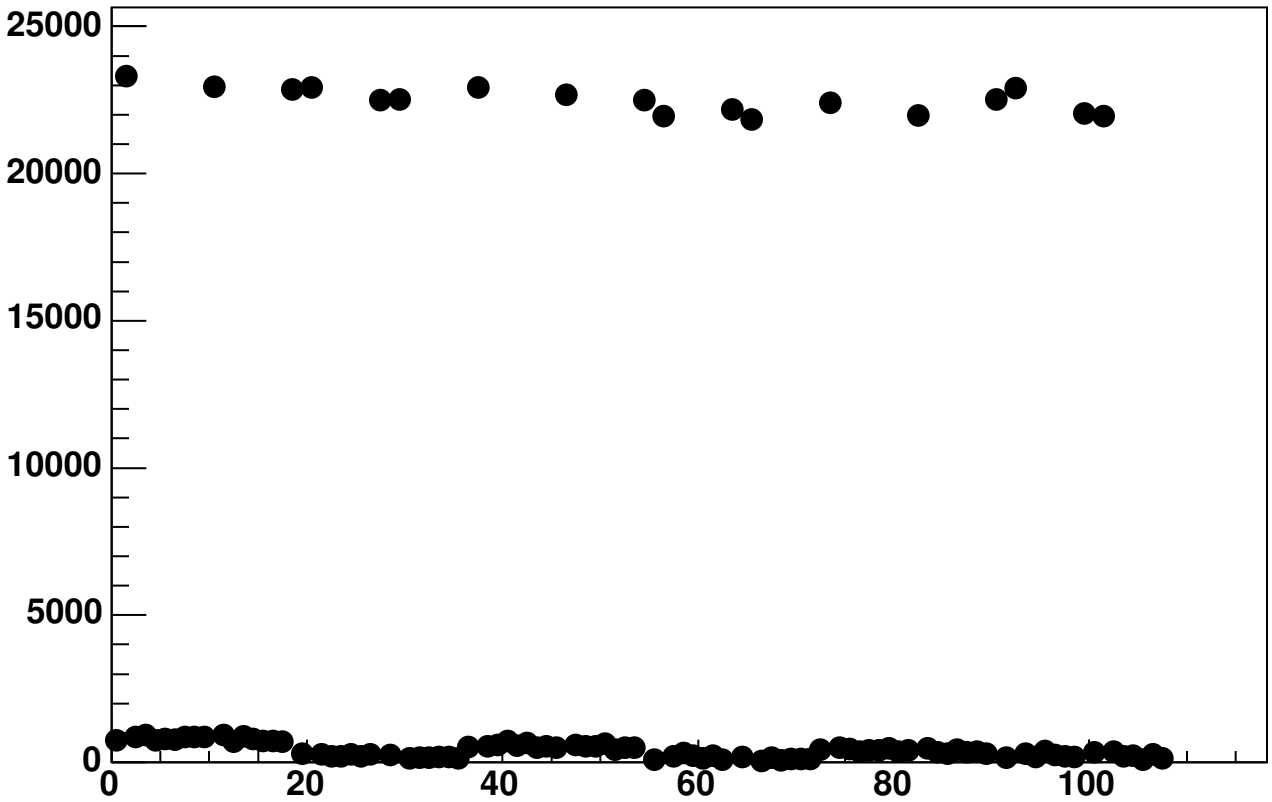
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



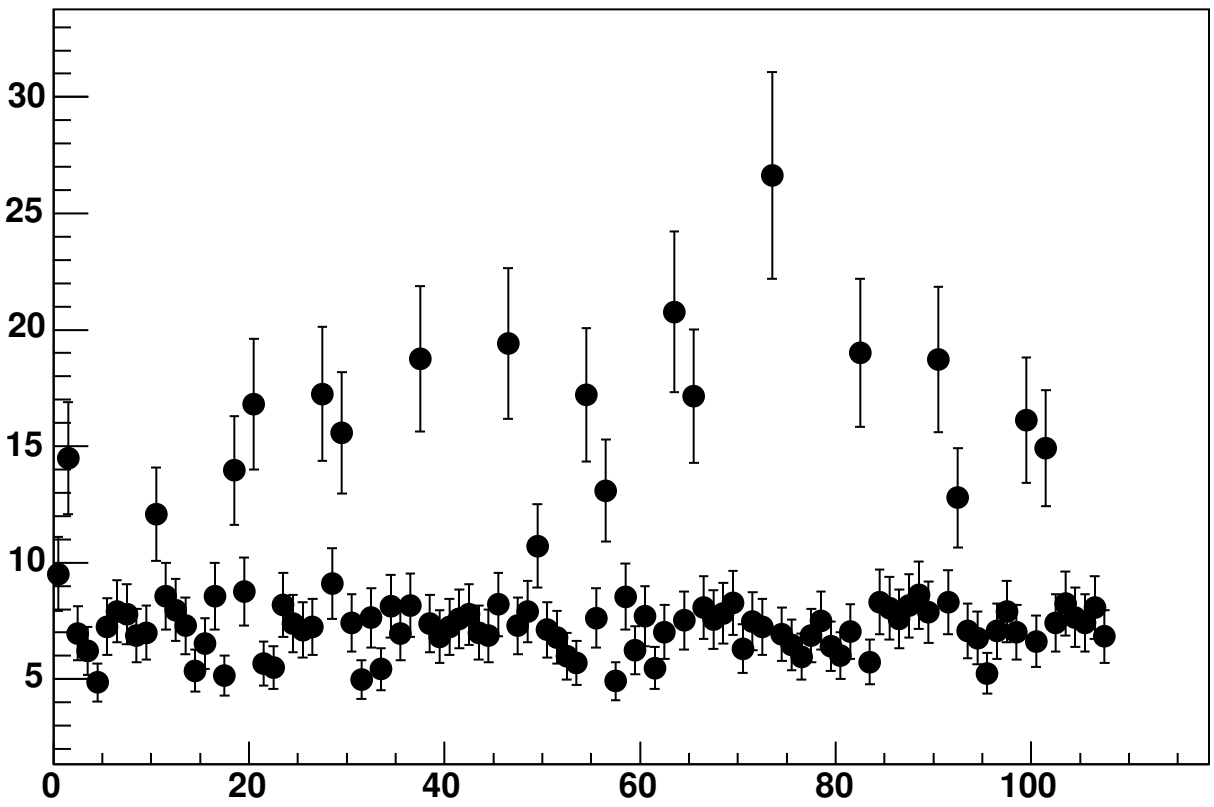
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



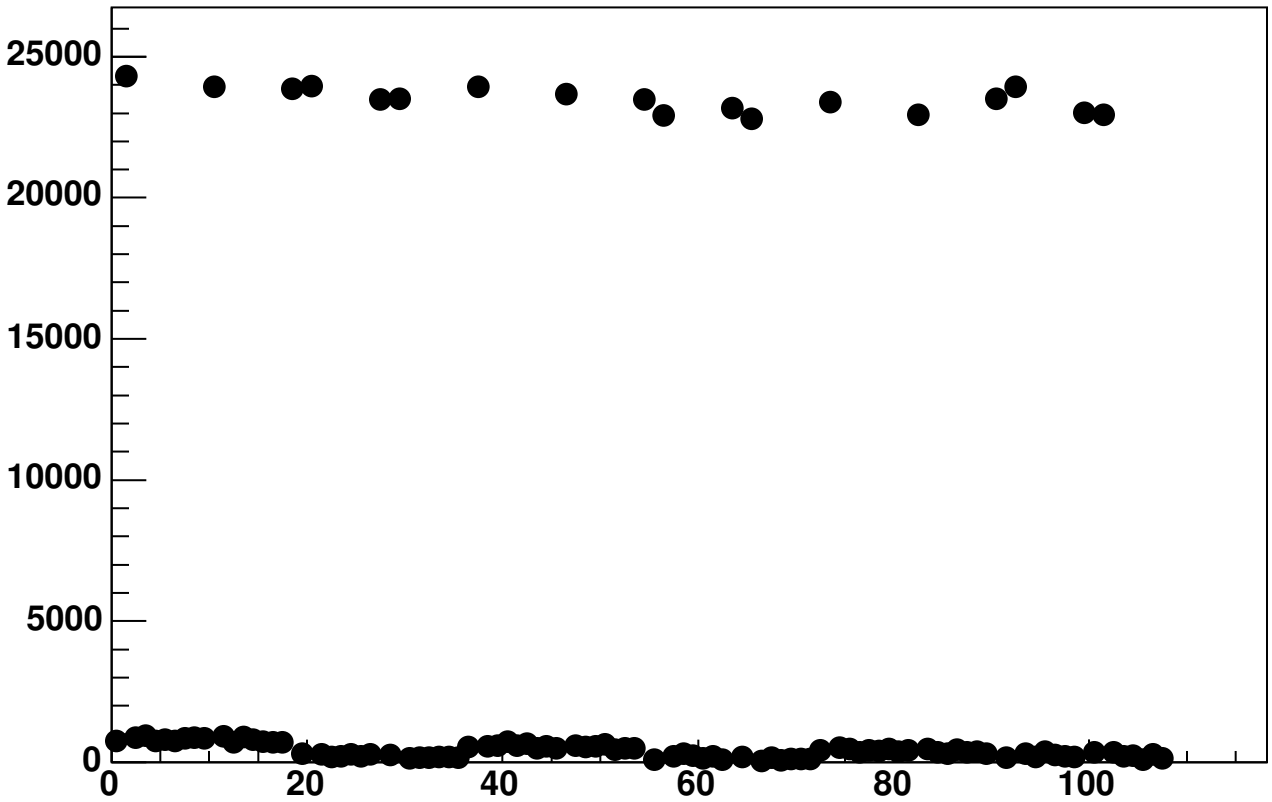
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



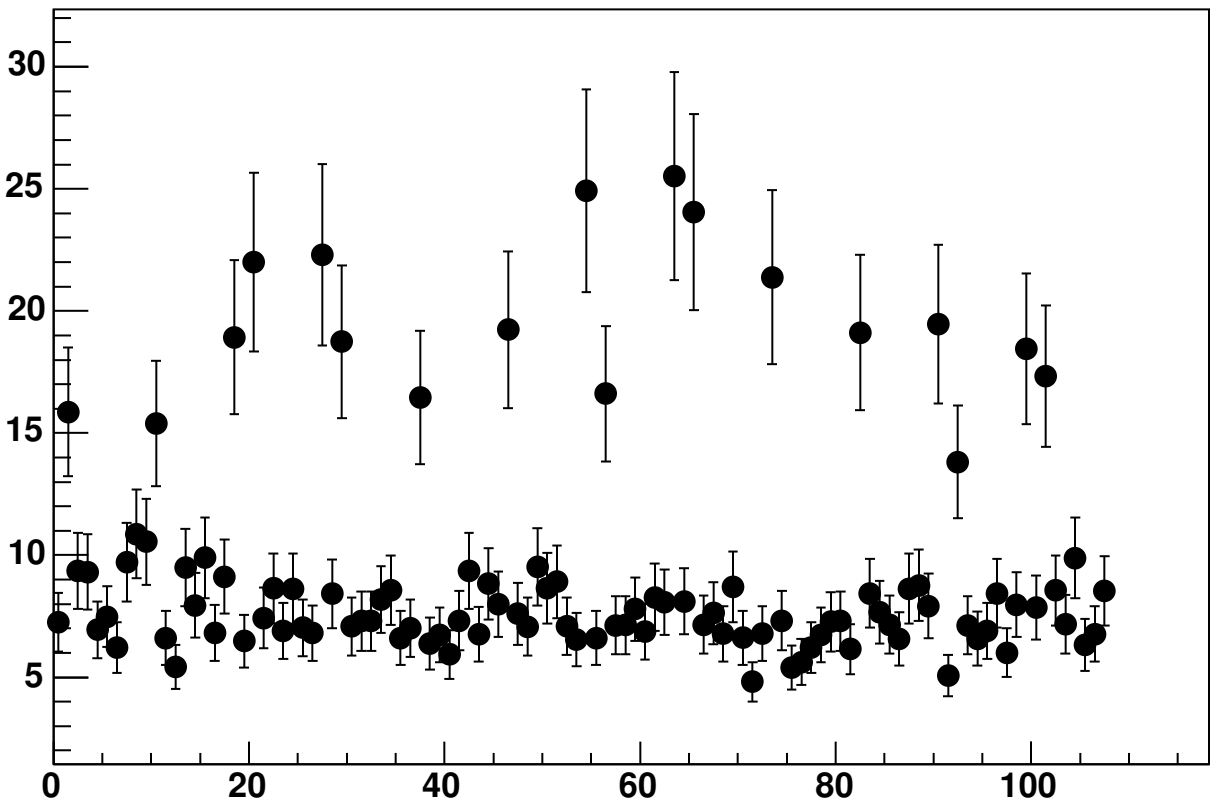
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



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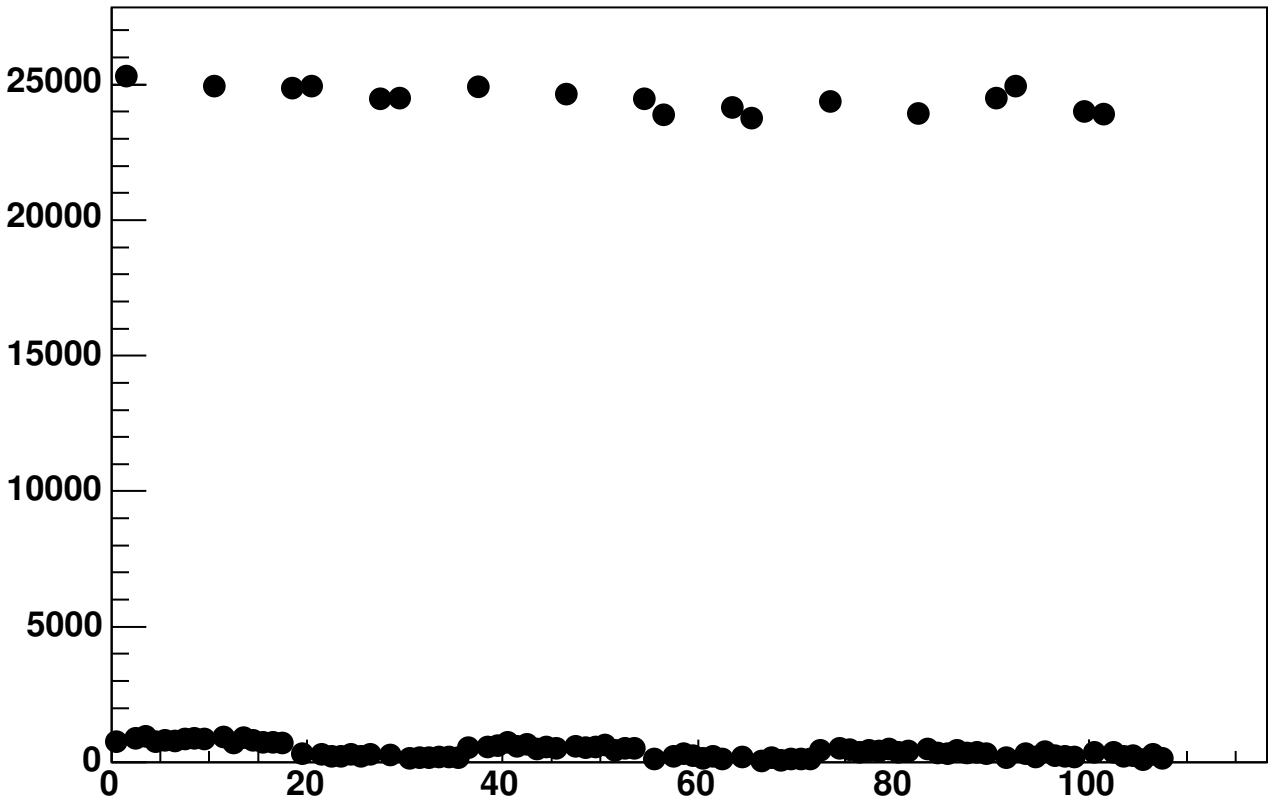
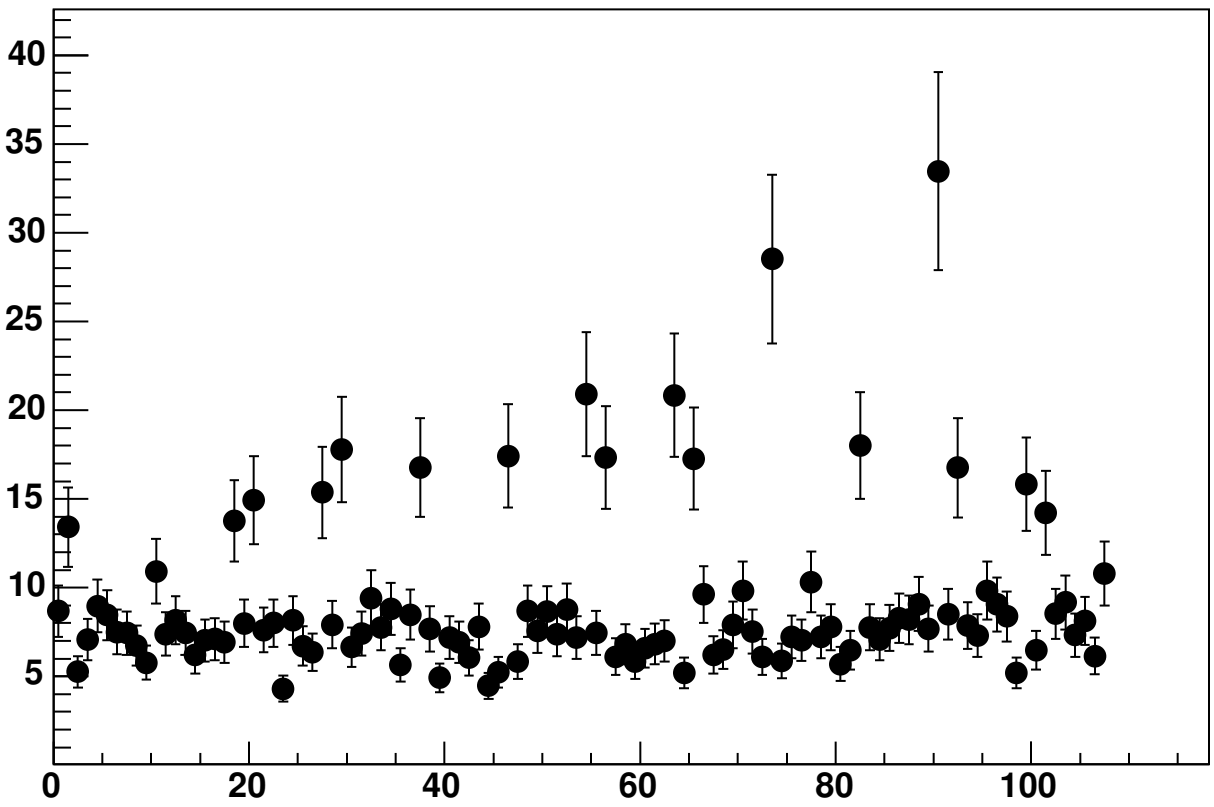
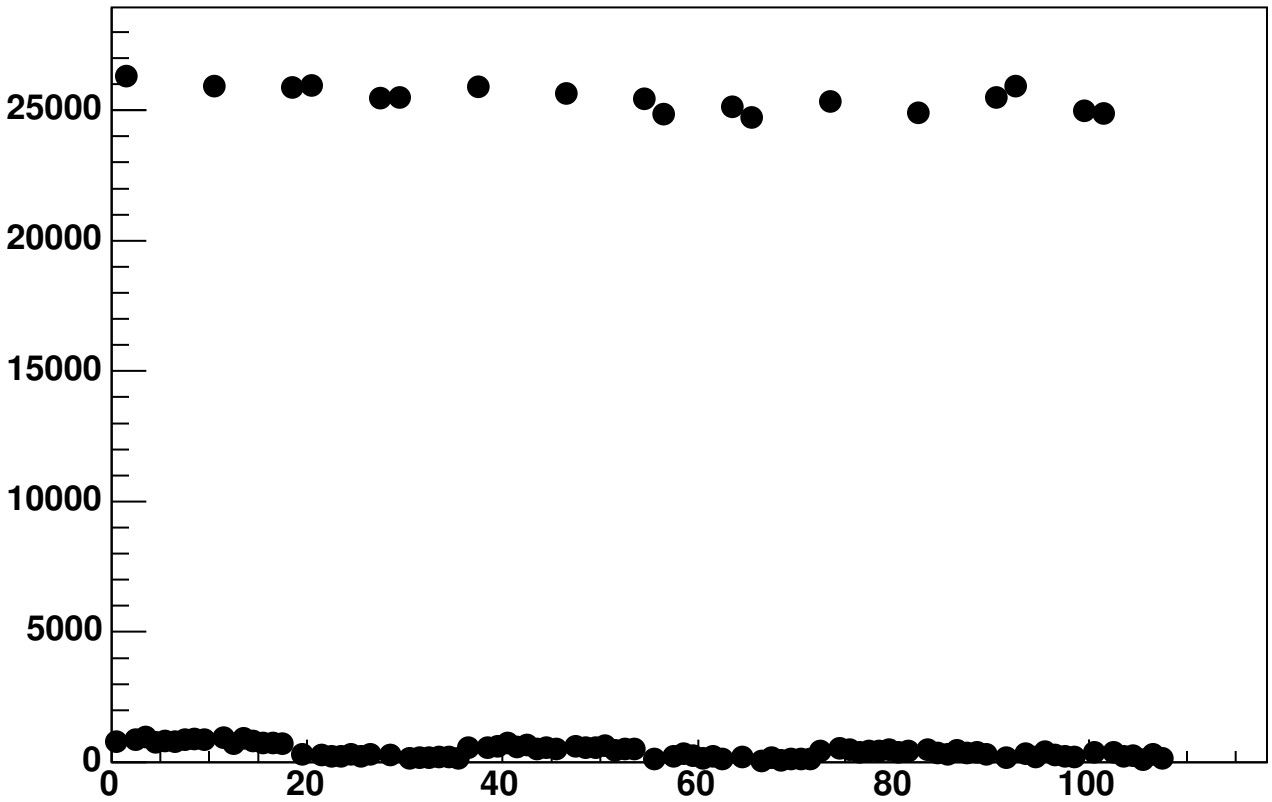


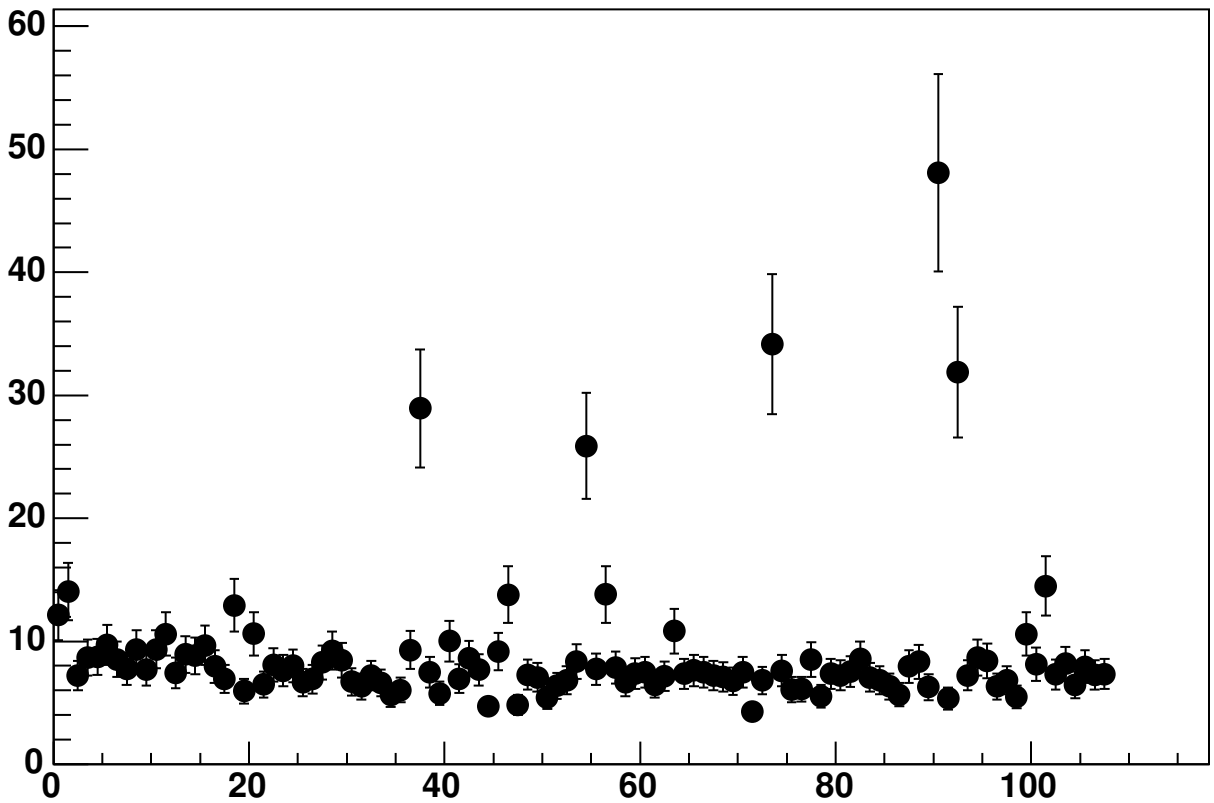
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



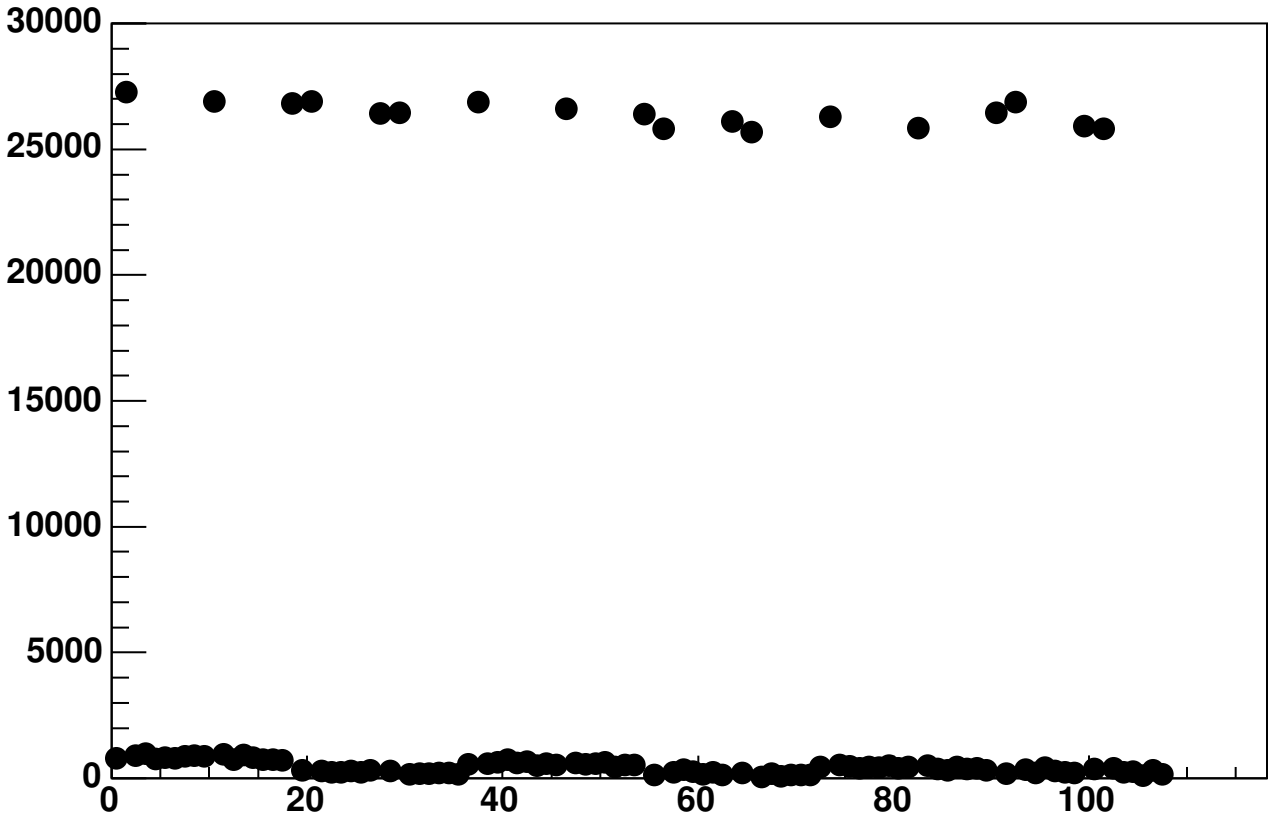
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



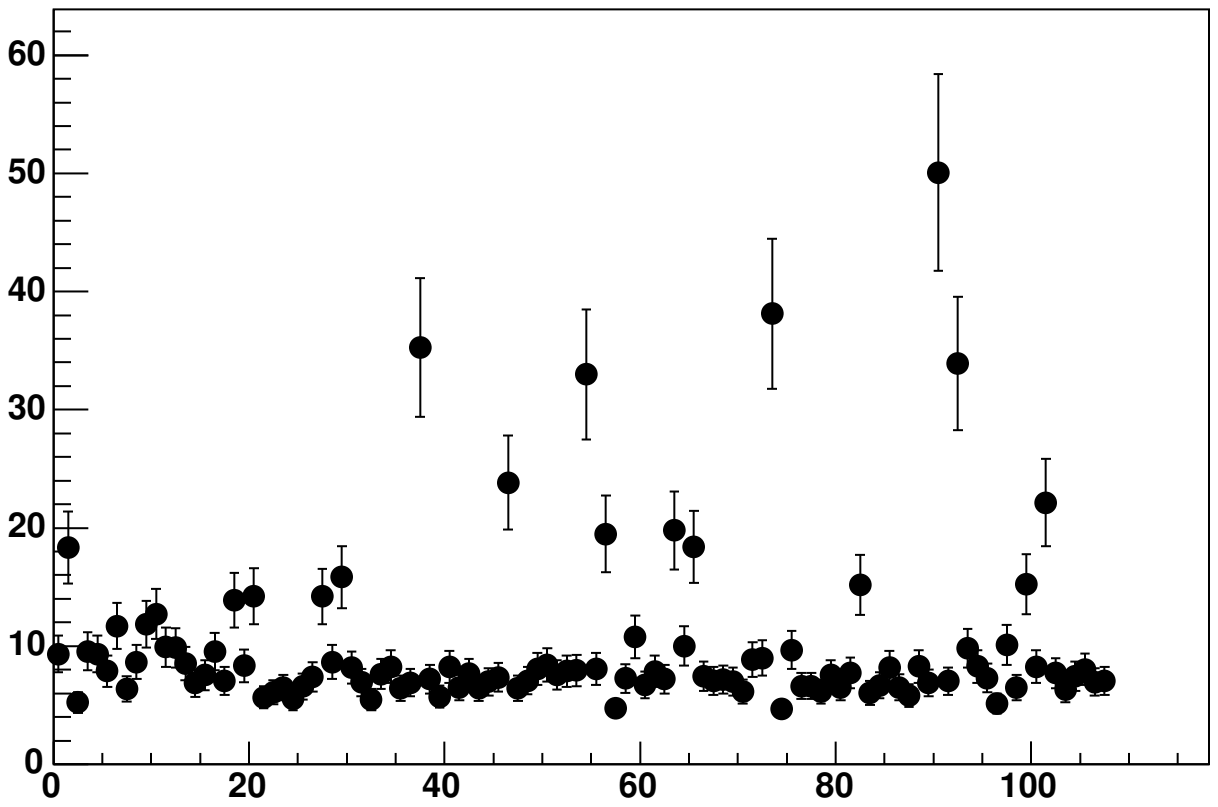
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



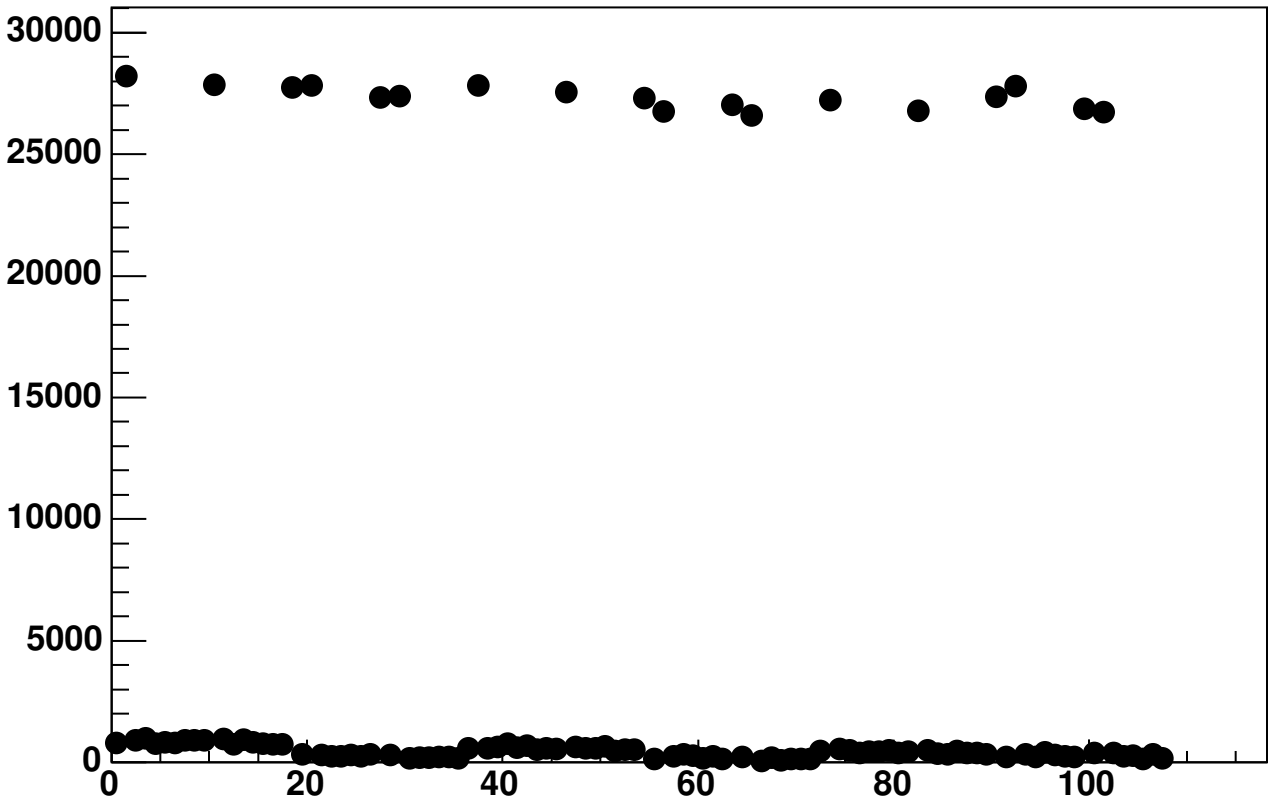
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



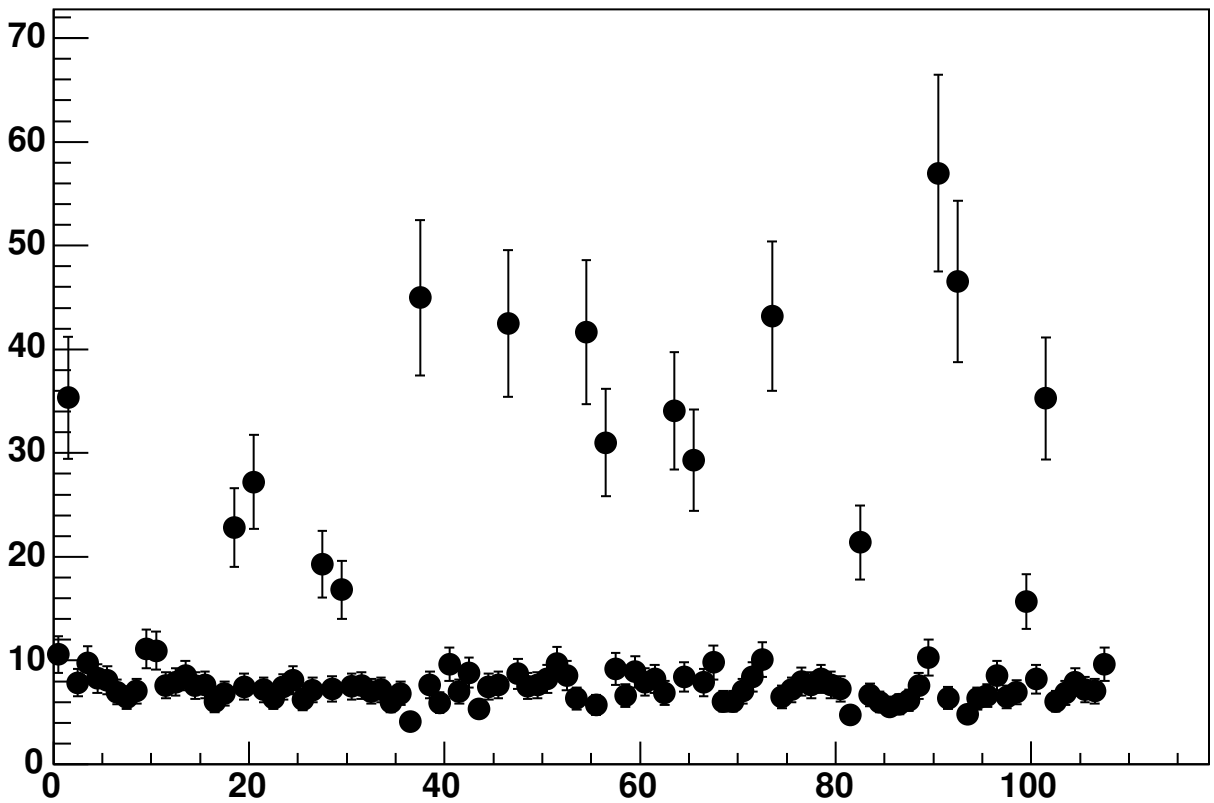
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



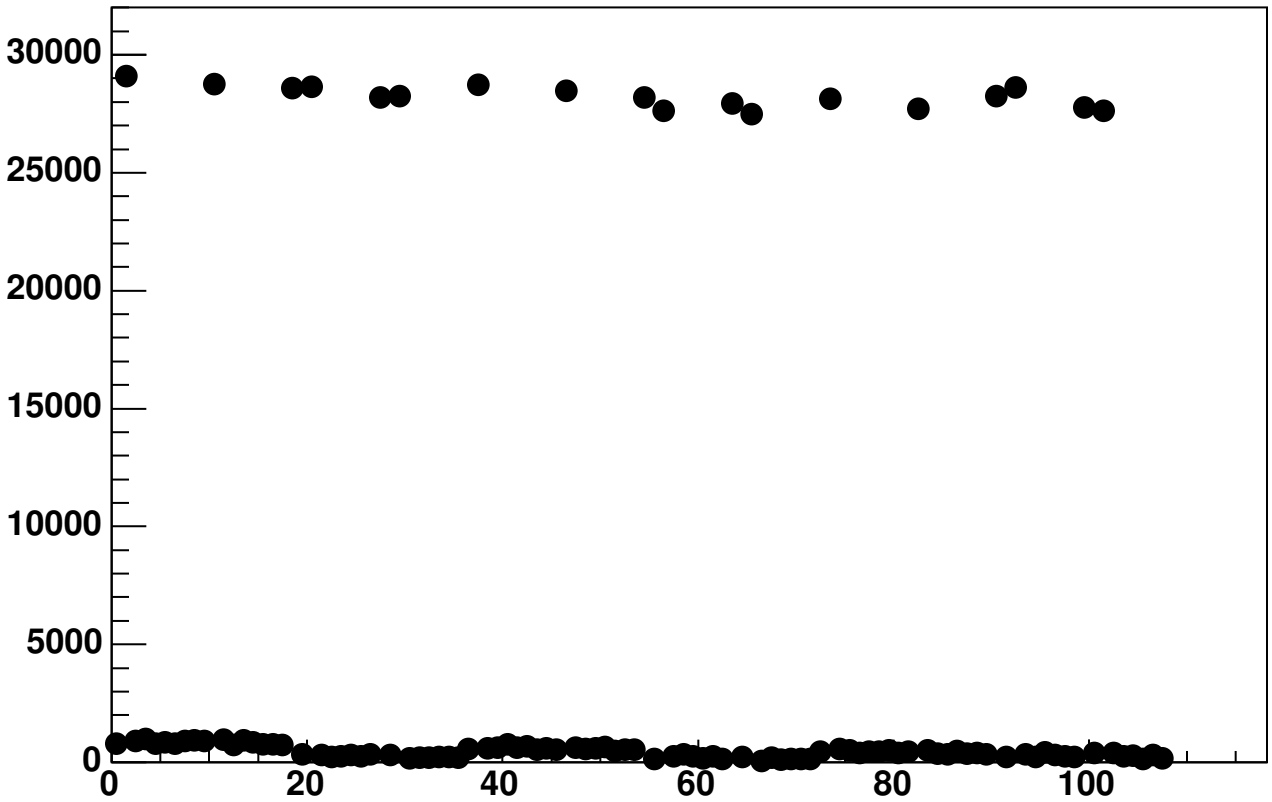
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



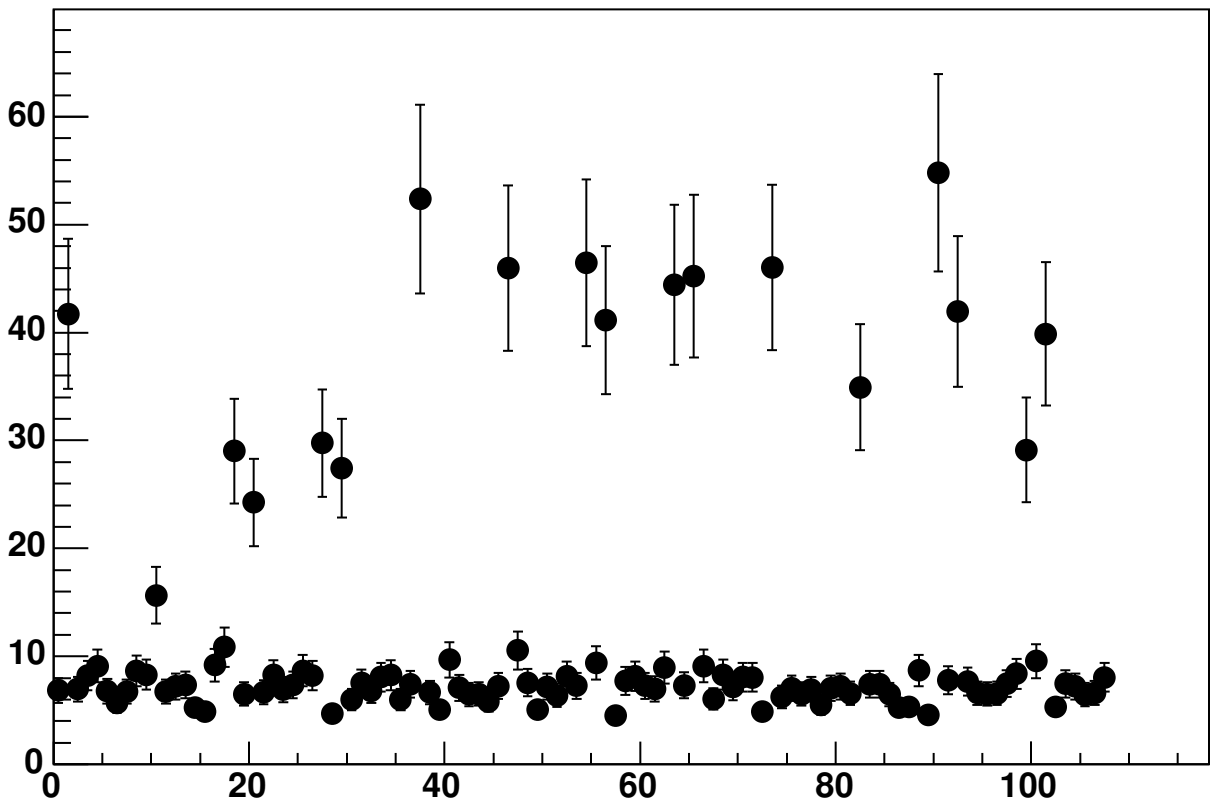
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



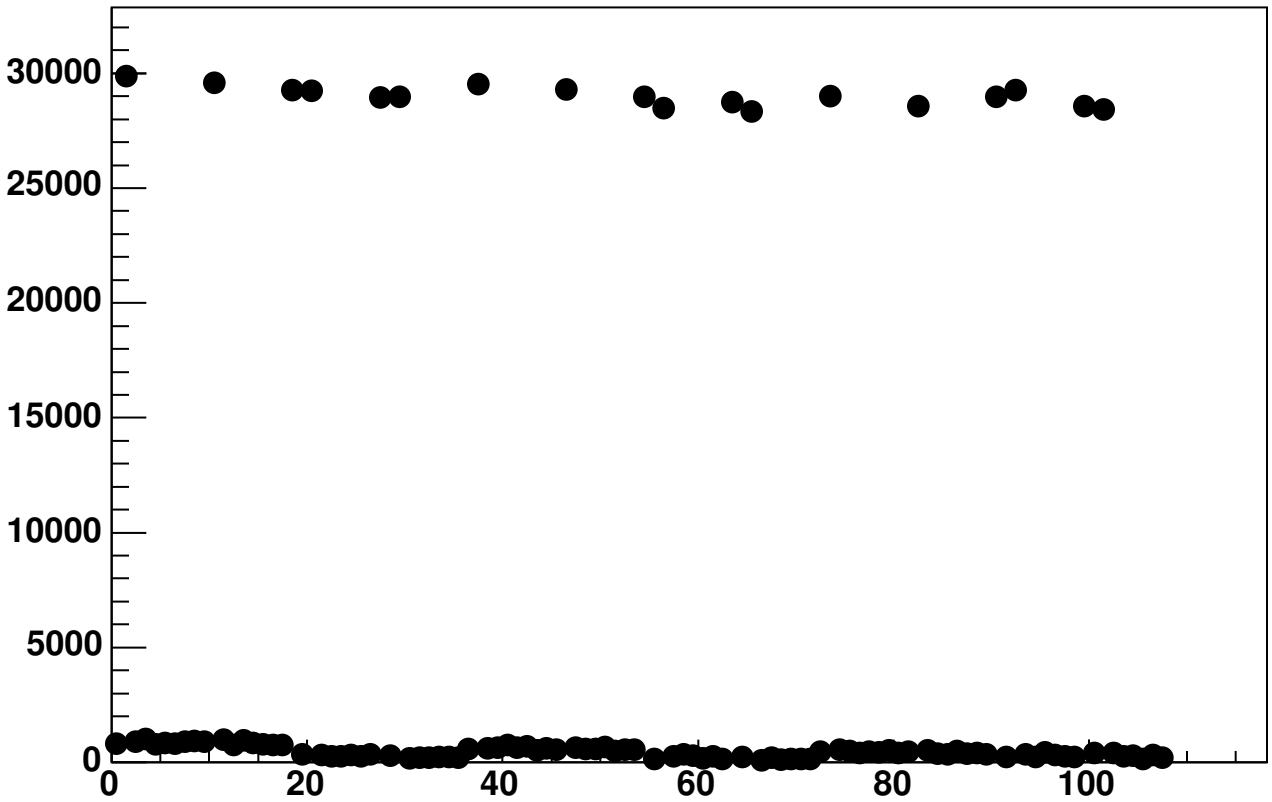
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



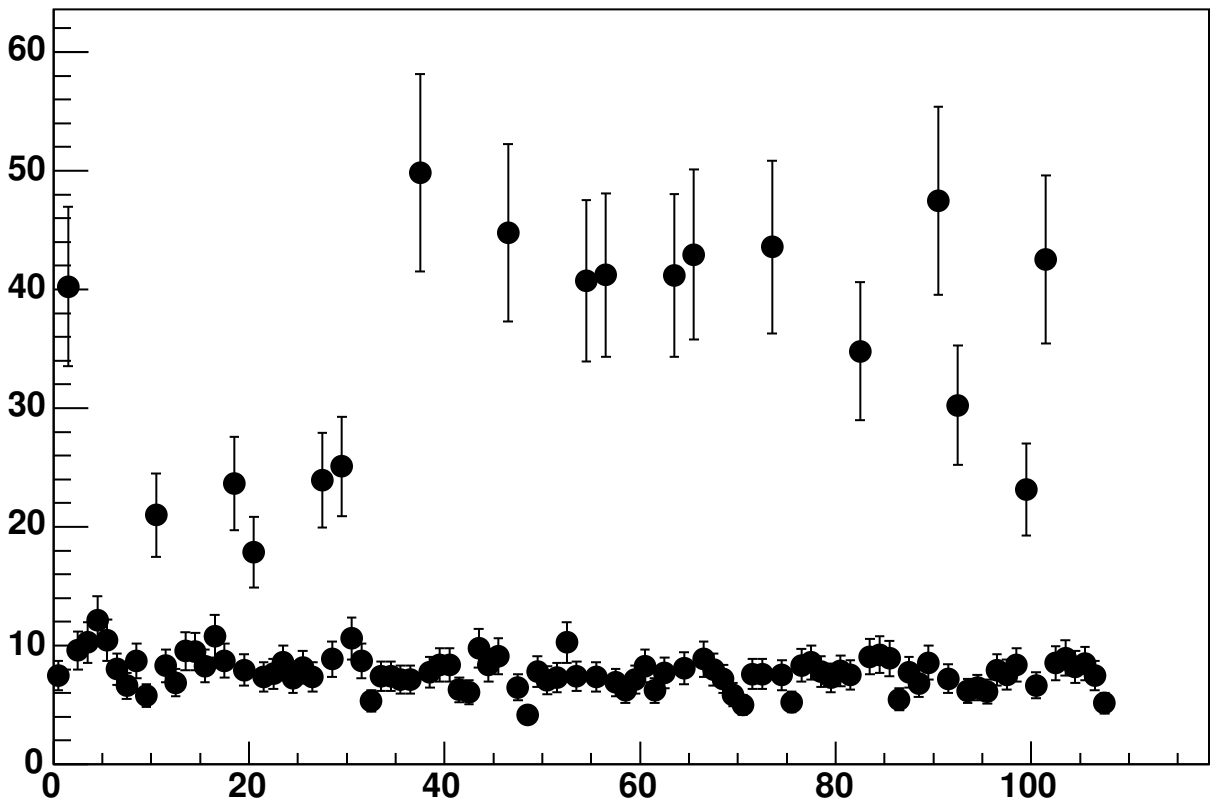
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



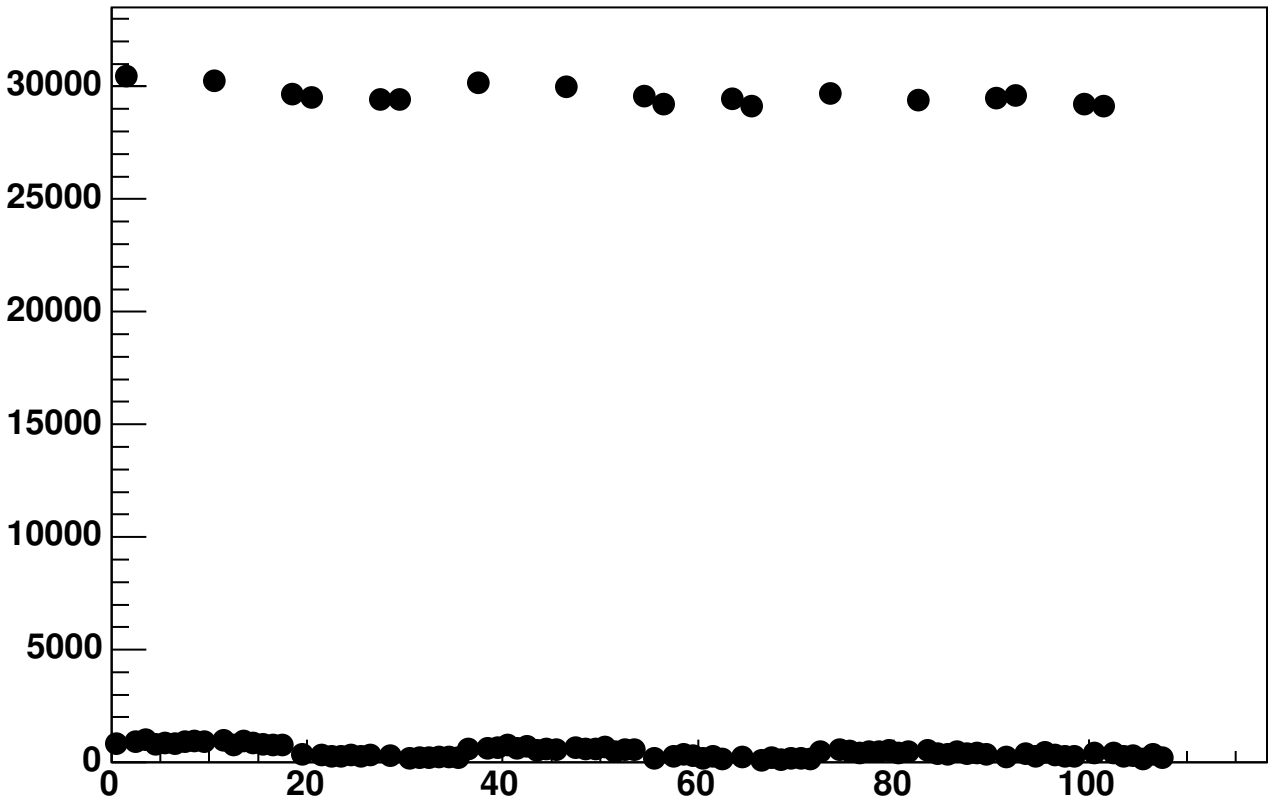
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



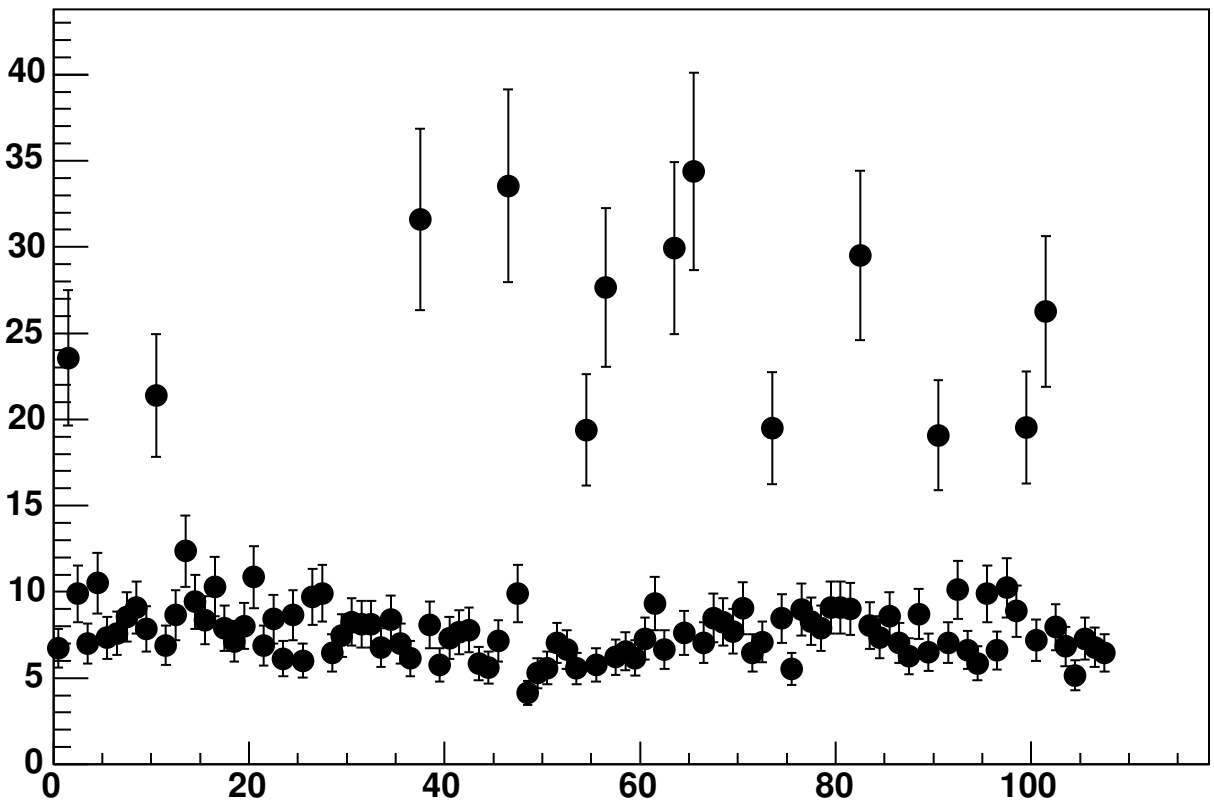
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



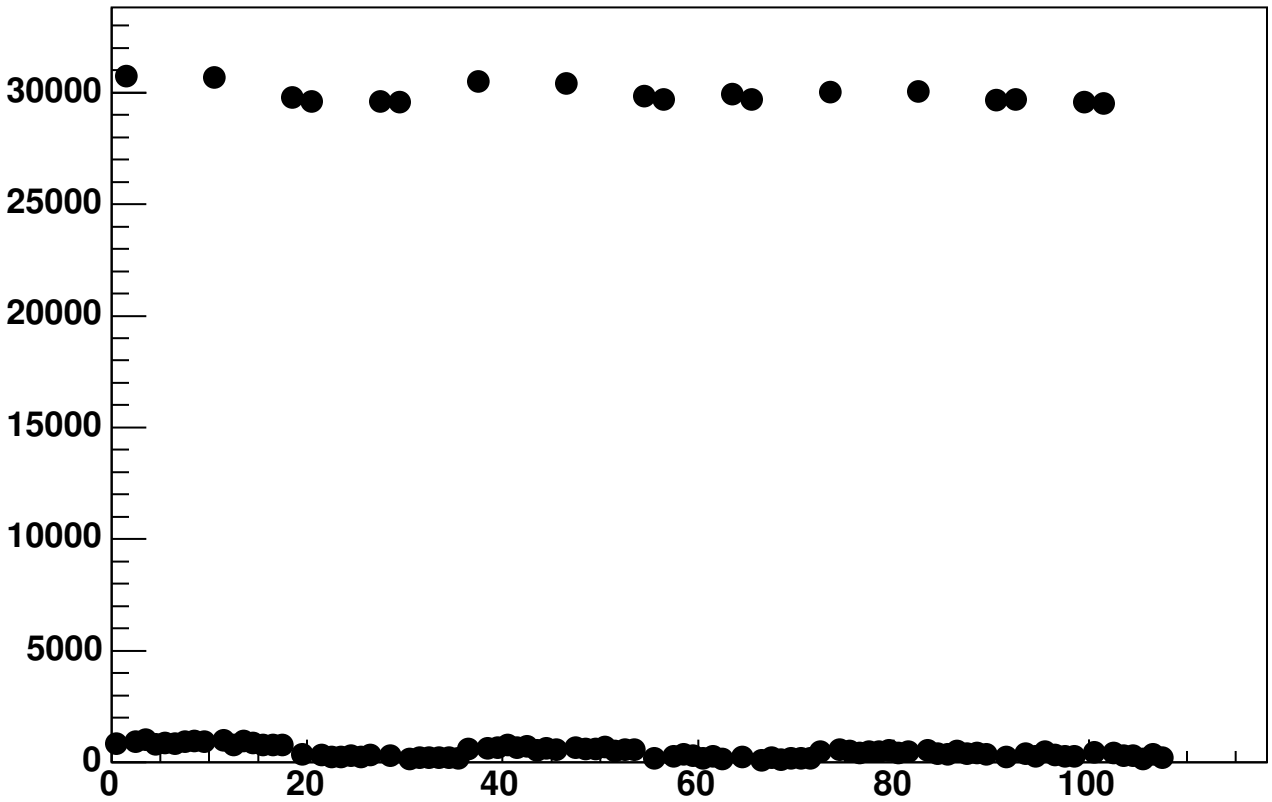
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



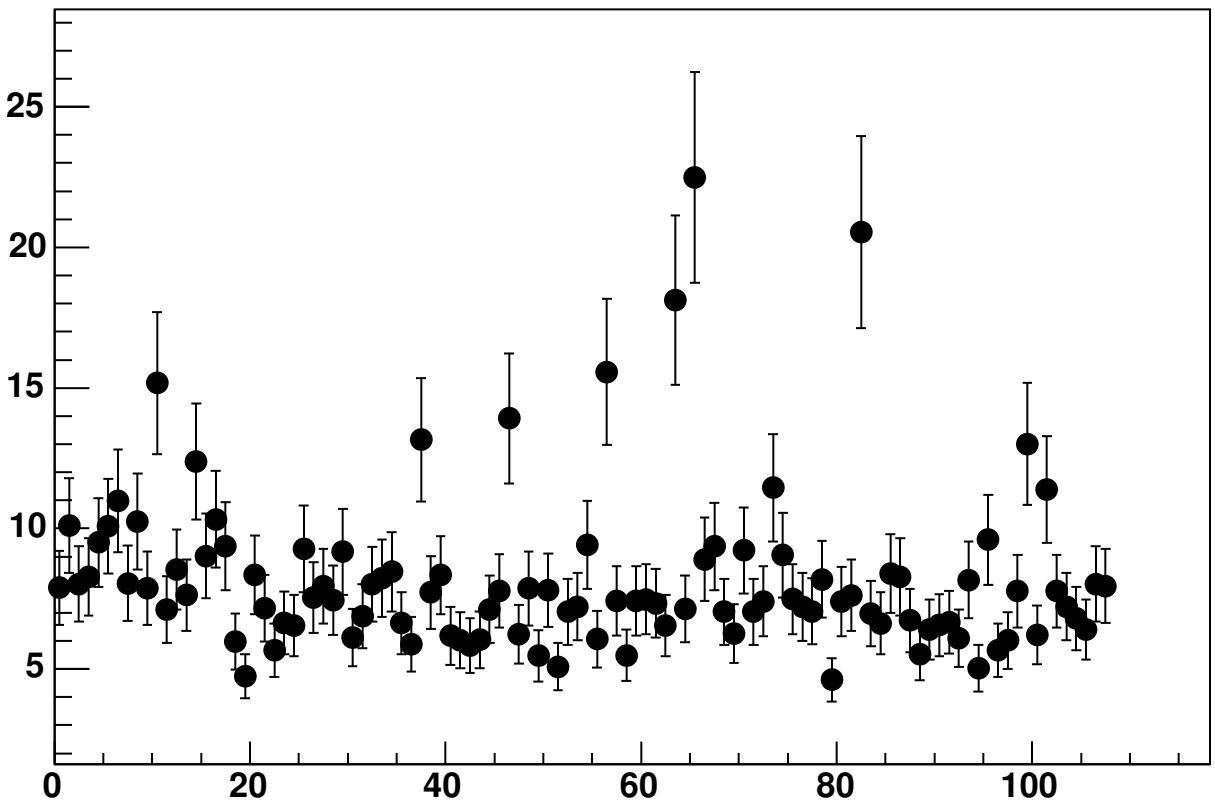
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



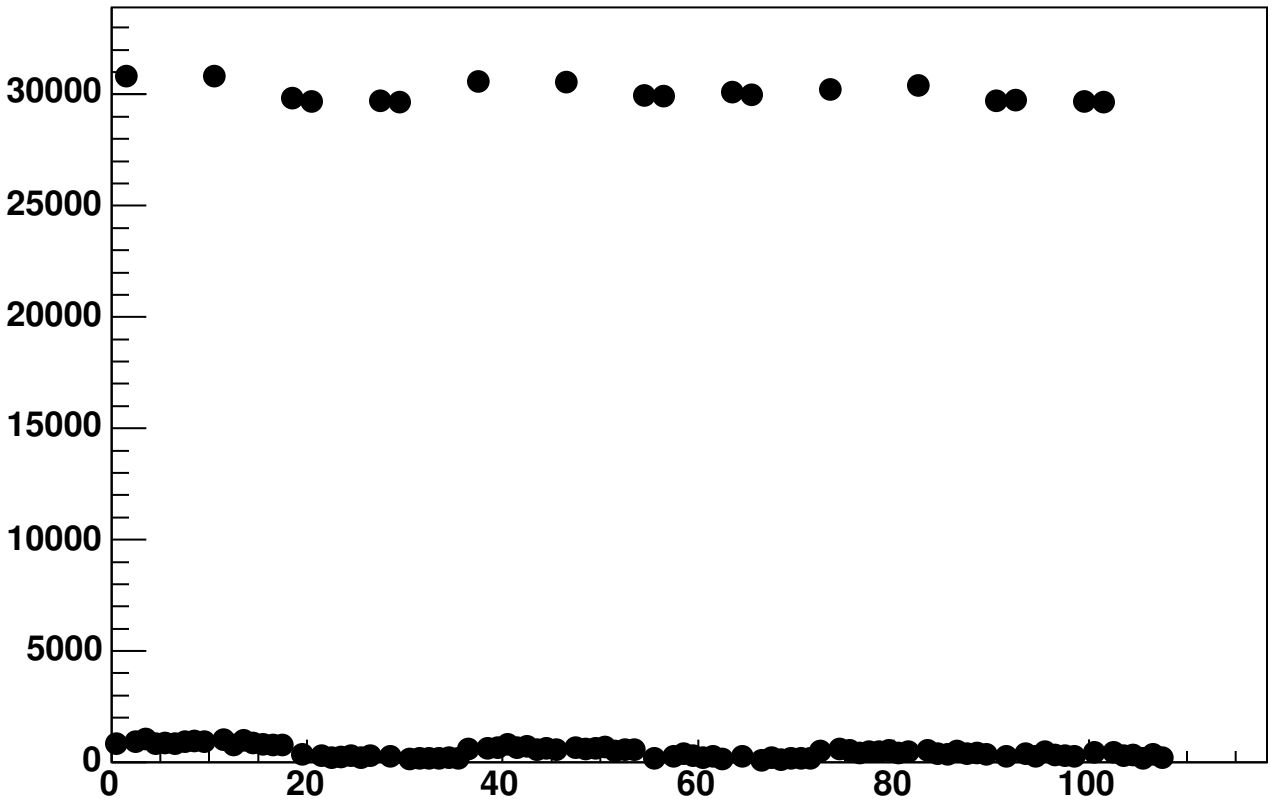
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



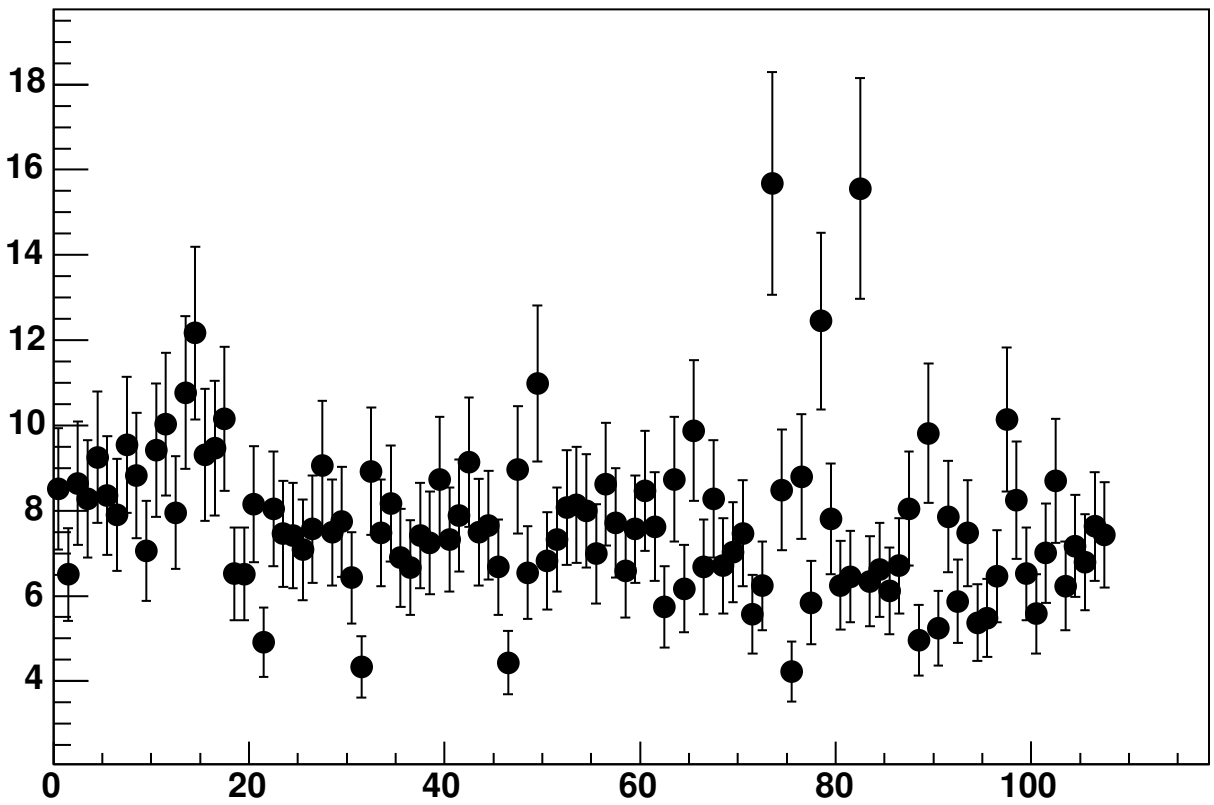
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



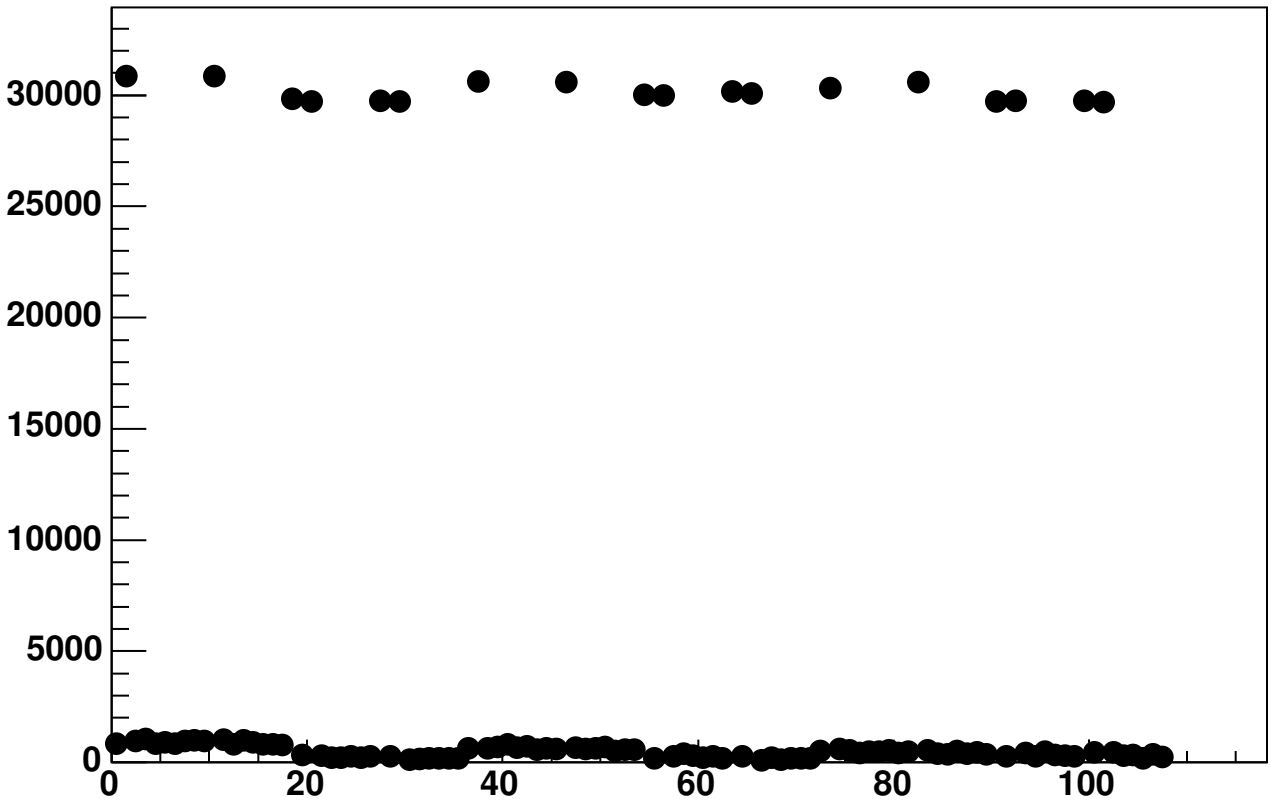
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



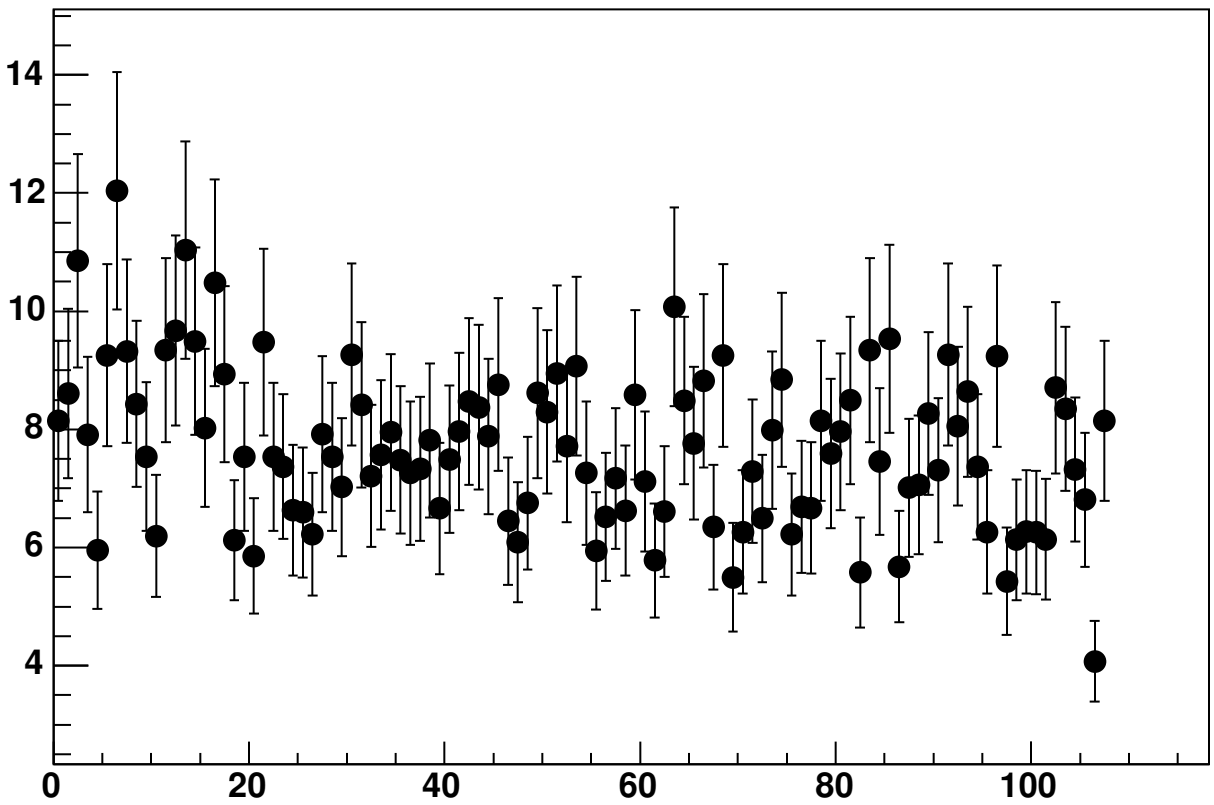
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



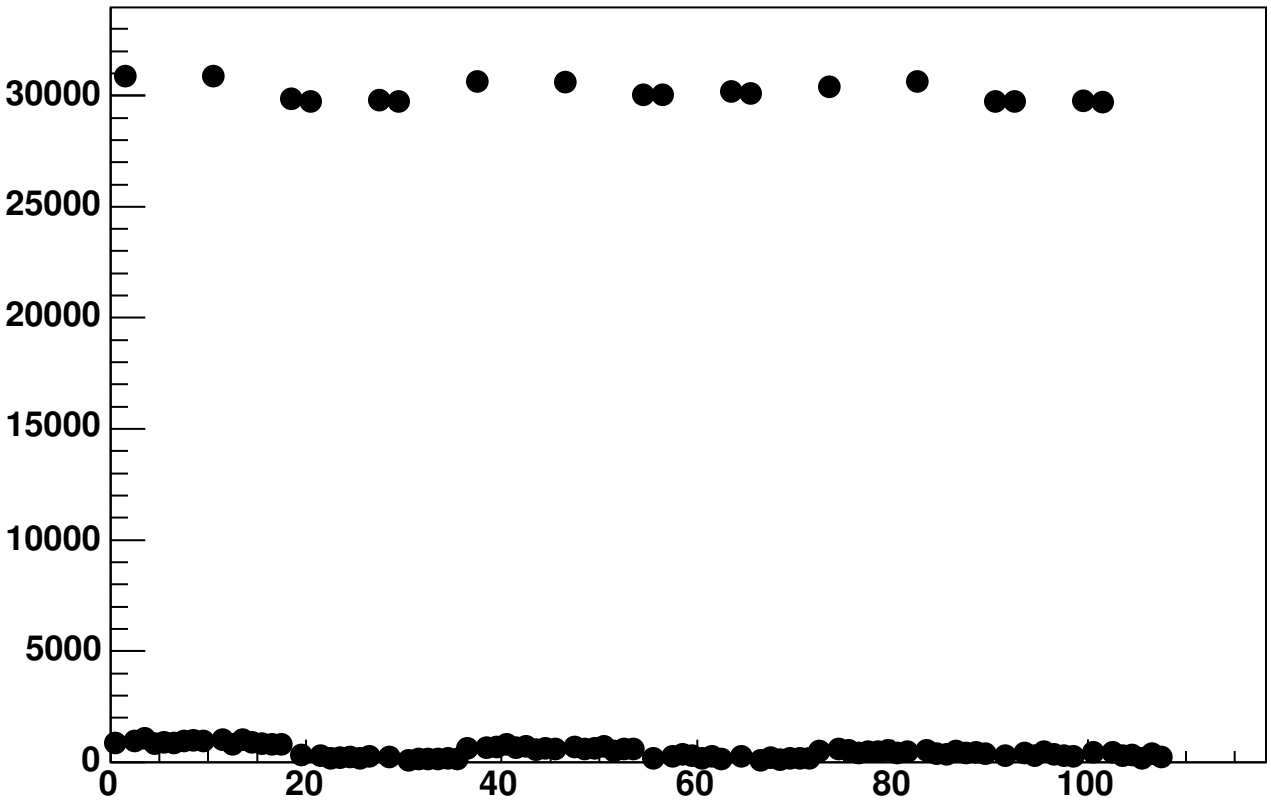
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



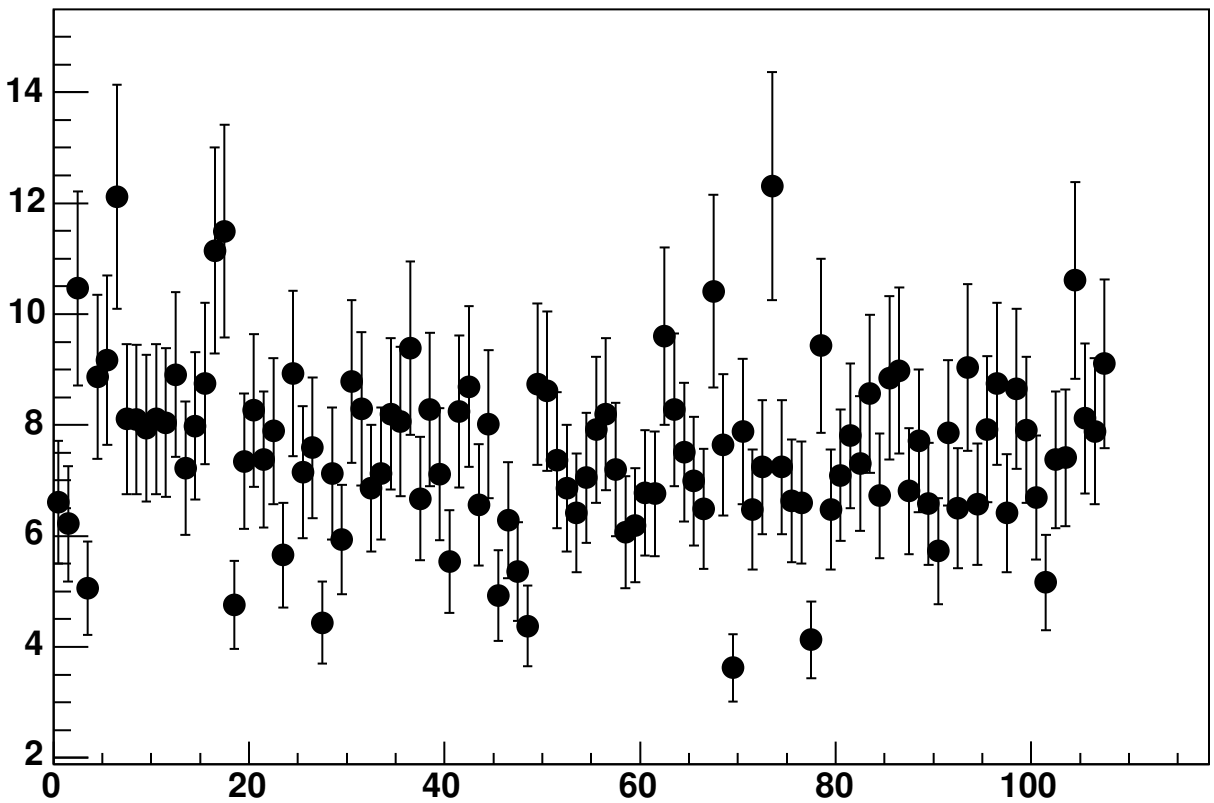
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



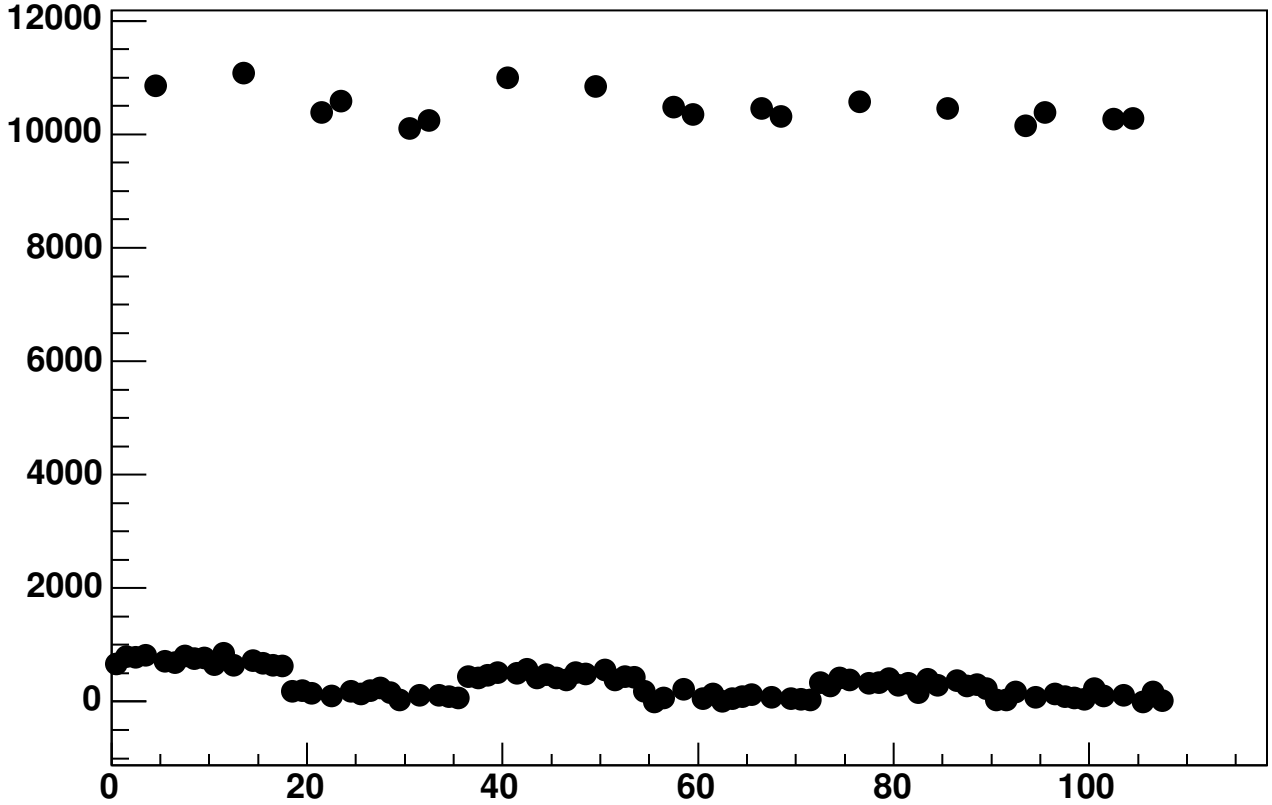
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



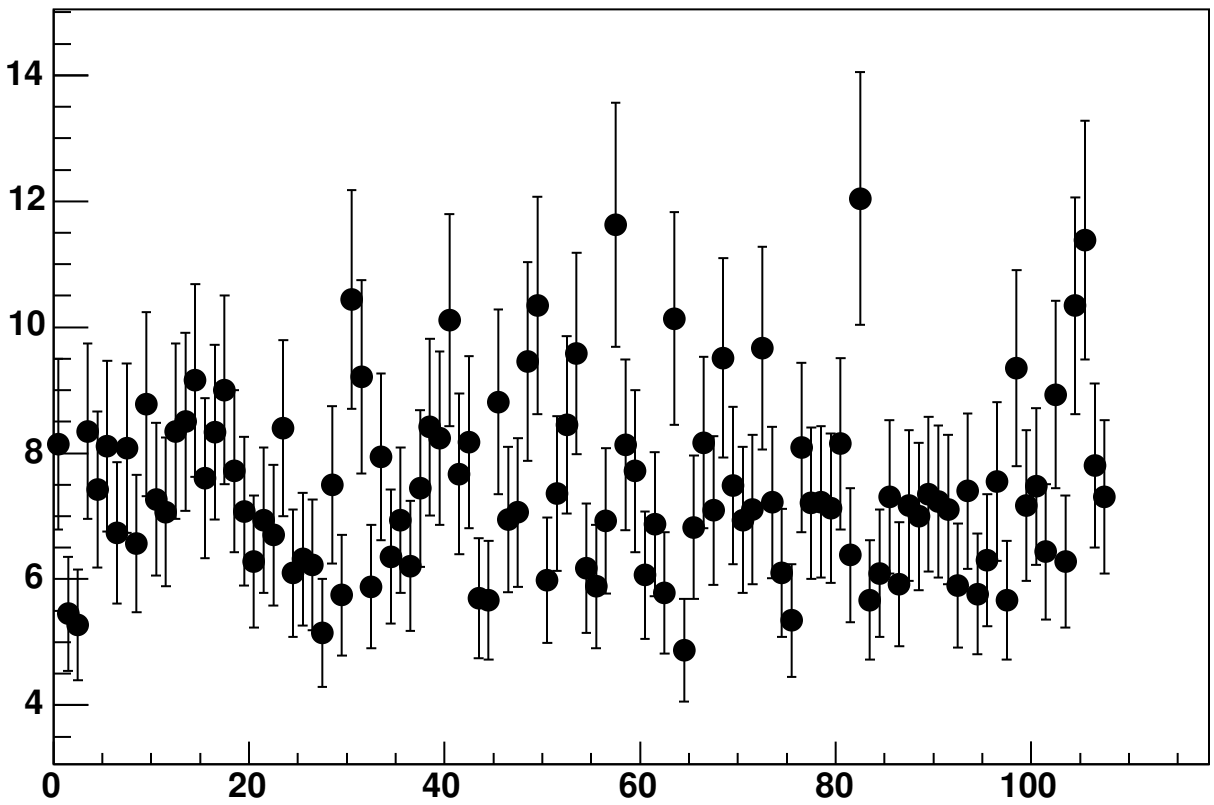
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



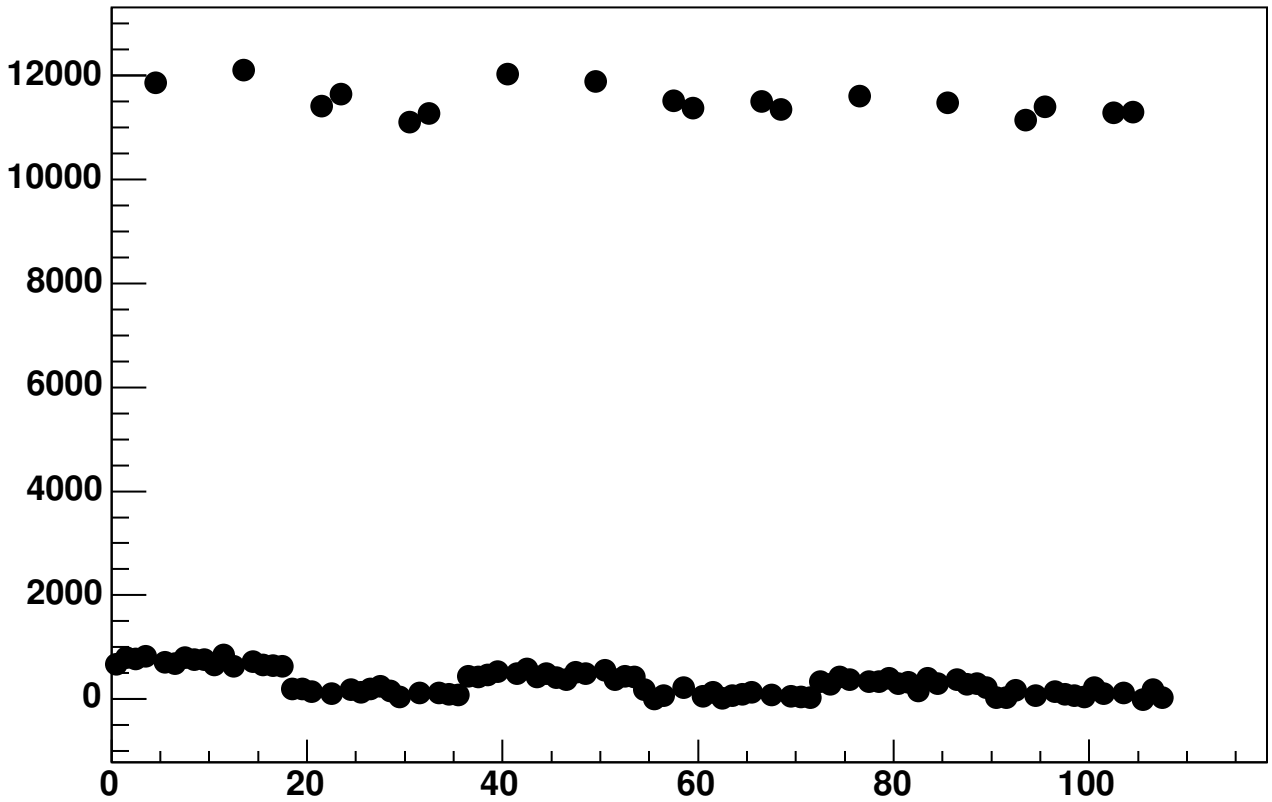
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



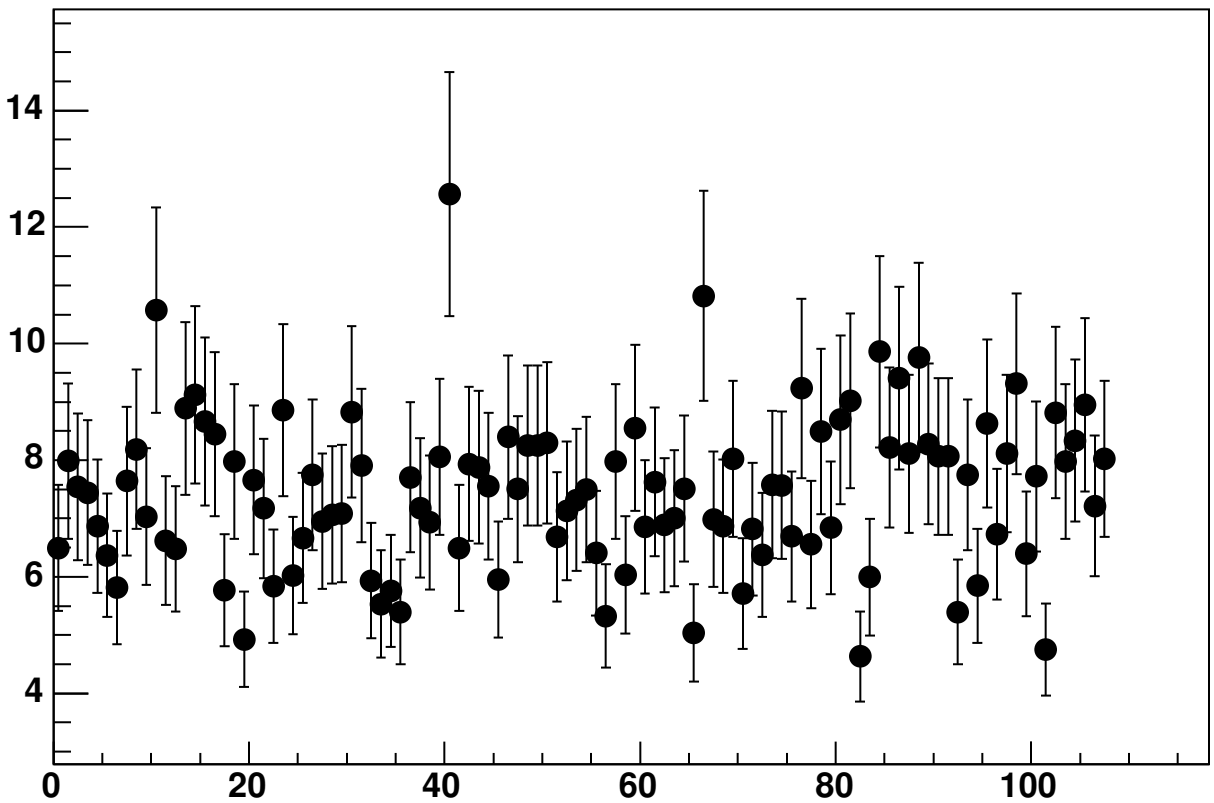
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



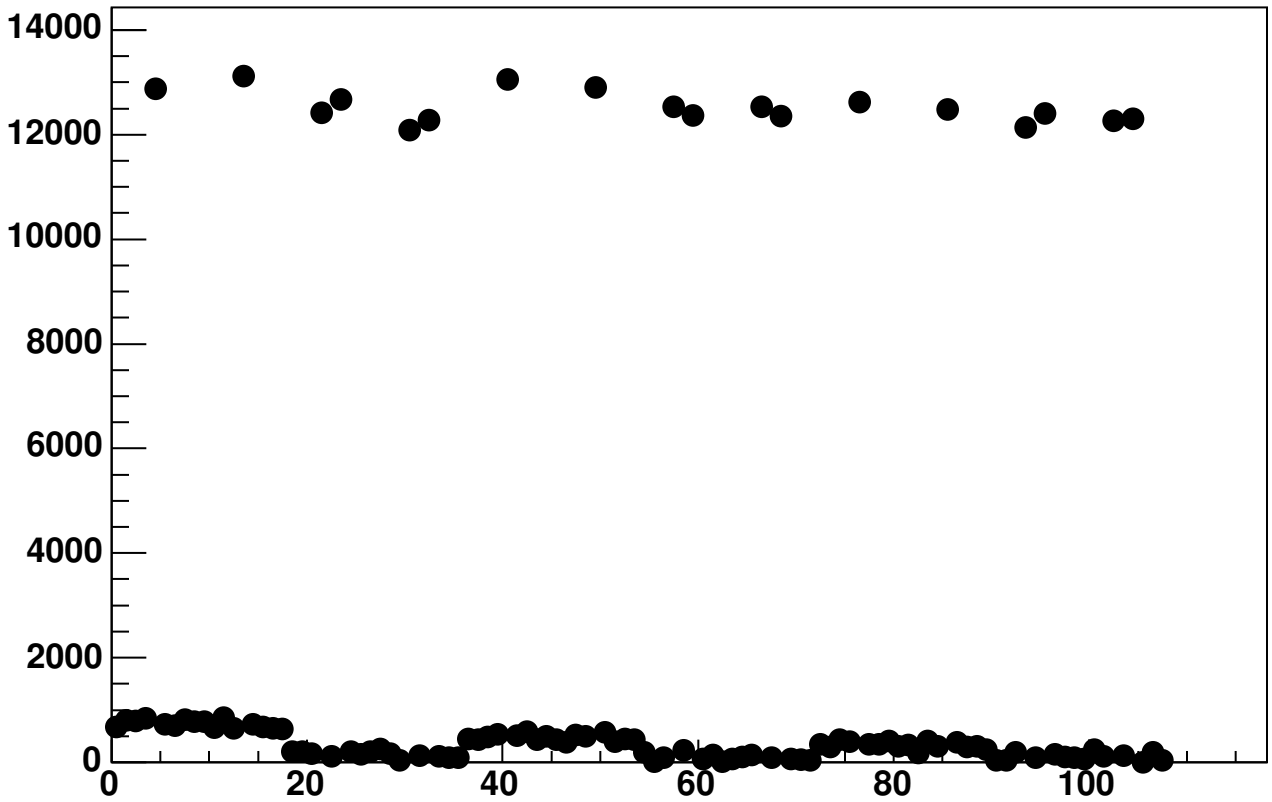
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



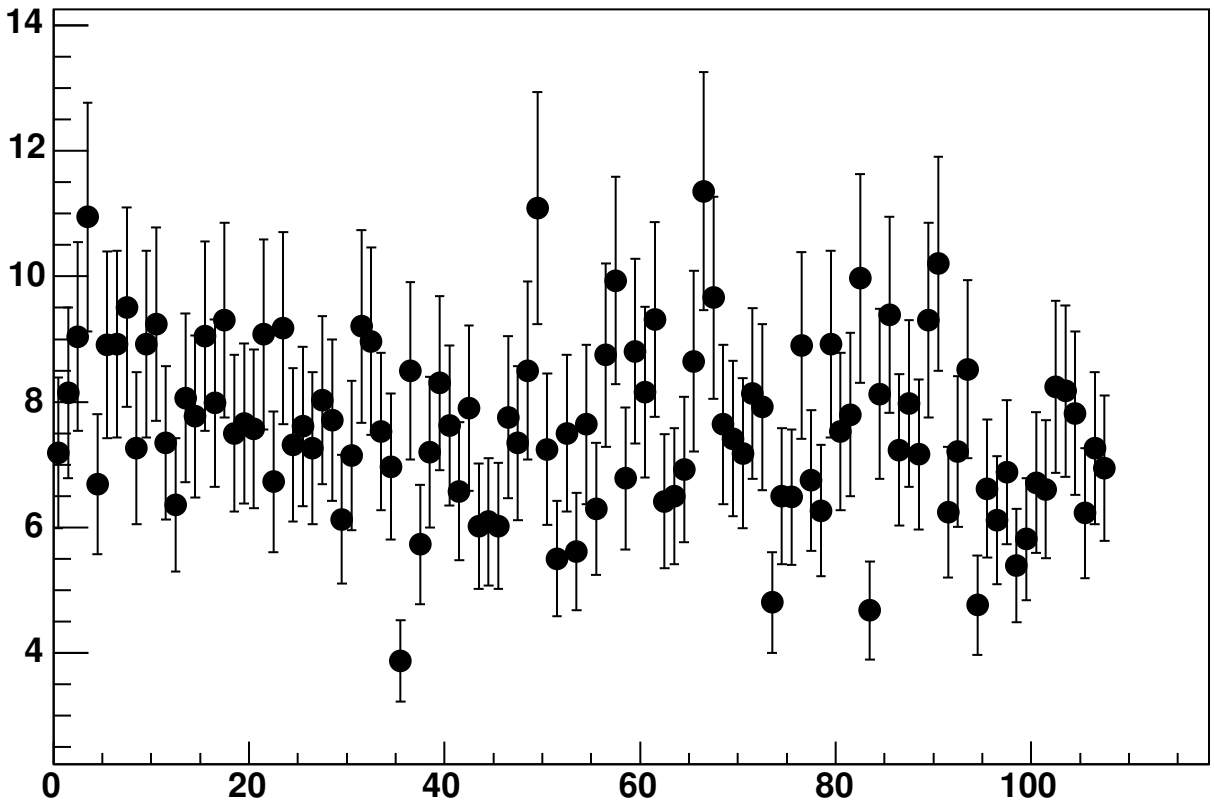
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



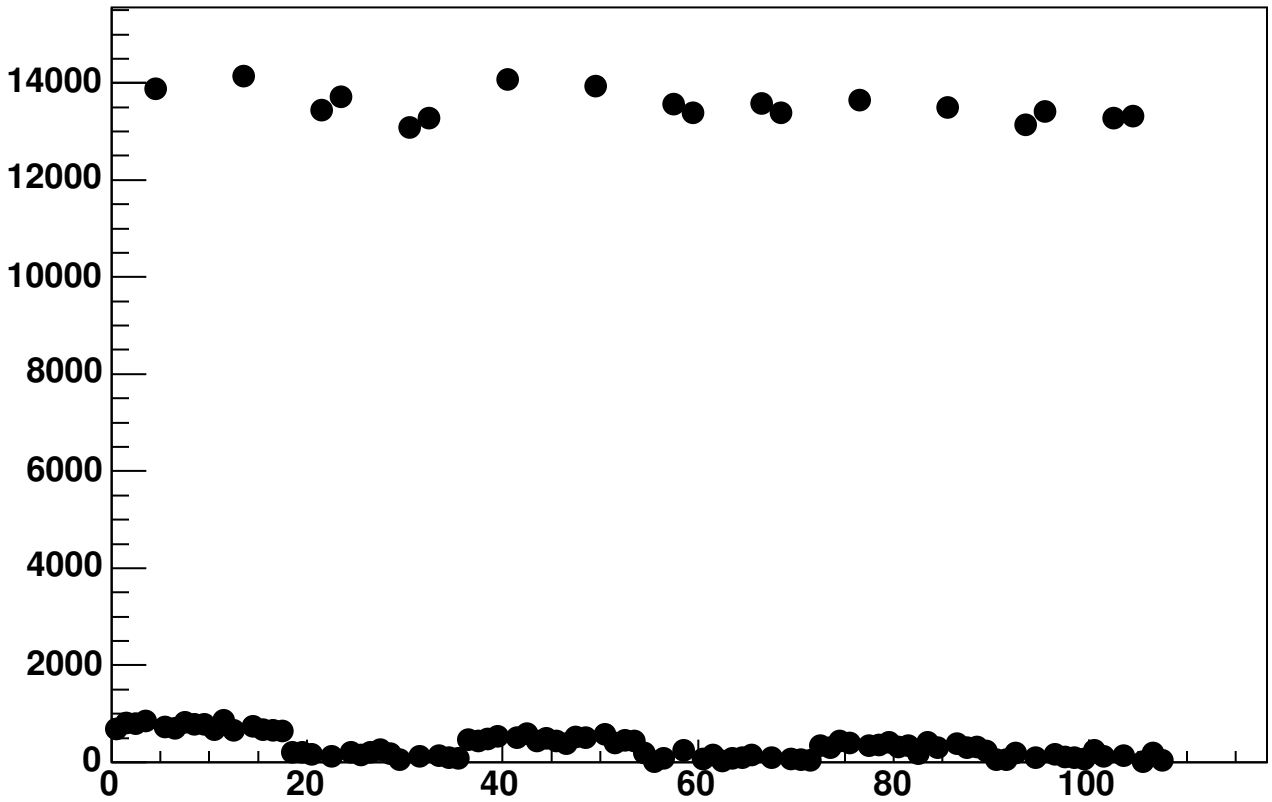
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



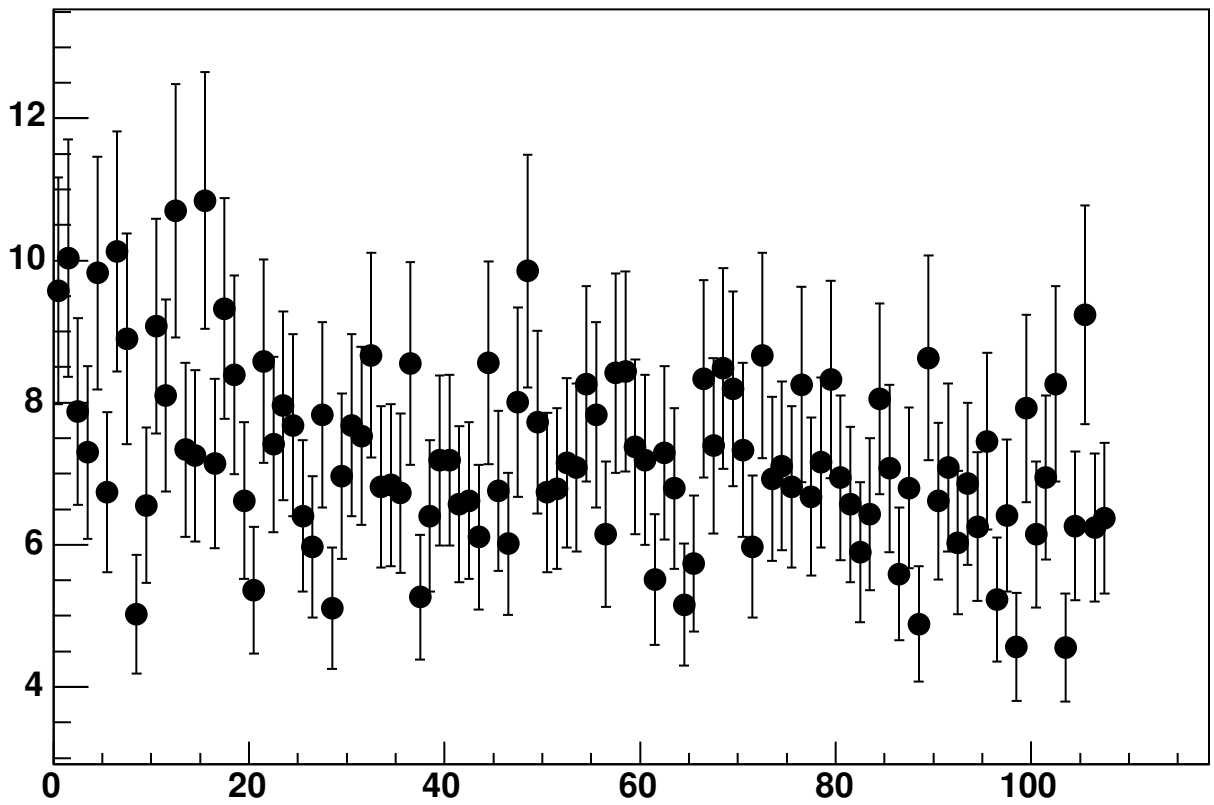
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



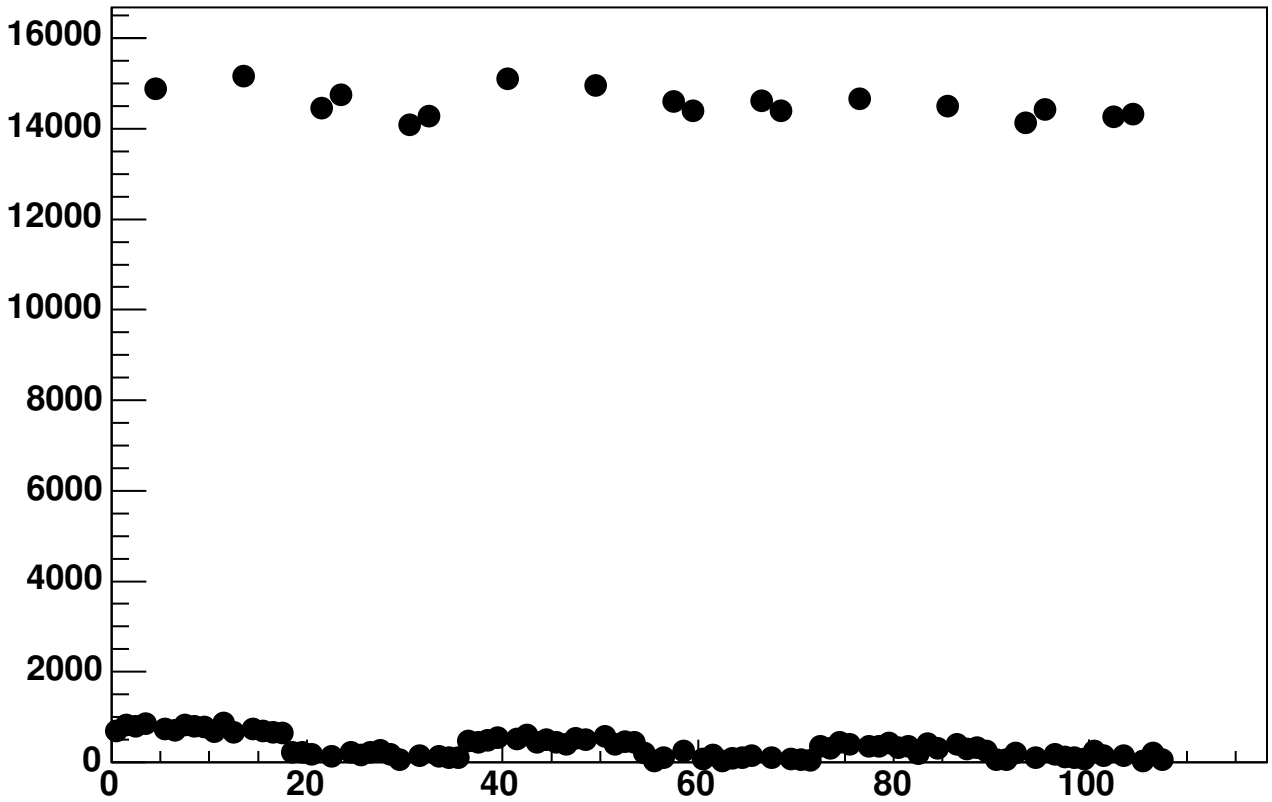
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



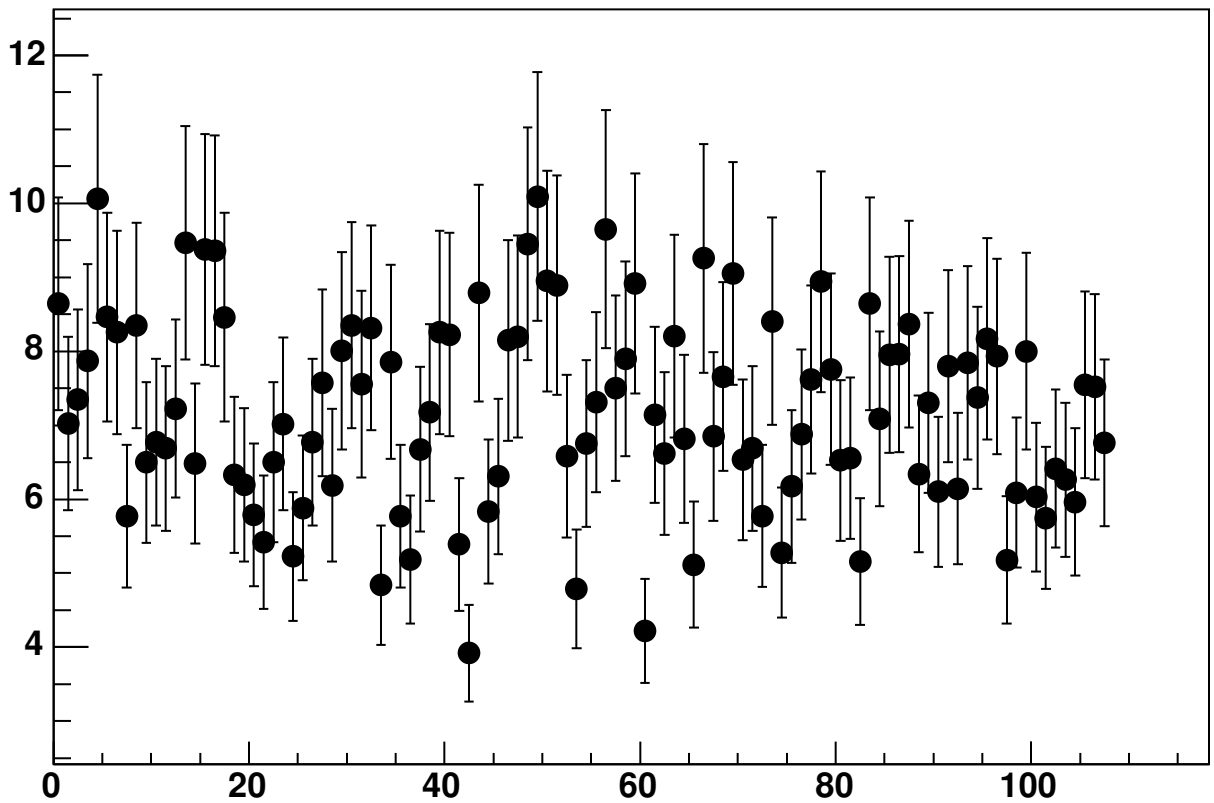
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



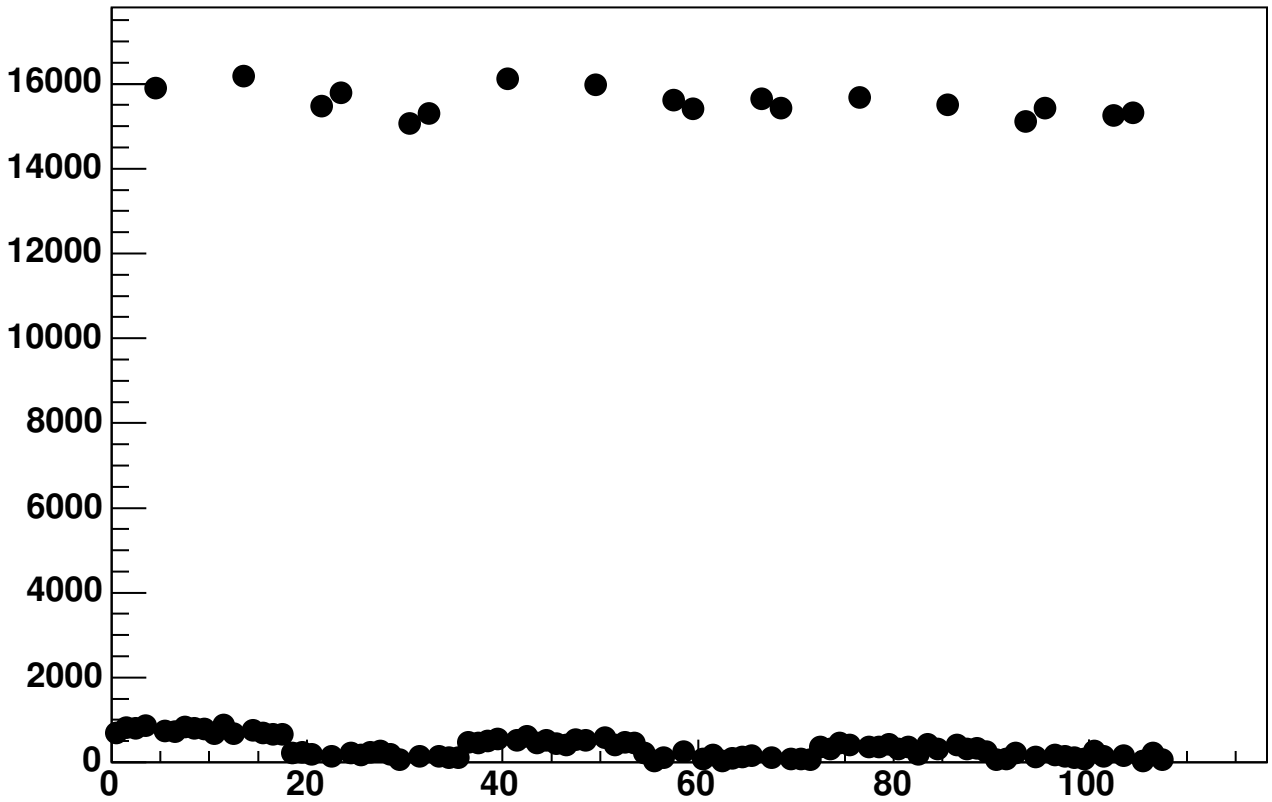
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



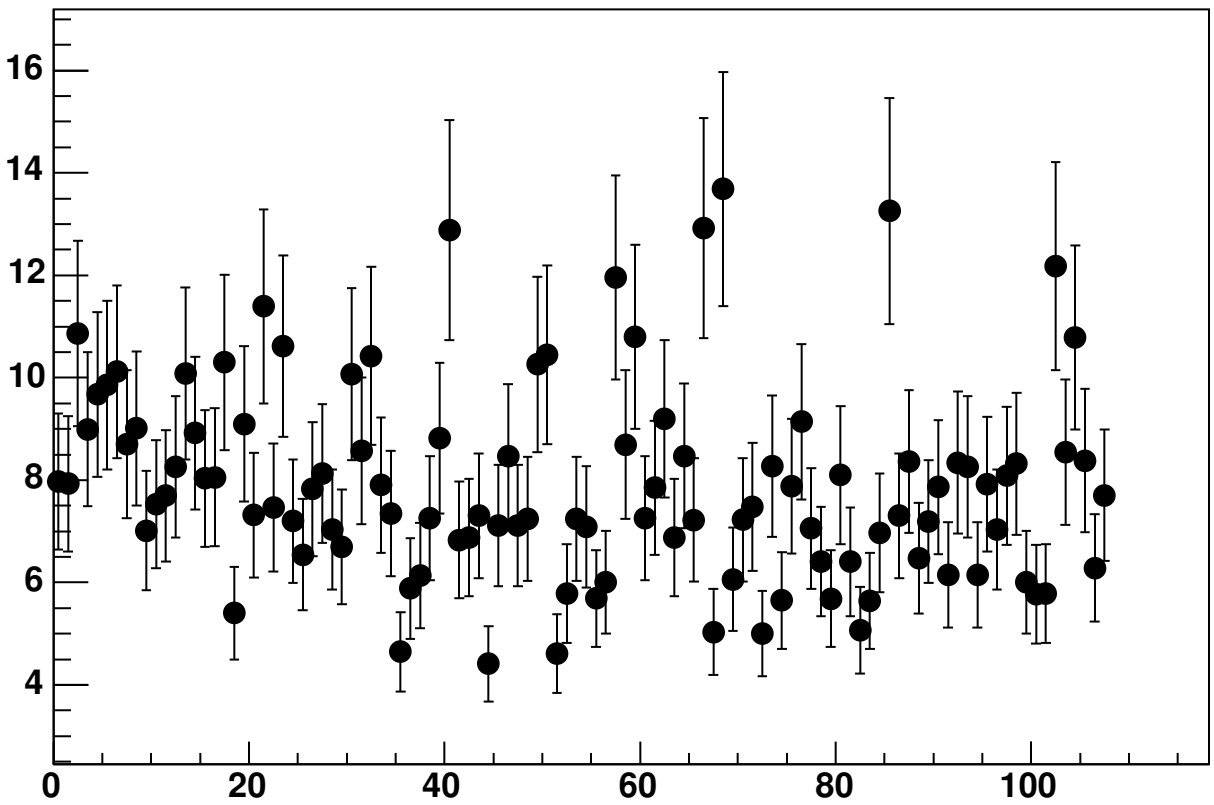
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



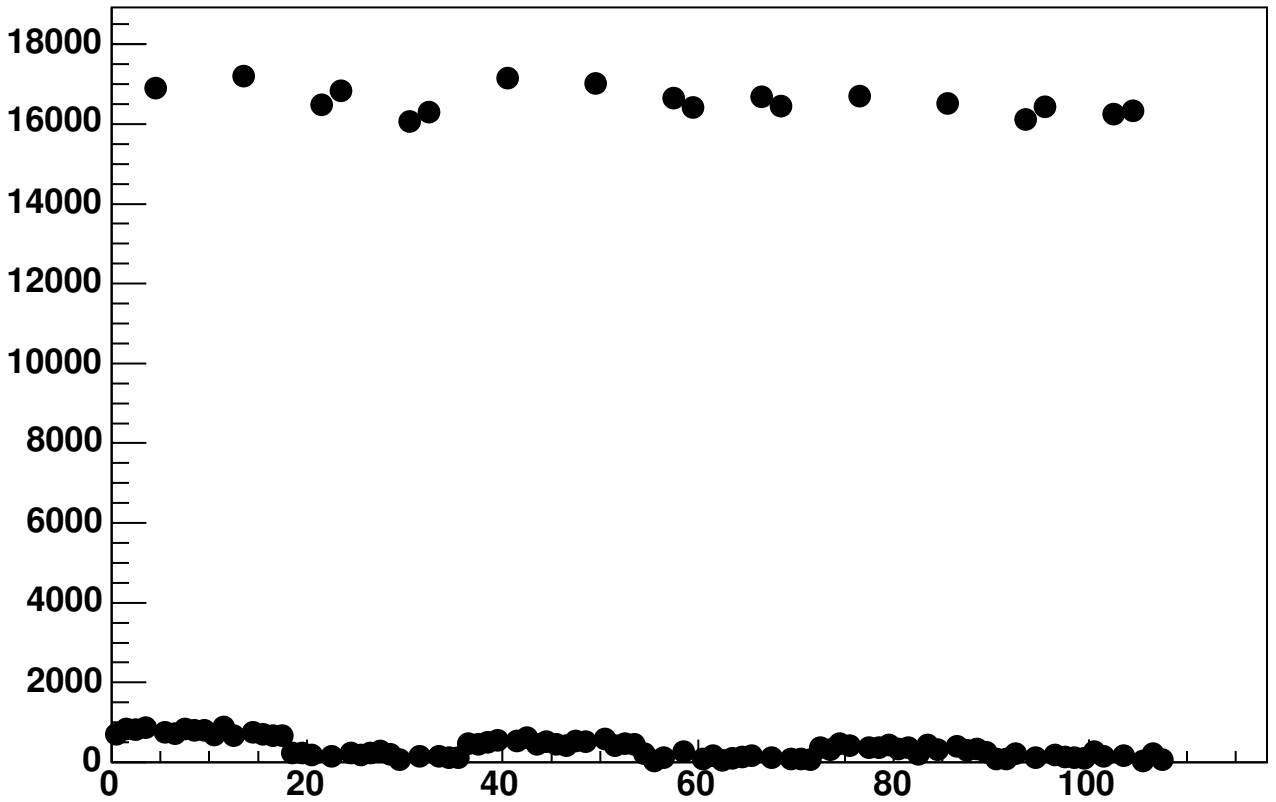
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



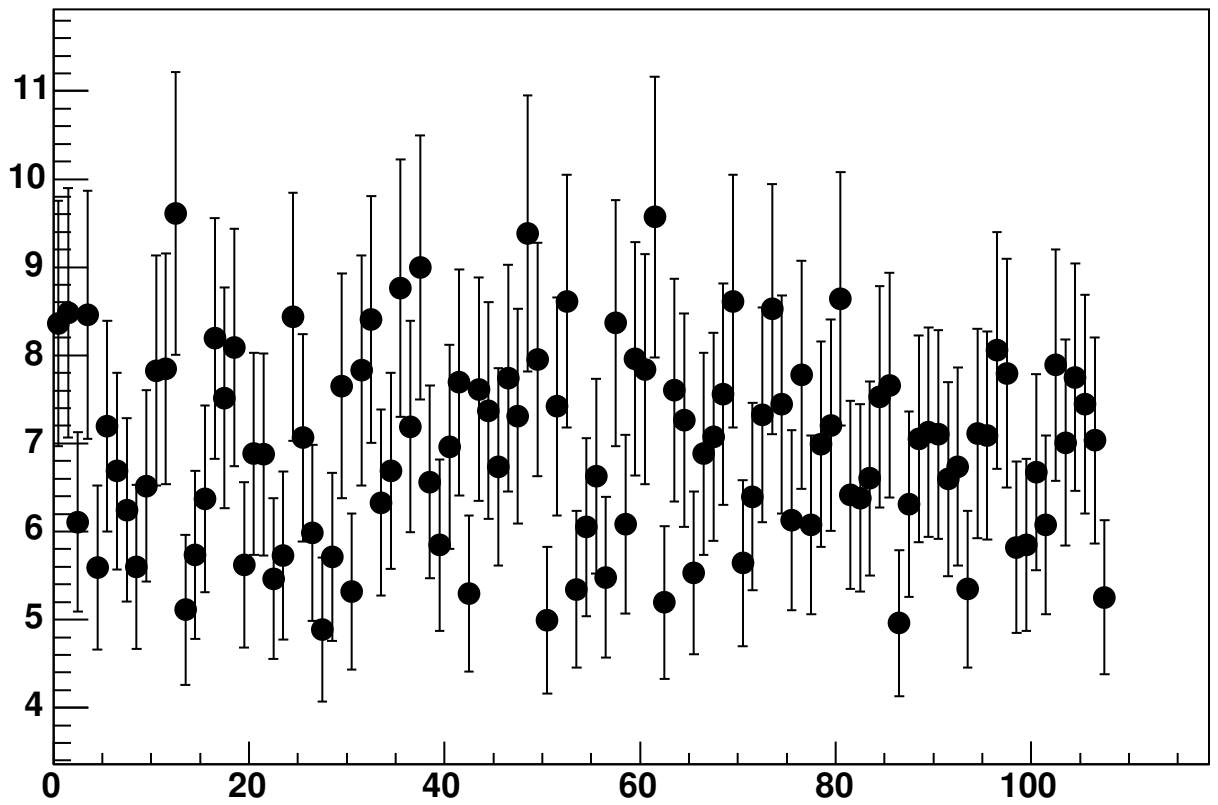
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



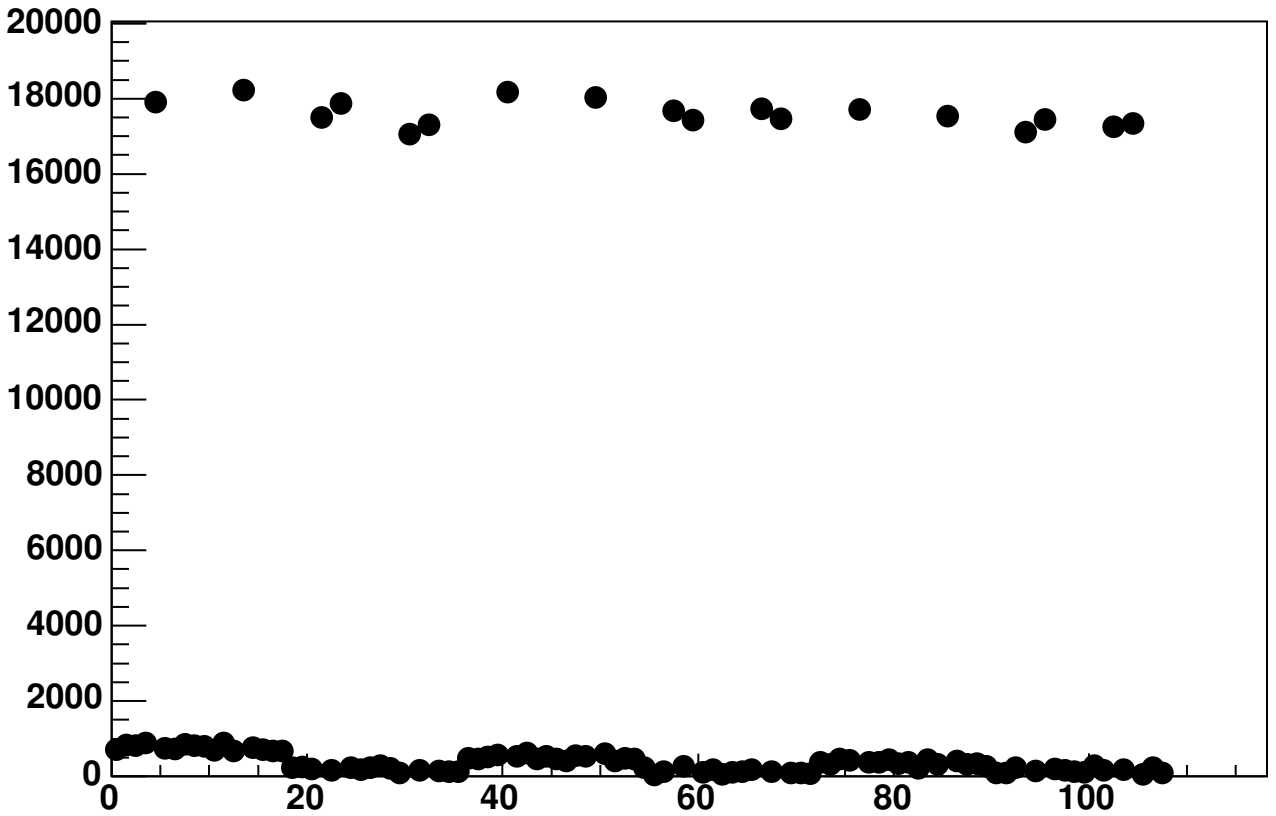
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



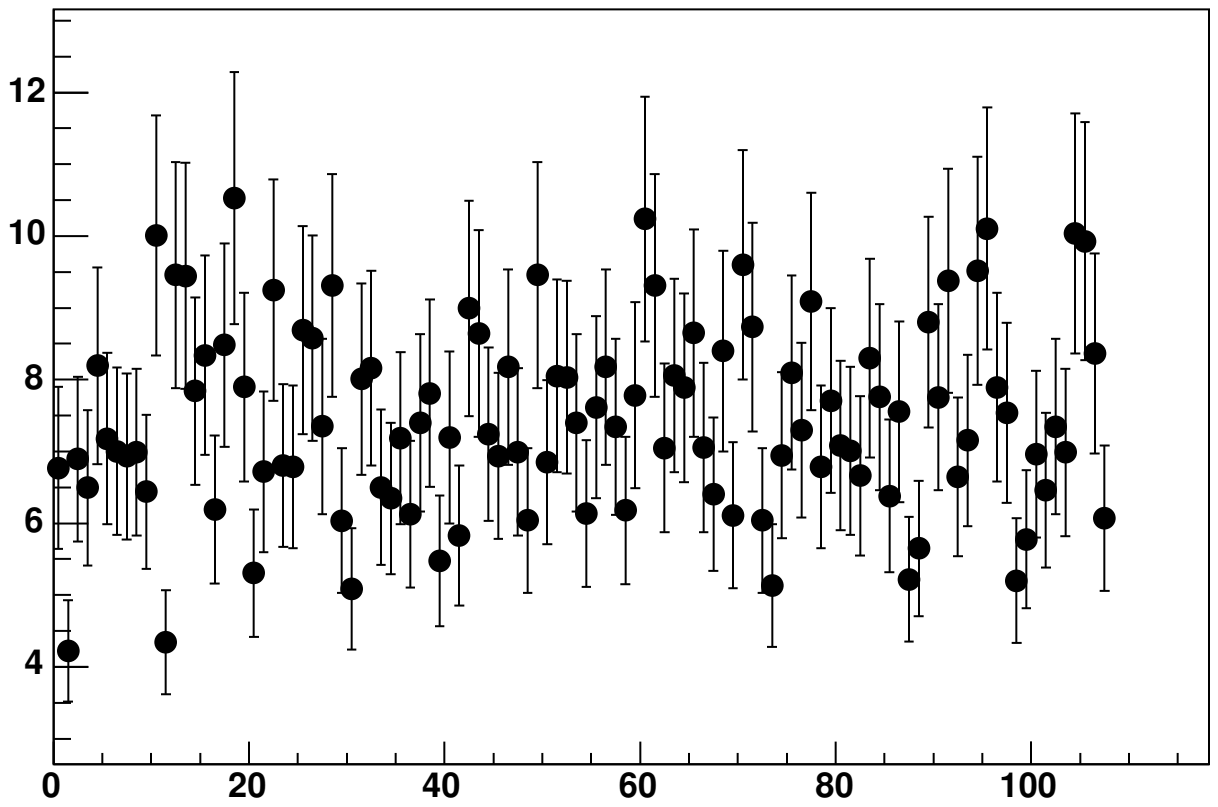
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



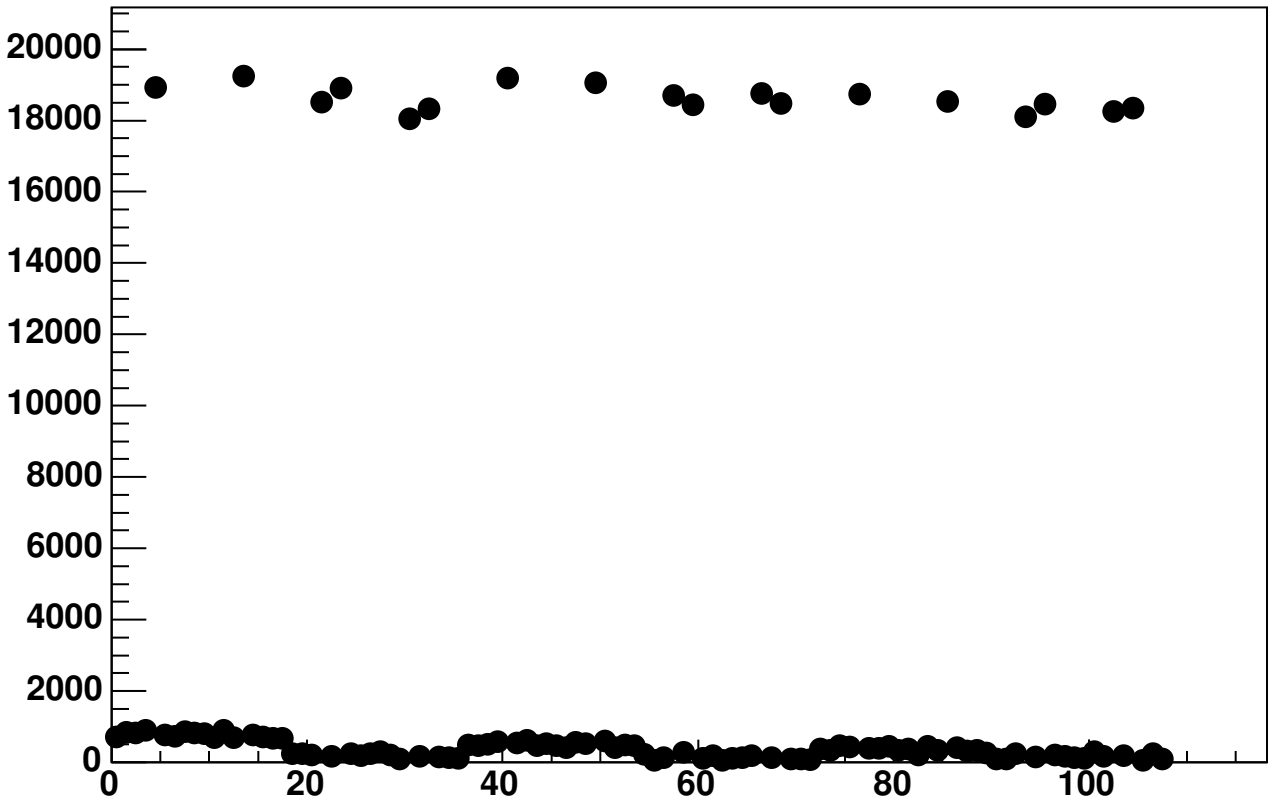
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



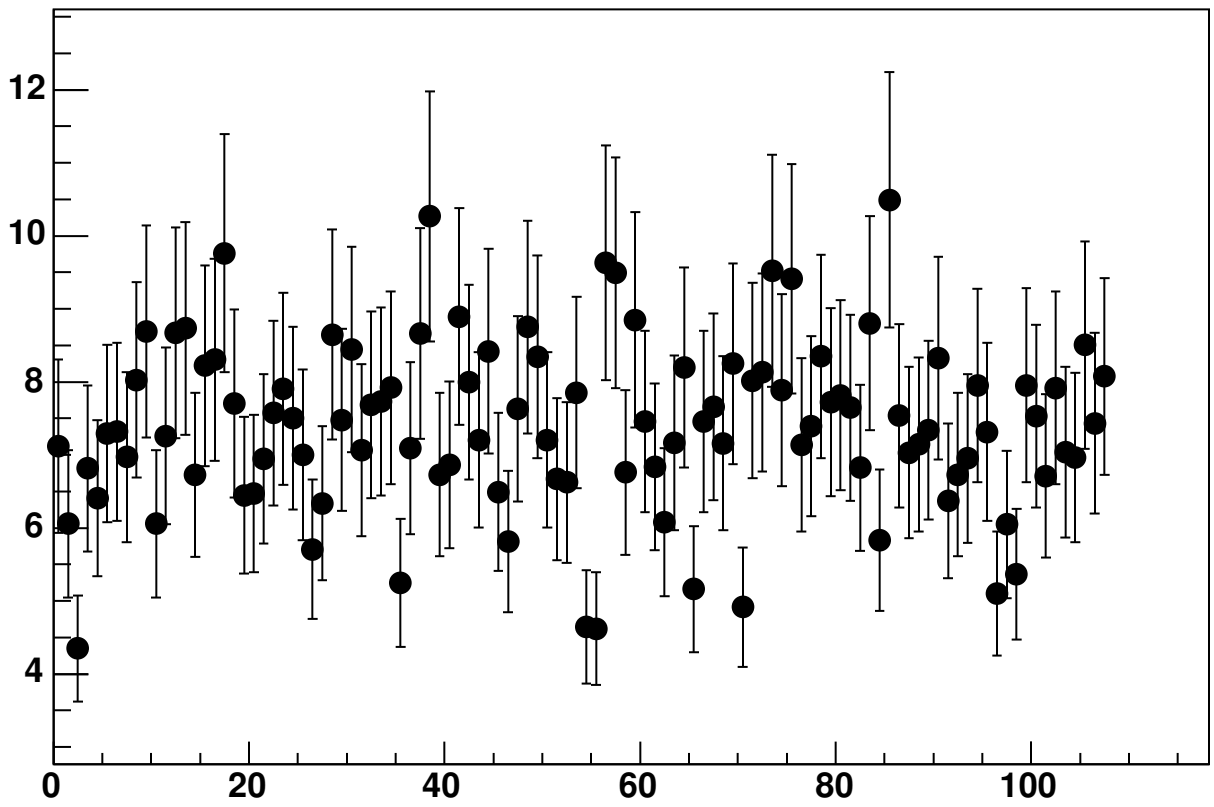
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



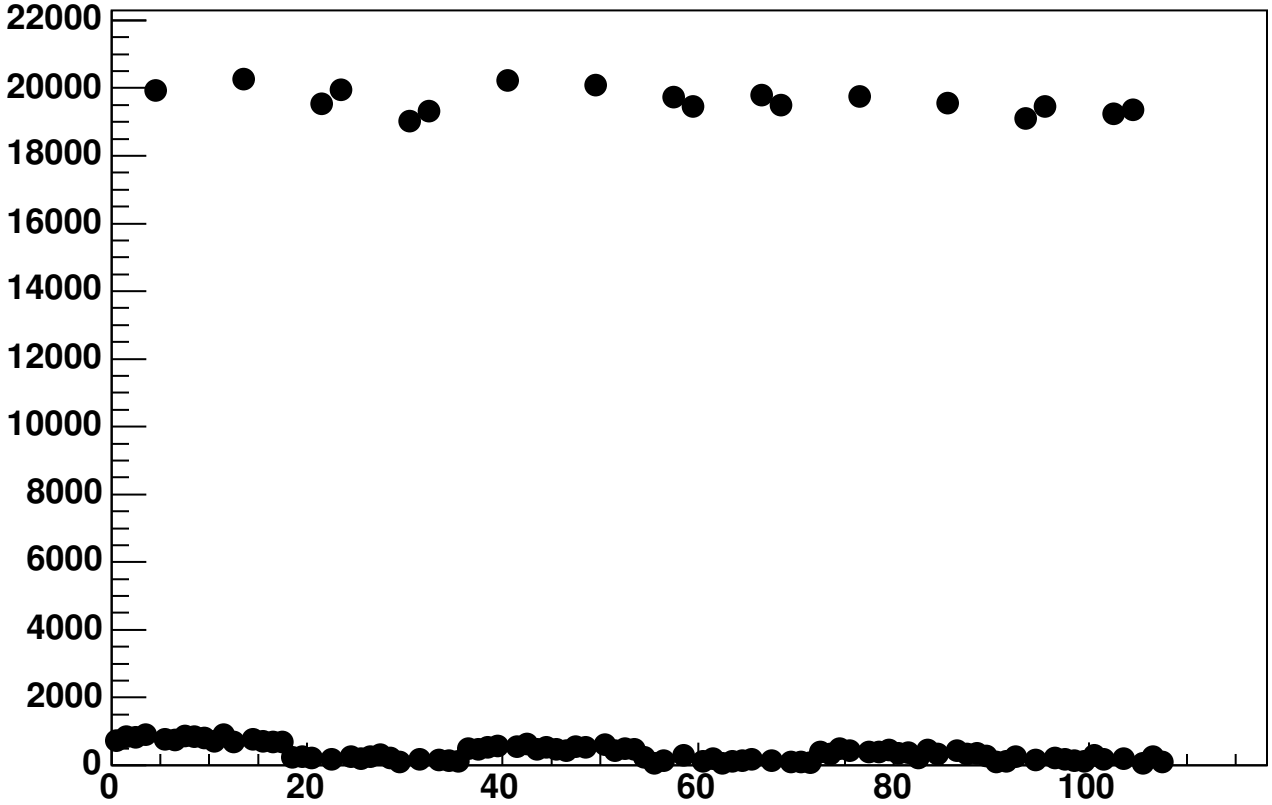
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



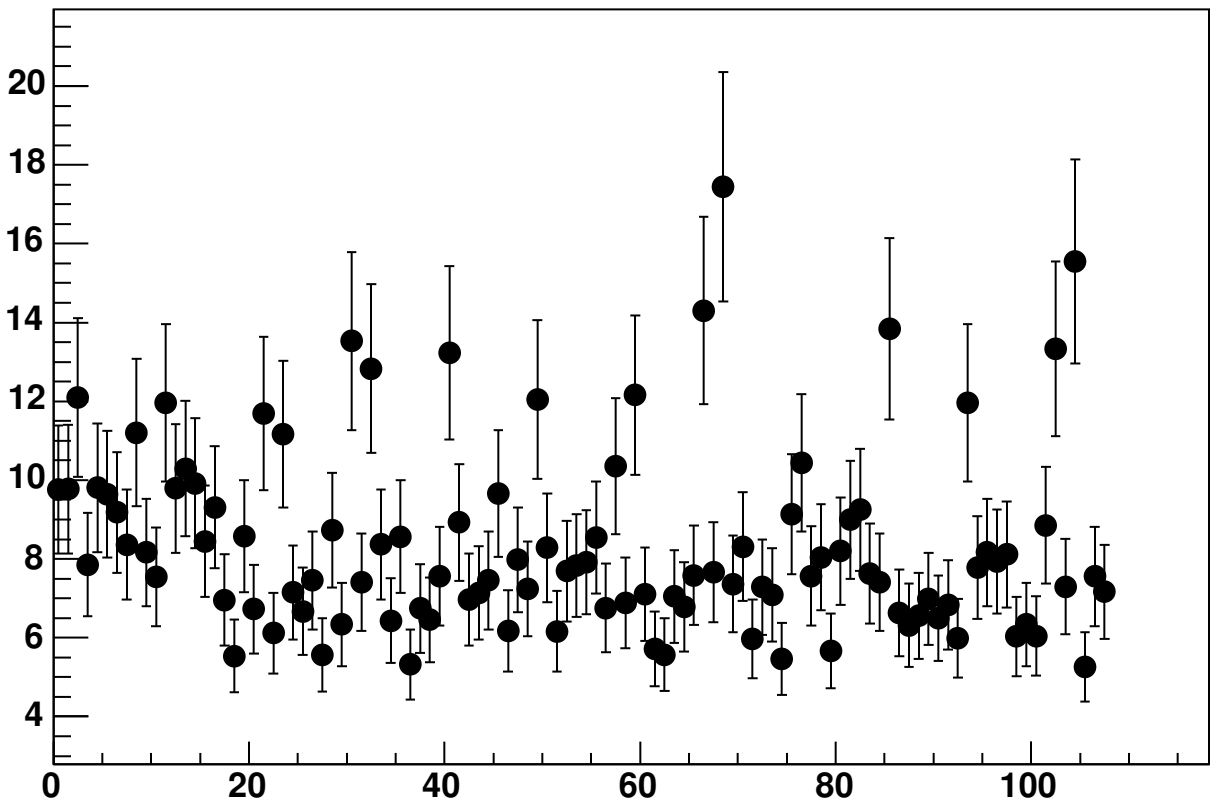
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



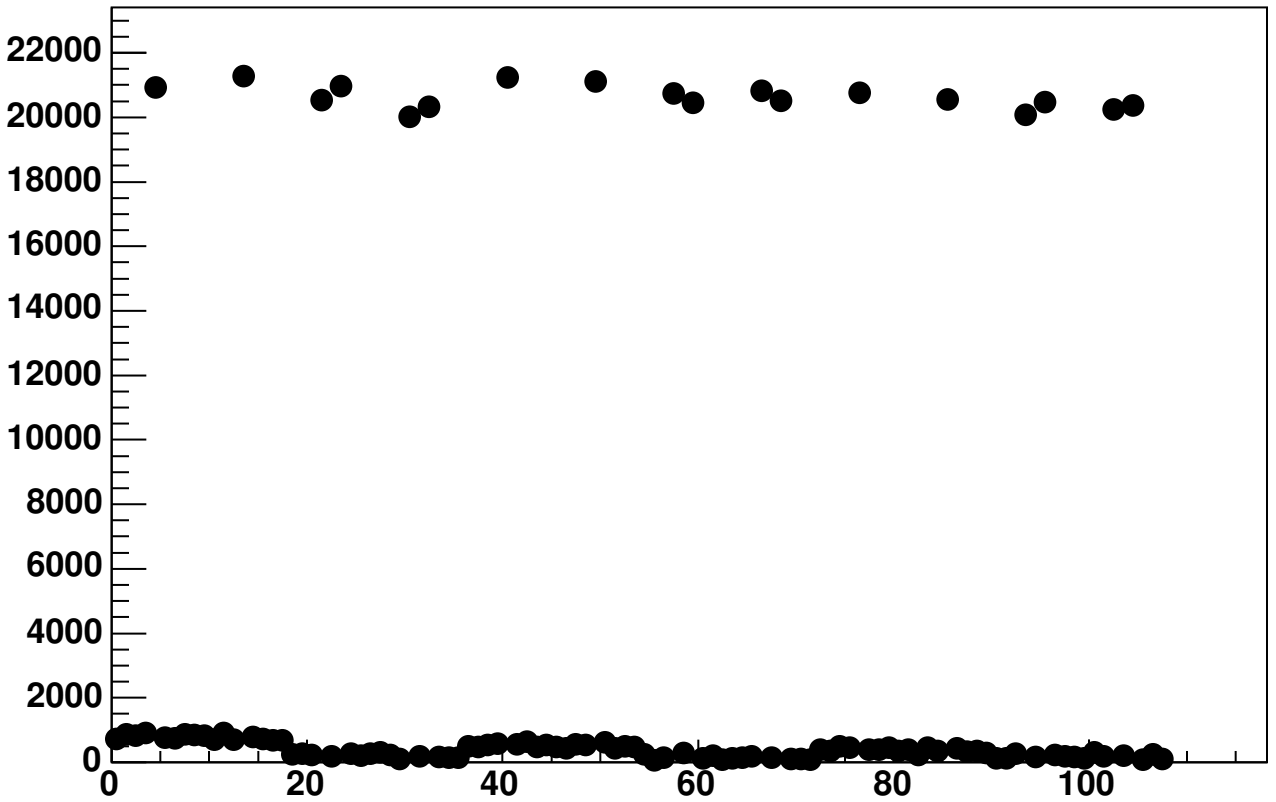
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



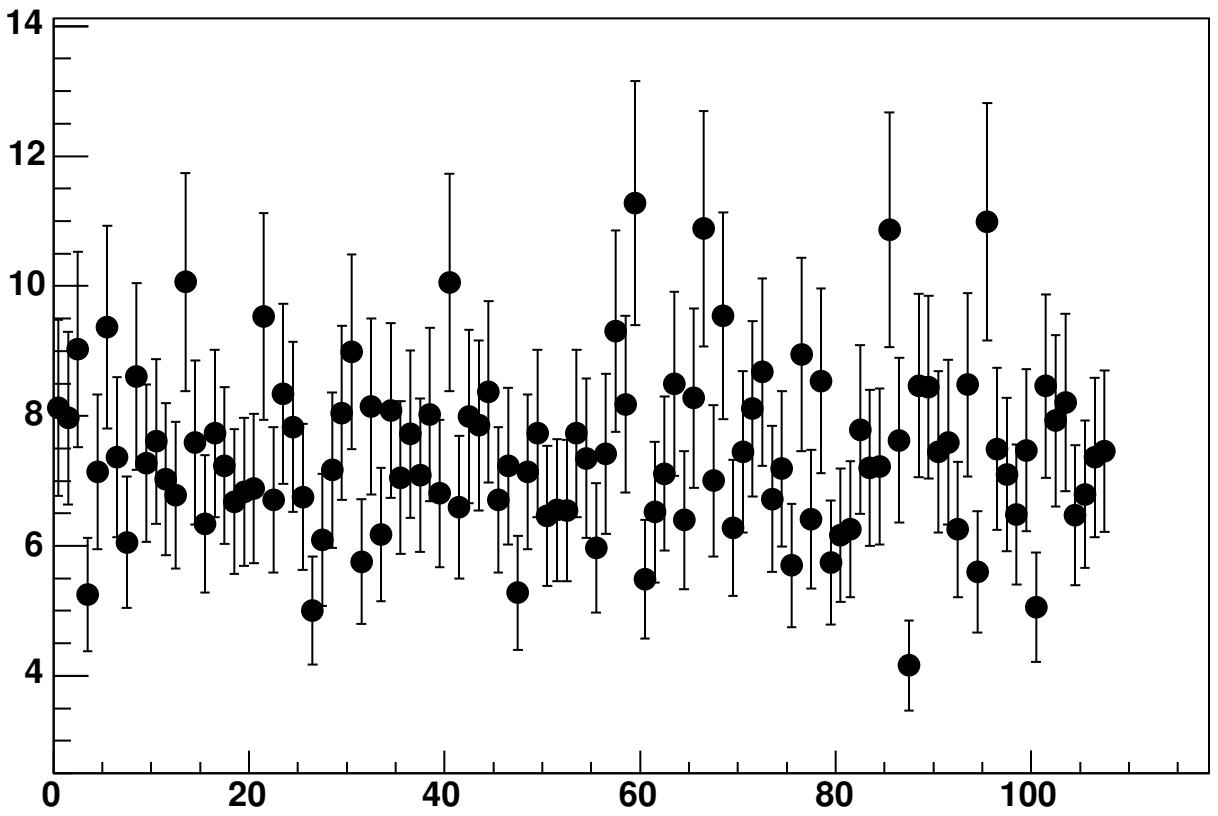
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



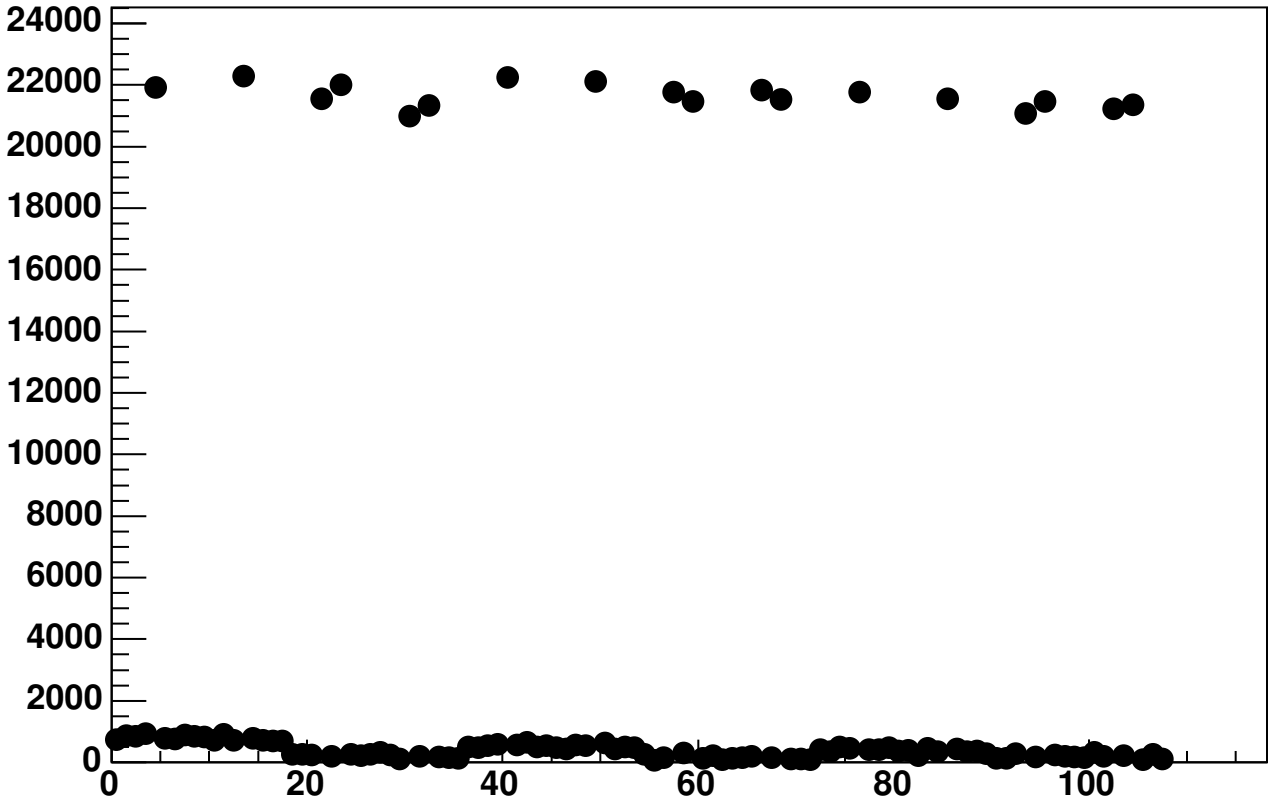
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



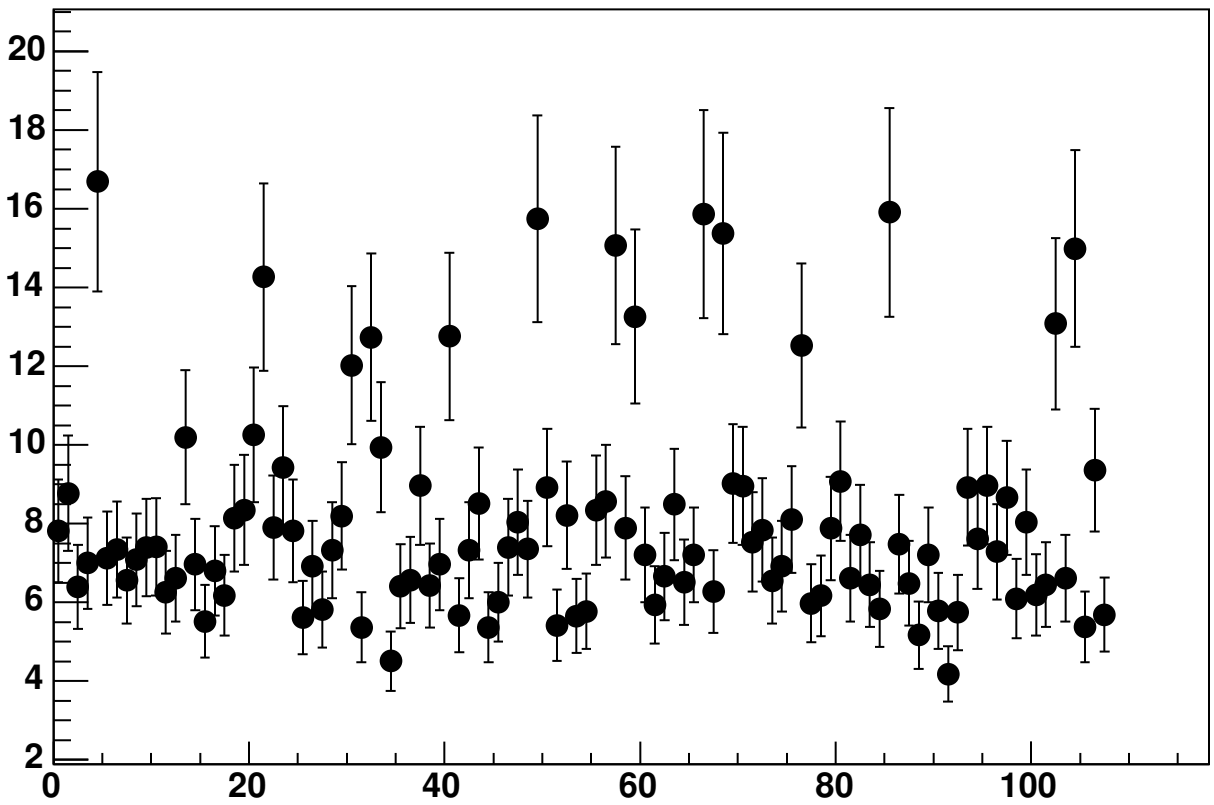
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



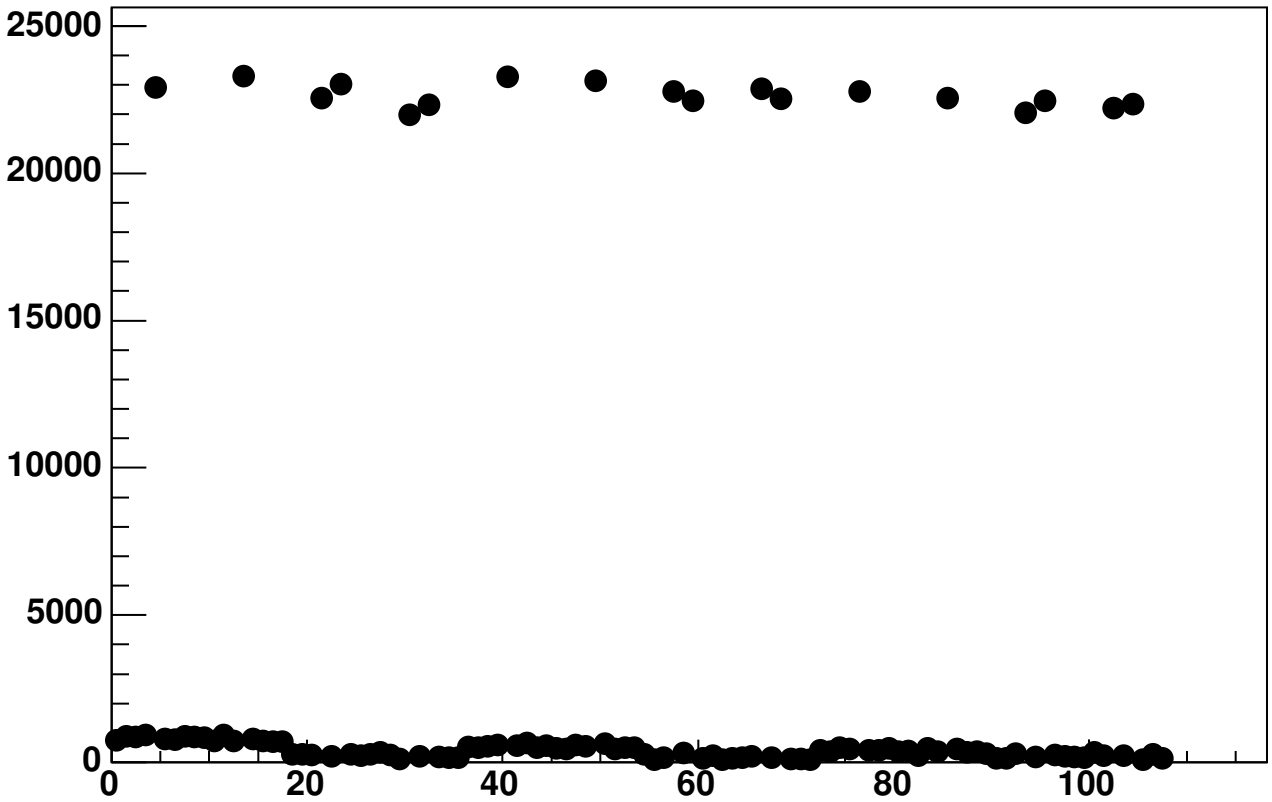
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



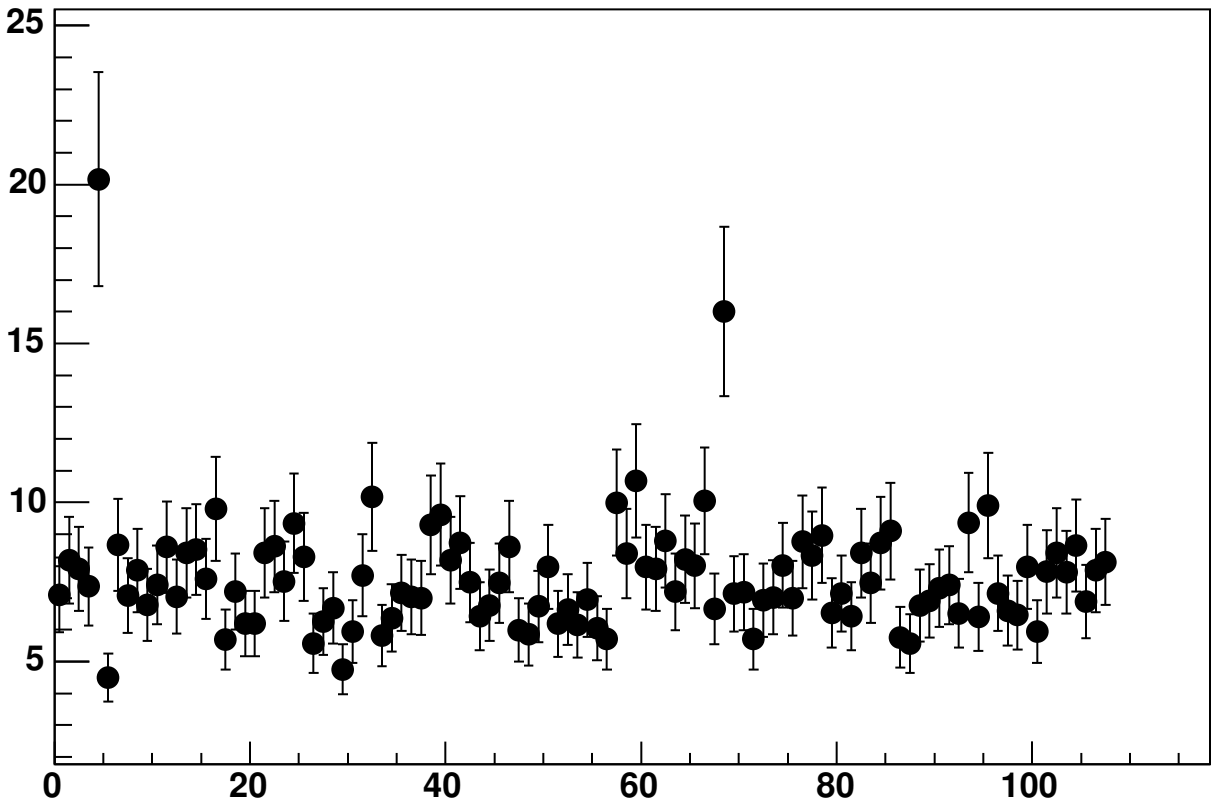
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



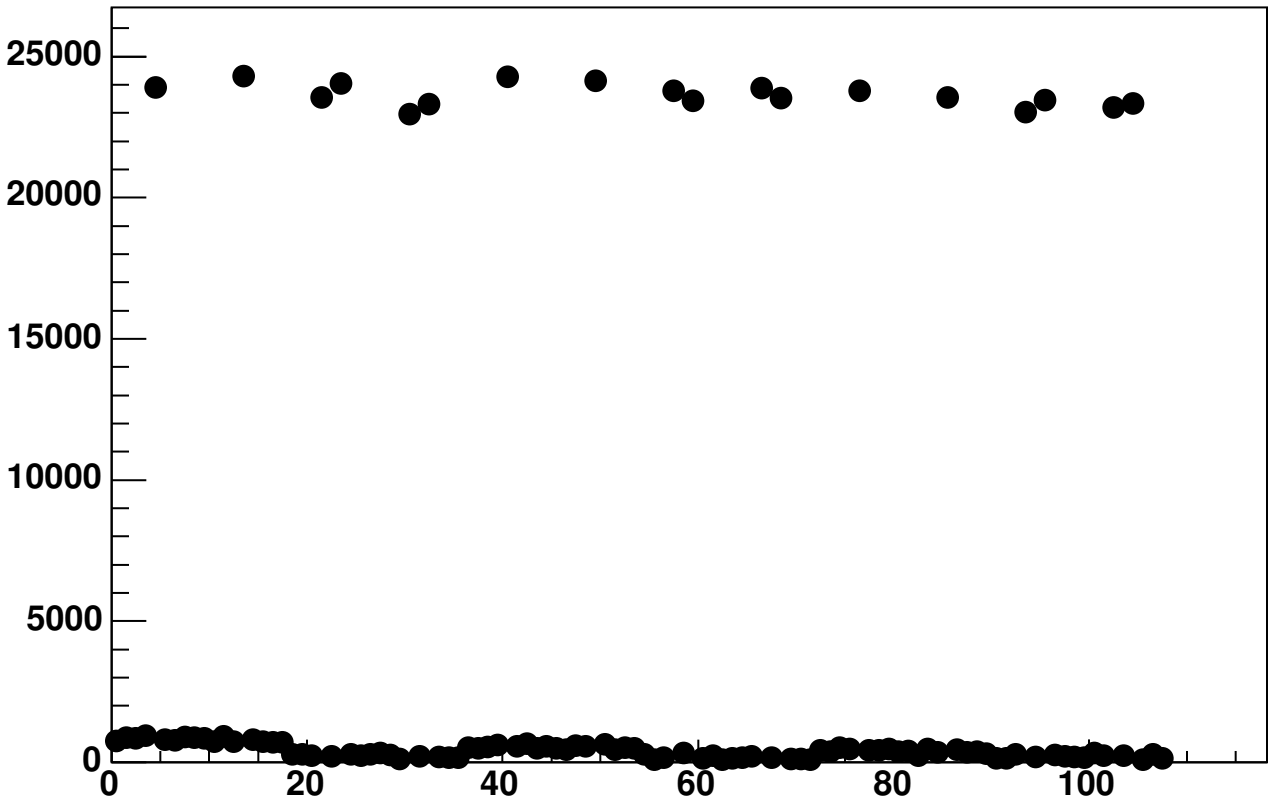
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



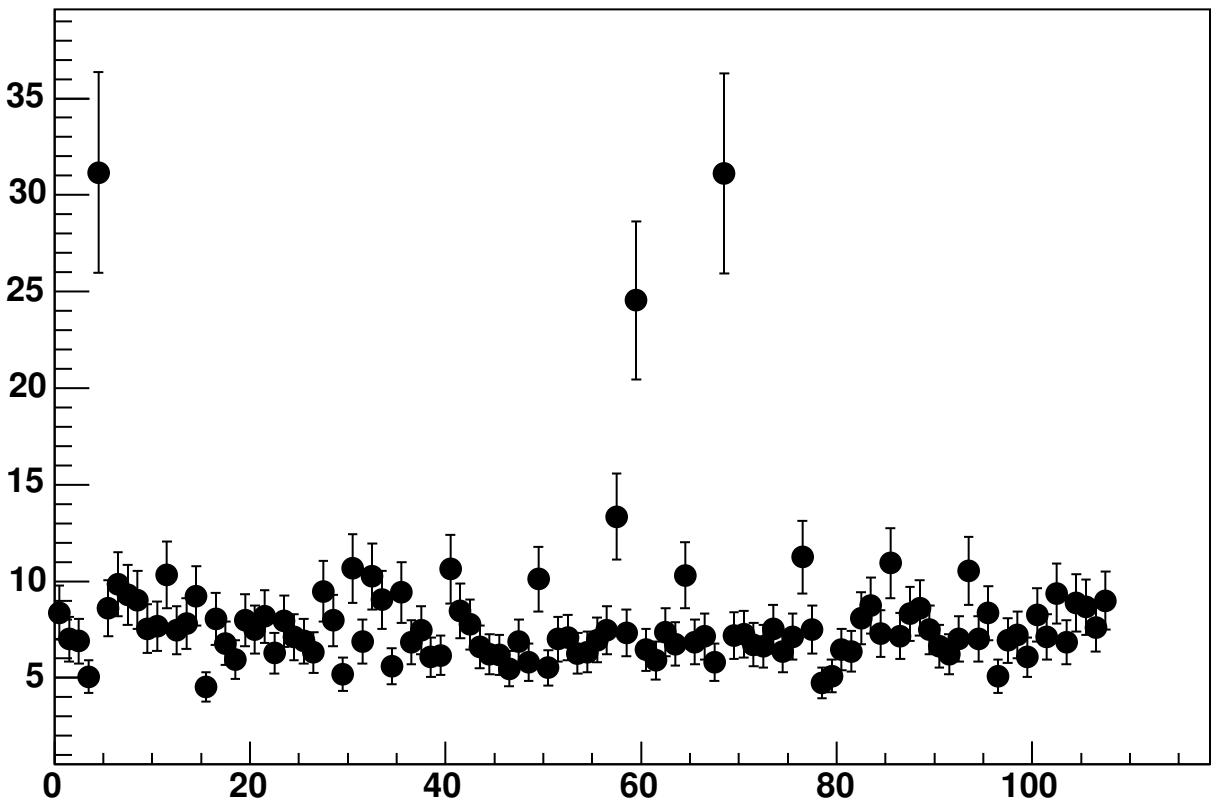
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



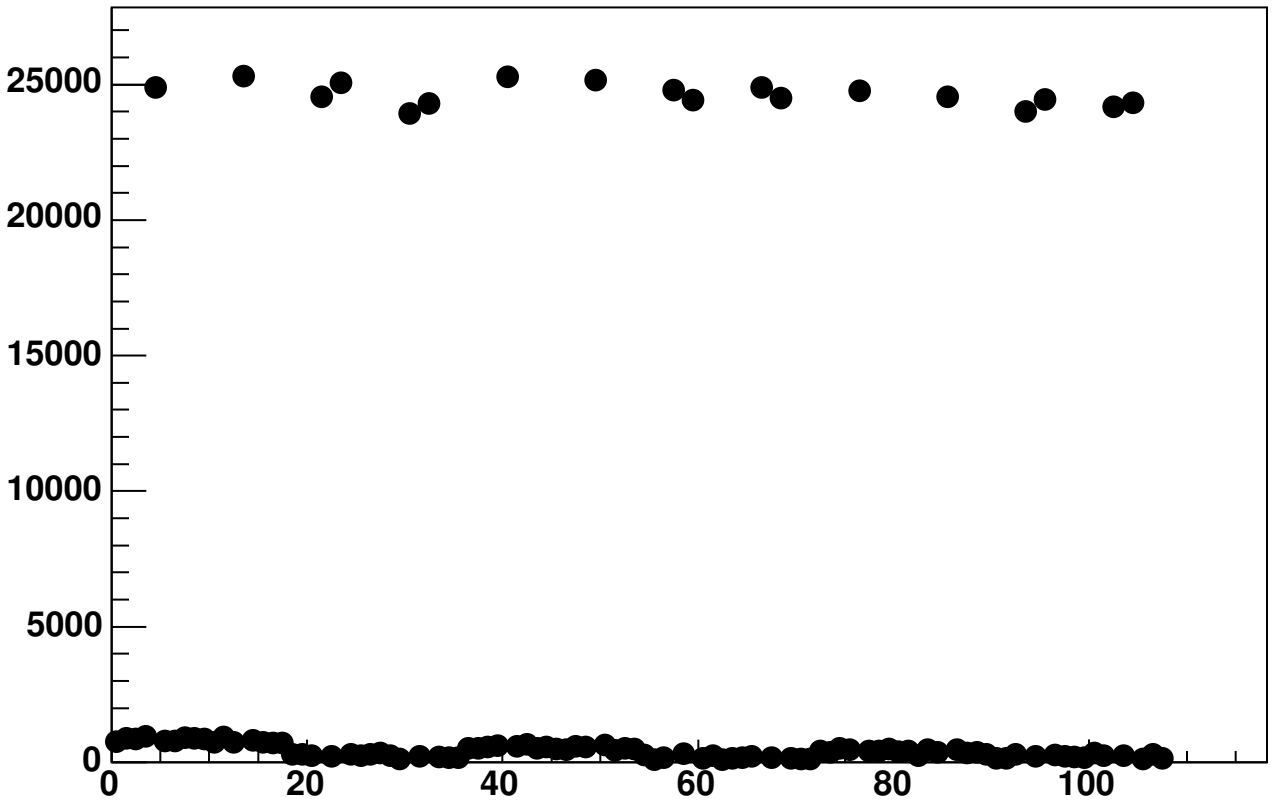
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



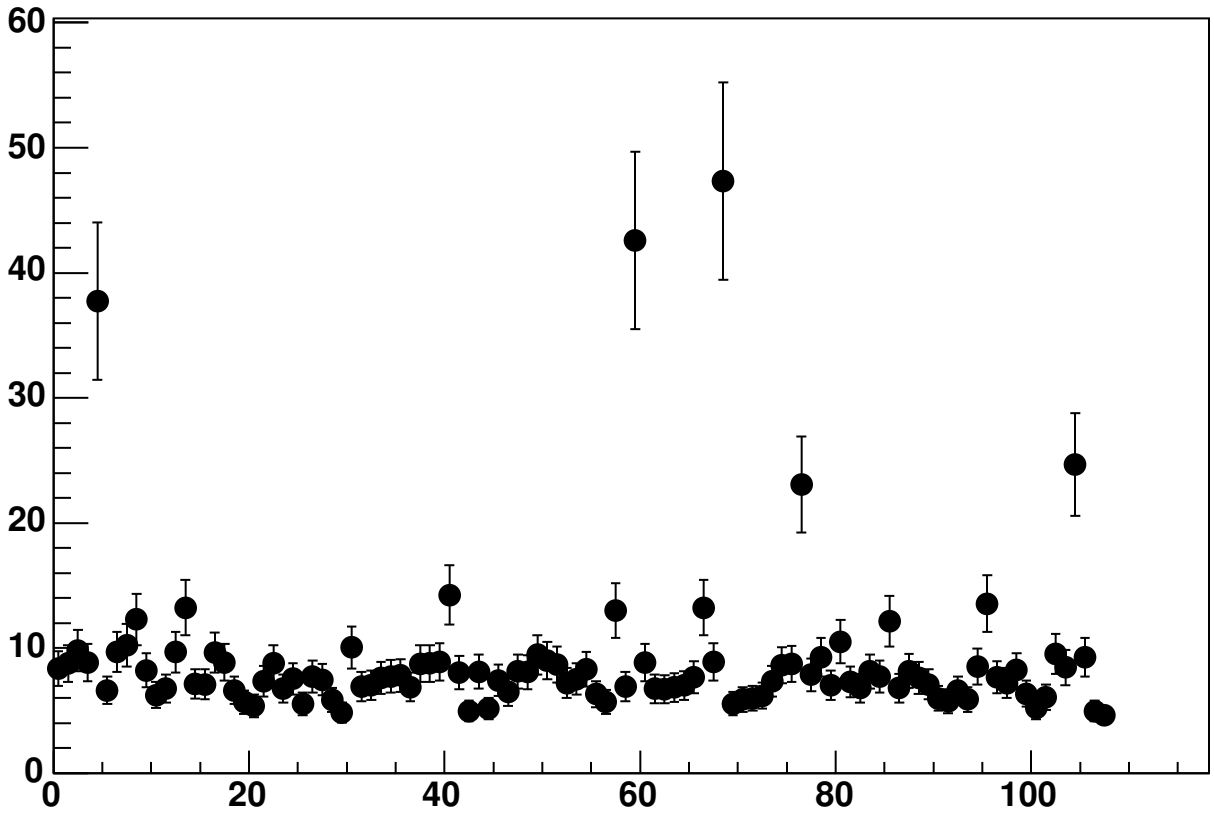
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



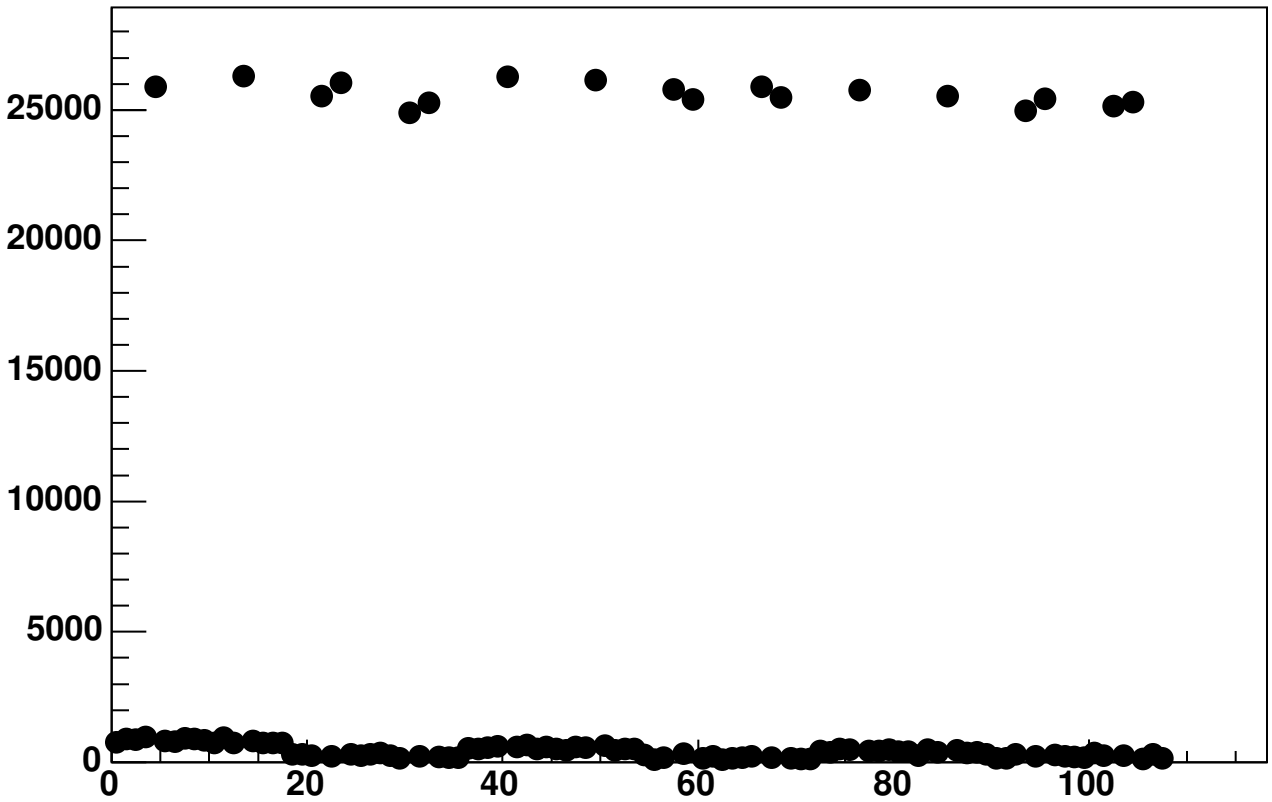
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



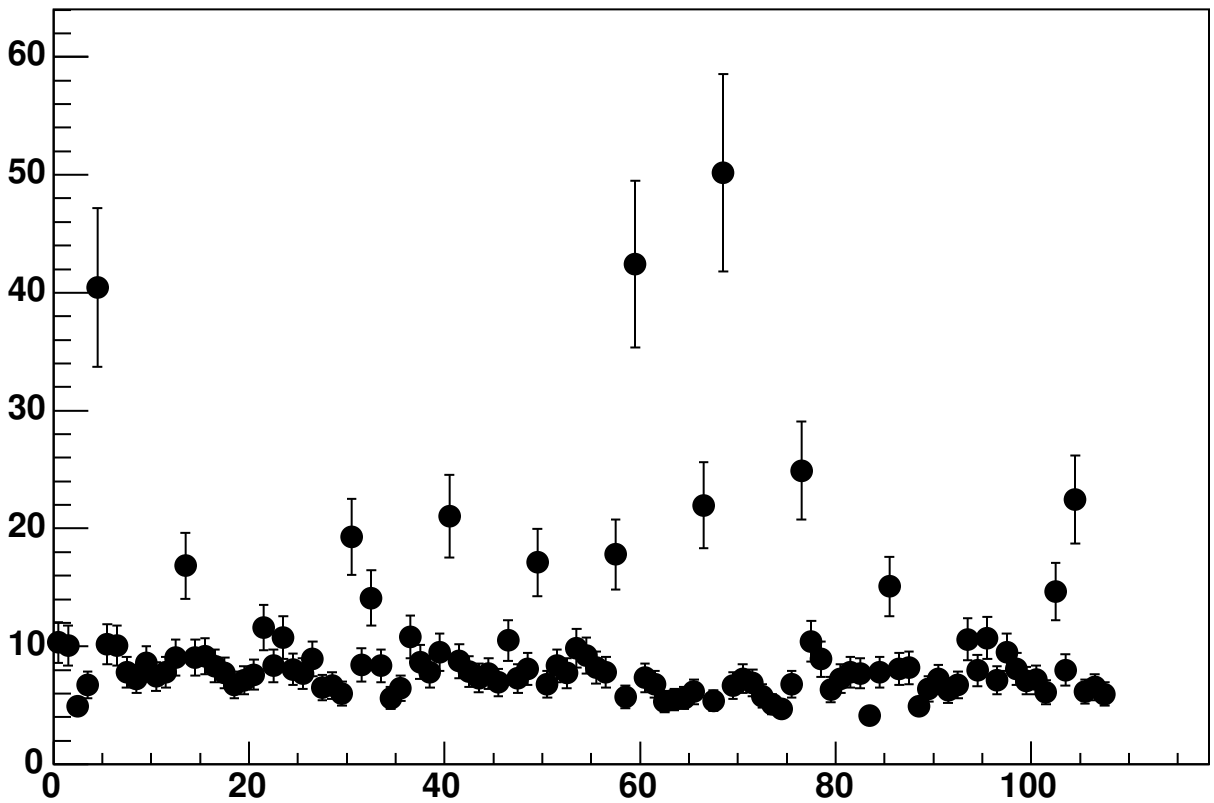
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



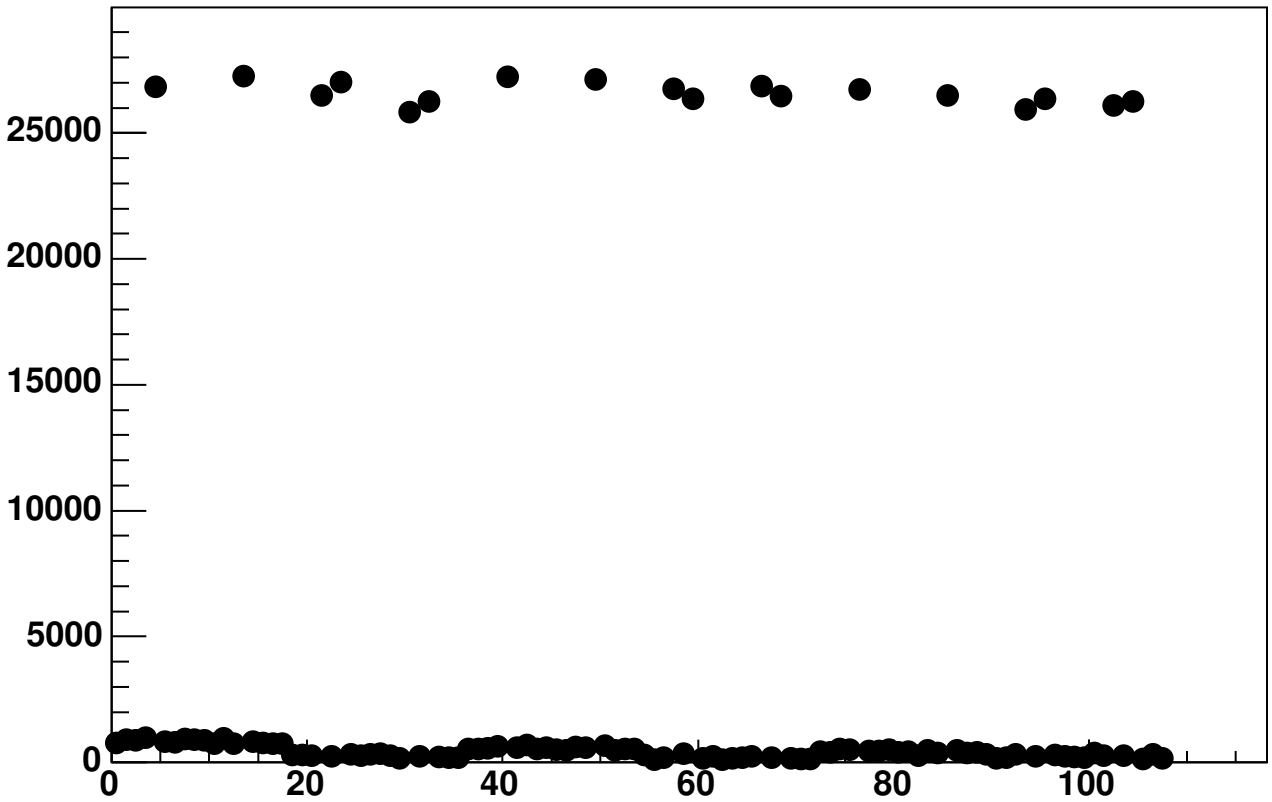
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



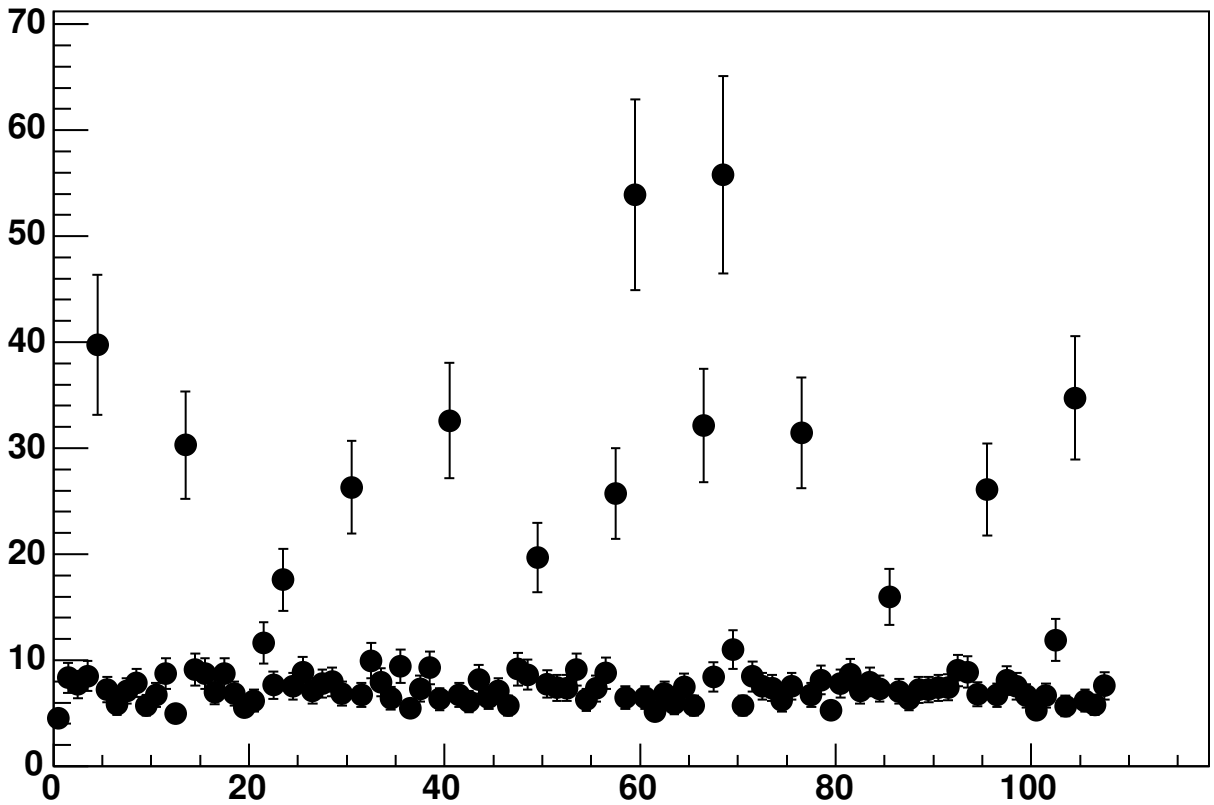
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



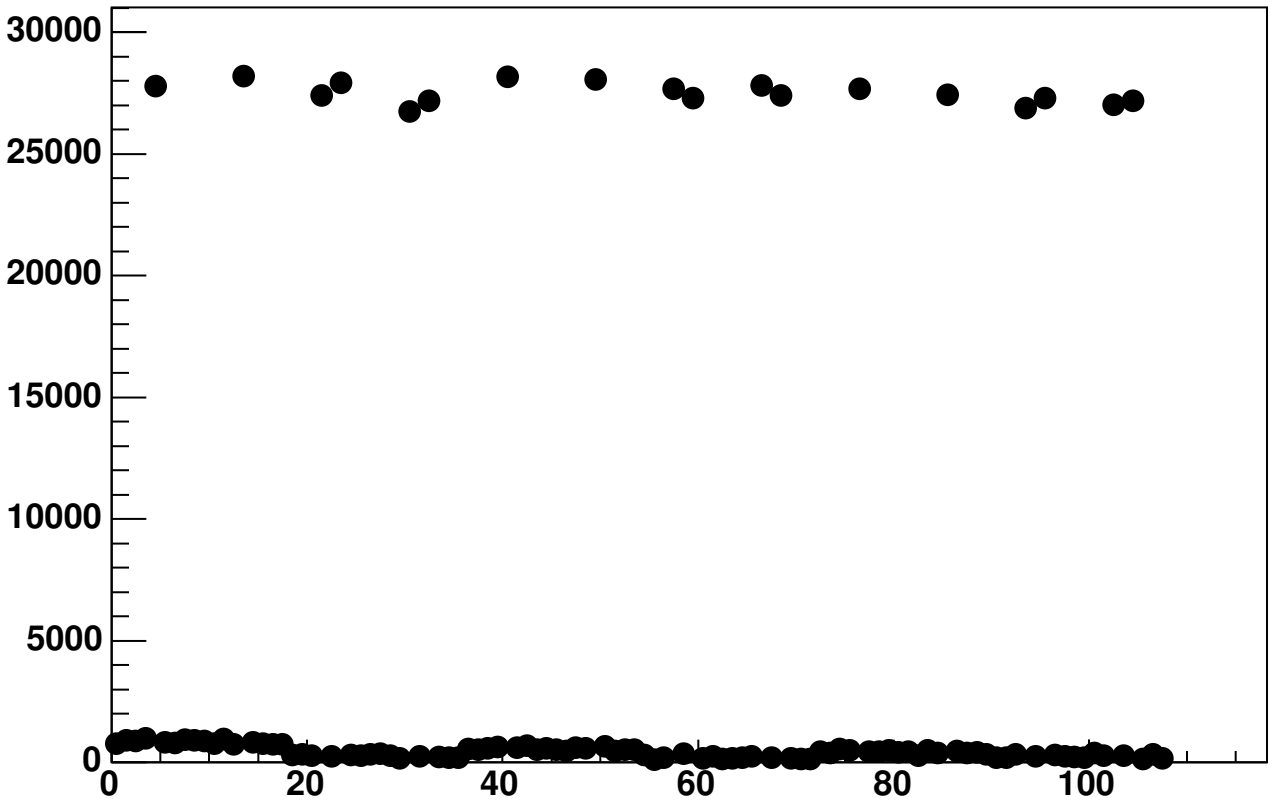
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



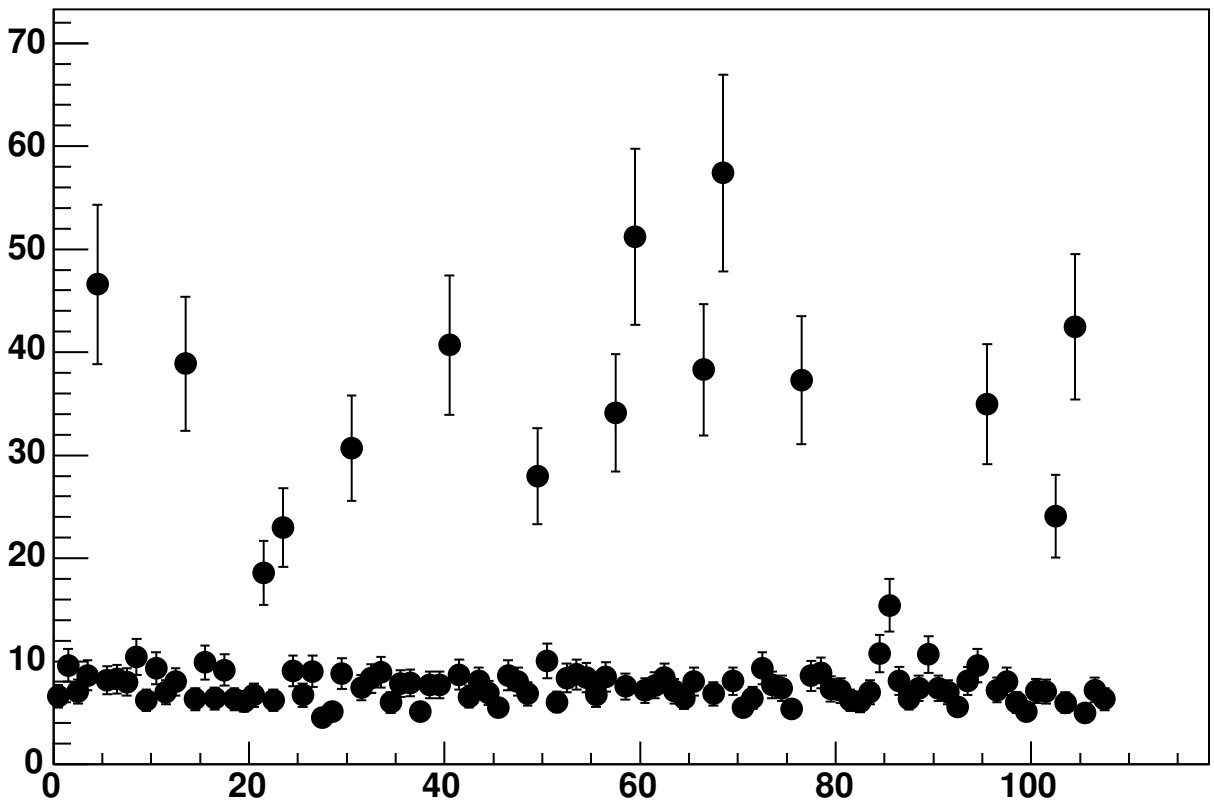
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



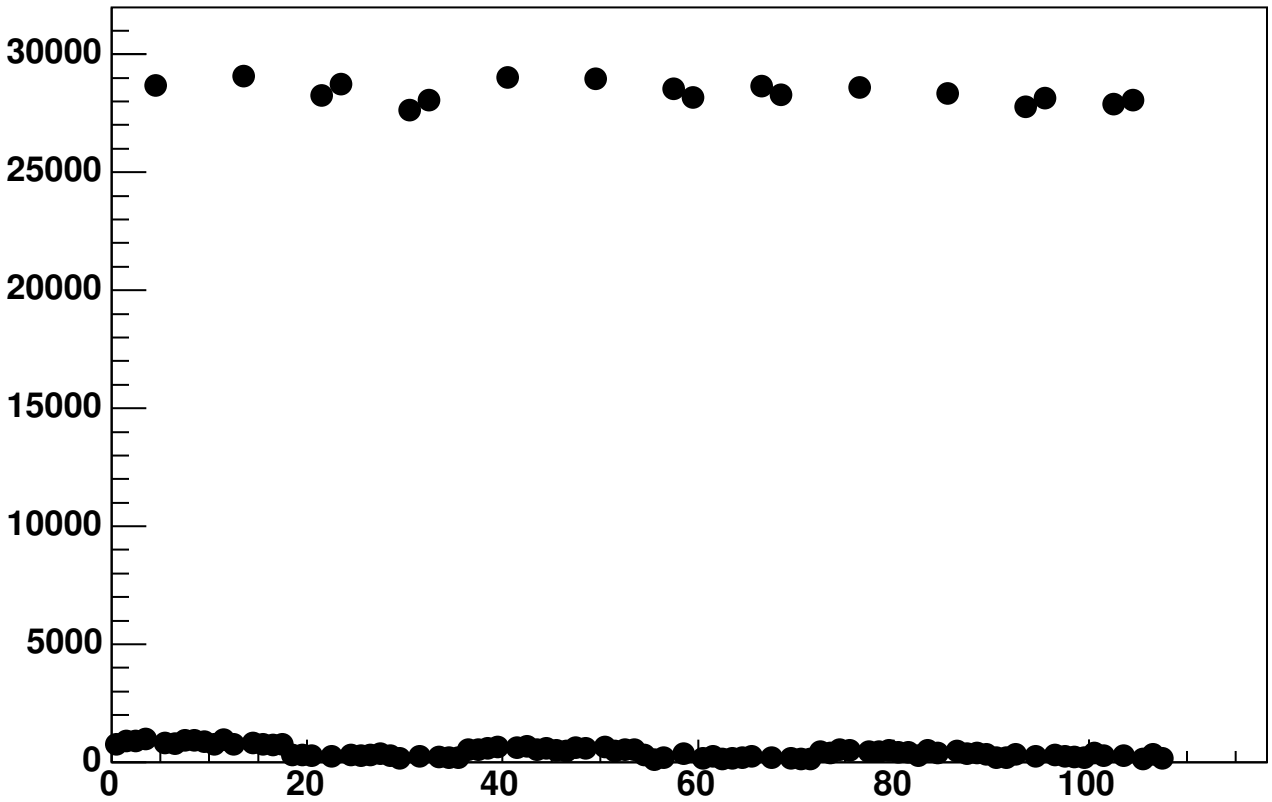
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



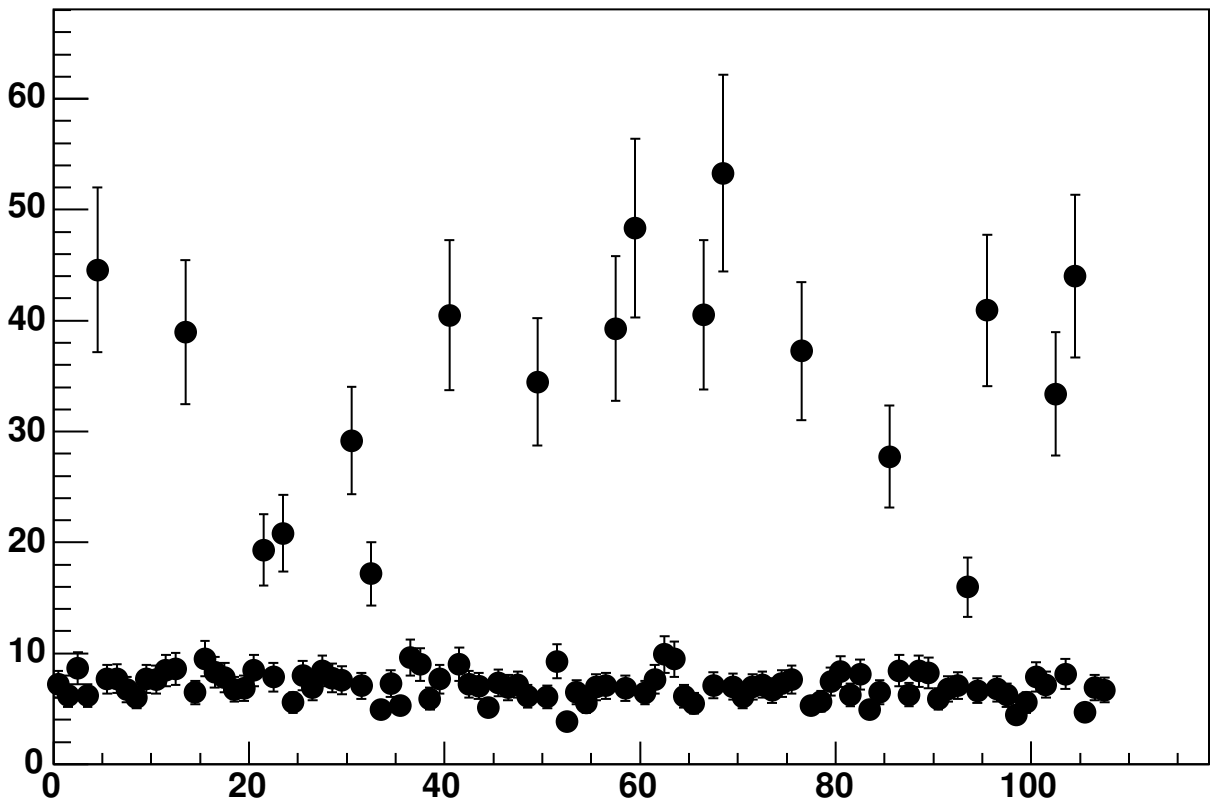
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



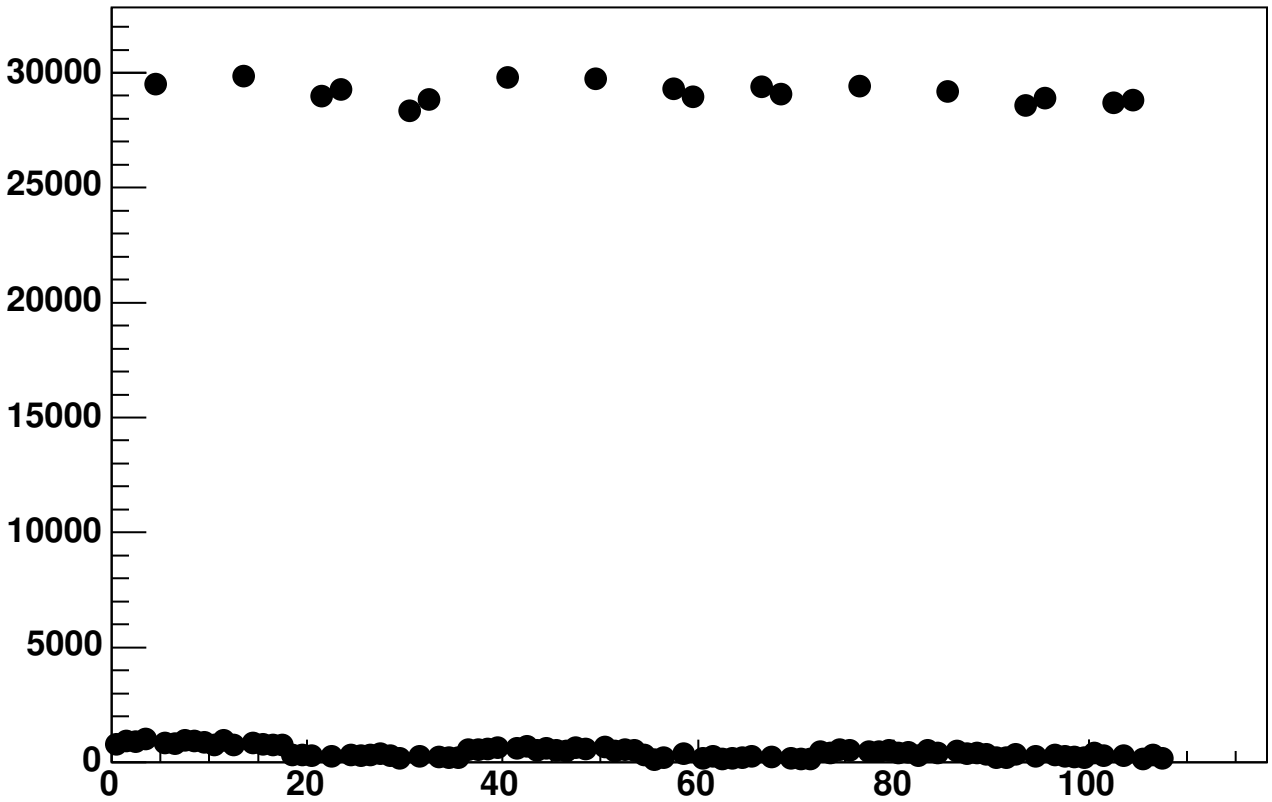
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



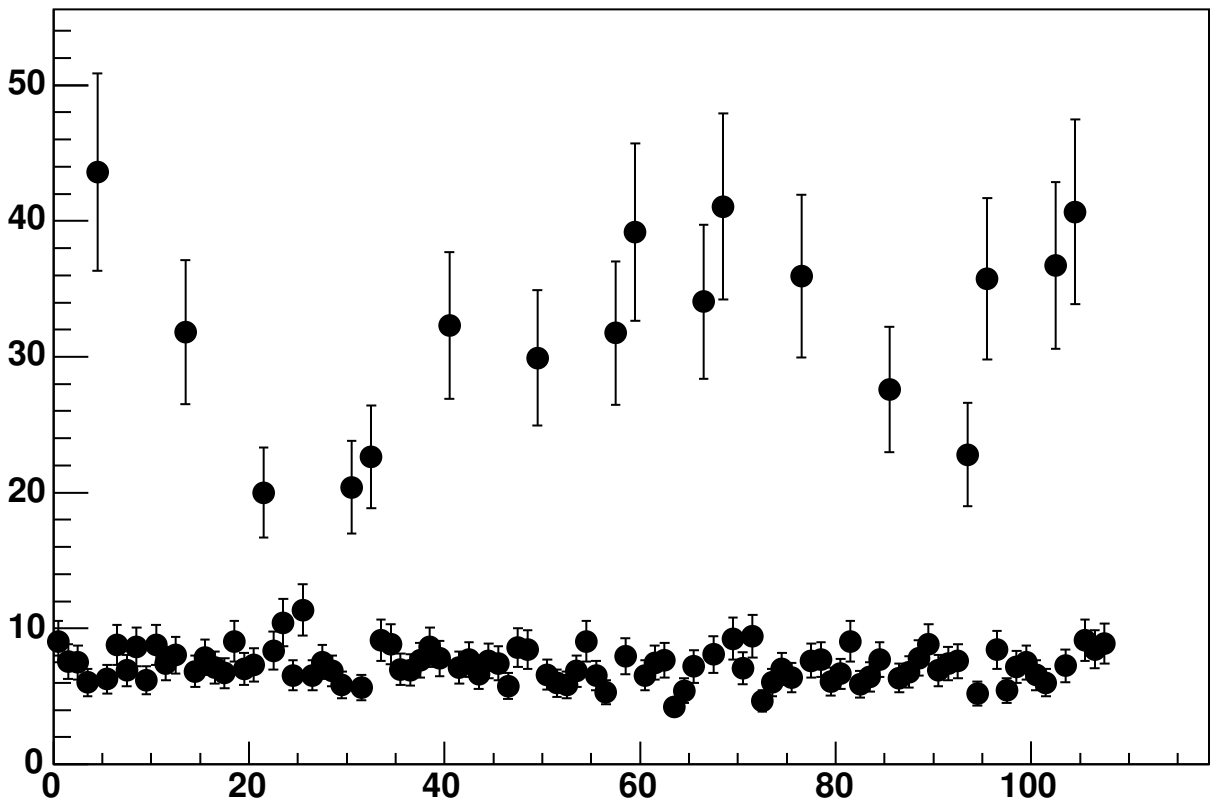
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



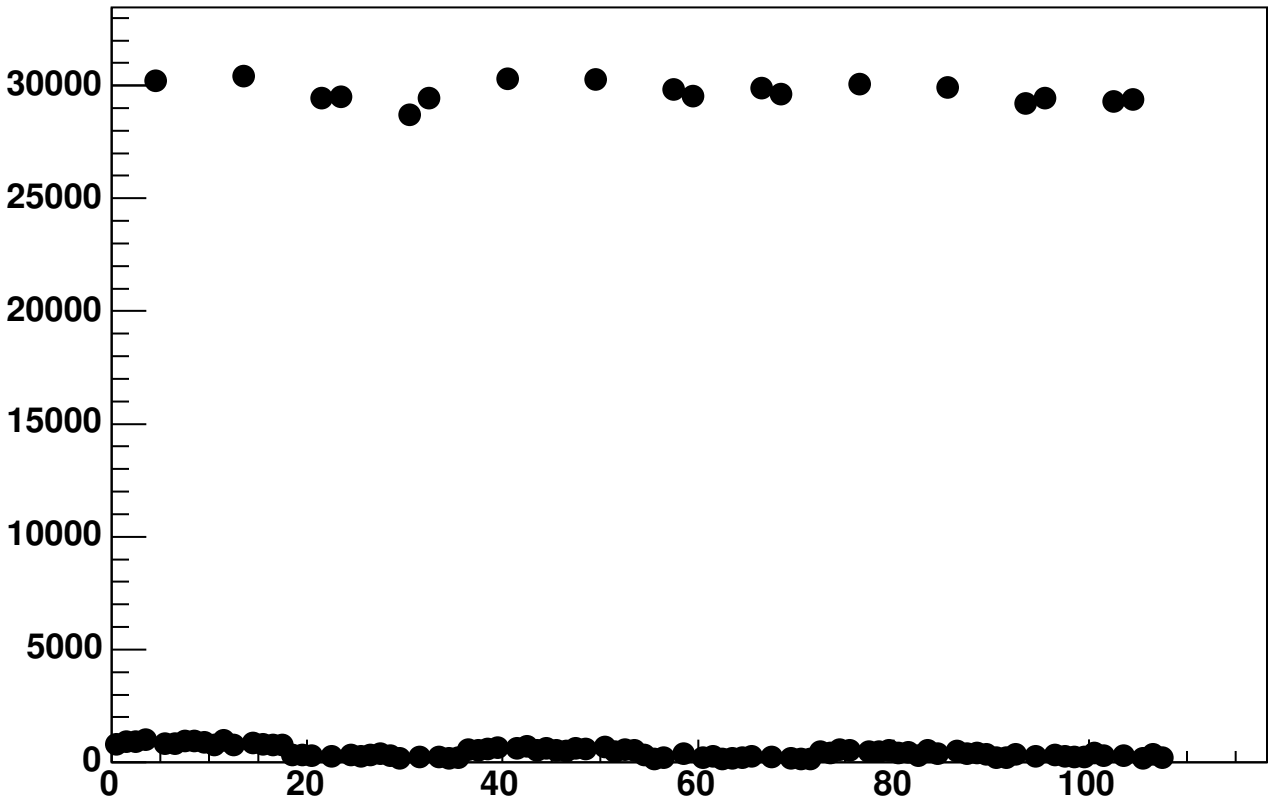
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



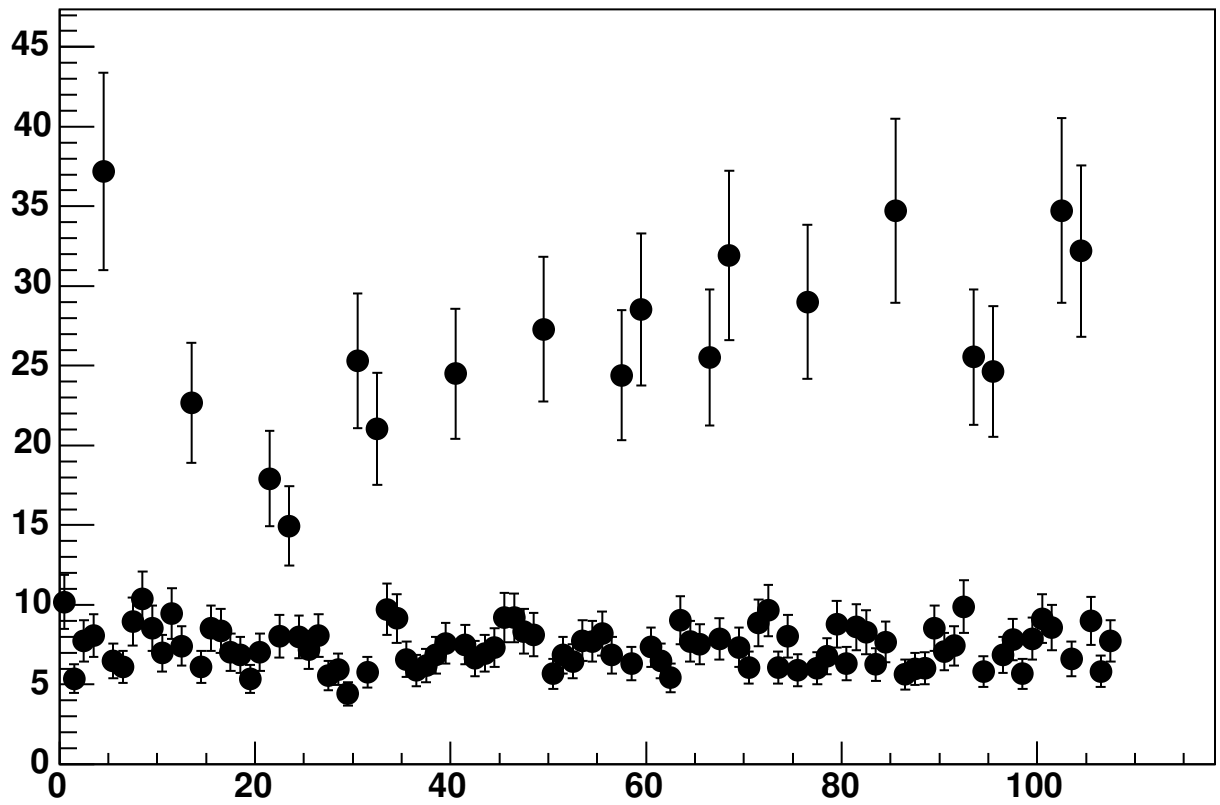
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



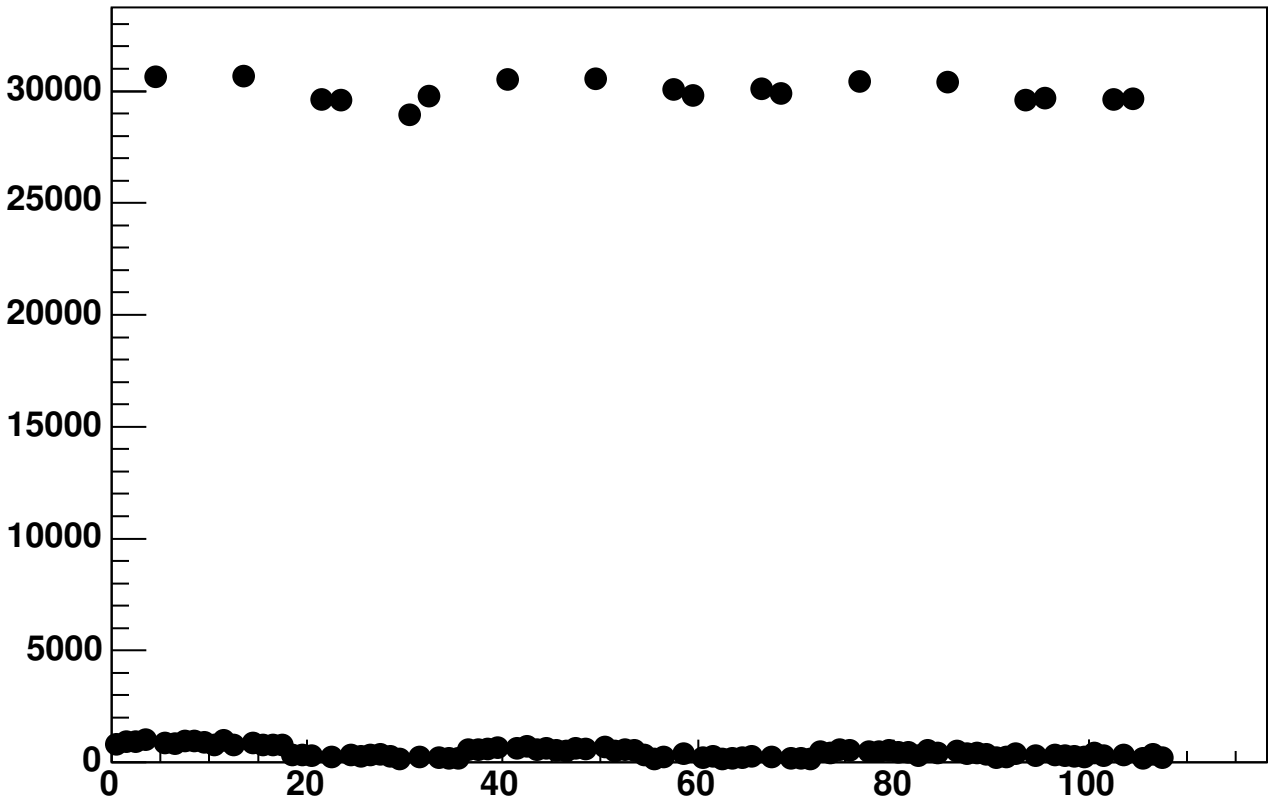
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



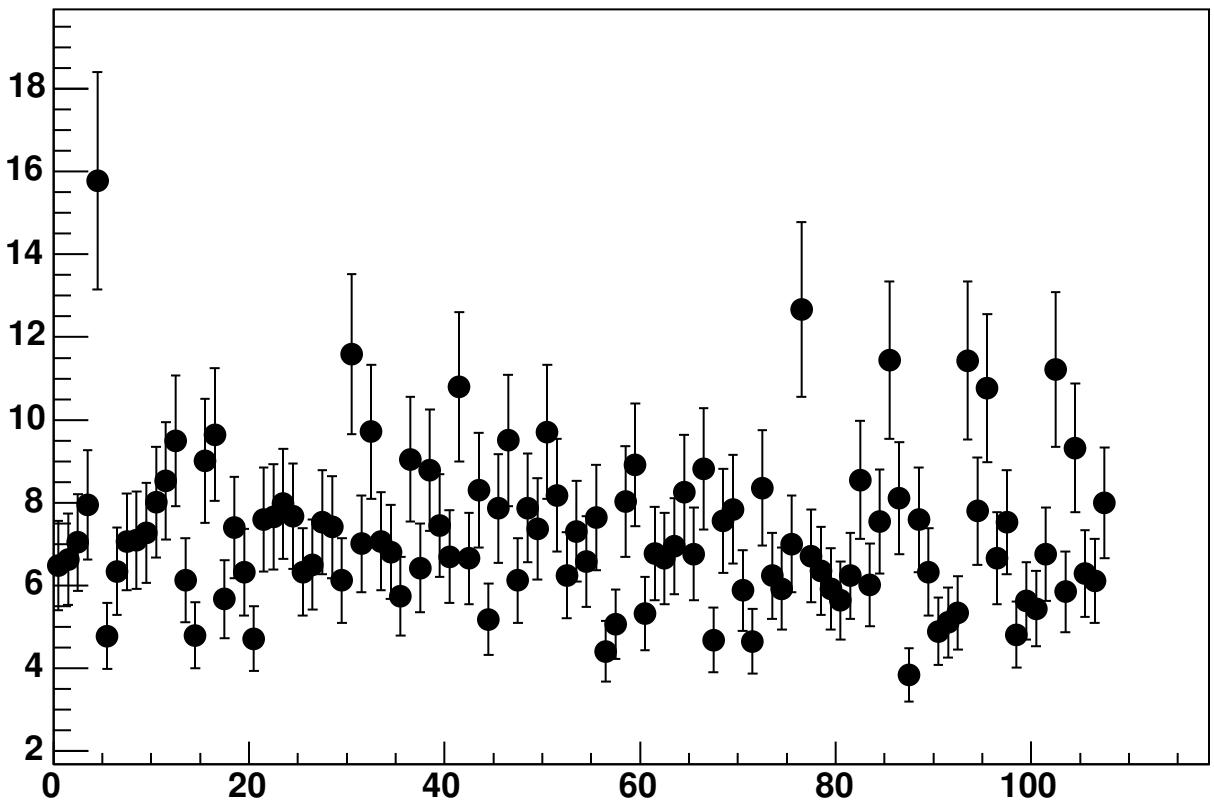
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



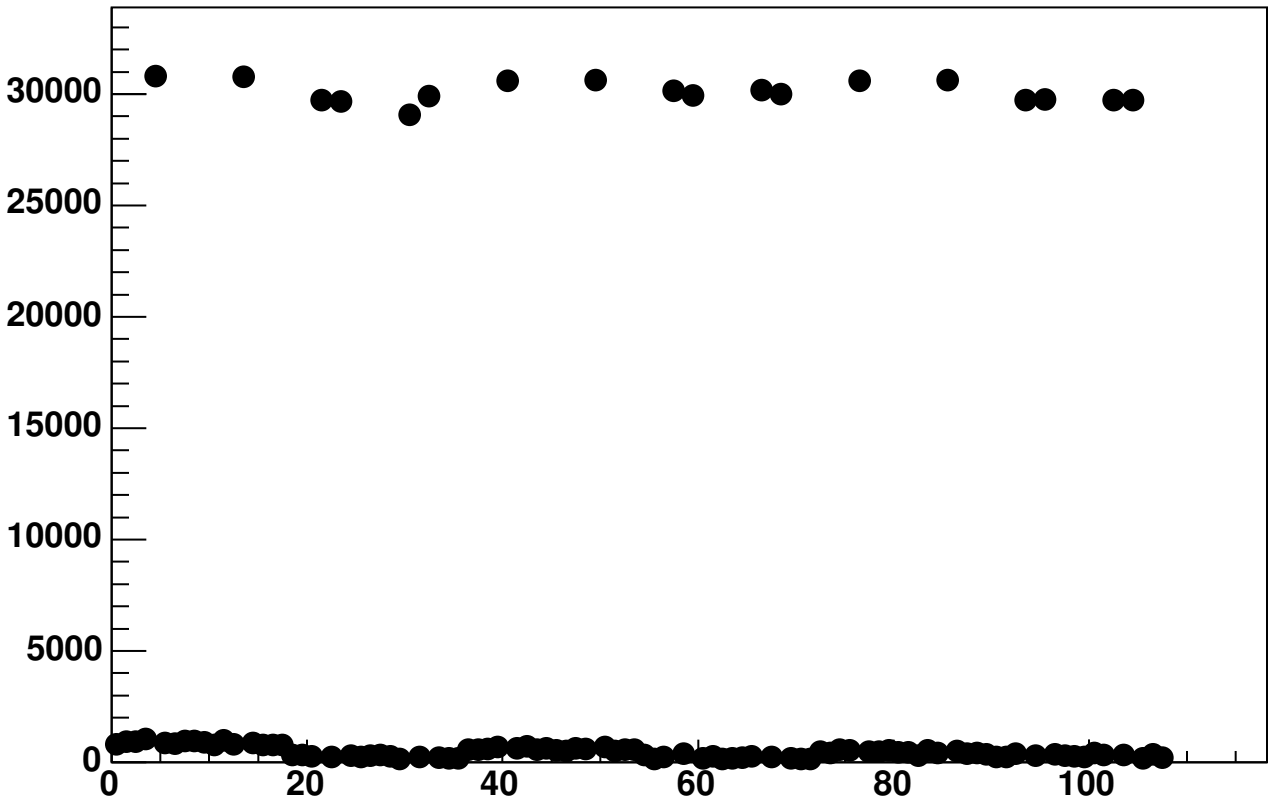
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



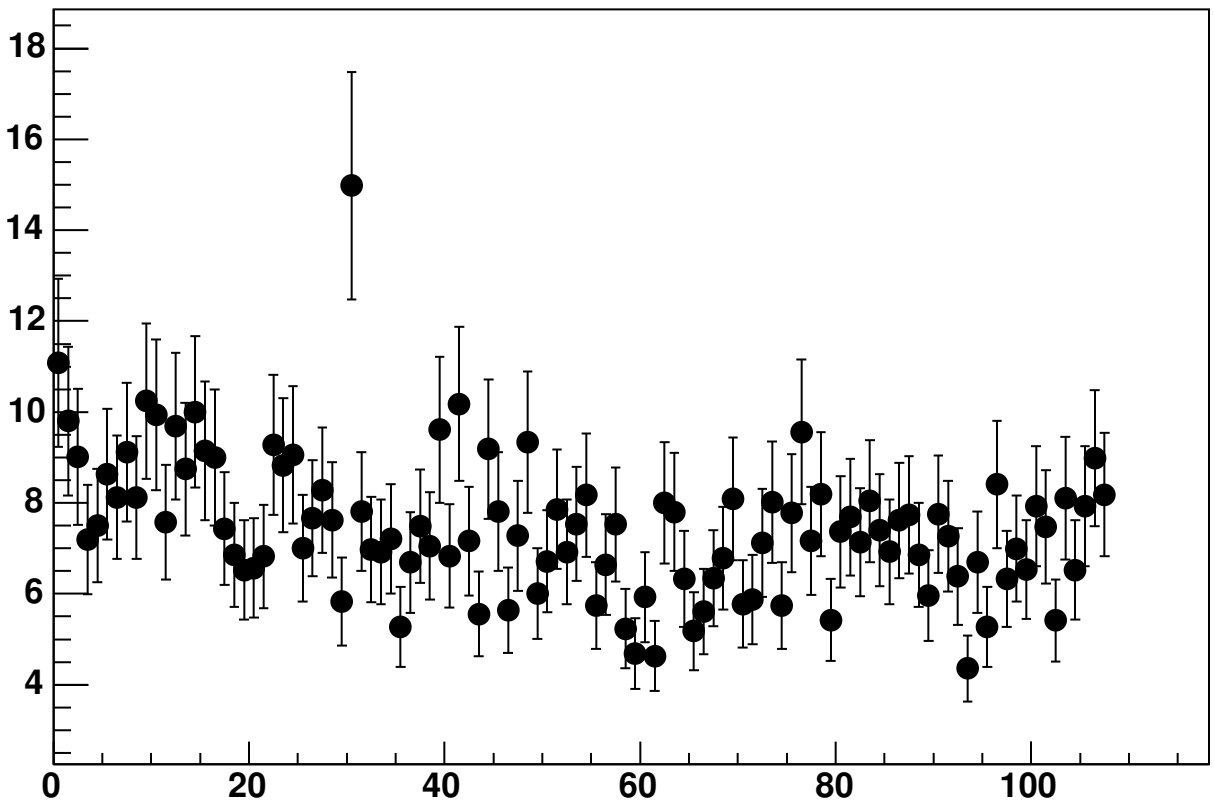
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



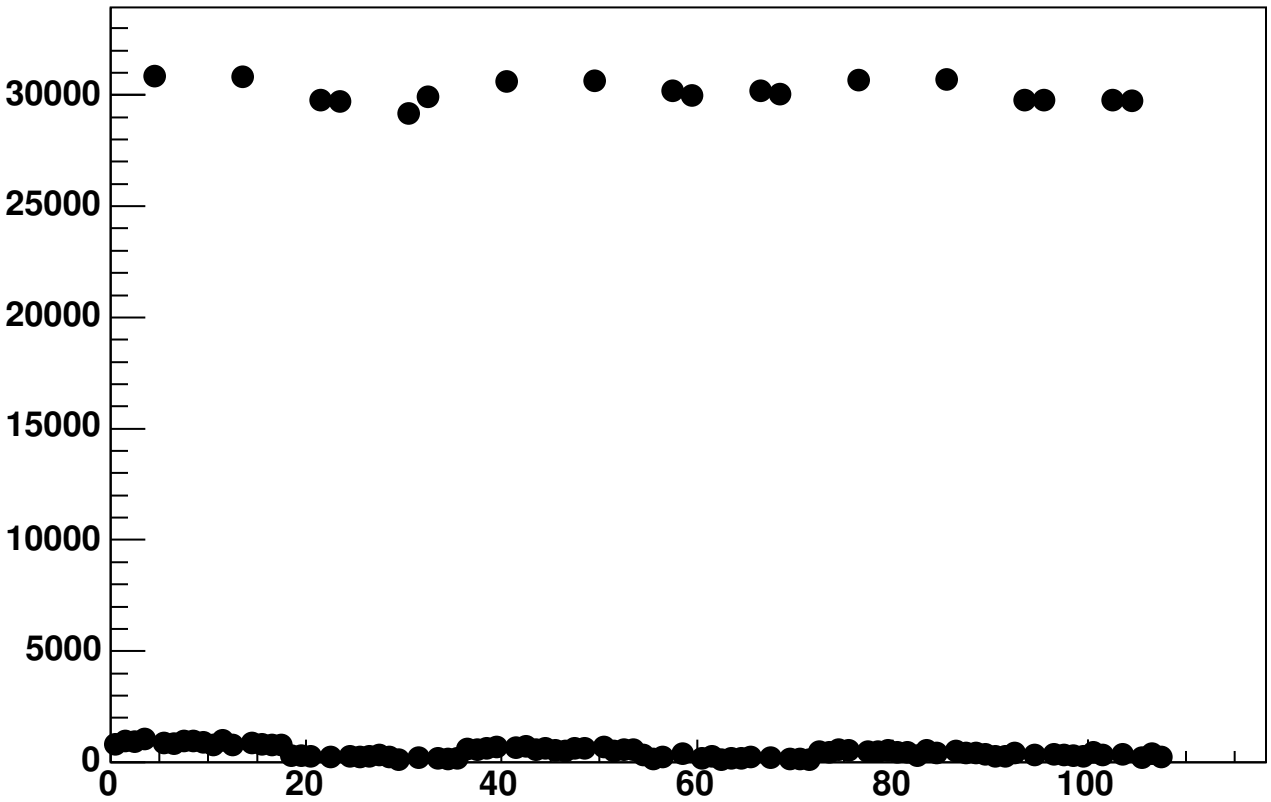
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



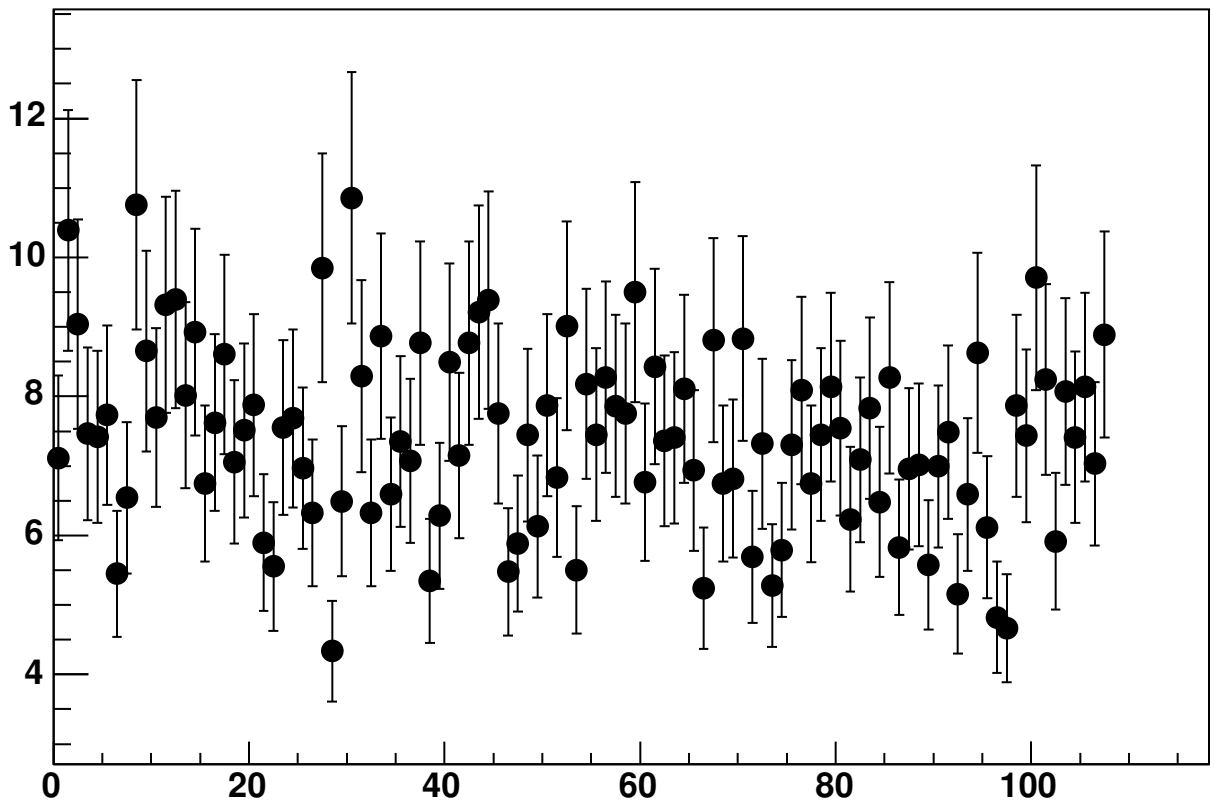
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



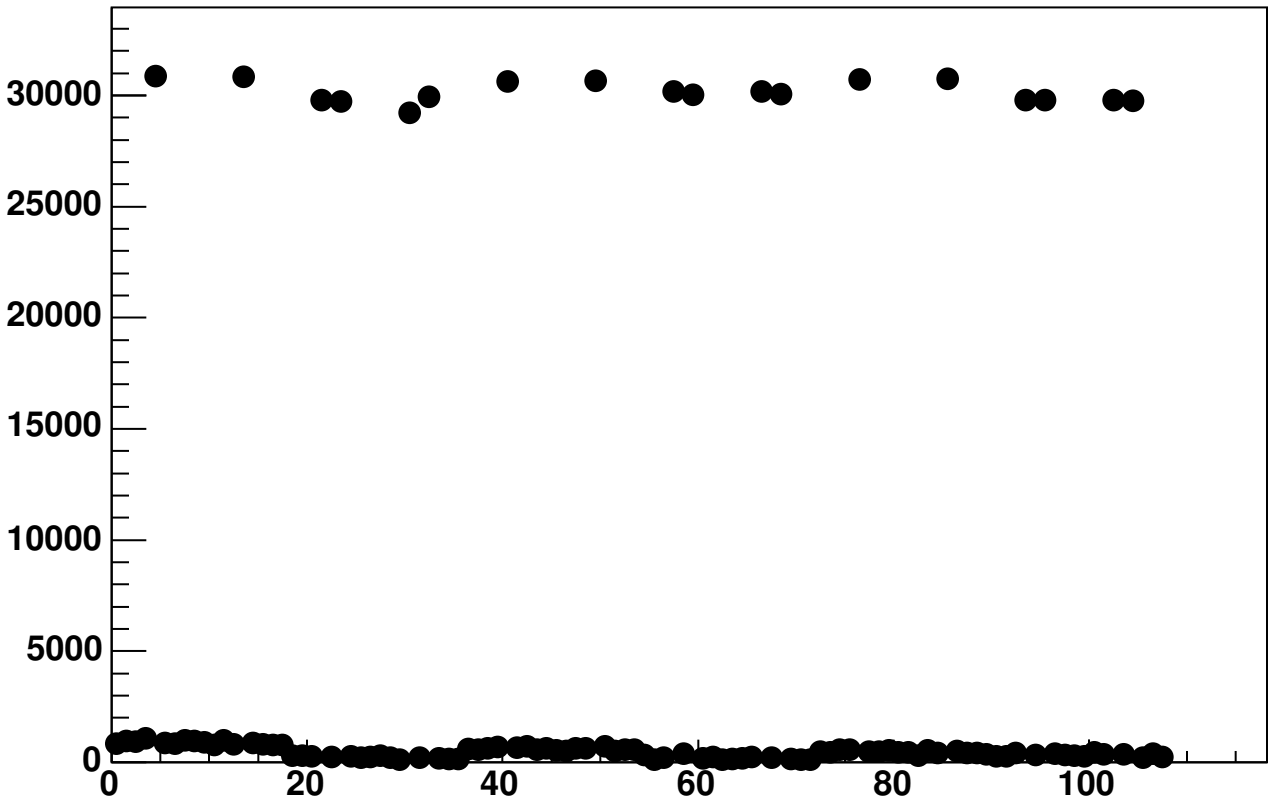
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



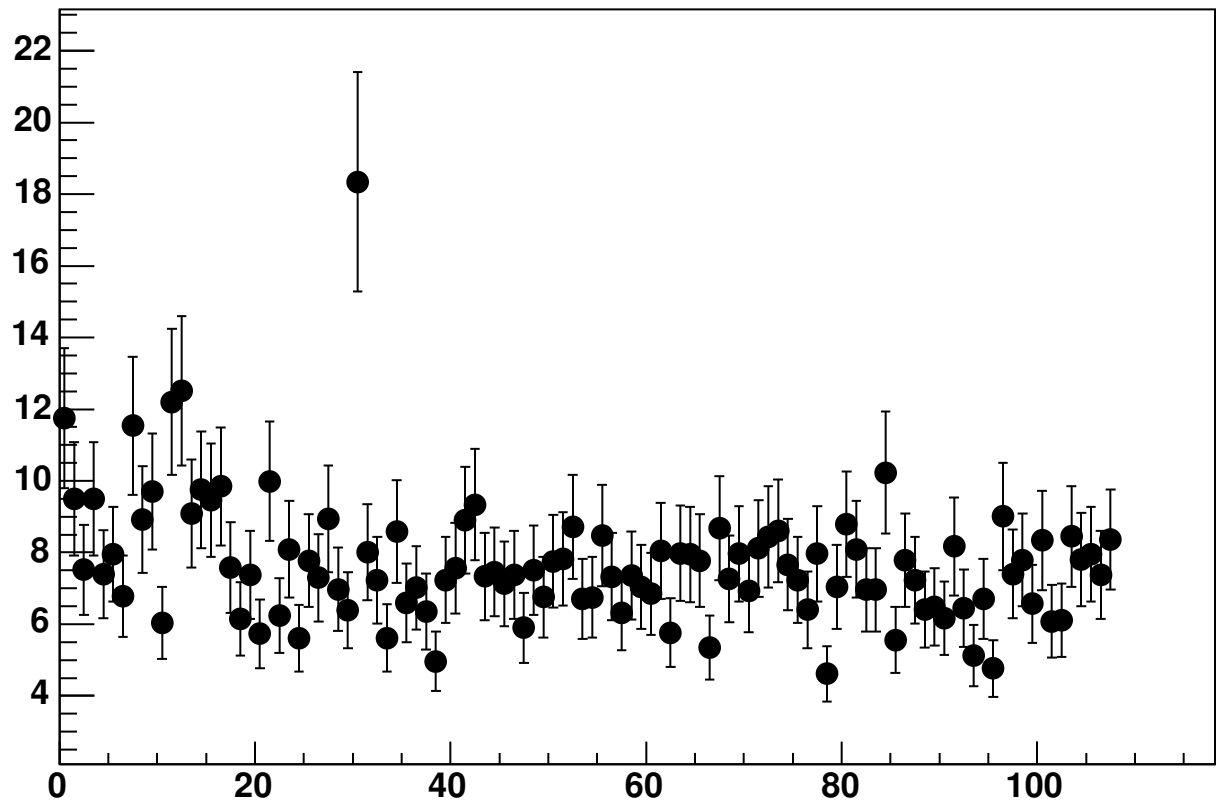
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan

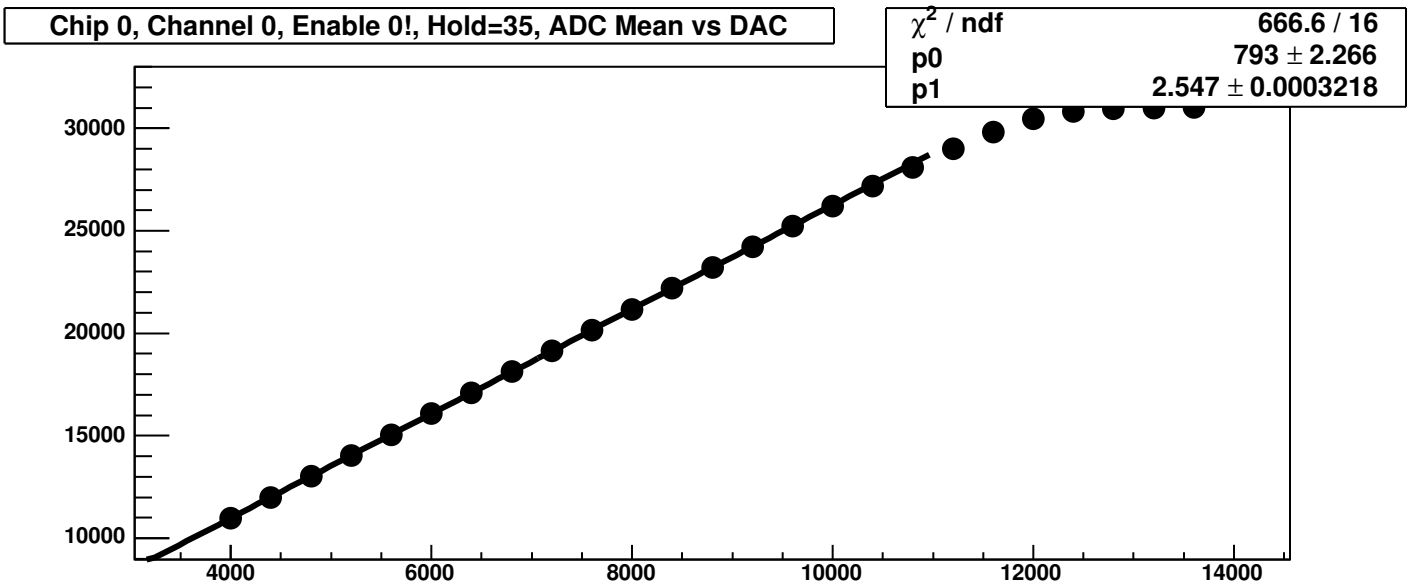


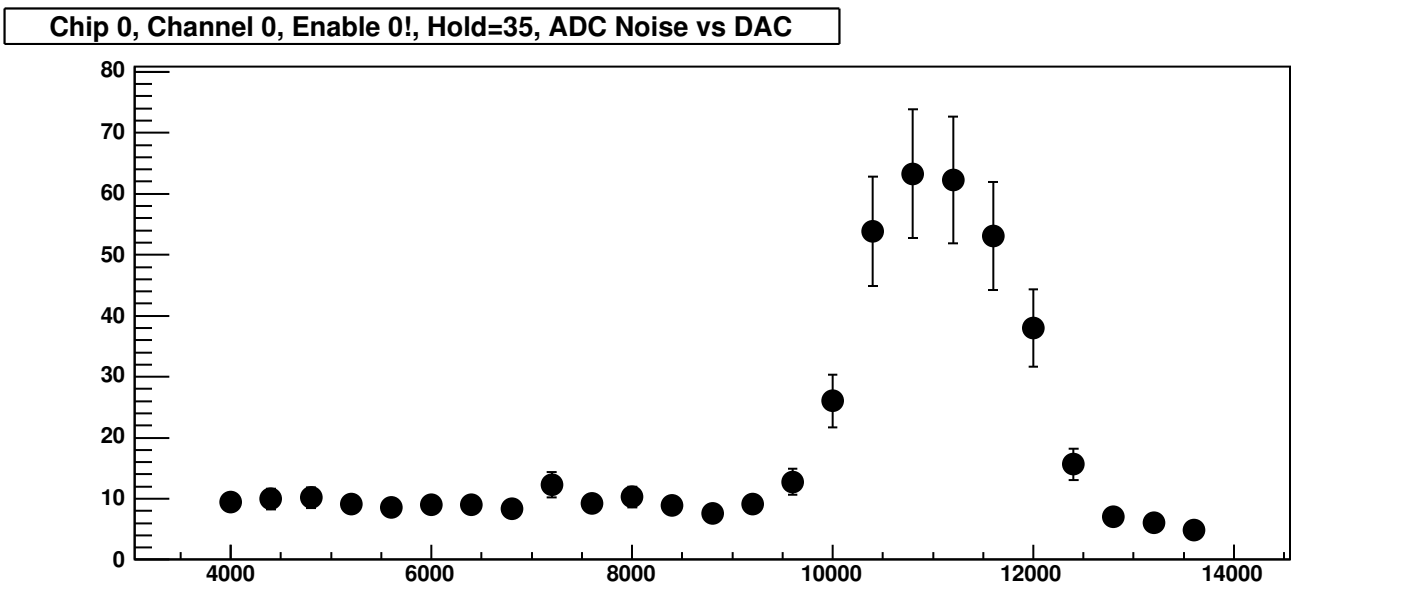
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan

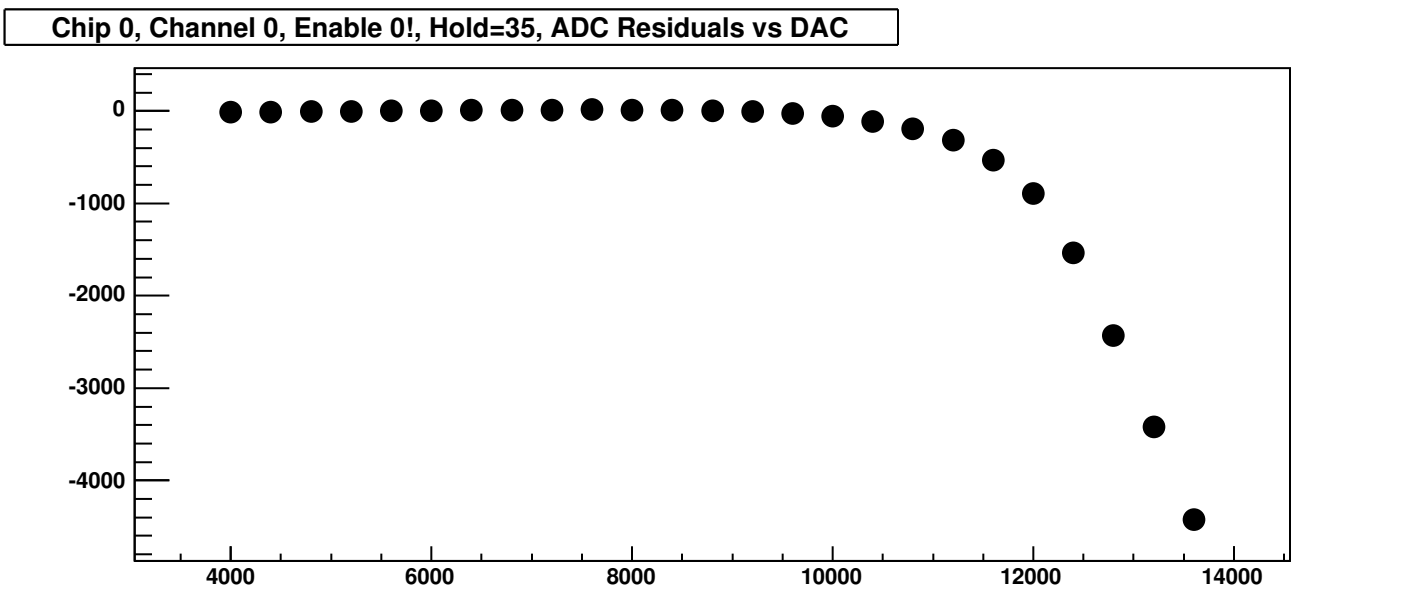


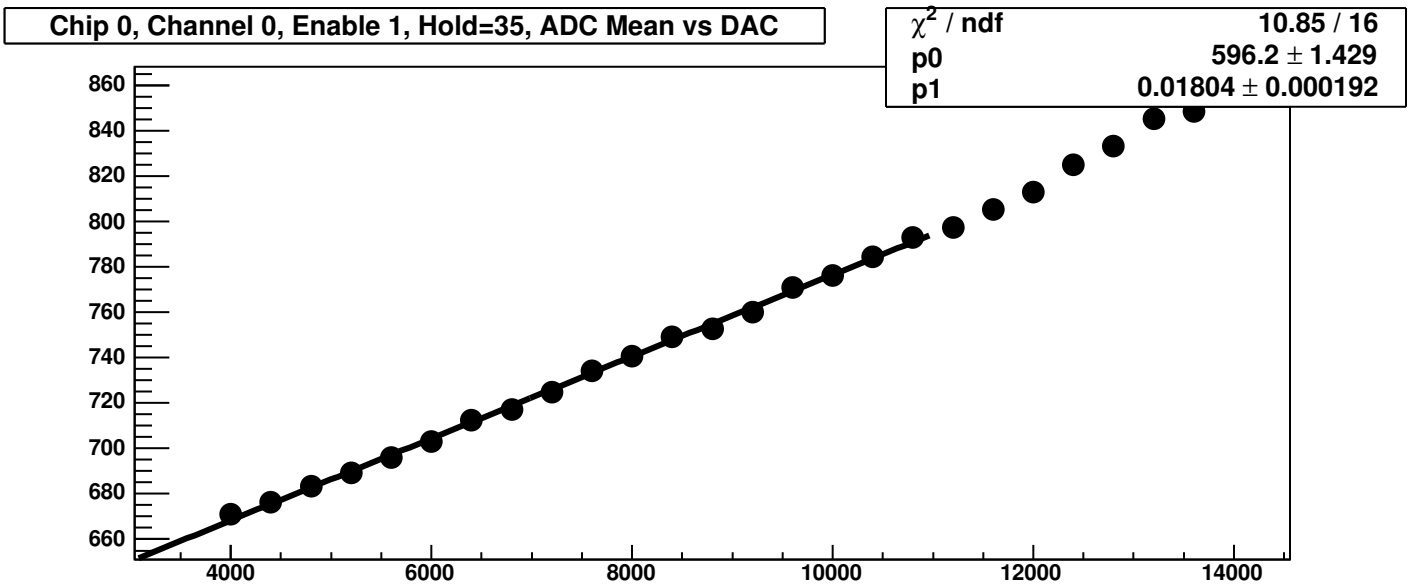
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan

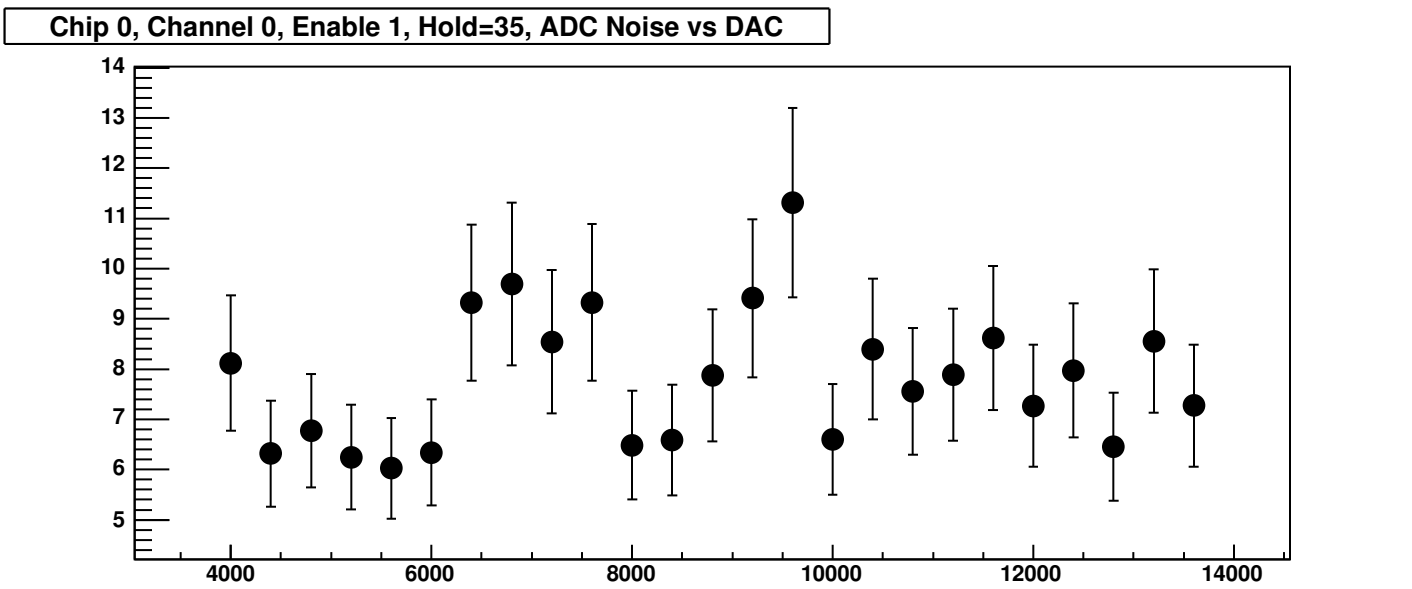


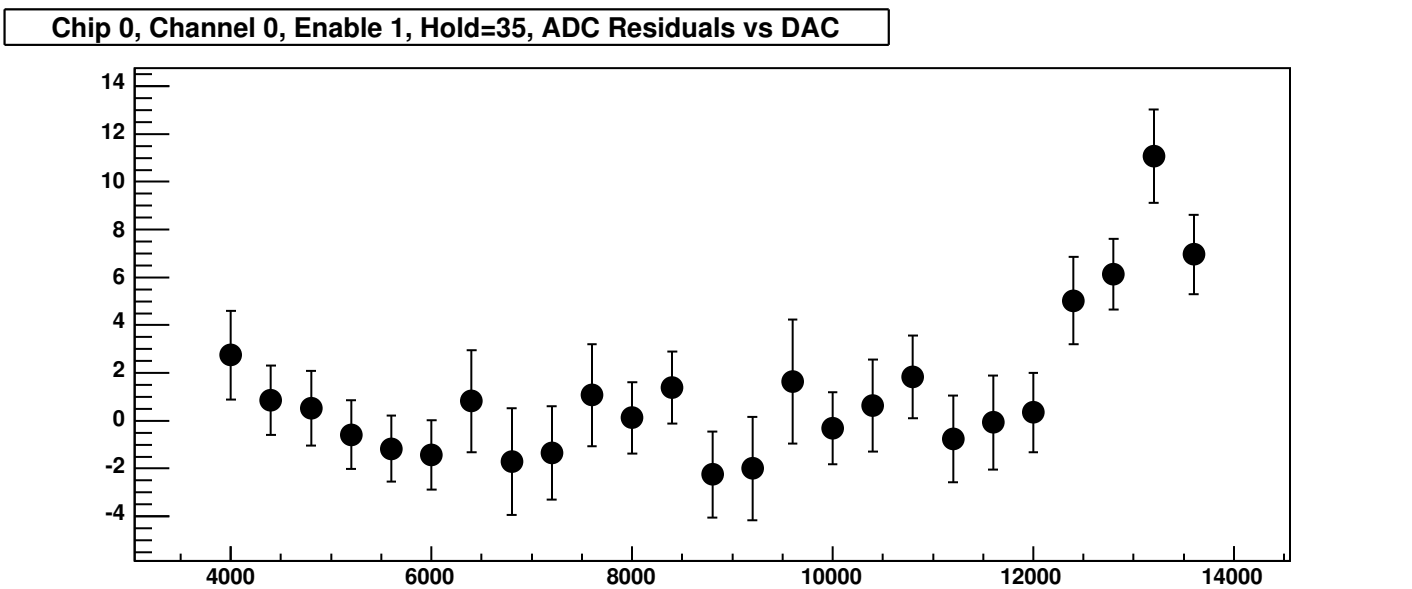

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 666.6 / 16
p0 793 ± 2.266
p1 2.547 ± 0.0003218
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

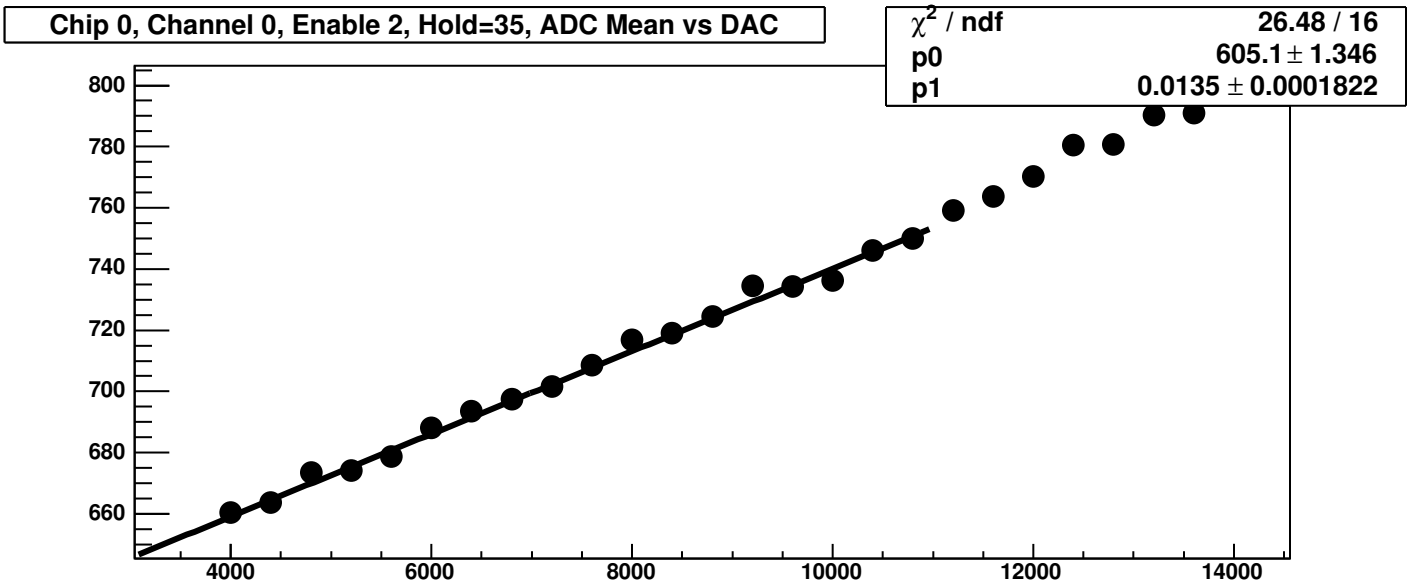

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

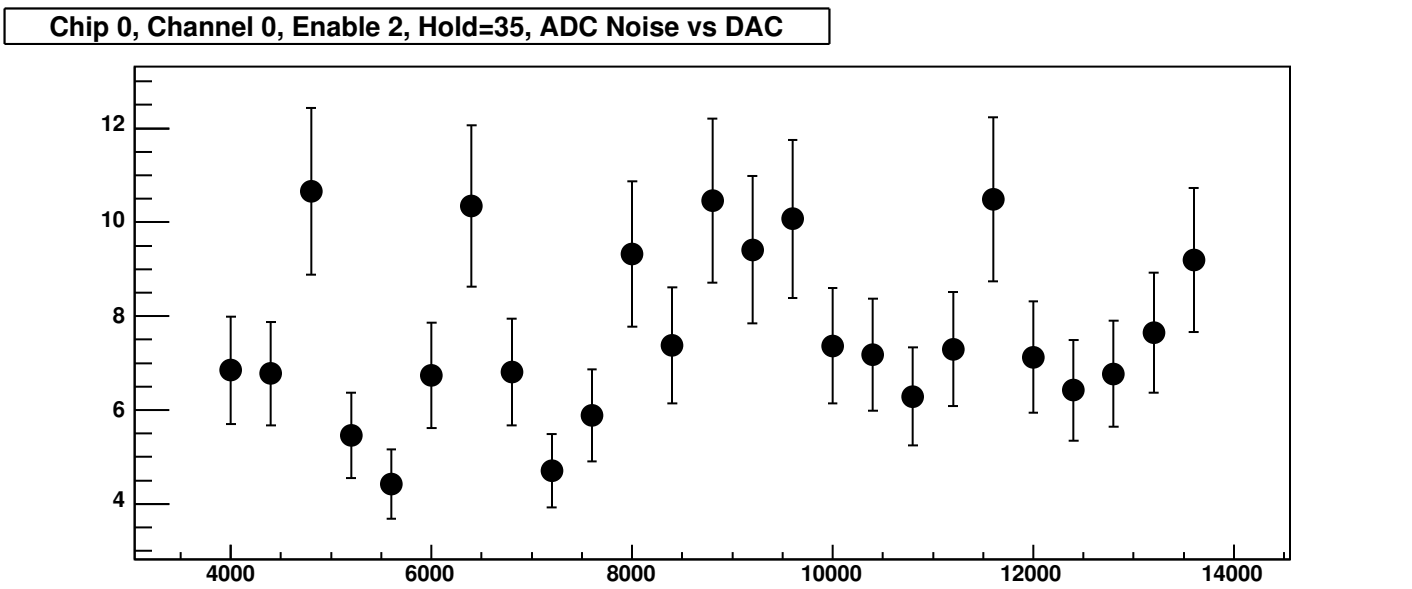

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

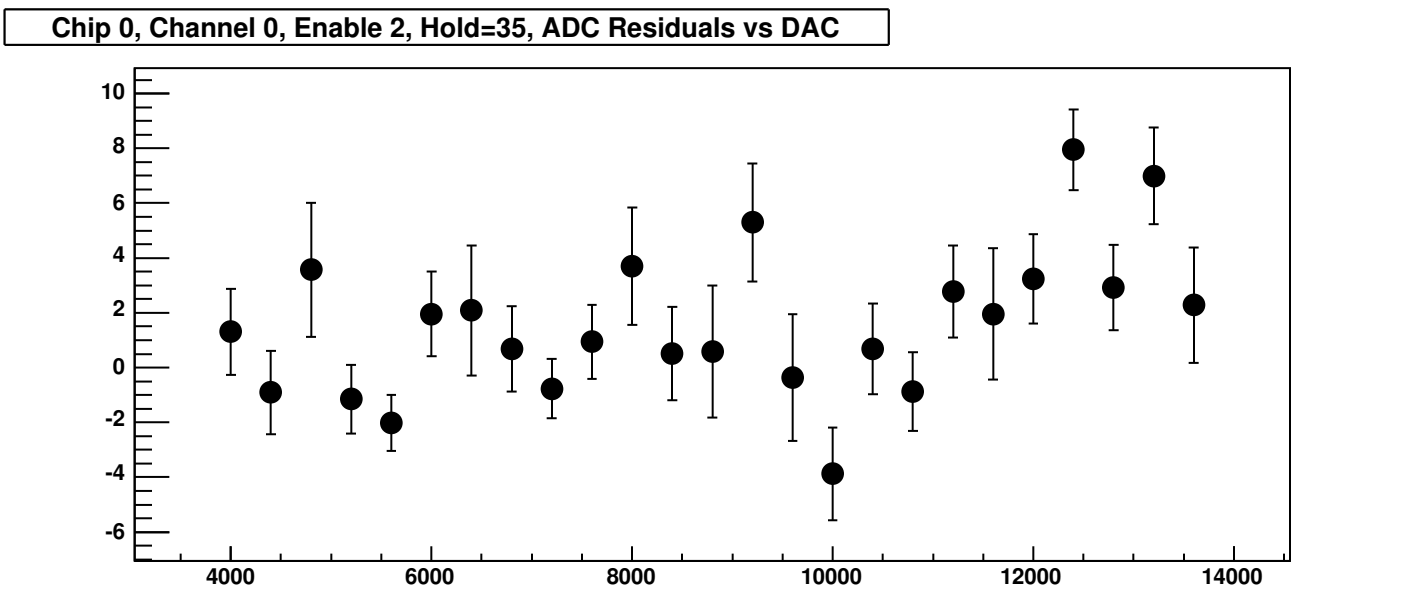

Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 10.85 / 16
p0 596.2 ± 1.429
p1 0.01804 ± 0.000192
860
840
820
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

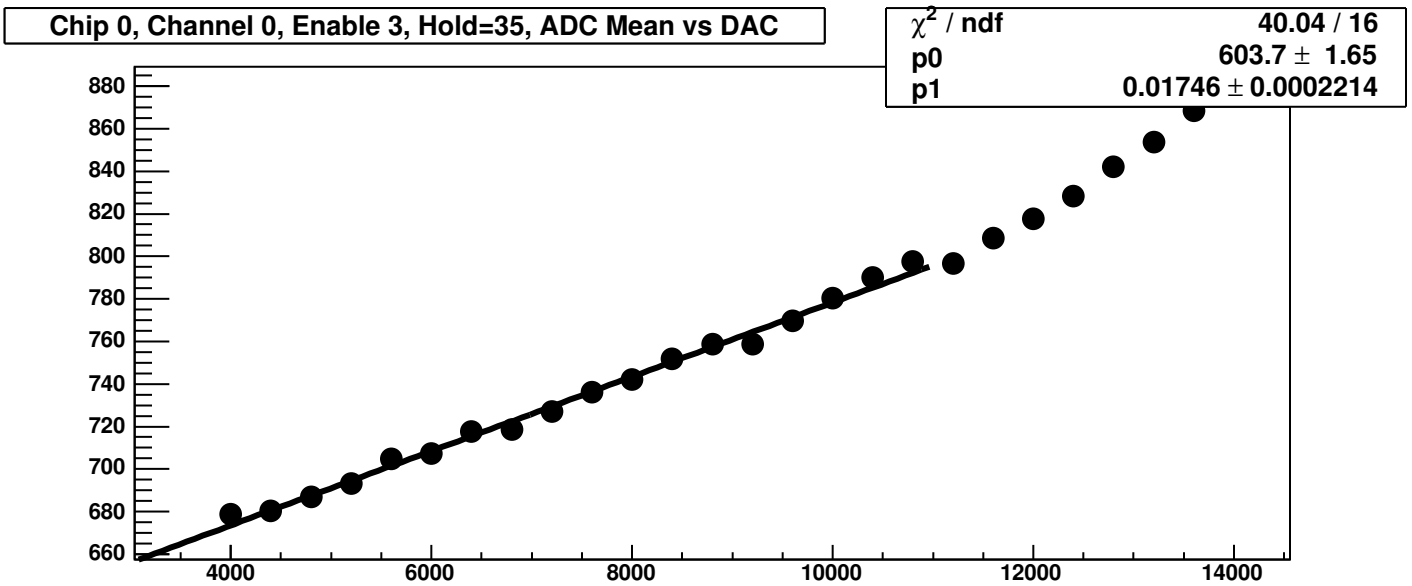

Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

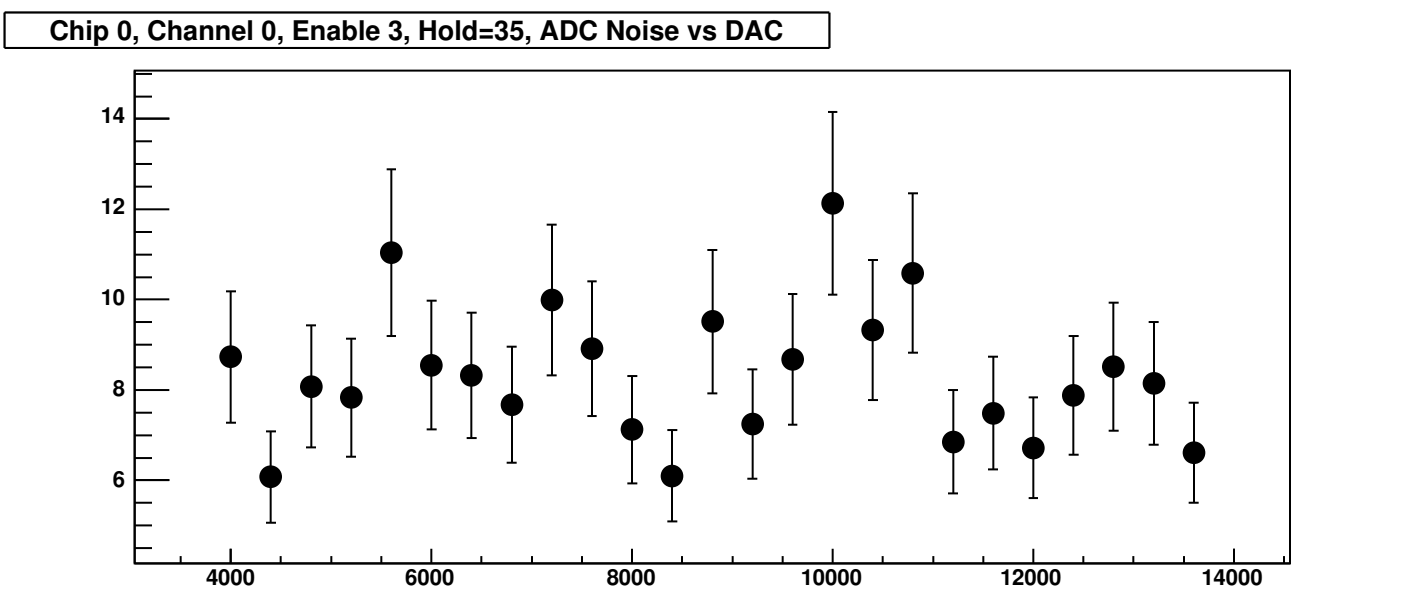

Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

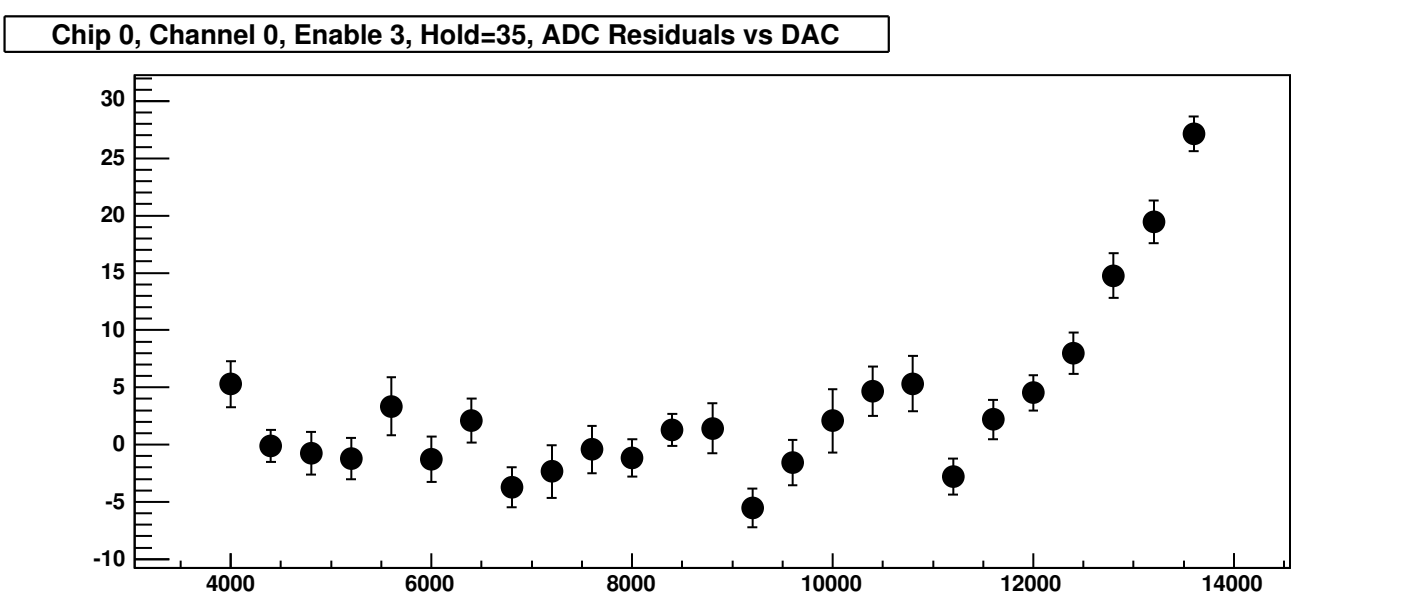

Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.48 / 16
p0 605.1 ± 1.346
p1 0.0135 ± 0.0001822
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

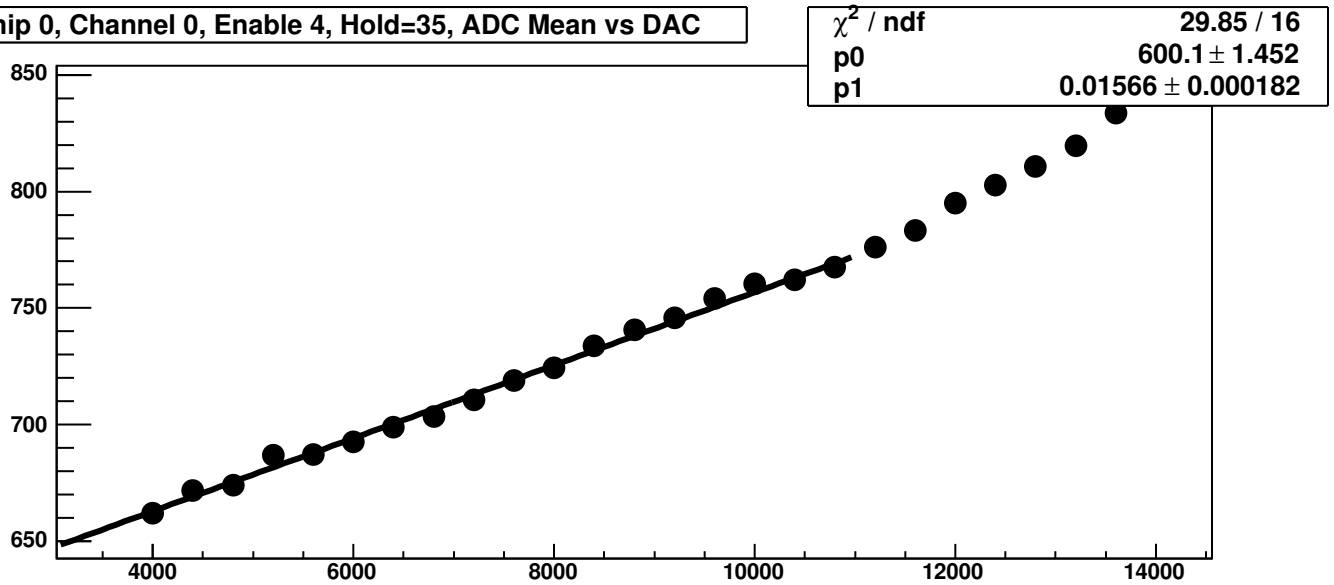

Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.04 / 16
p0 603.7 ± 1.65
p1 0.01746 ± 0.0002214
880
860
840
820
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

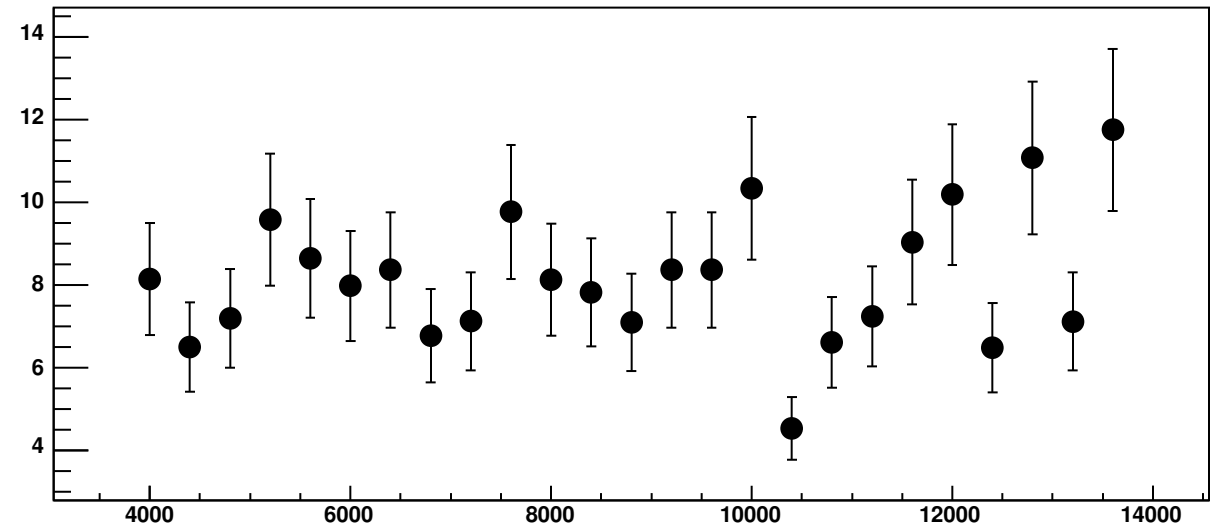

Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

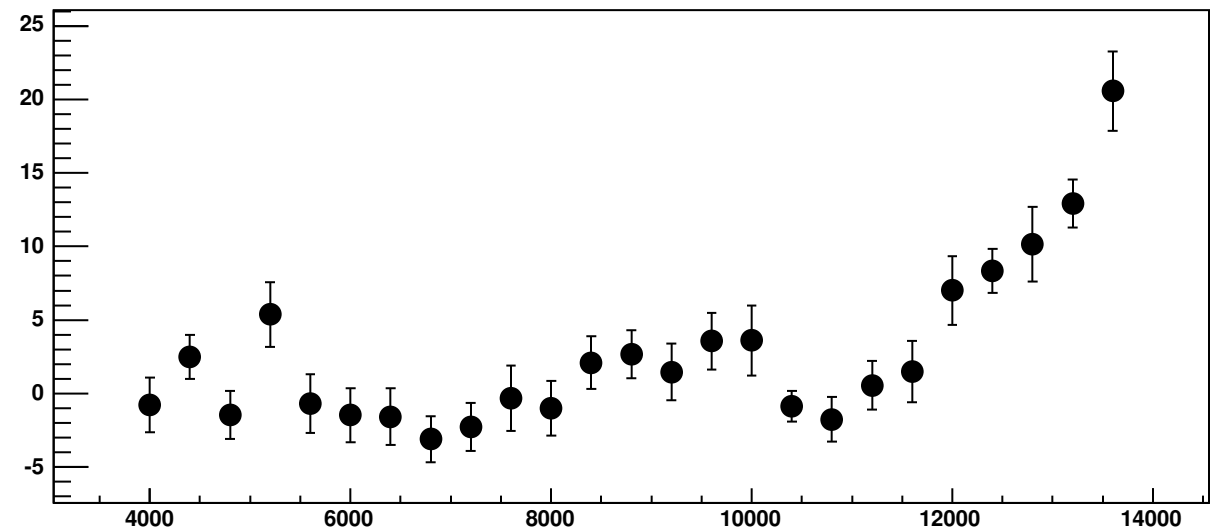
Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

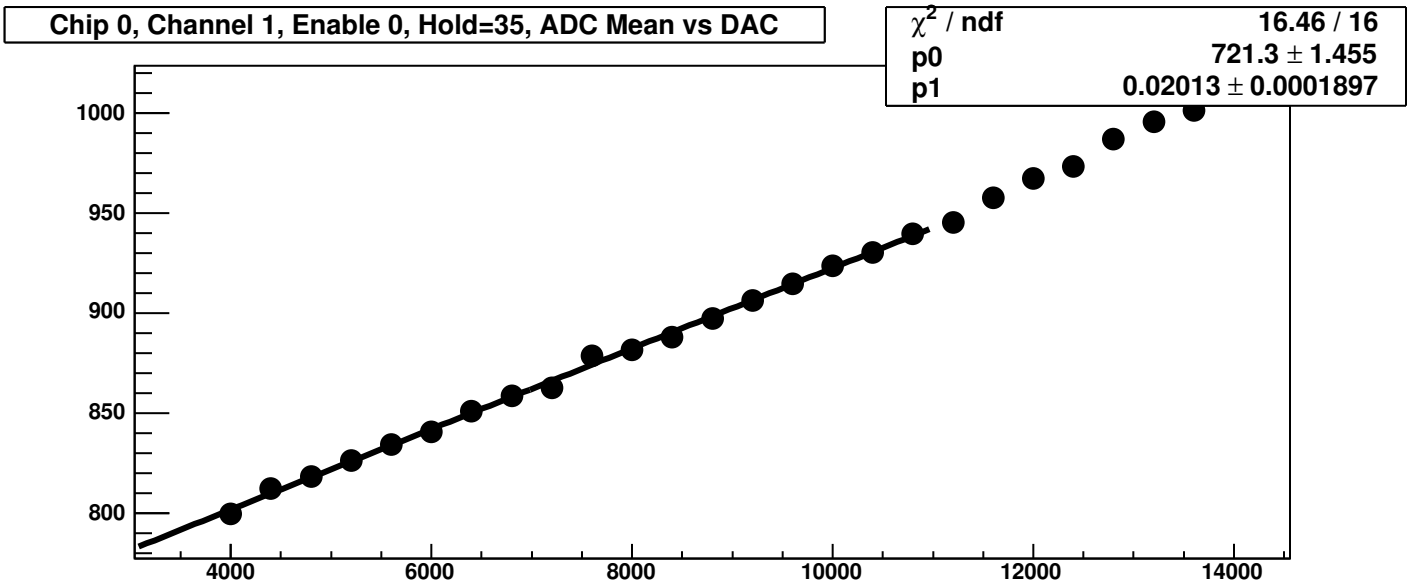


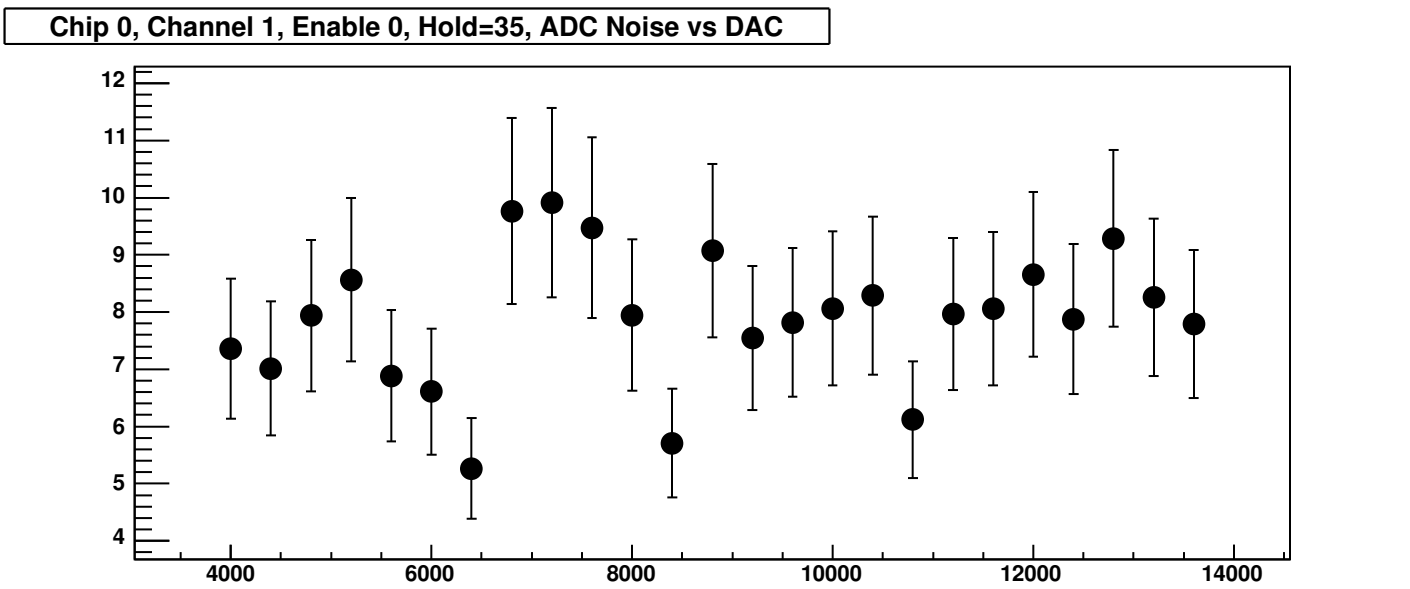
Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

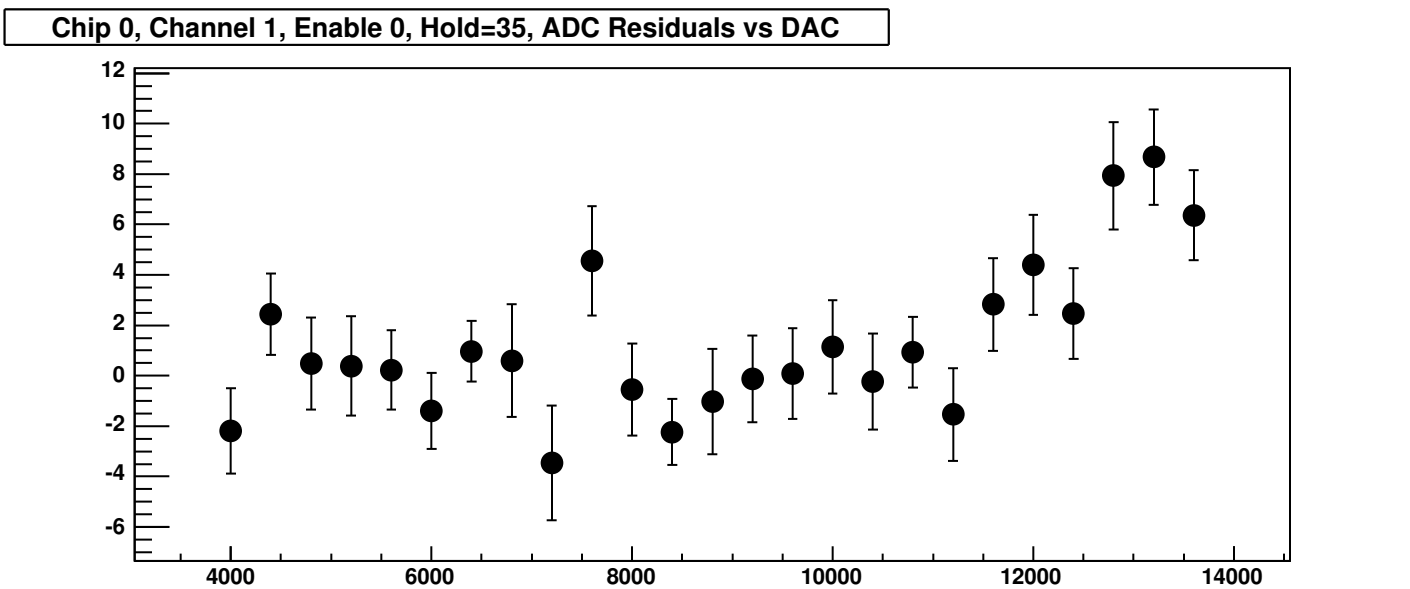


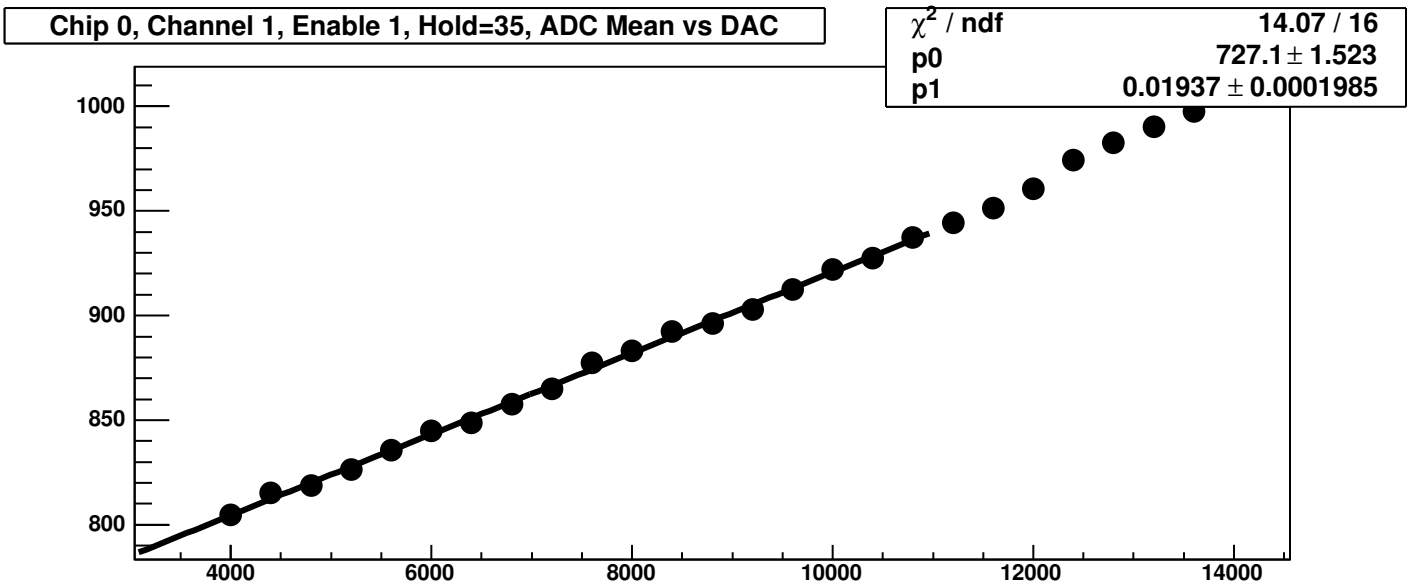
Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

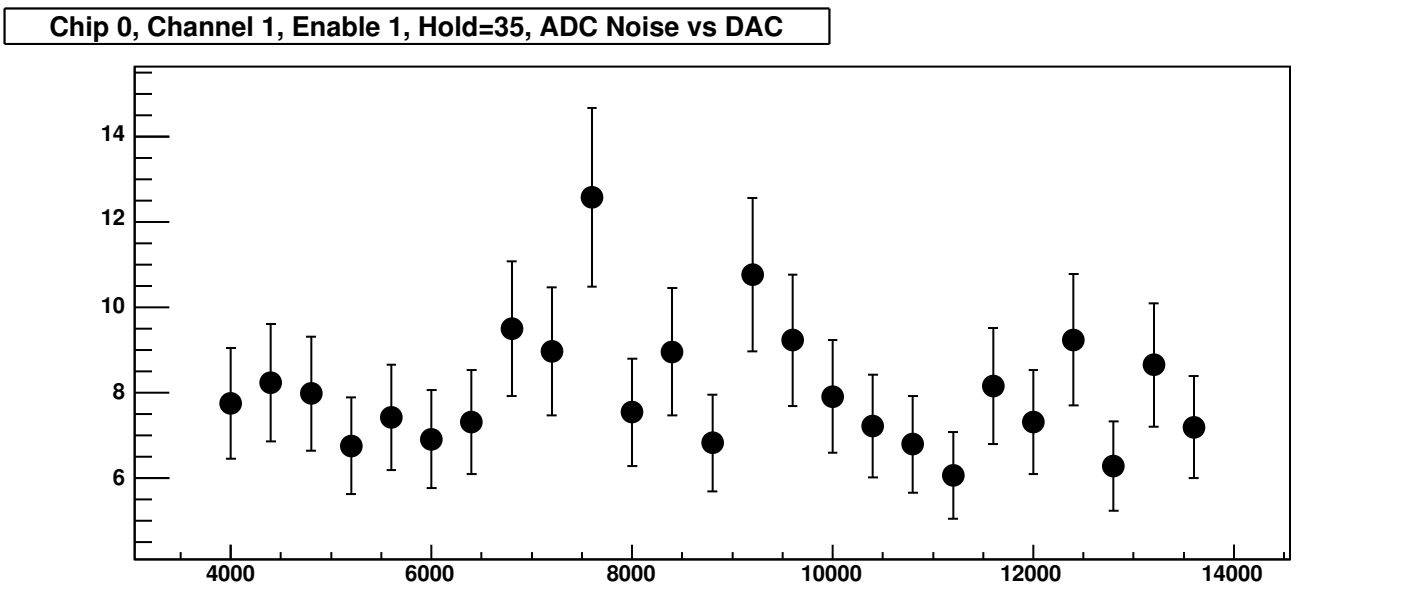


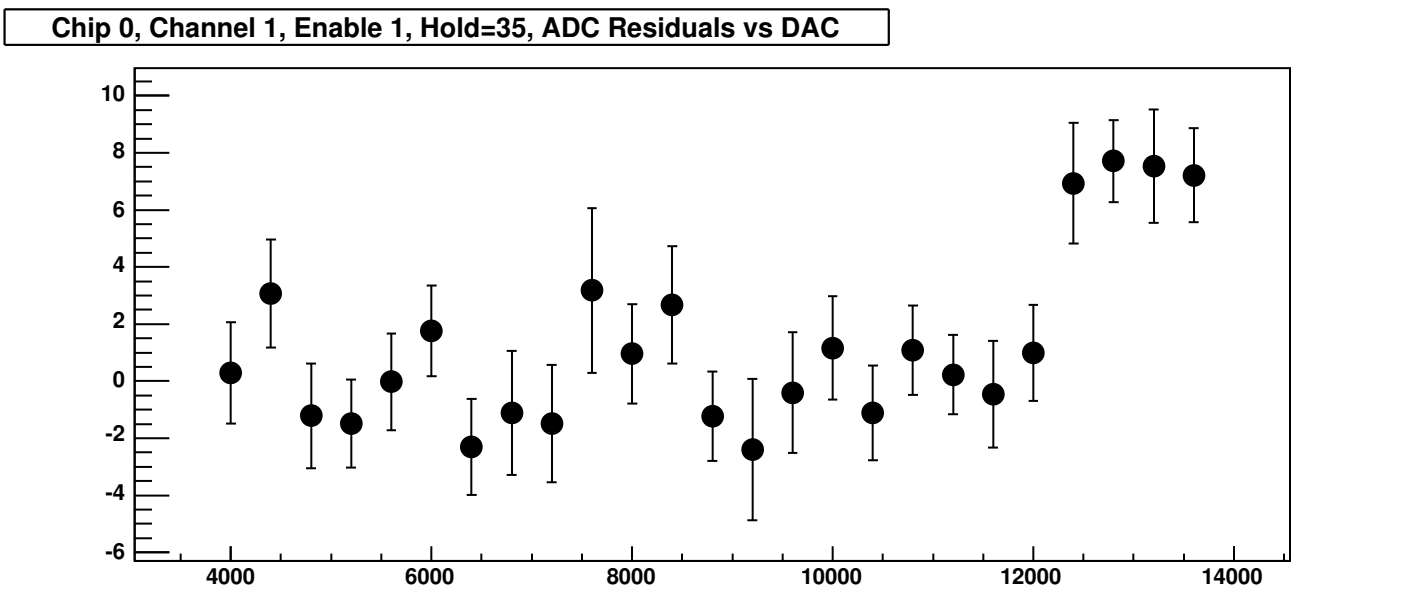

Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 16.46 / 16
p0 721.3 ± 1.455
p1 0.02013 ± 0.0001897
1000
950
900
850
800
4000
6000
8000
10000
12000
14000

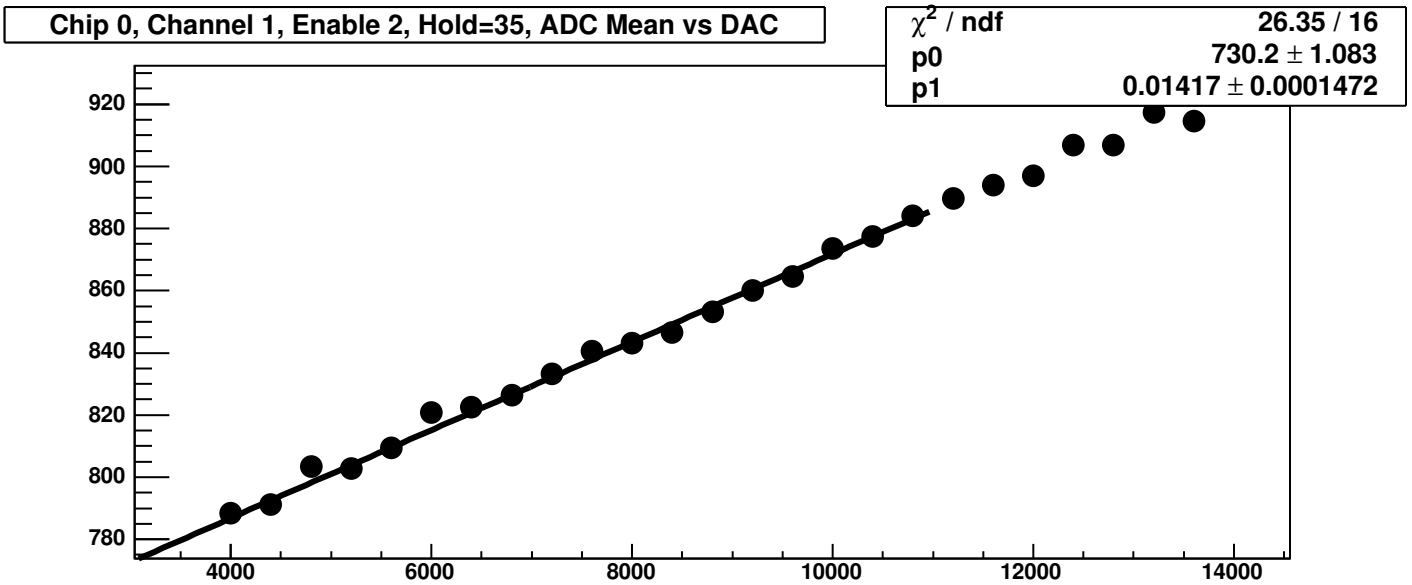

Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

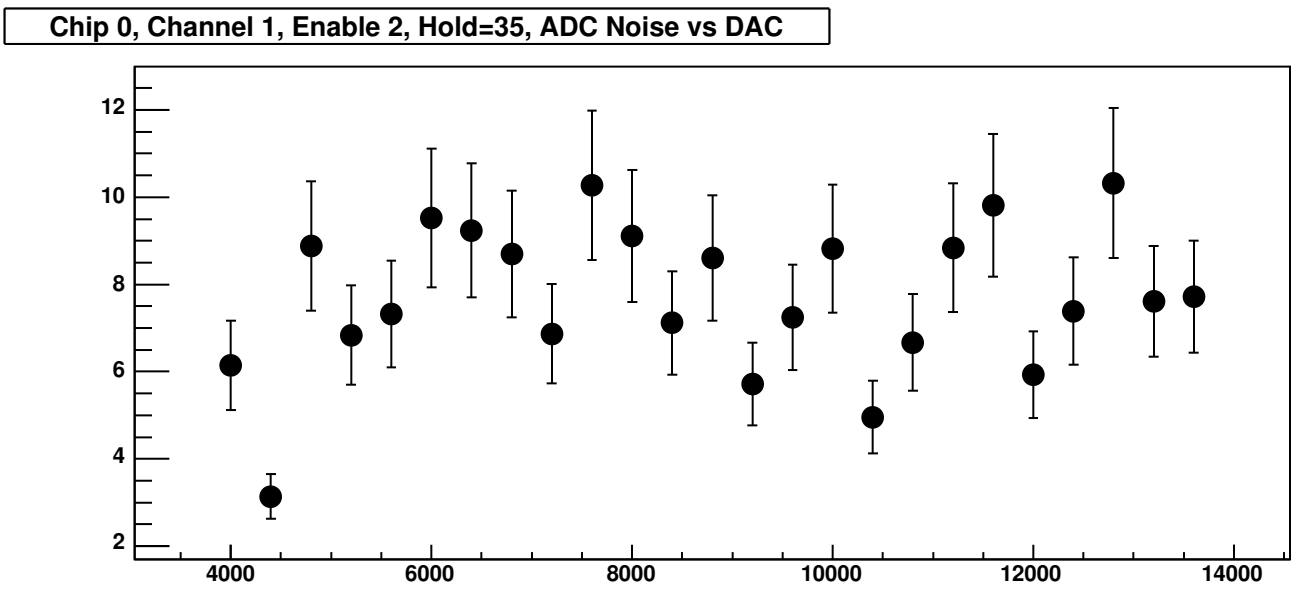

Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

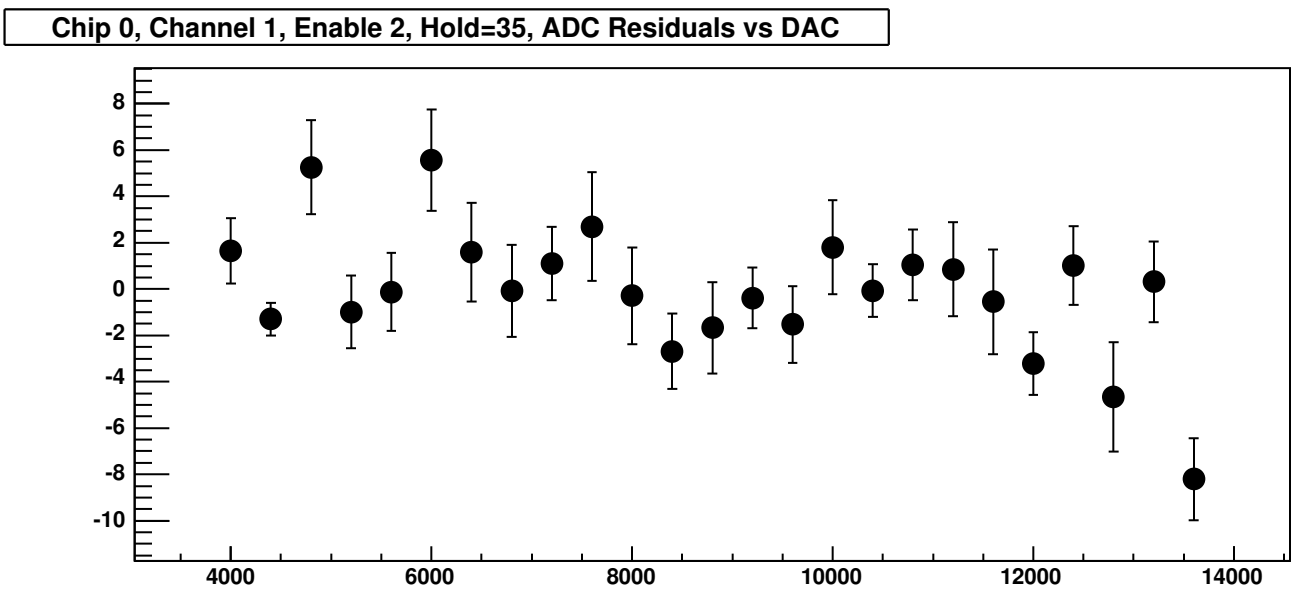

Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.07 / 16
p0 727.1 ± 1.523
p1 0.01937 ± 0.0001985
1000
950
900
850
800
4000
6000
8000
10000
12000
14000

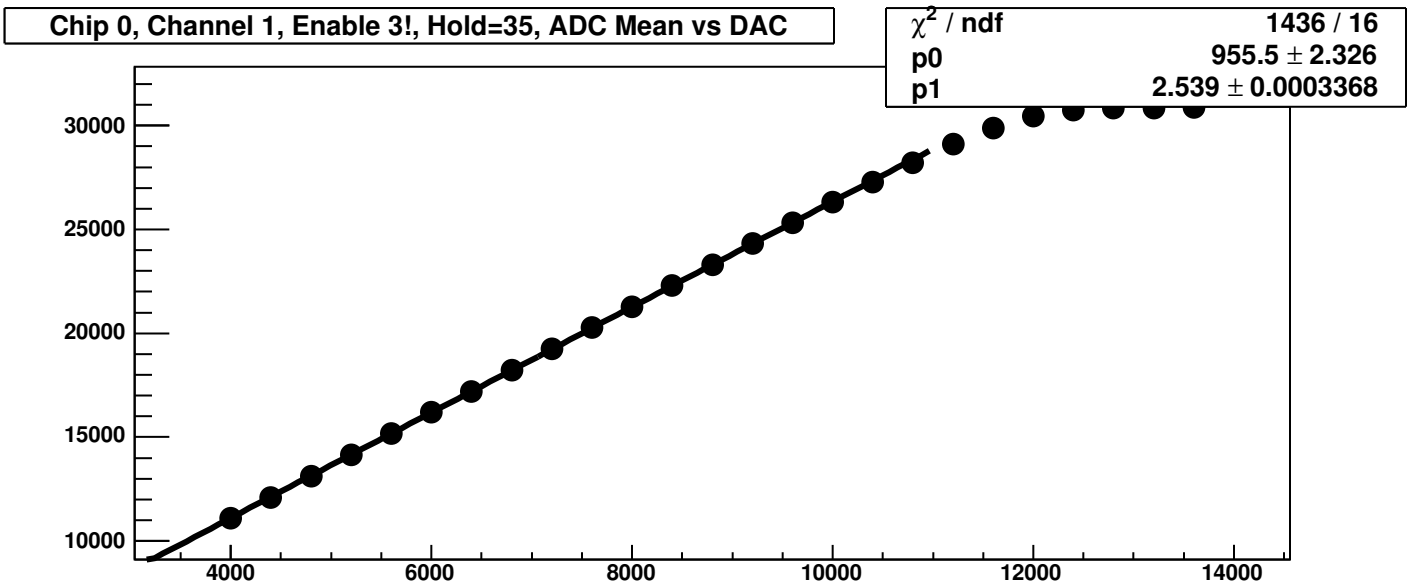

Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4000
6000
8000
10000
12000
14000

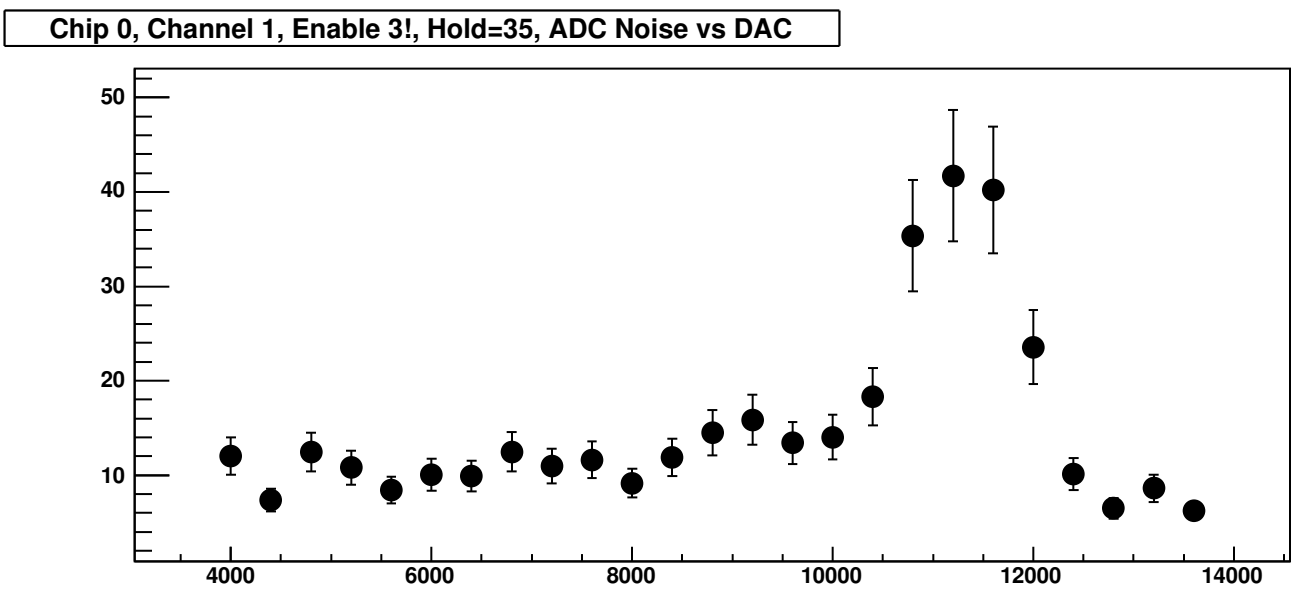

Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

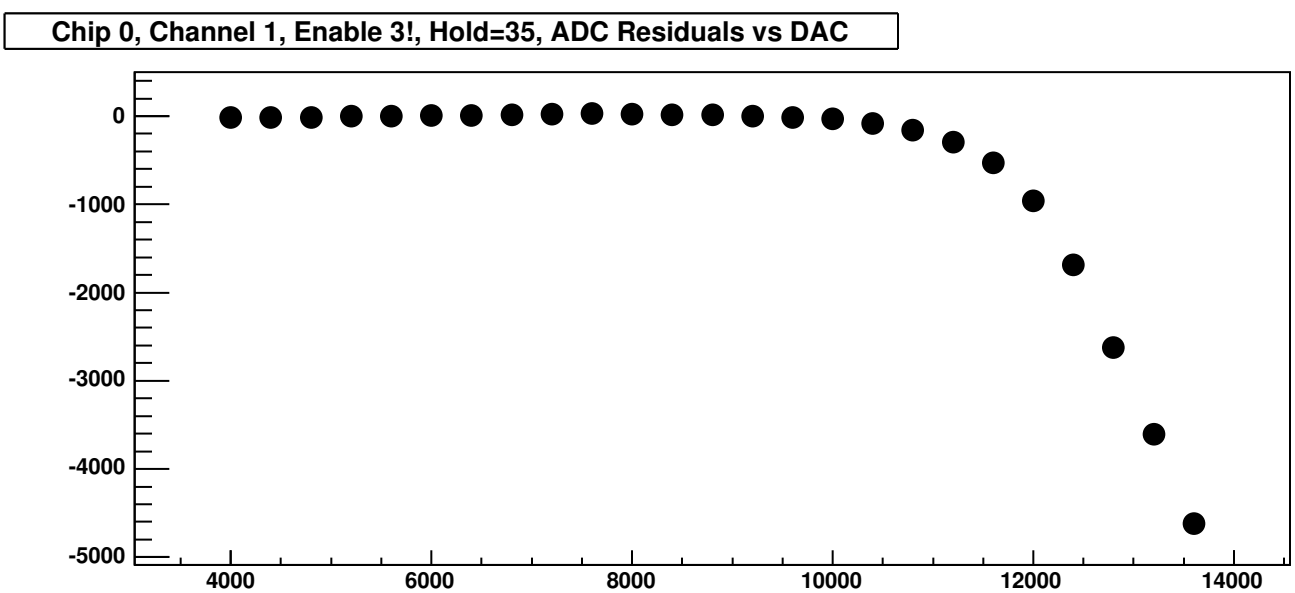

Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.35 / 16
p0 730.2 ± 1.083
p1 0.01417 ± 0.0001472
920
900
880
860
840
820
800
780
4000
6000
8000
10000
12000
14000

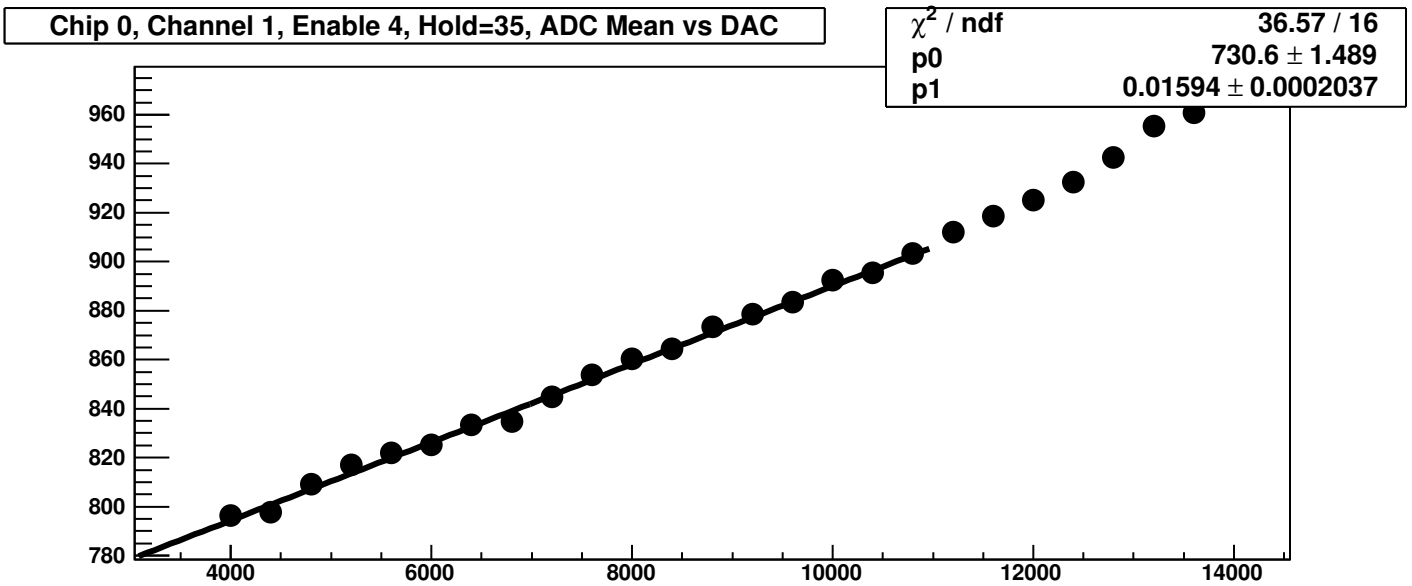

Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
4000
6000
8000
10000
12000
14000

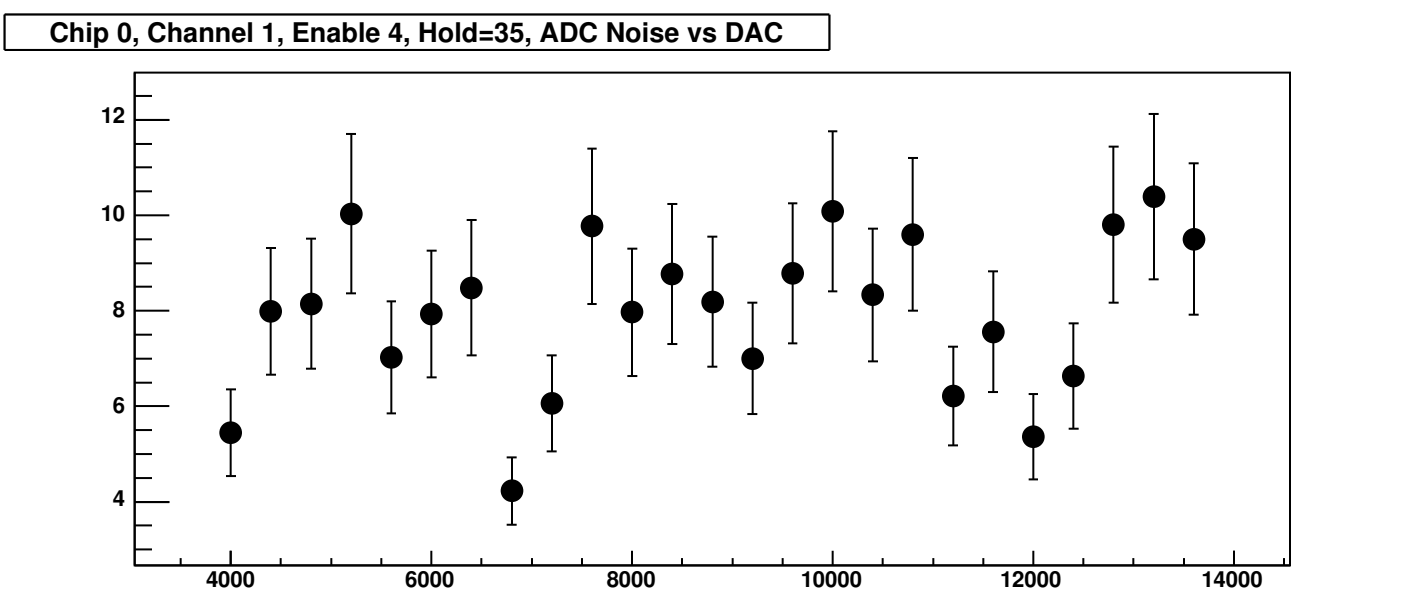

Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

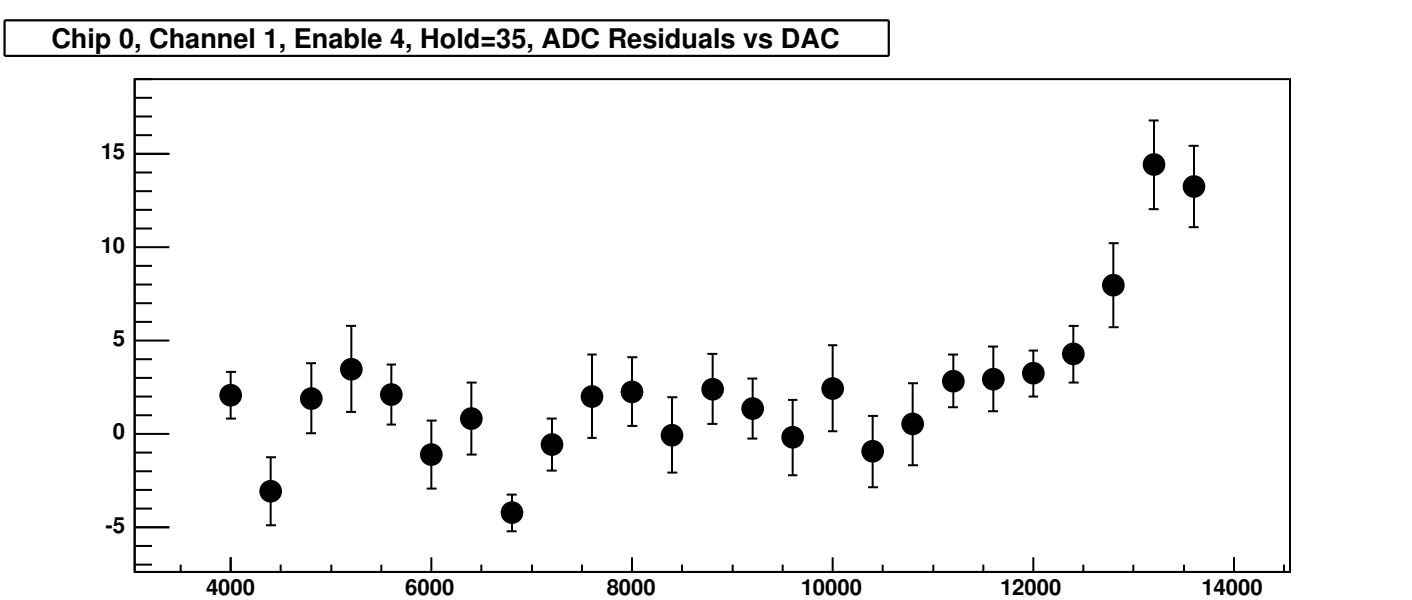

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1436 / 16
p0 955.5 ± 2.326
p1 2.539 ± 0.0003368
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

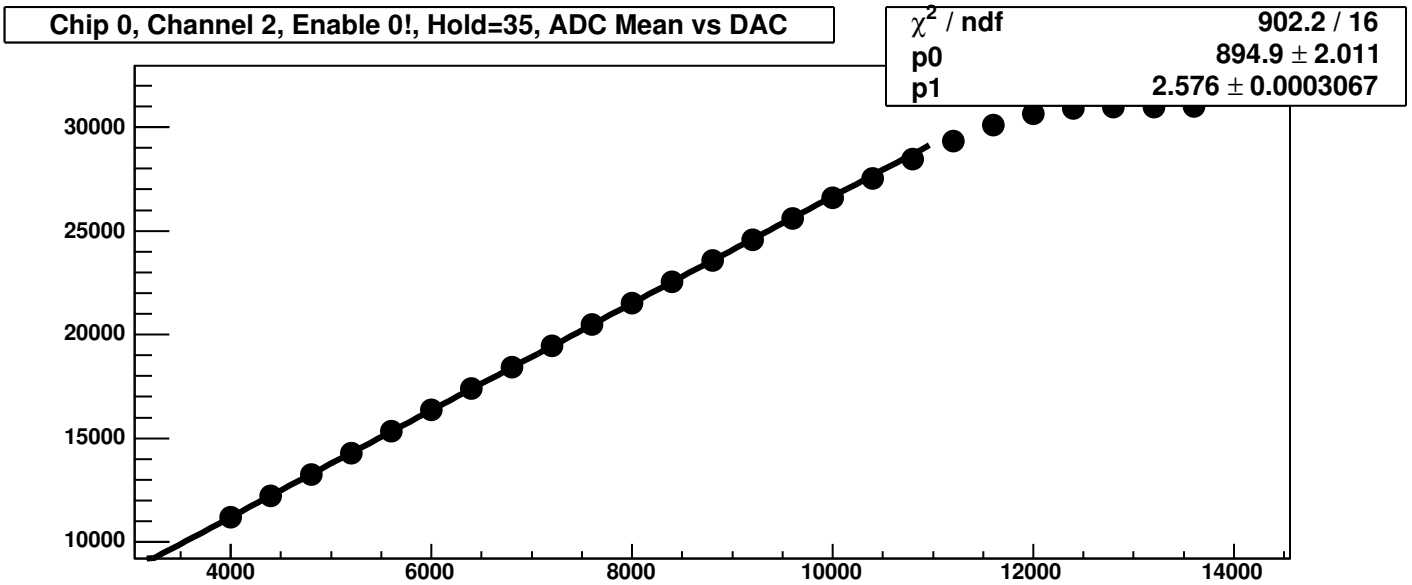

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

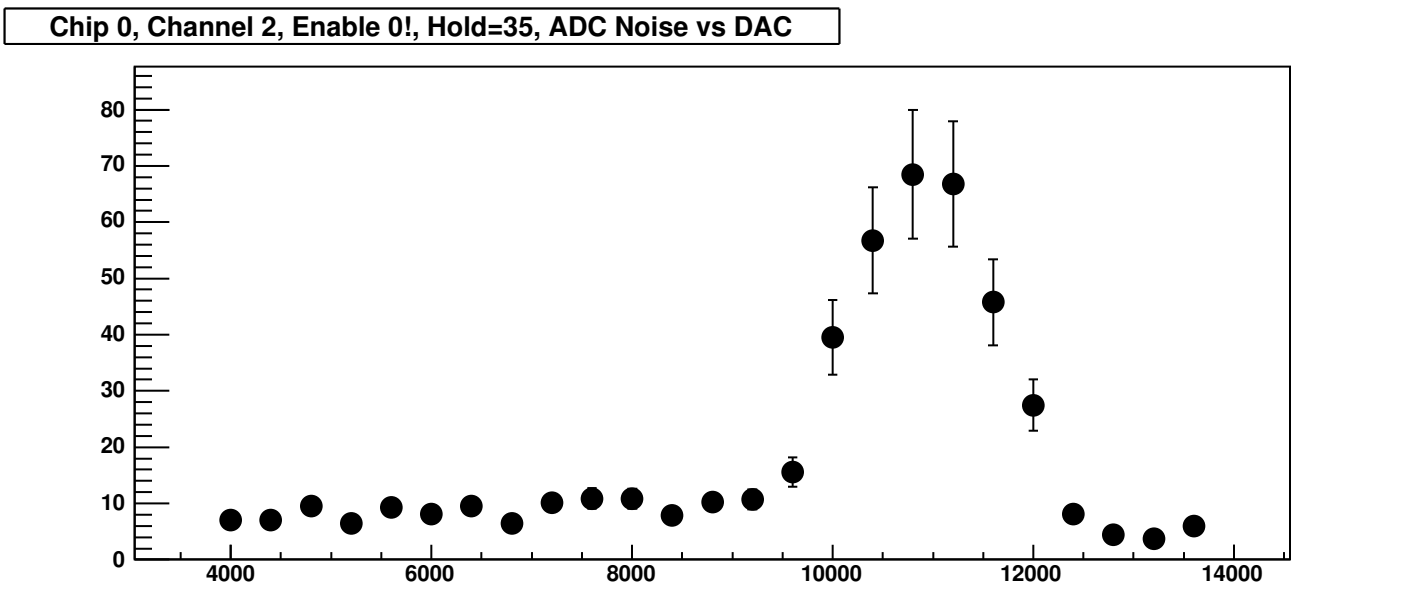

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

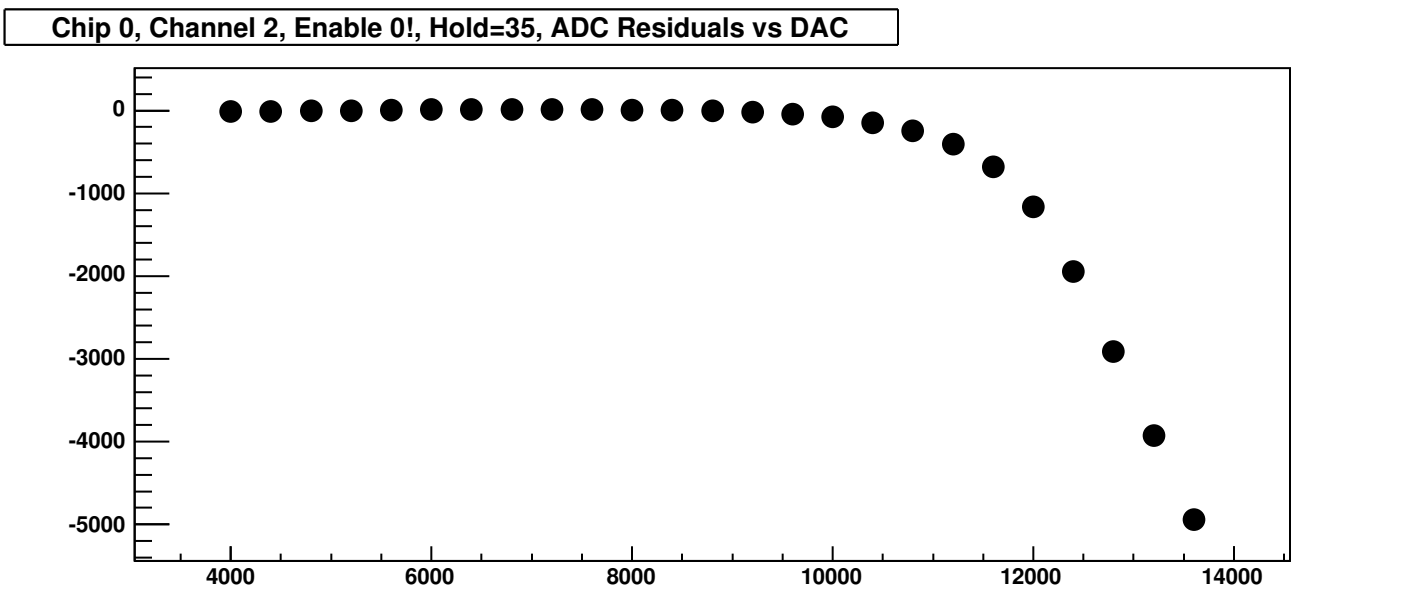

Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.57 / 16
p0 730.6 ± 1.489
p1 0.01594 ± 0.0002037
960
940
920
900
880
860
840
820
800
780
4000
6000
8000
10000
12000
14000


Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

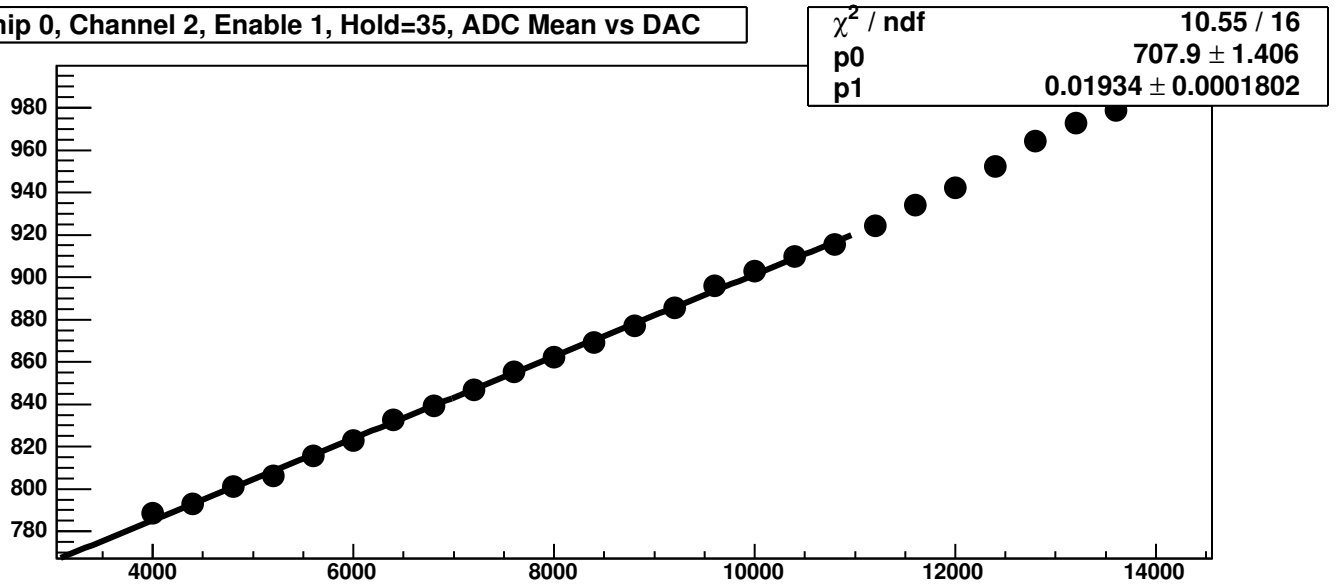

Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 902.2 / 16
p0 894.9 ± 2.011
p1 2.576 ± 0.0003067
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

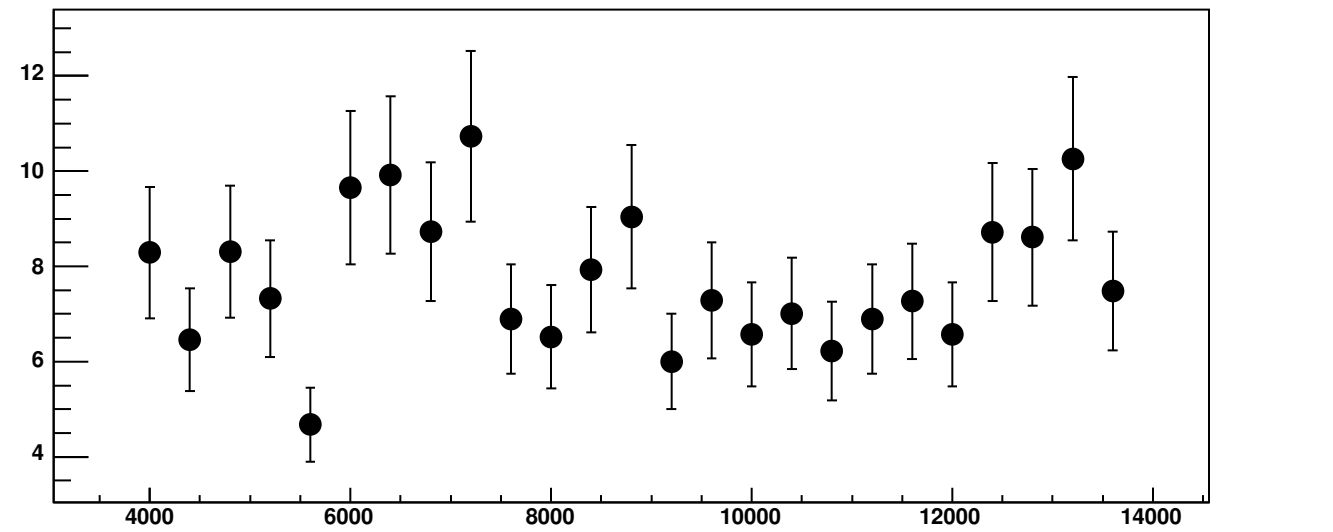

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

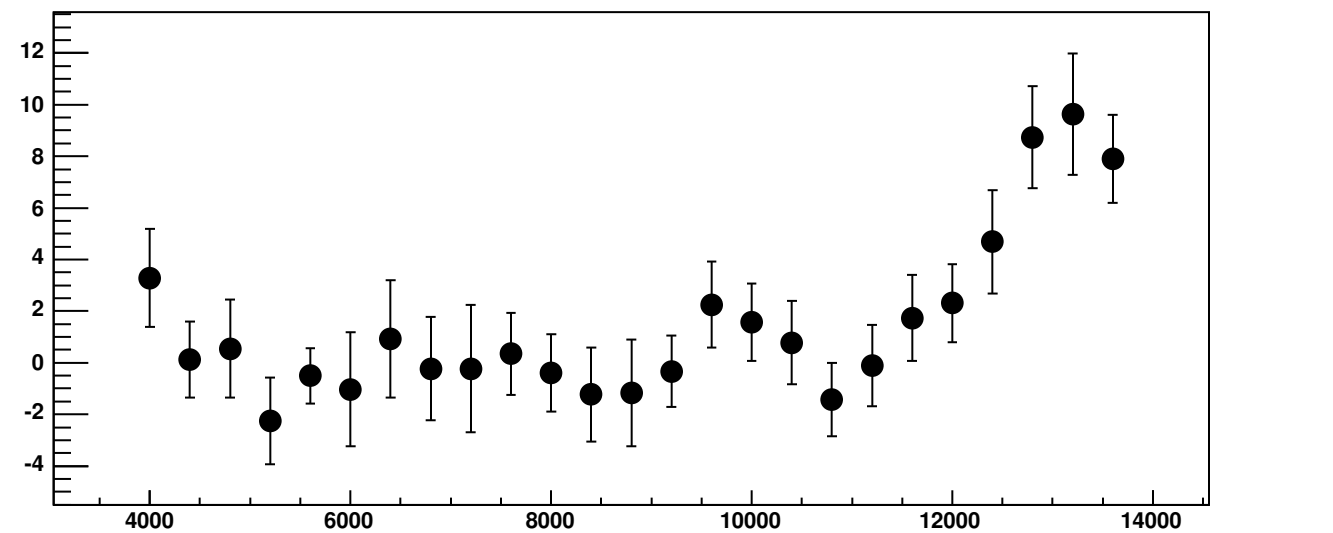
Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



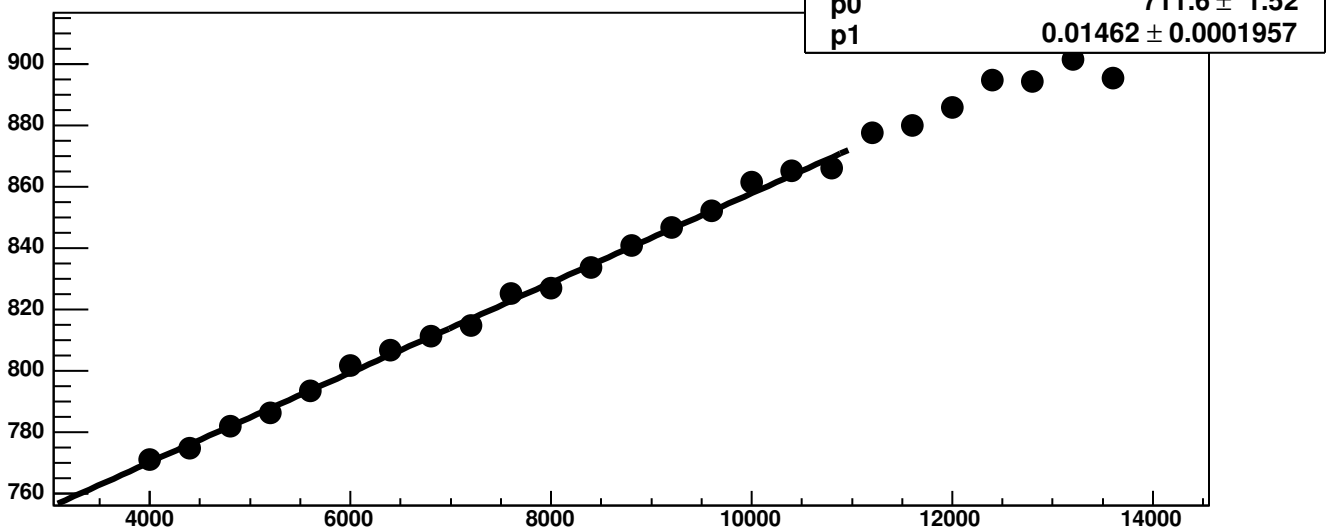
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



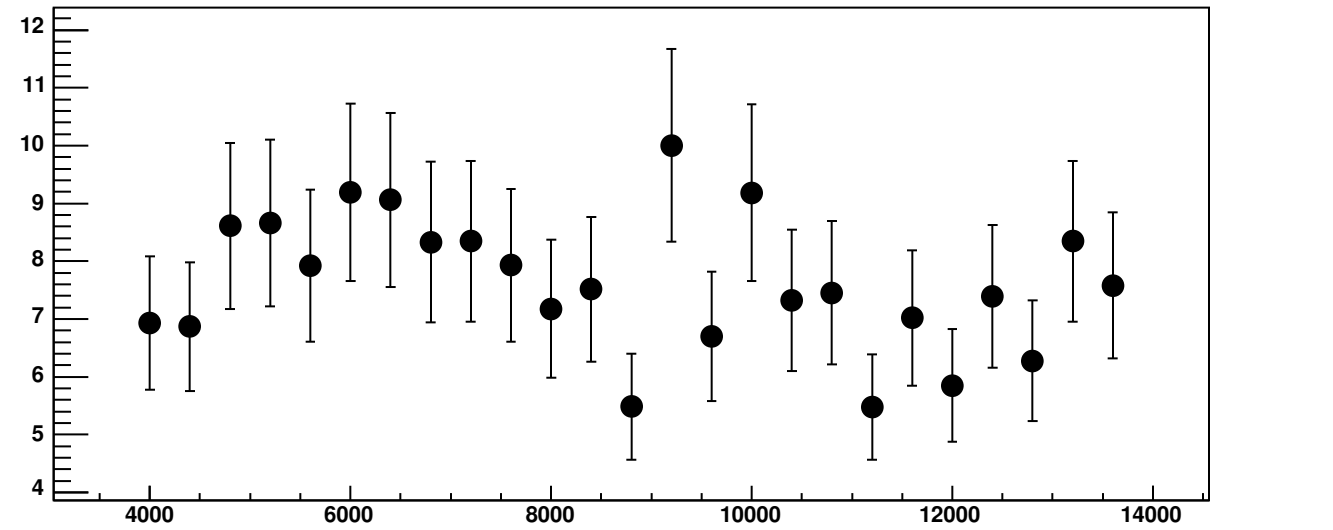
Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



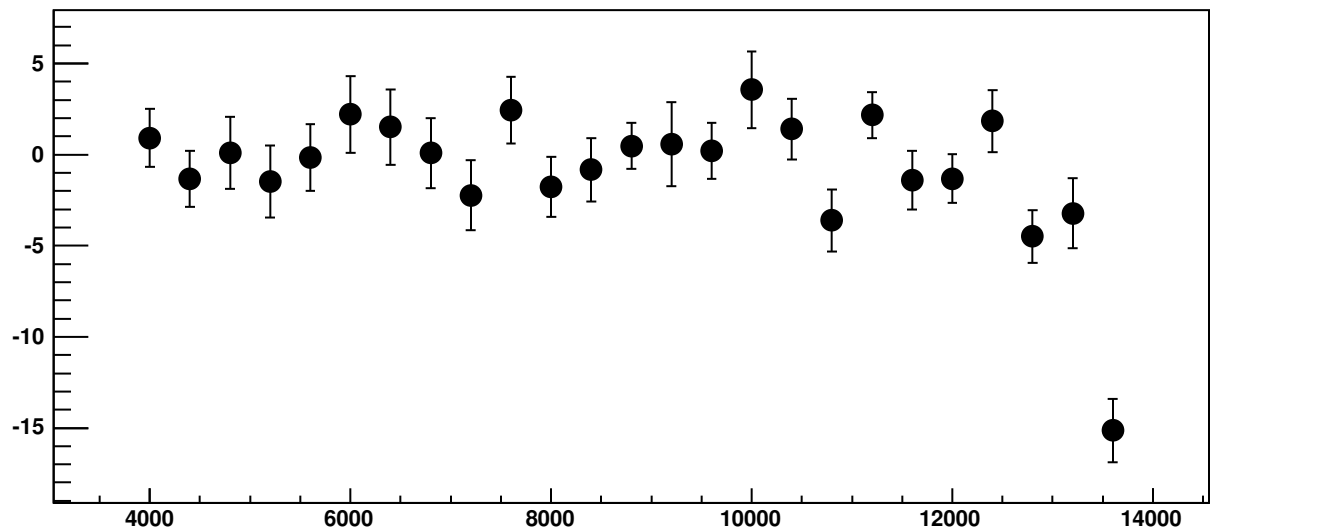
Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

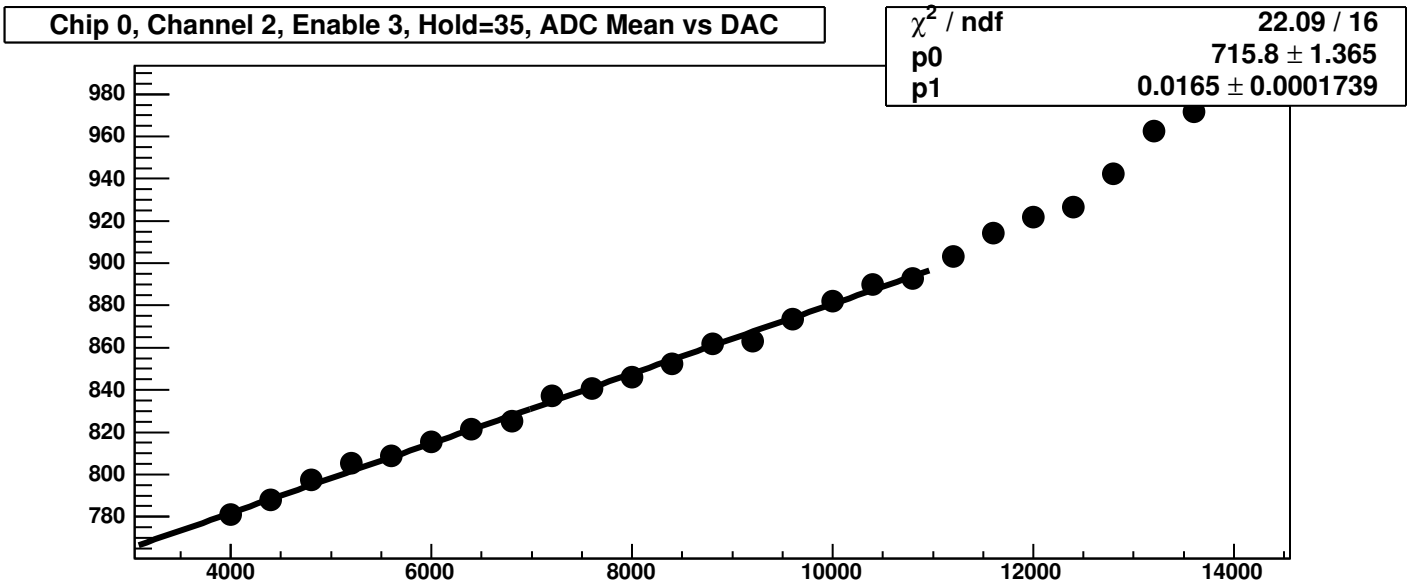


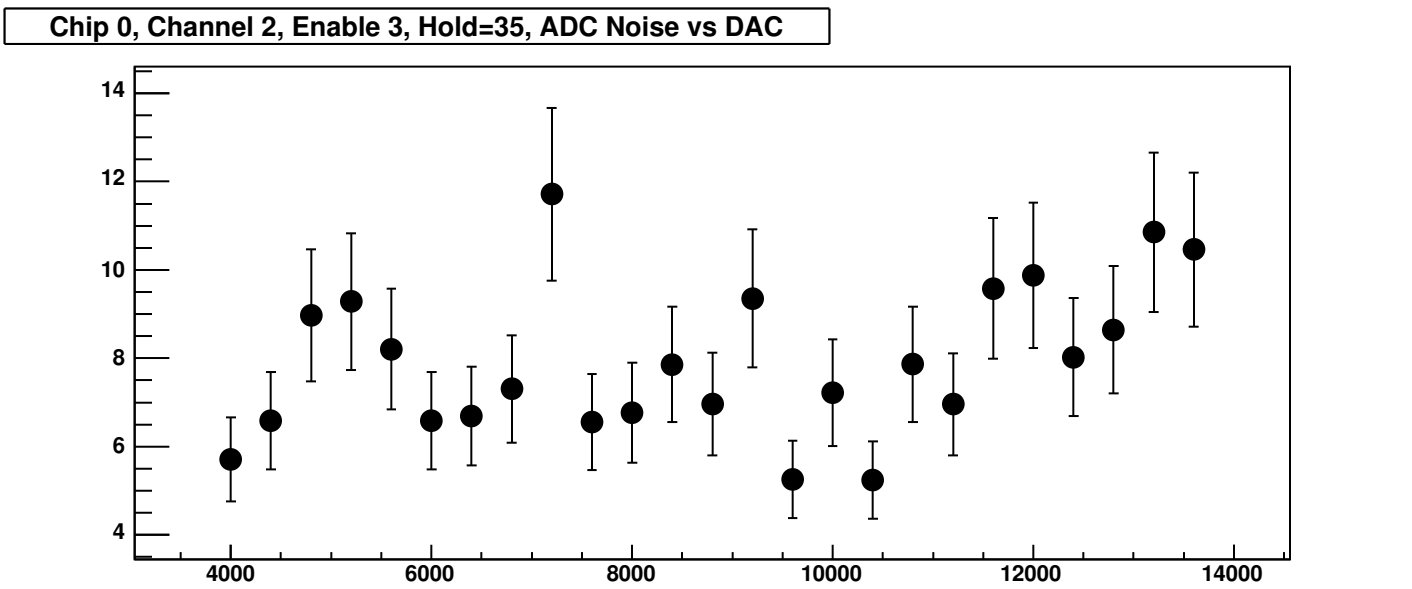
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

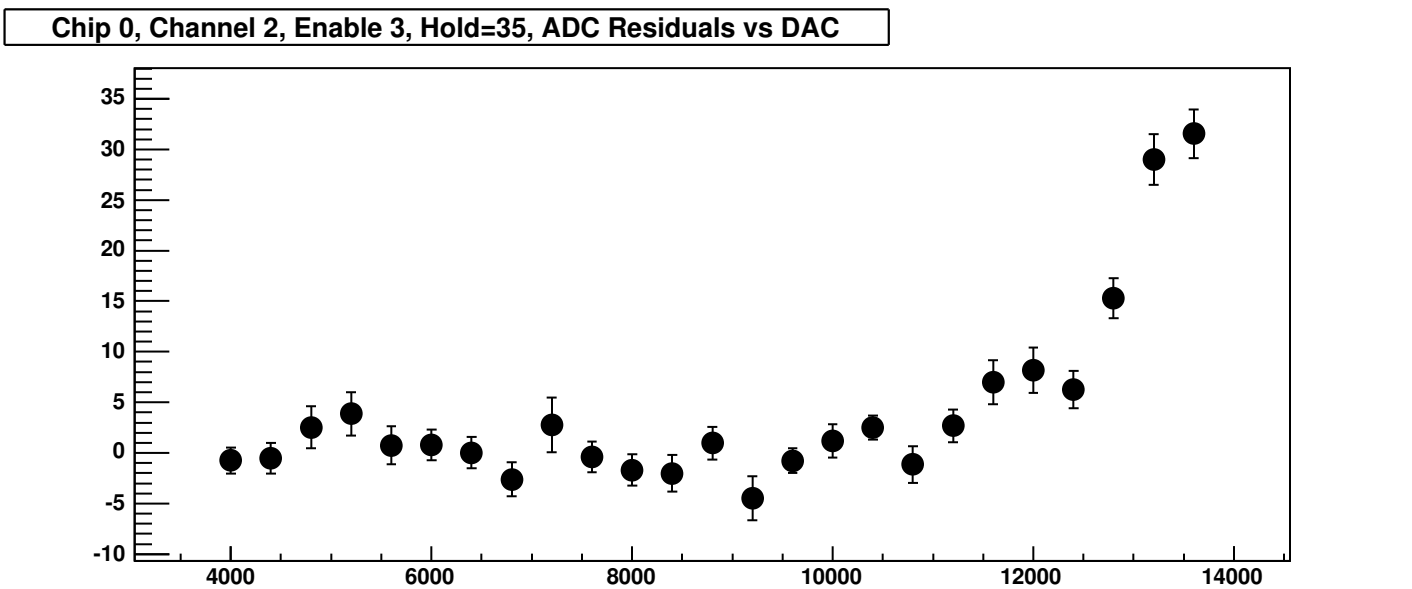


Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

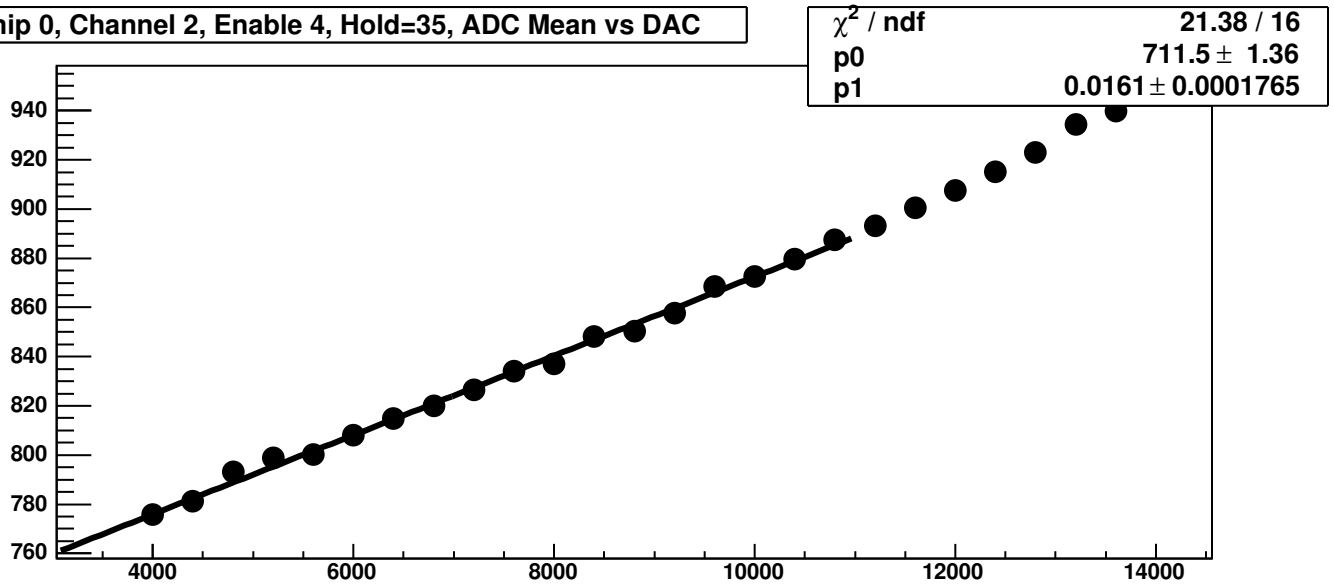



Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.09 / 16
p0 715.8 ± 1.365
p1 0.0165 ± 0.0001739
980
960
940
920
900
880
860
840
820
800
780
4000
6000
8000
10000
12000
14000

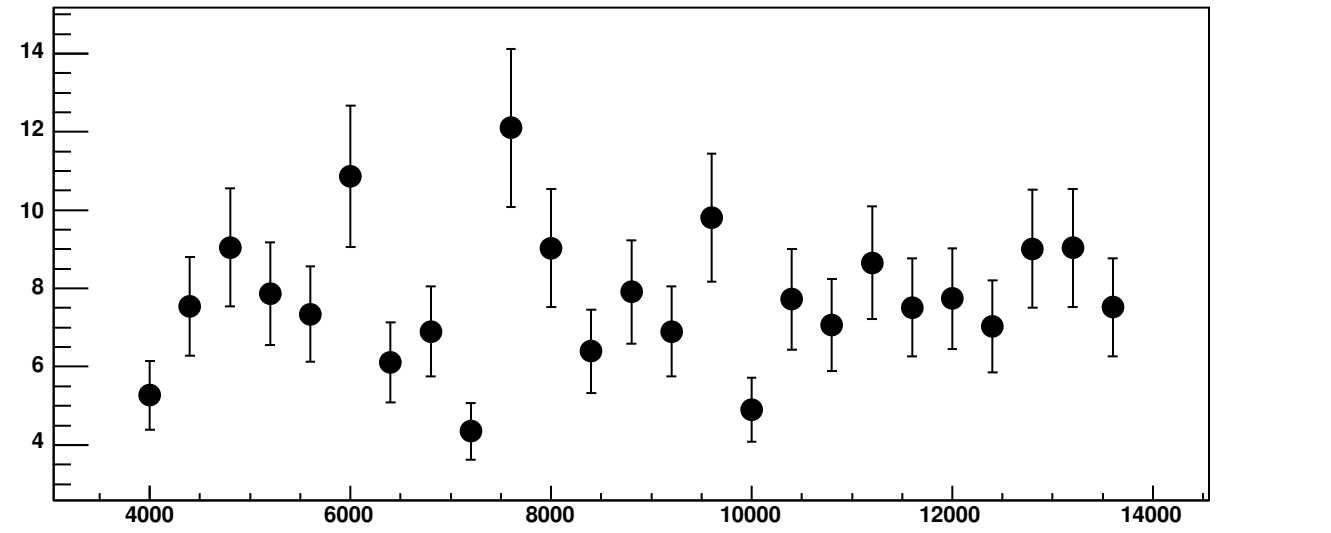

Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

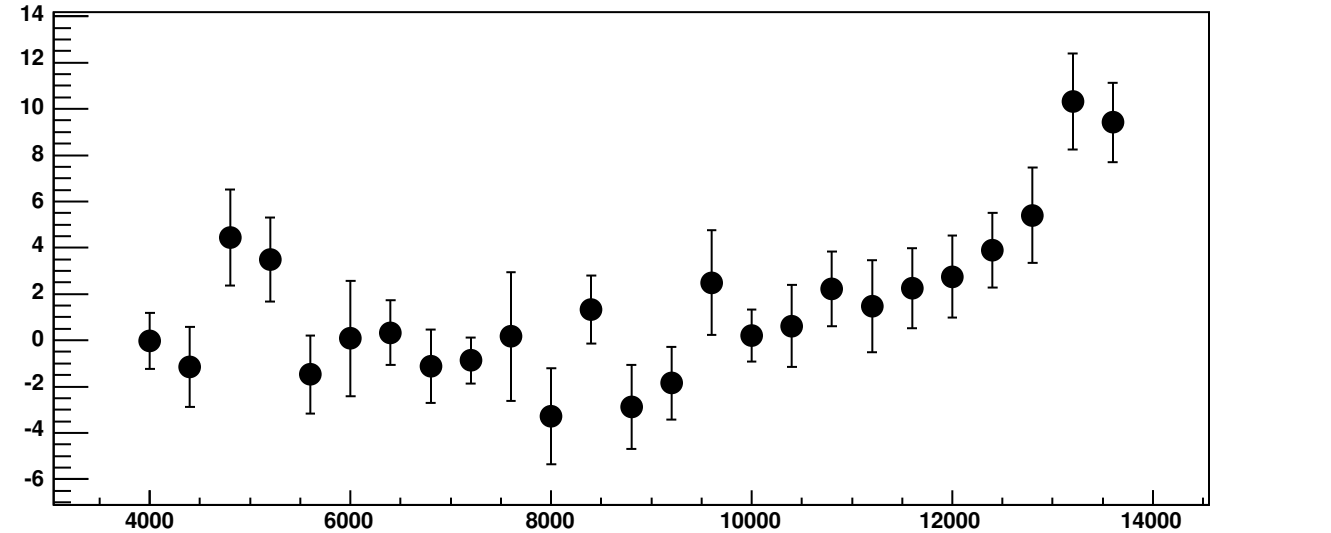
Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

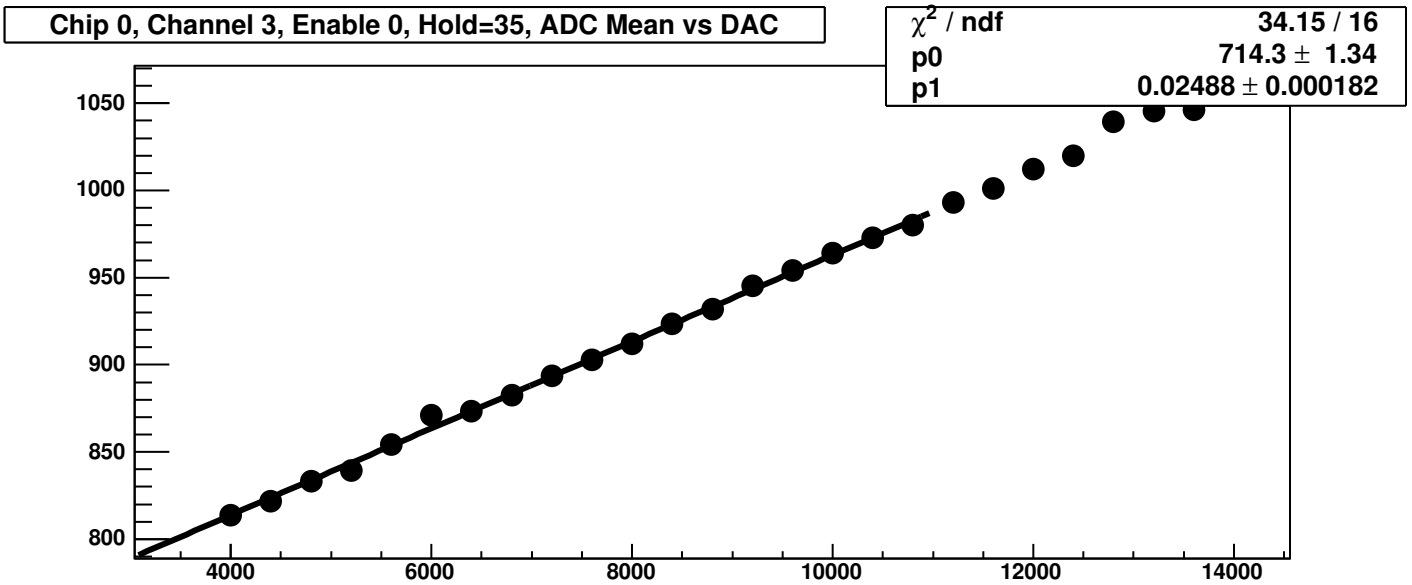


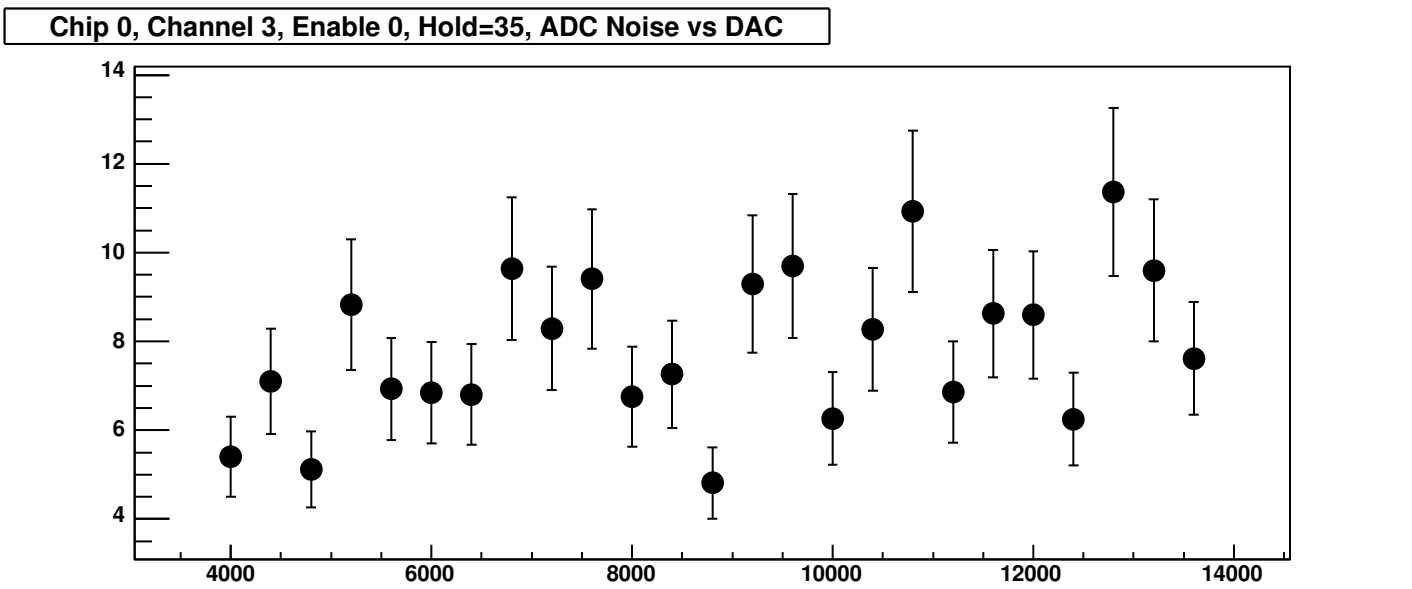
Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

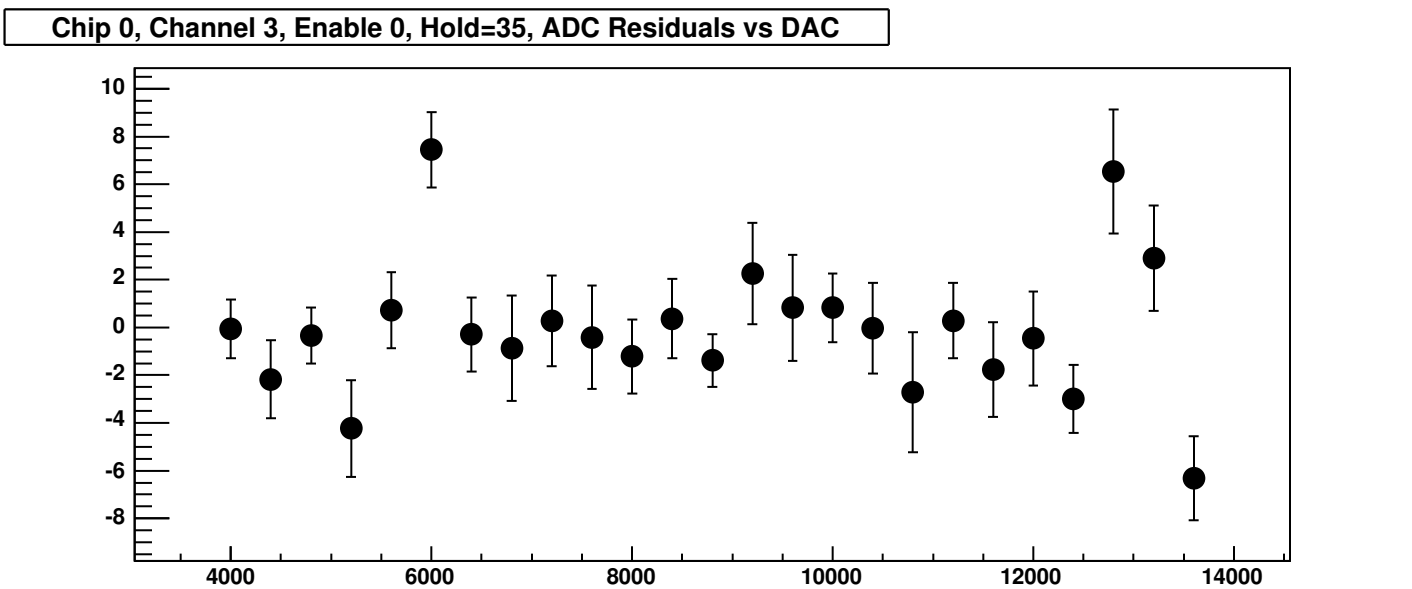


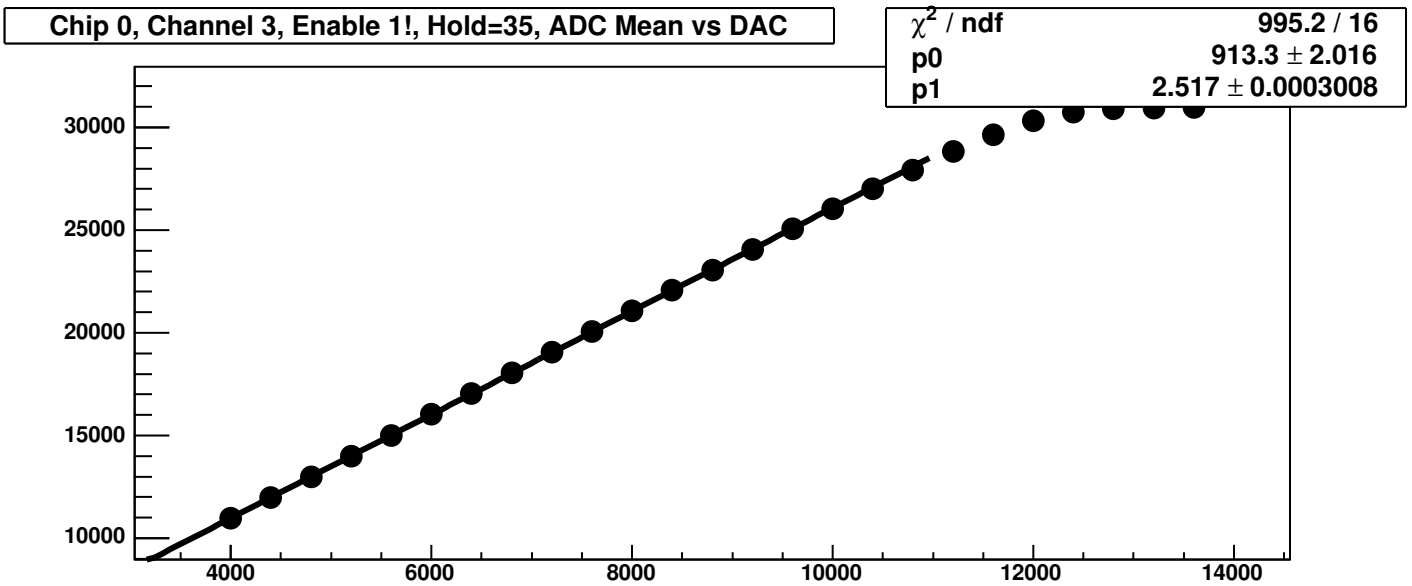
Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

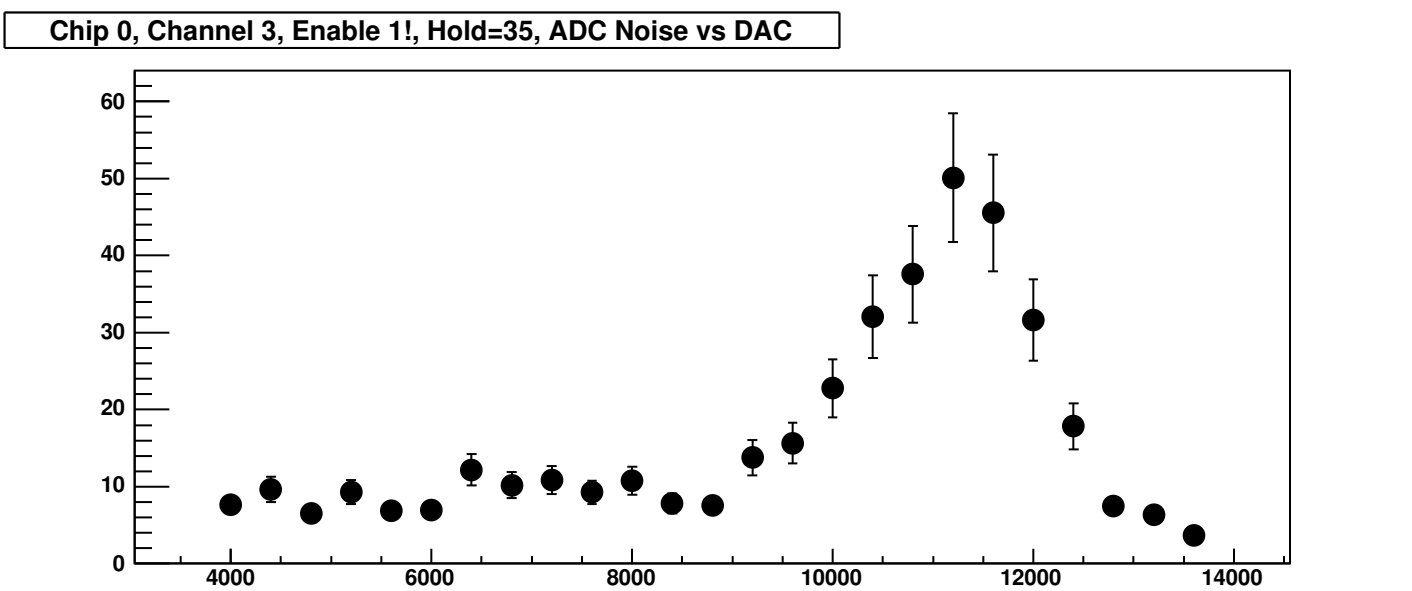


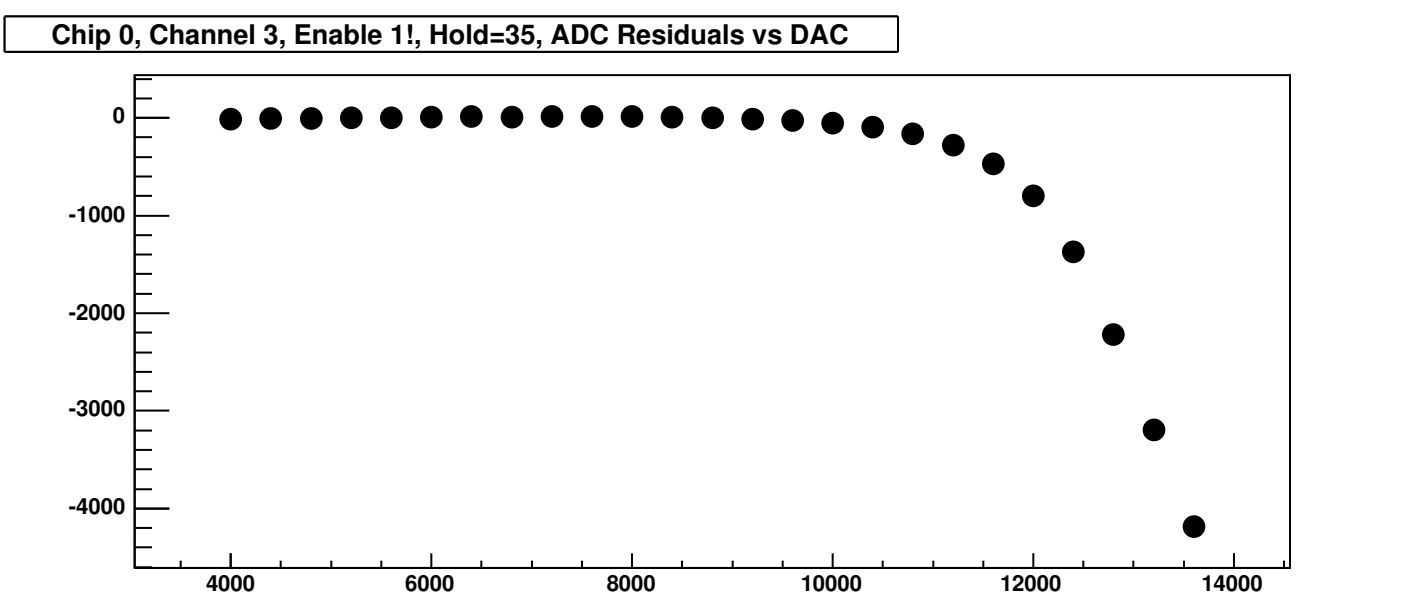

Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 34.15 / 16
p0 714.3 ± 1.34
p1 0.02488 ± 0.000182
1050
1000
950
900
850
800
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

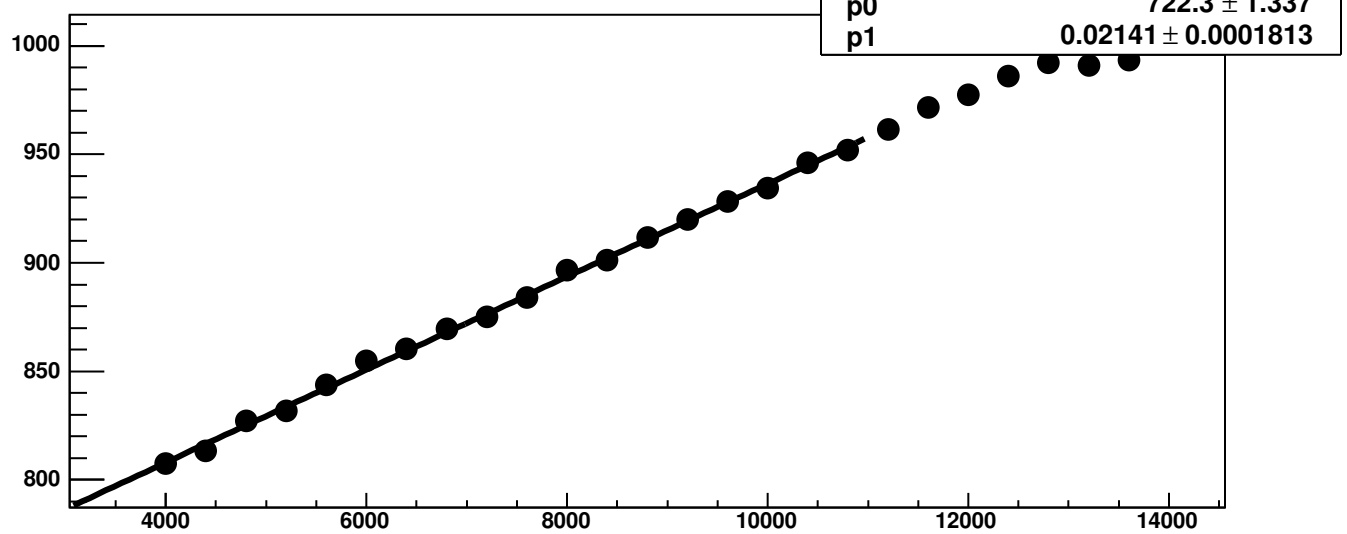

Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 995.2 / 16
p0 913.3 ± 2.016
p1 2.517 ± 0.0003008
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

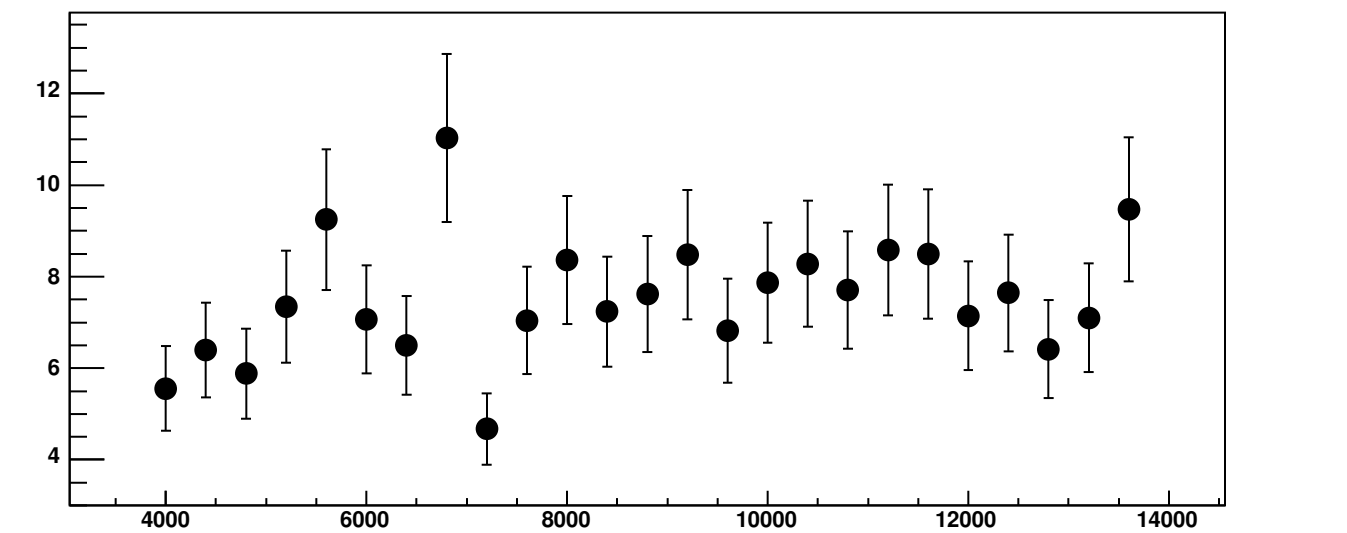

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

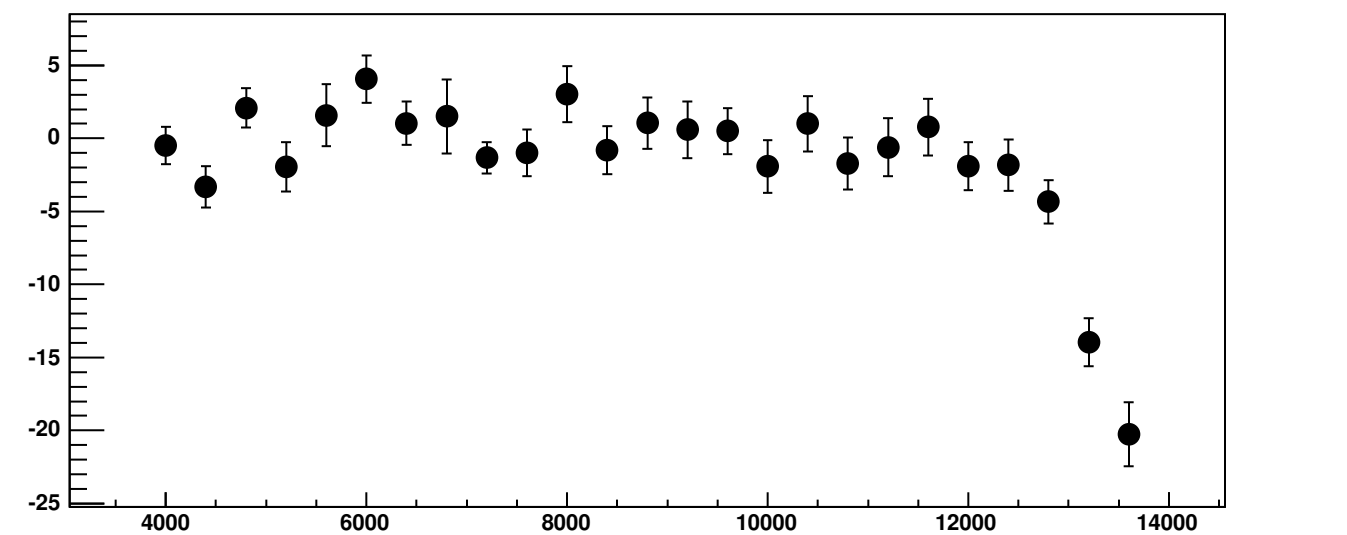
Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

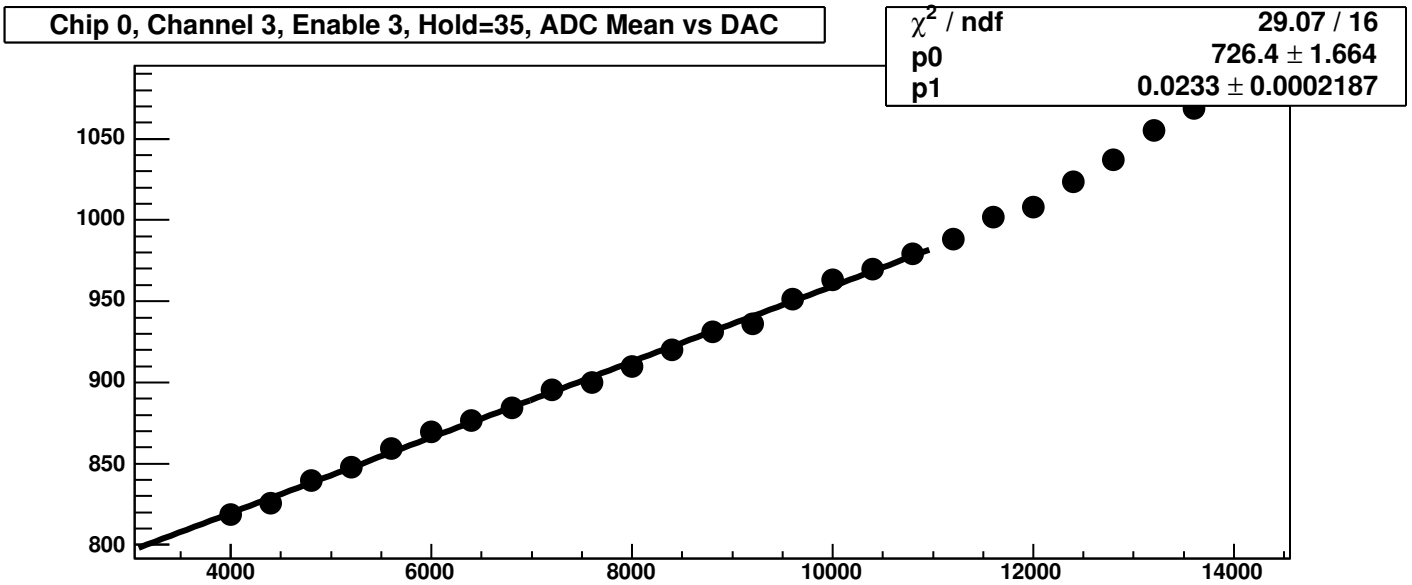


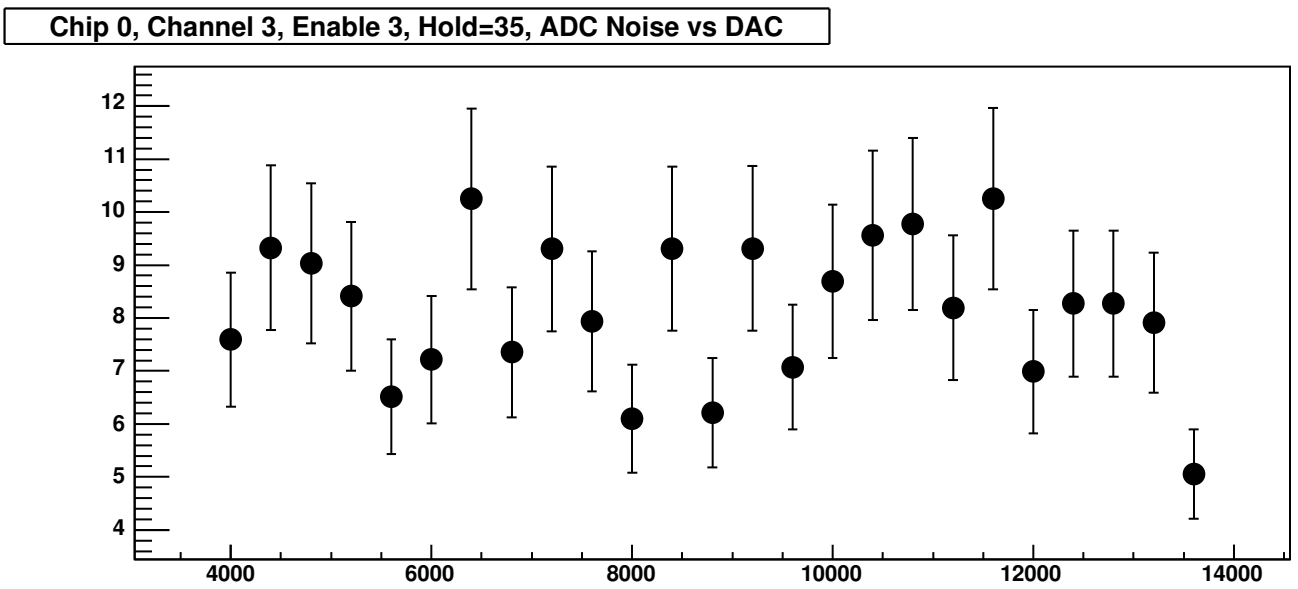
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

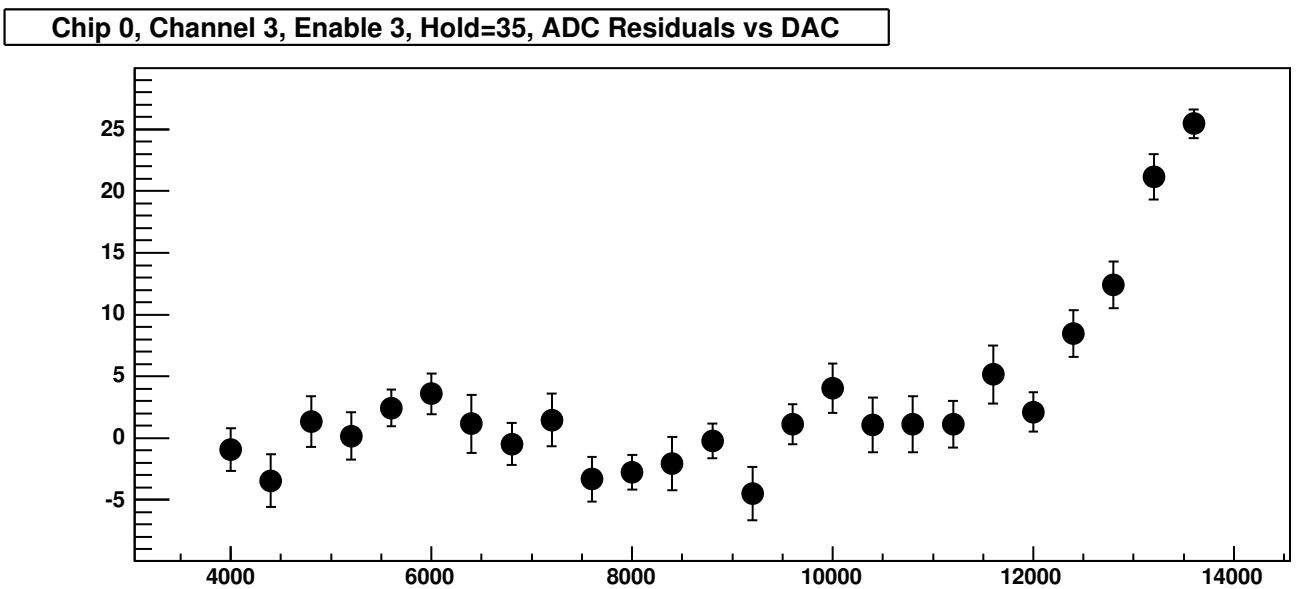


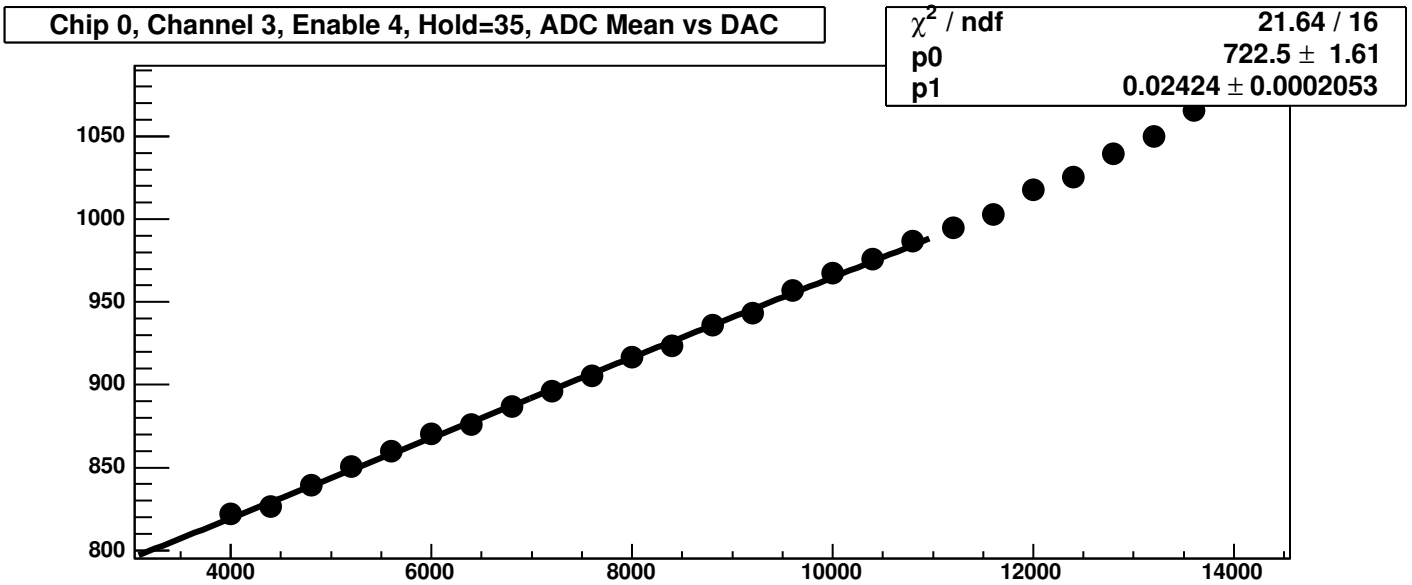
Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

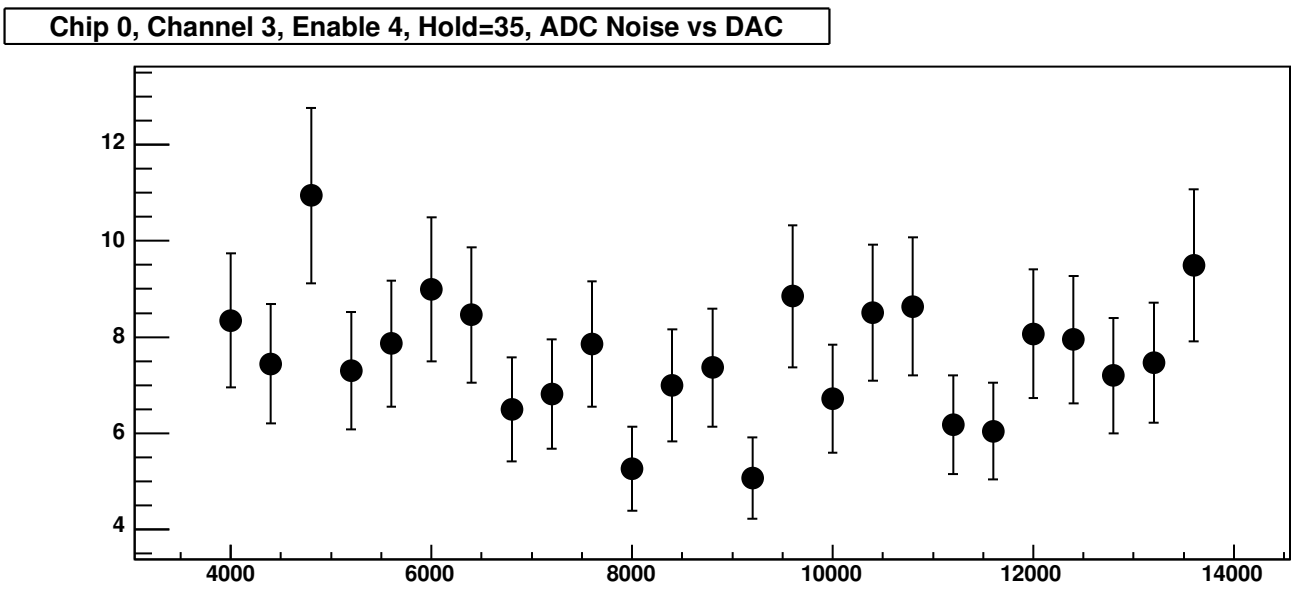


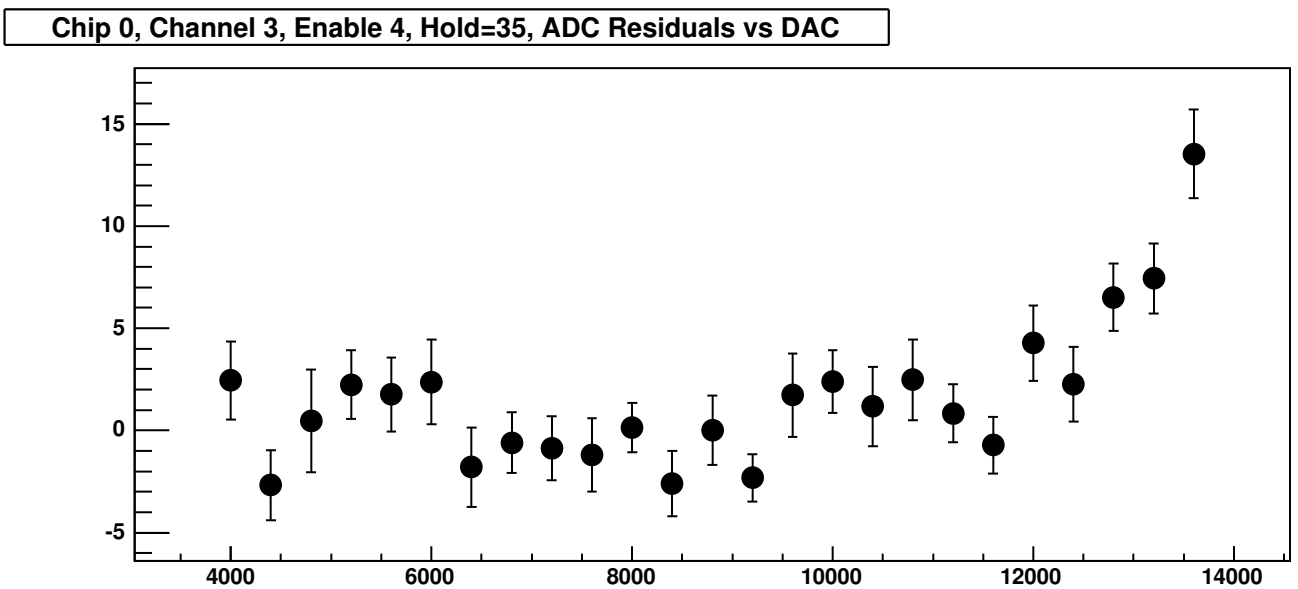

Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.07 / 16
p0 726.4 ± 1.664
p1 0.0233 ± 0.0002187
1050
1000
950
900
850
800
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

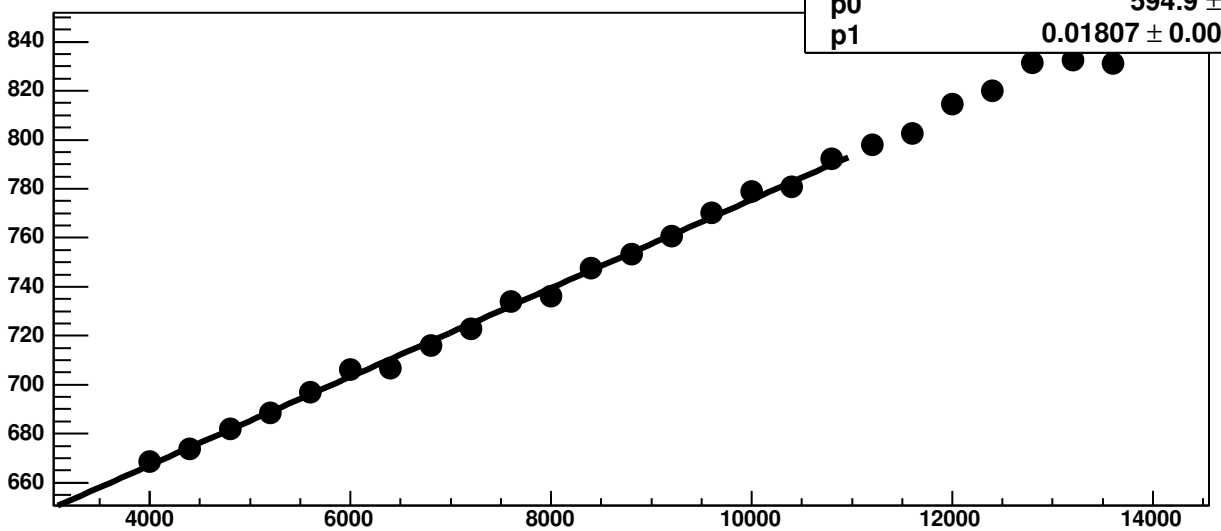

Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.64 / 16
p0 722.5 ± 1.61
p1 0.02424 ± 0.0002053
1050
1000
950
900
850
800
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

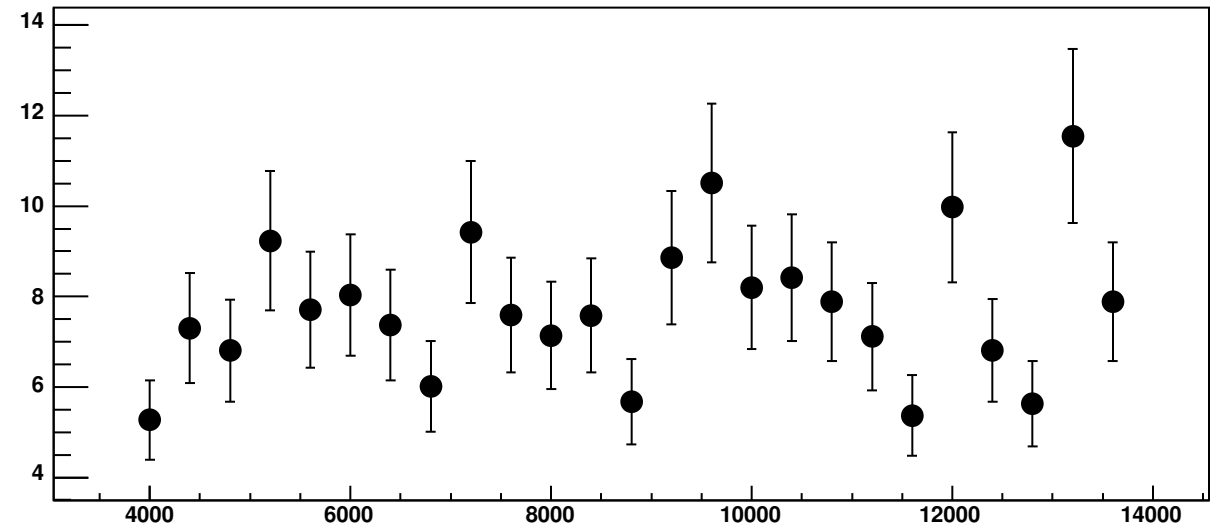

Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

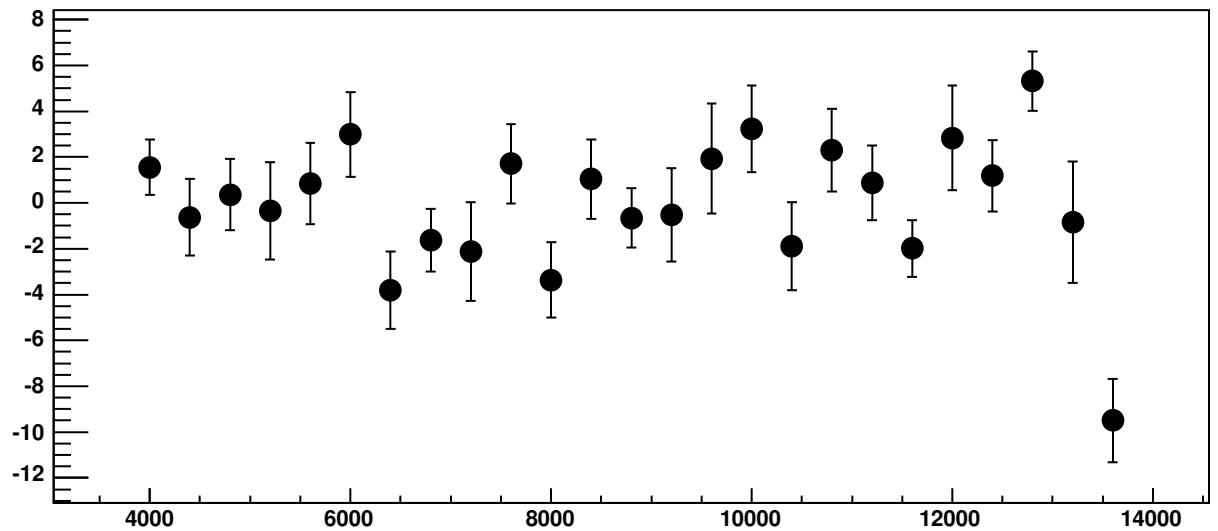
χ^2 / ndf	24.2 / 16
p0	594.9 ± 1.409
p1	0.01807 ± 0.0001899

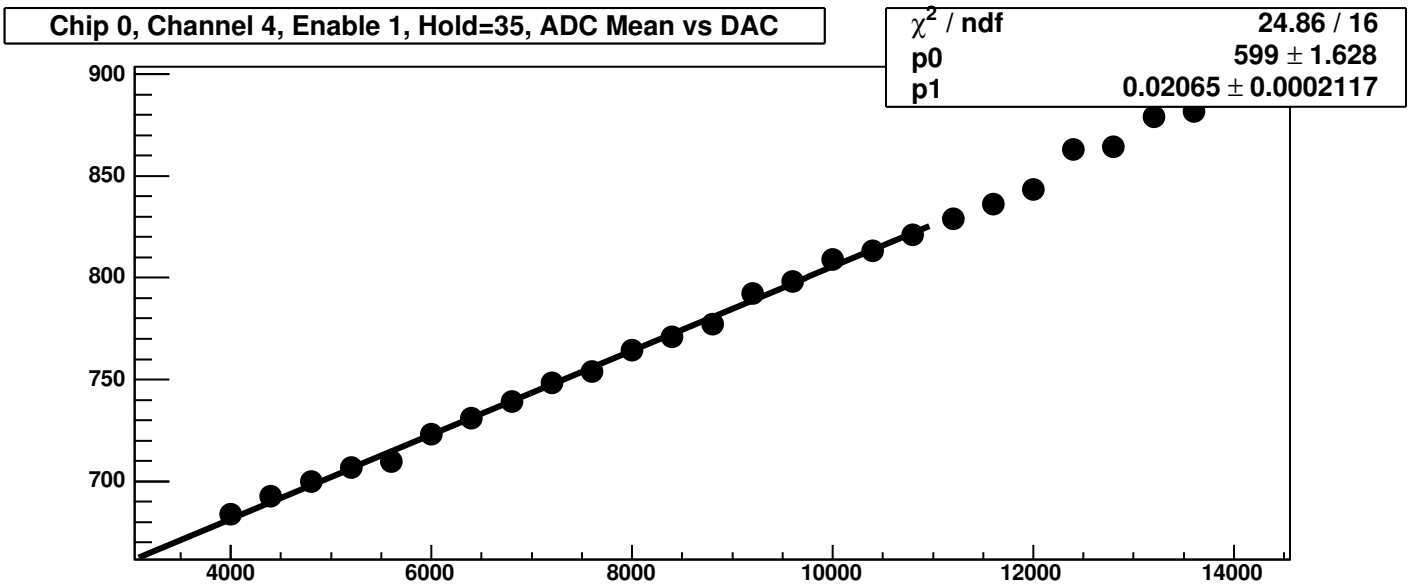


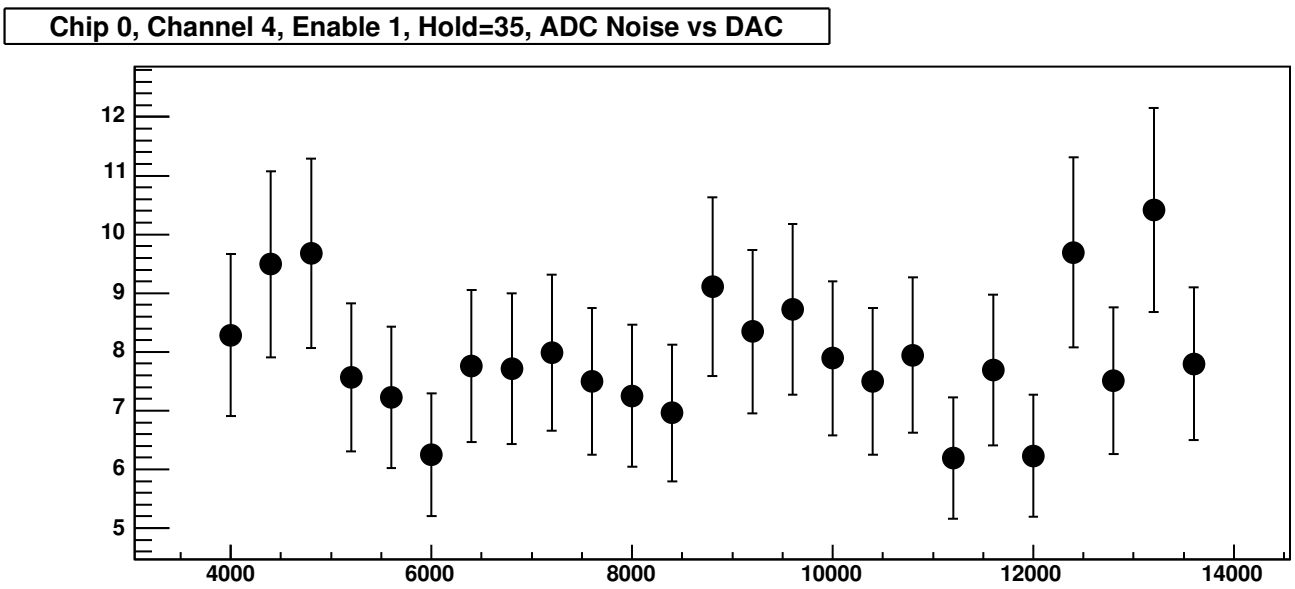
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

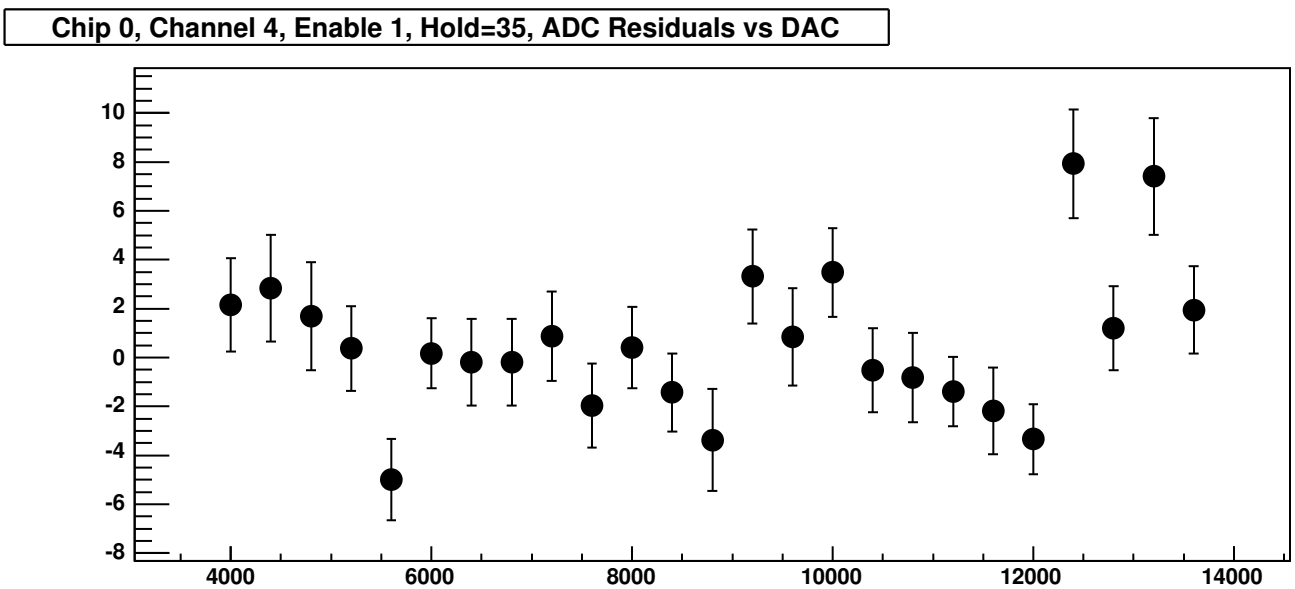


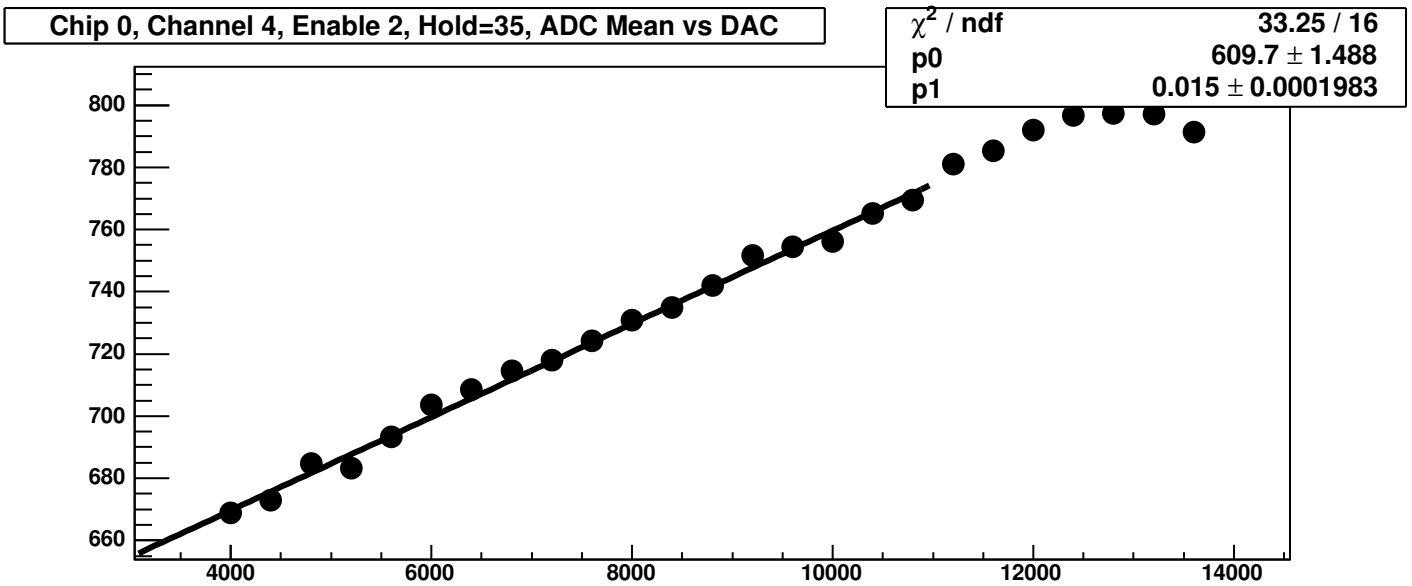
Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

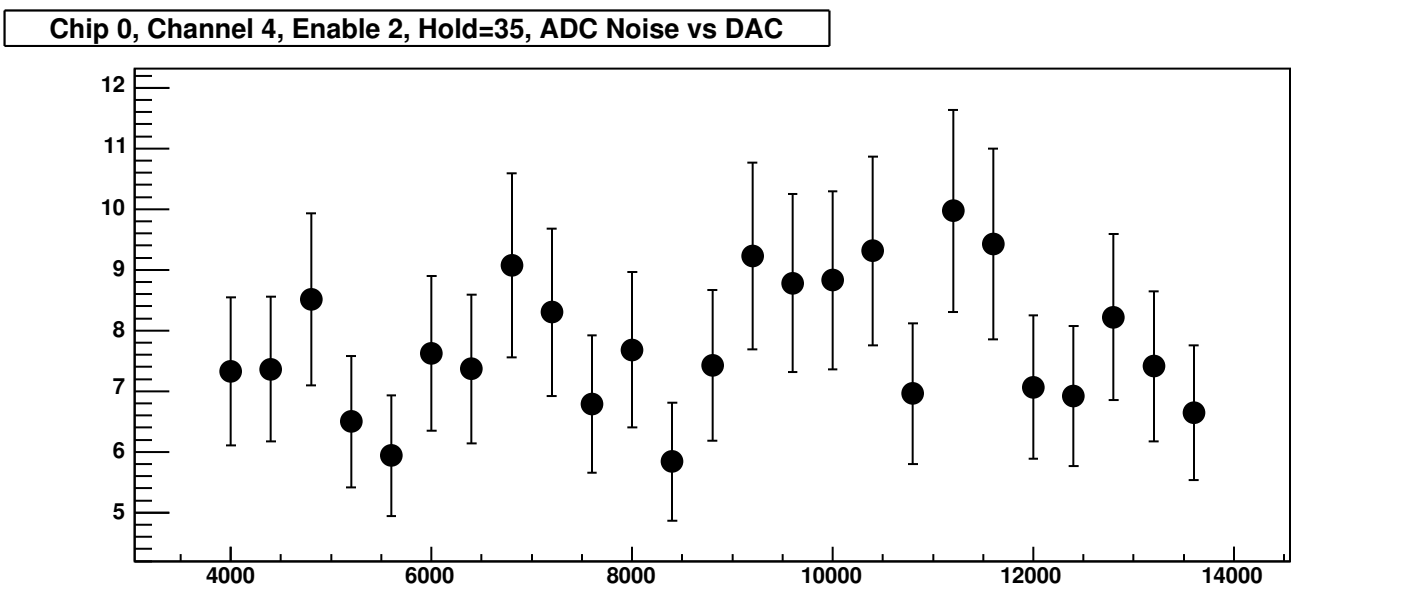


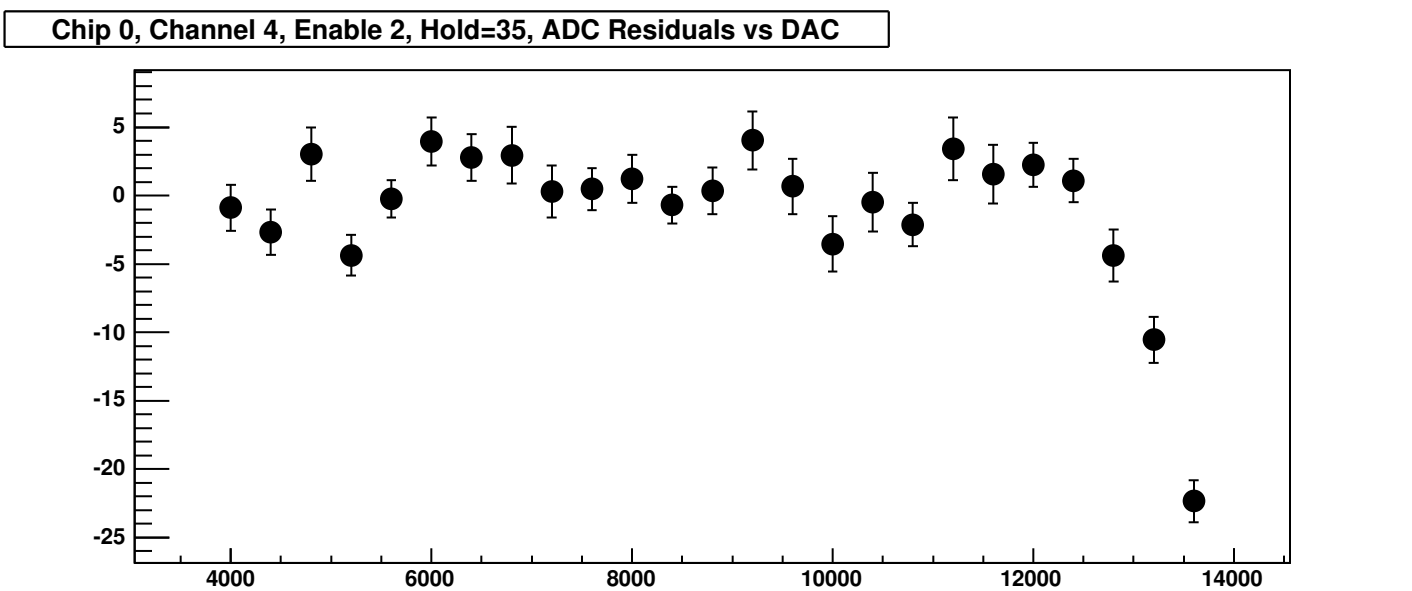

Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.86 / 16
p0 599 ± 1.628
p1 0.02065 ± 0.0002117
900
850
800
750
700
4000
6000
8000
10000
12000
14000

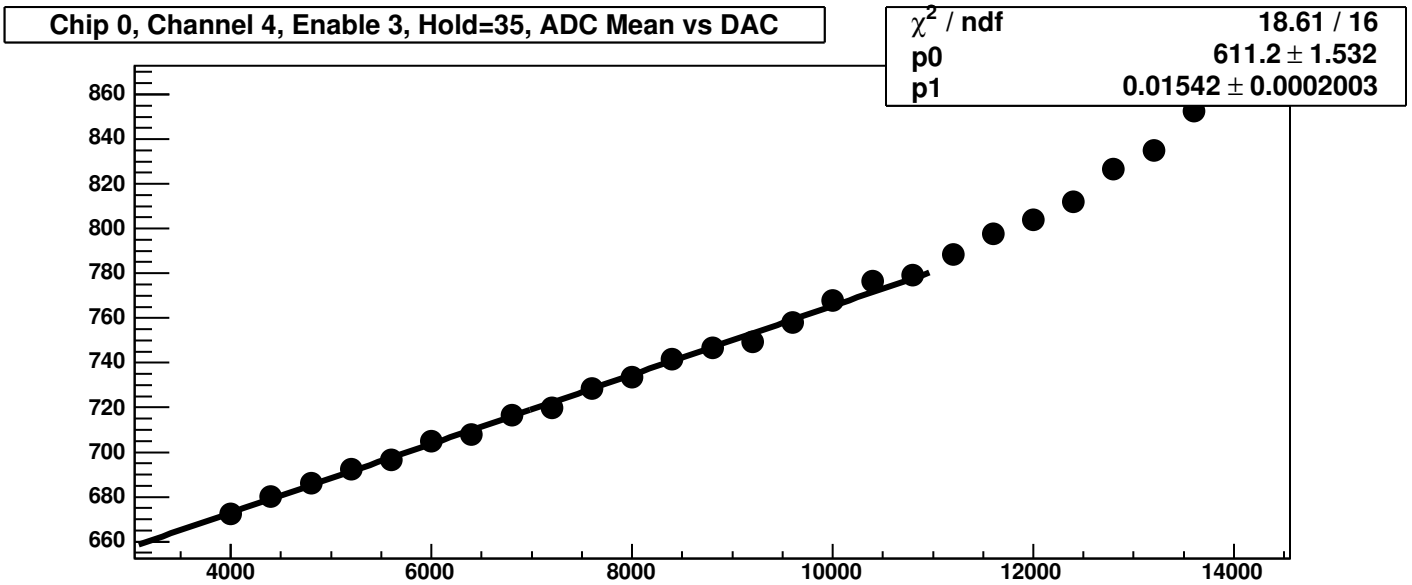

Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

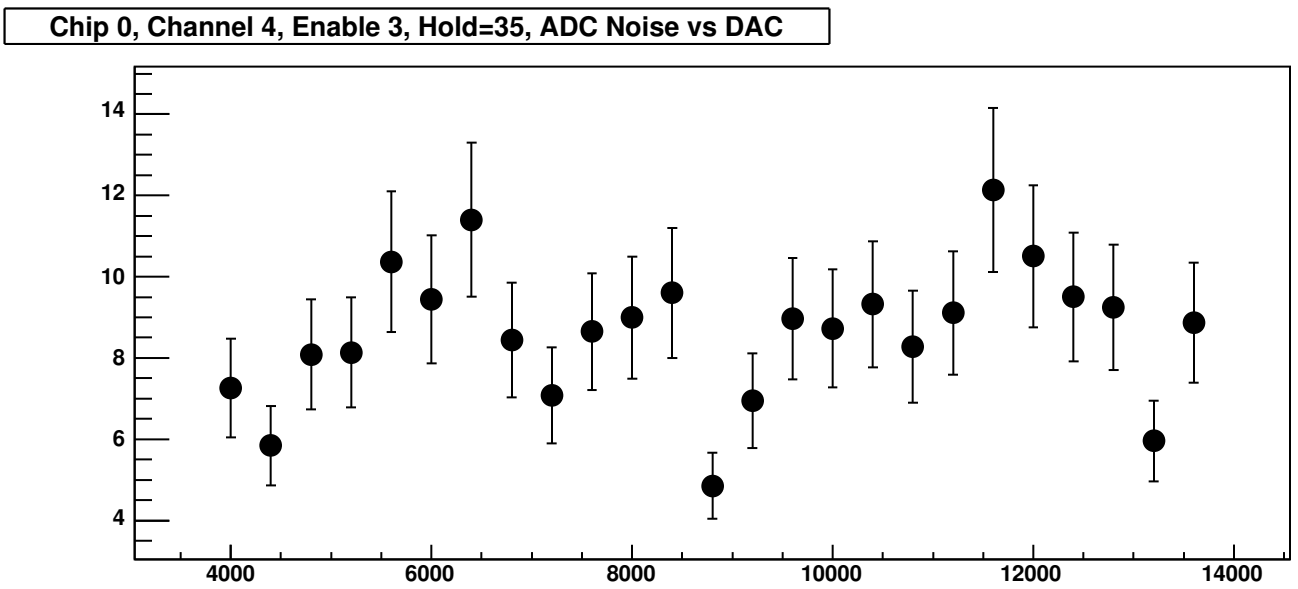

Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

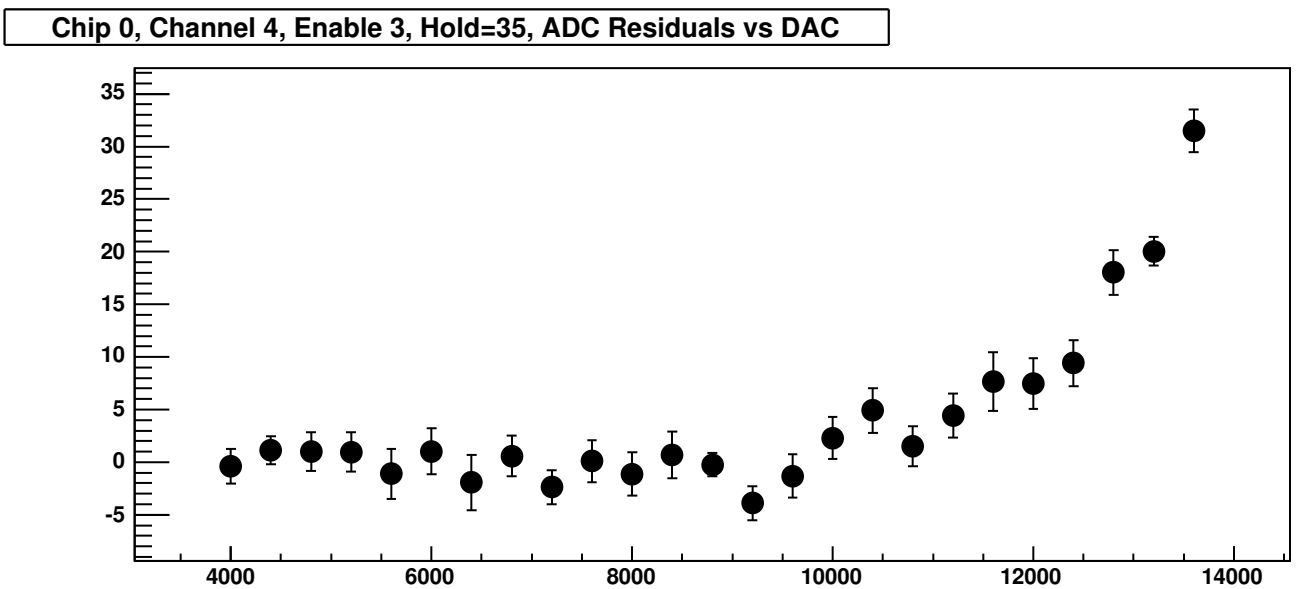

Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.25 / 16
p0 609.7 ± 1.488
p1 0.015 ± 0.0001983
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

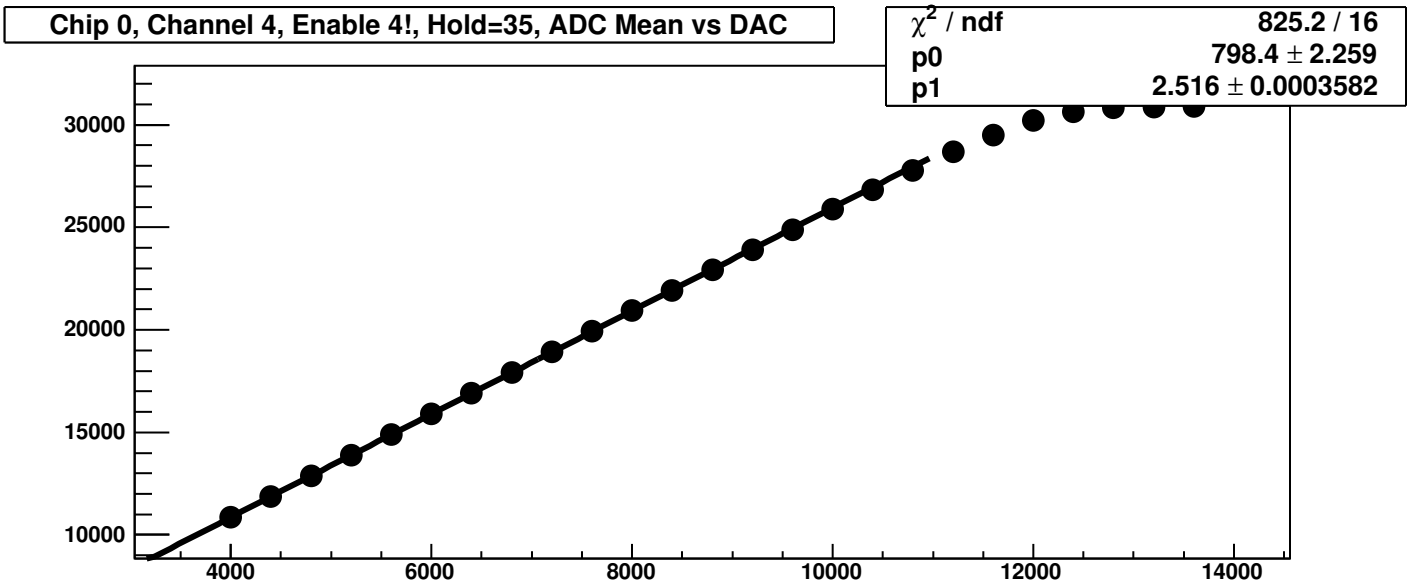

Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

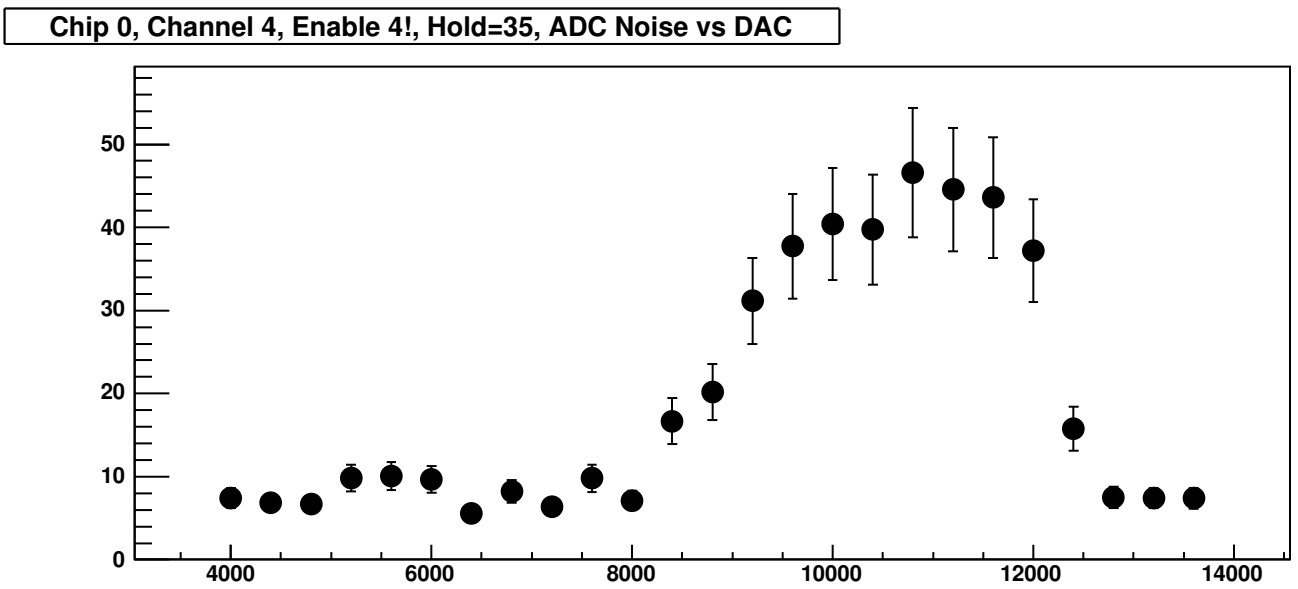

Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

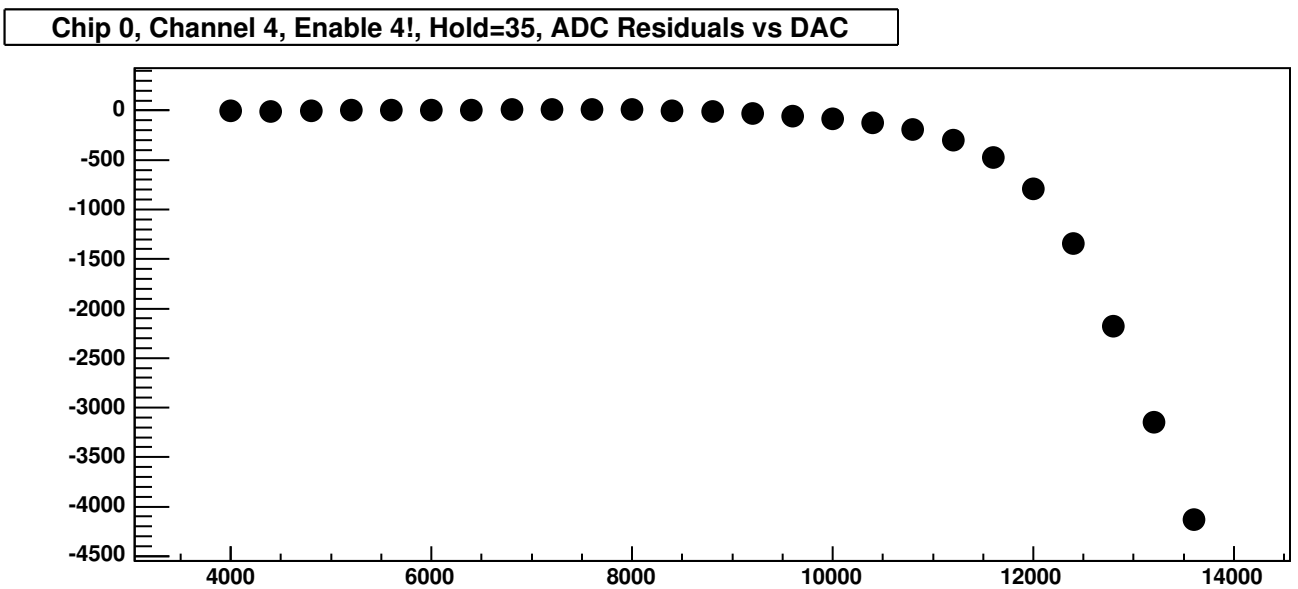

Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.61 / 16
p0 611.2 ± 1.532
p1 0.01542 ± 0.0002003
860
840
820
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

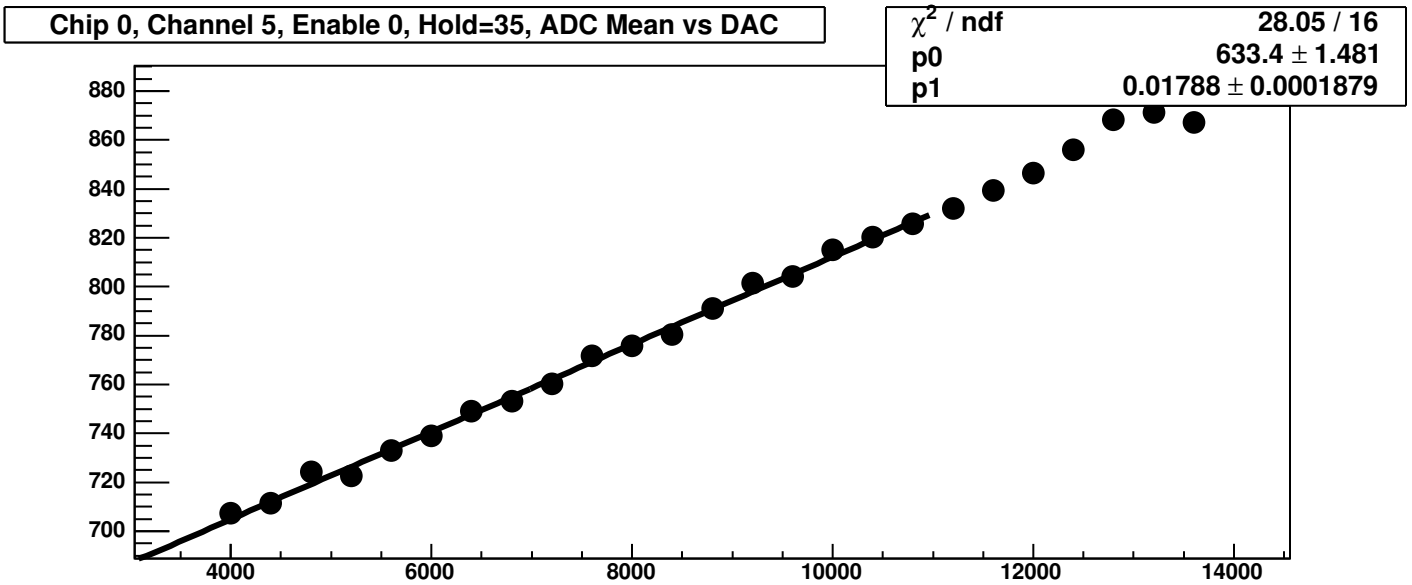

Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

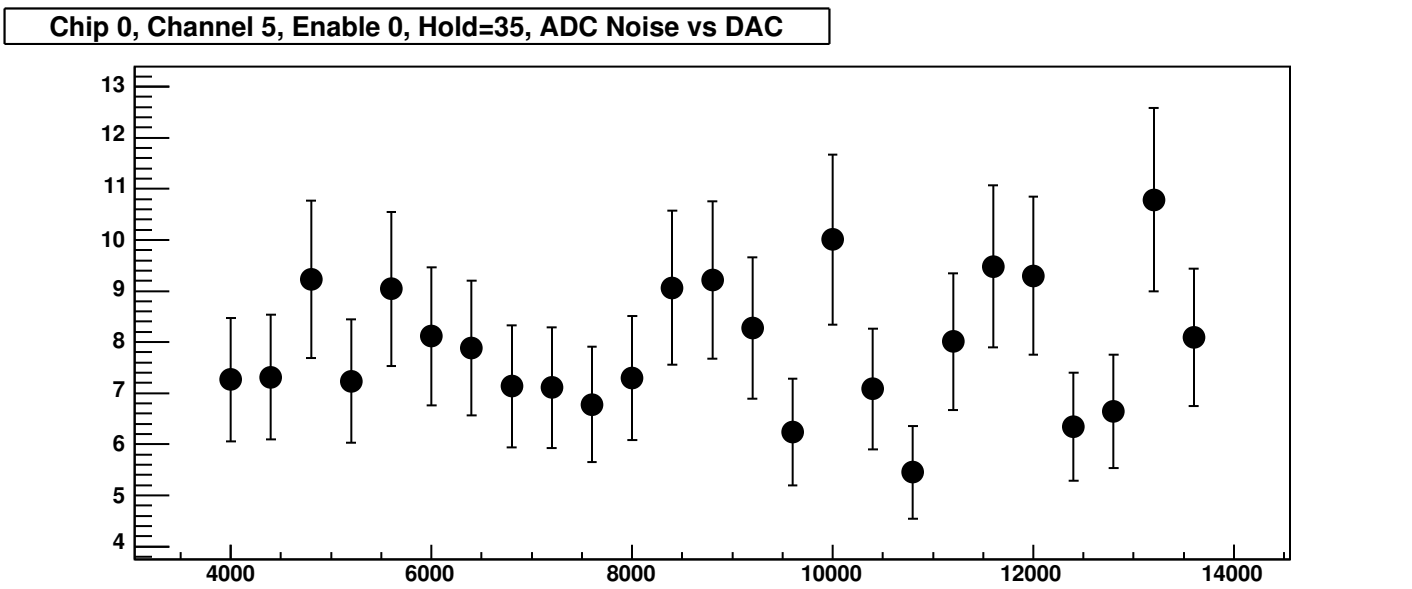

Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

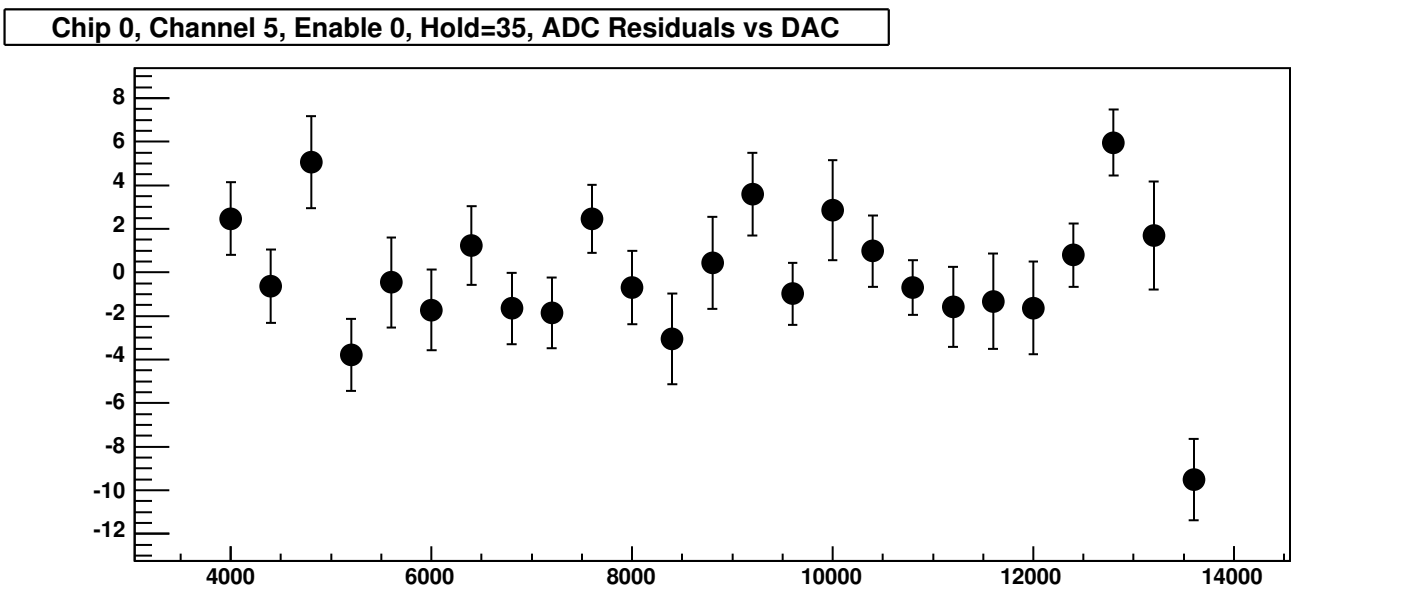

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 825.2 / 16
p0 798.4 ± 2.259
p1 2.516 ± 0.0003582
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

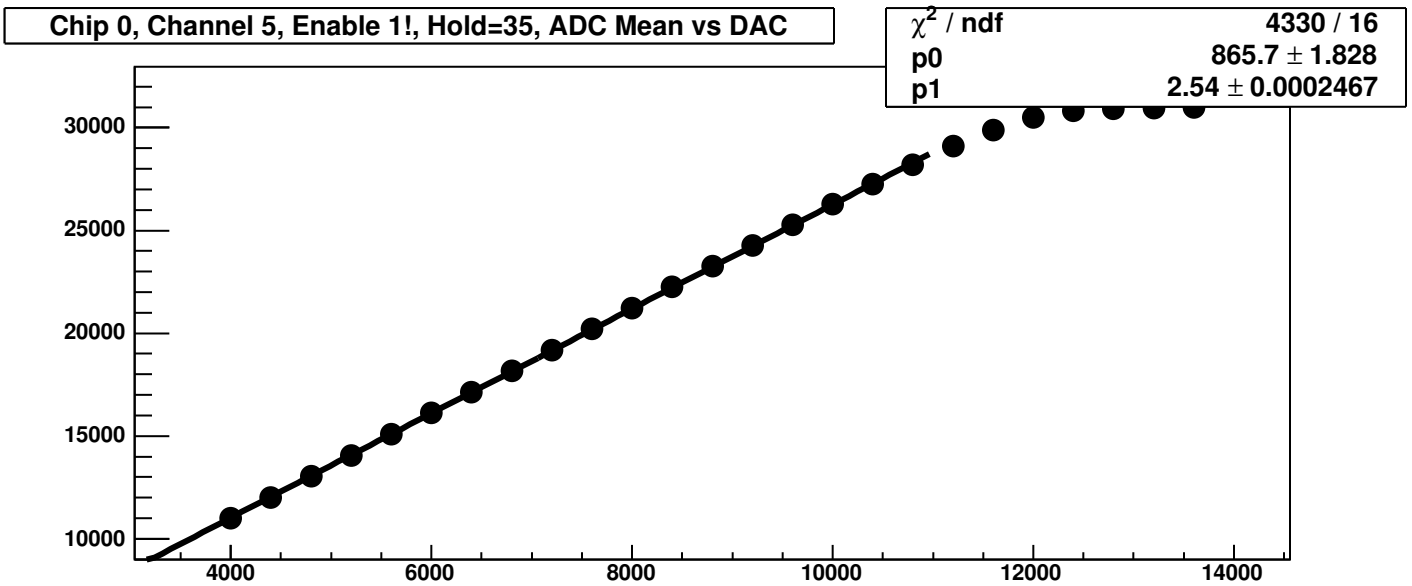

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

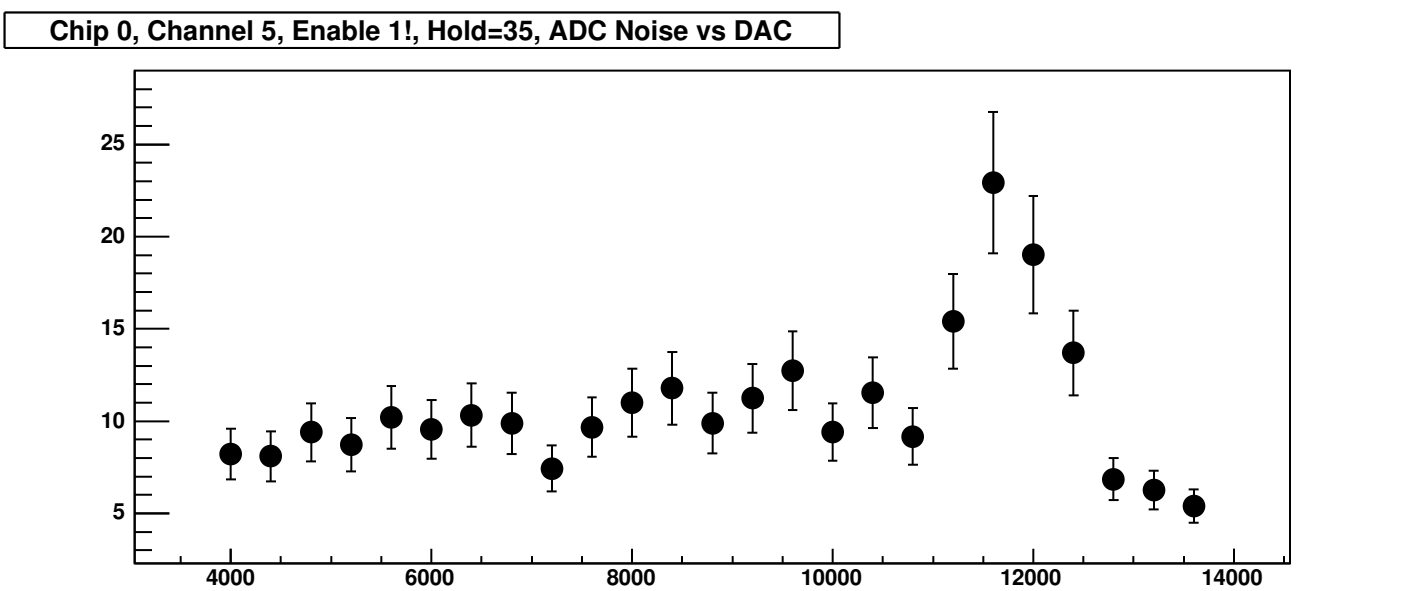

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000

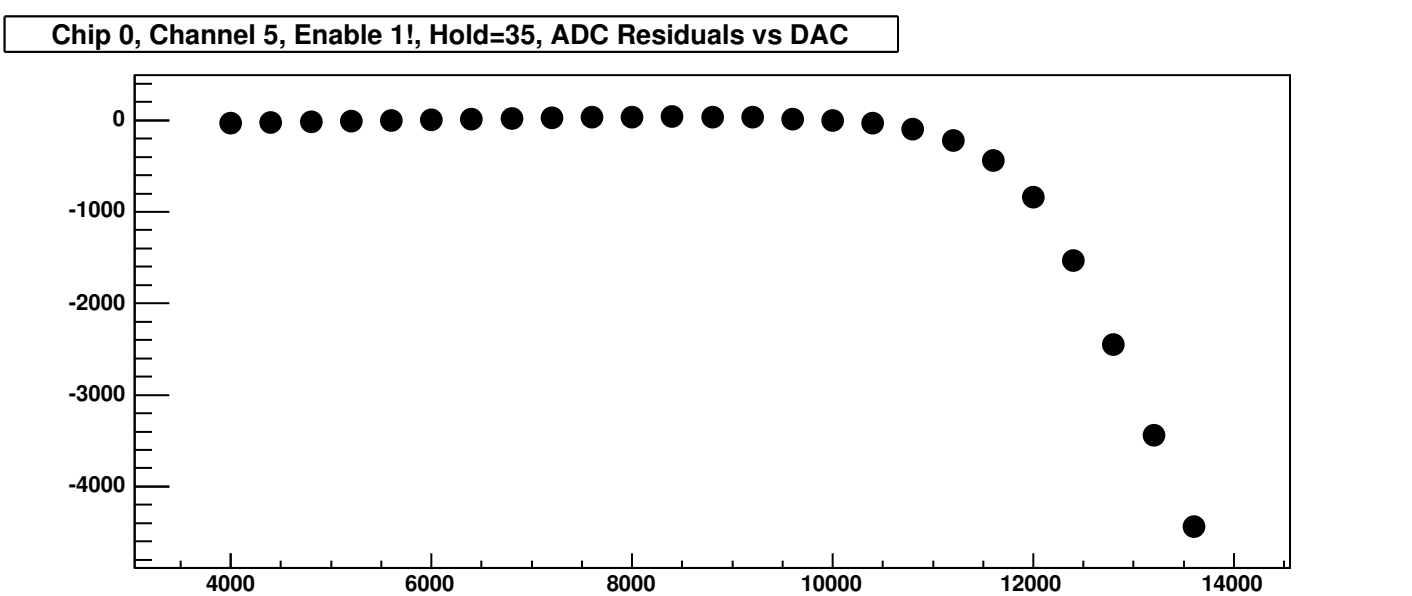

Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 28.05 / 16
p0 633.4 ± 1.481
p1 0.01788 ± 0.0001879
880
860
840
820
800
780
760
740
720
700
4000
6000
8000
10000
12000
14000

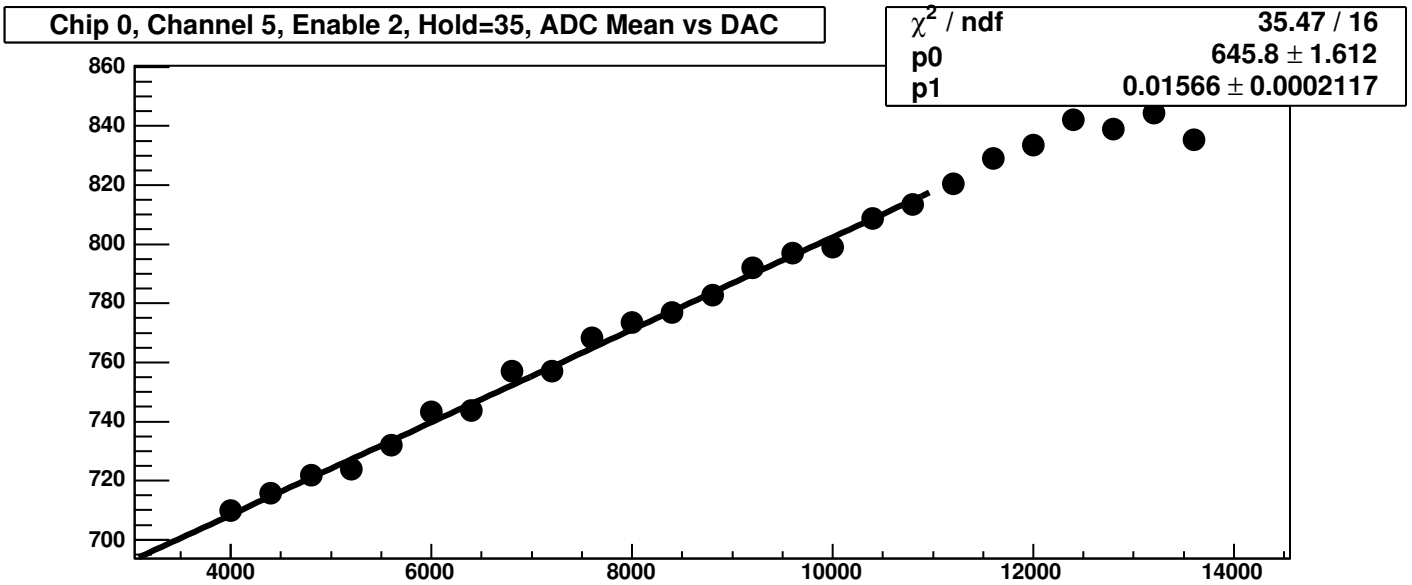

Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

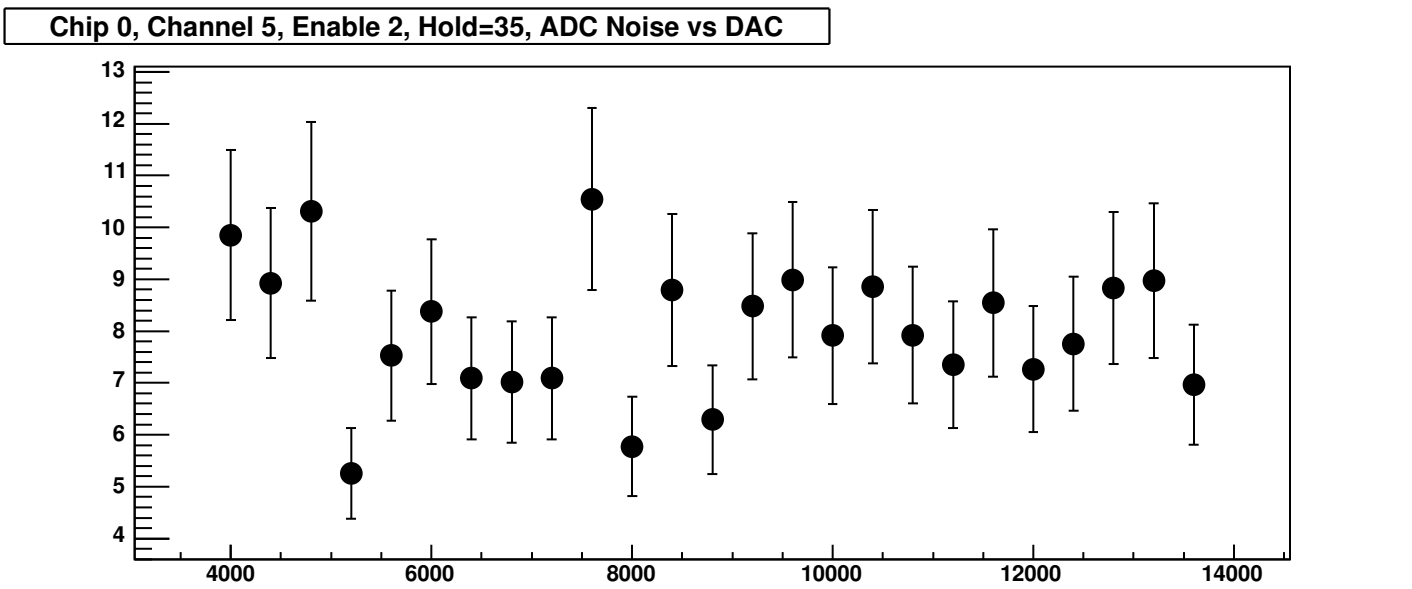

Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000

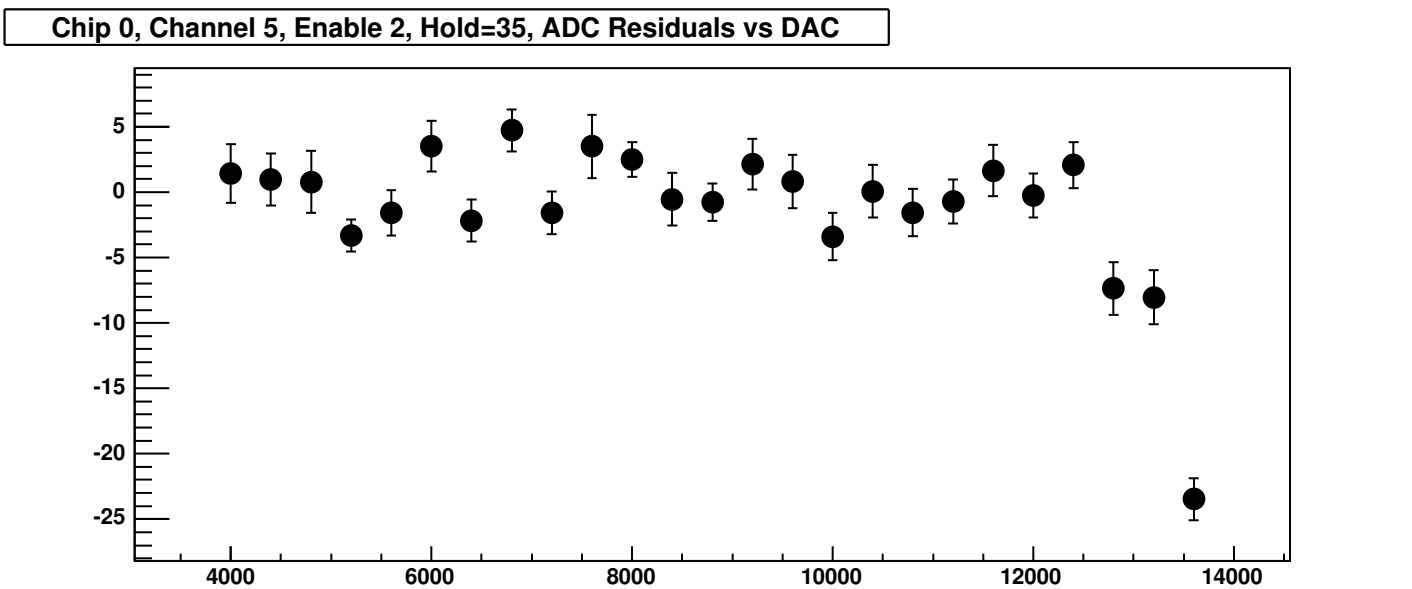

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 4330 / 16
p0 865.7 ± 1.828
p1 2.54 ± 0.0002467
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
4000
6000
8000
10000
12000
14000

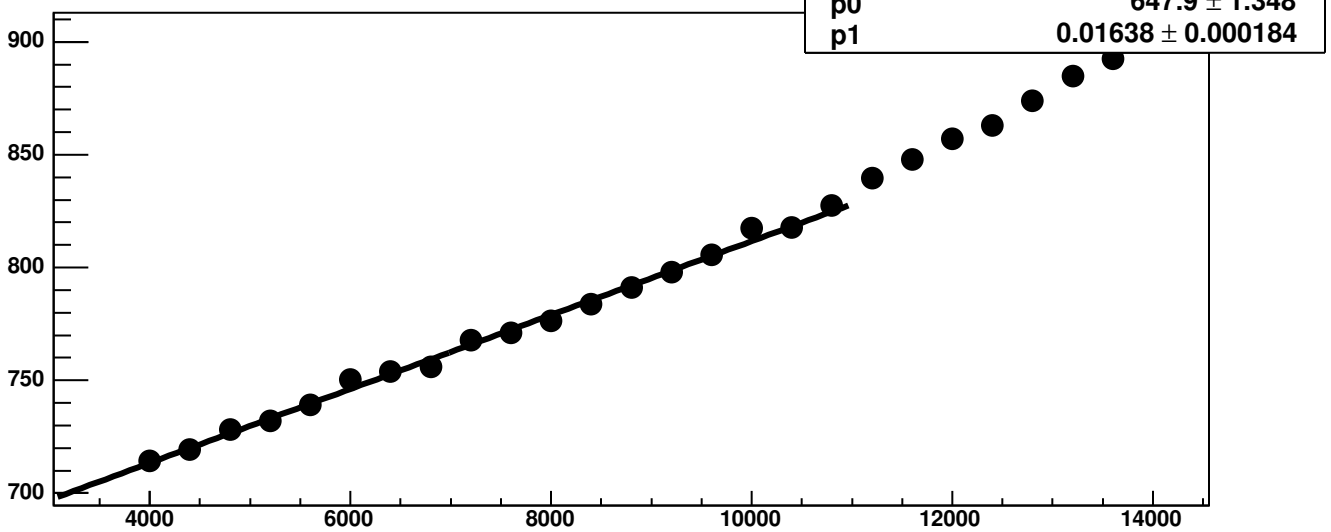

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.47 / 16
p0 645.8 ± 1.612
p1 0.01566 ± 0.0002117
860
840
820
800
780
760
740
720
700
4000
6000
8000
10000
12000
14000

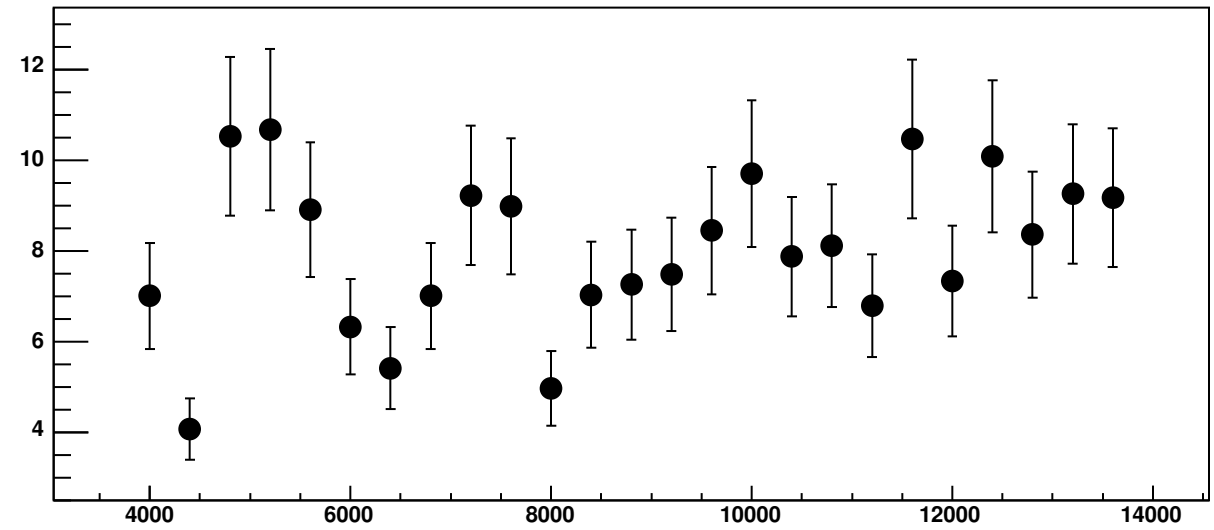

Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

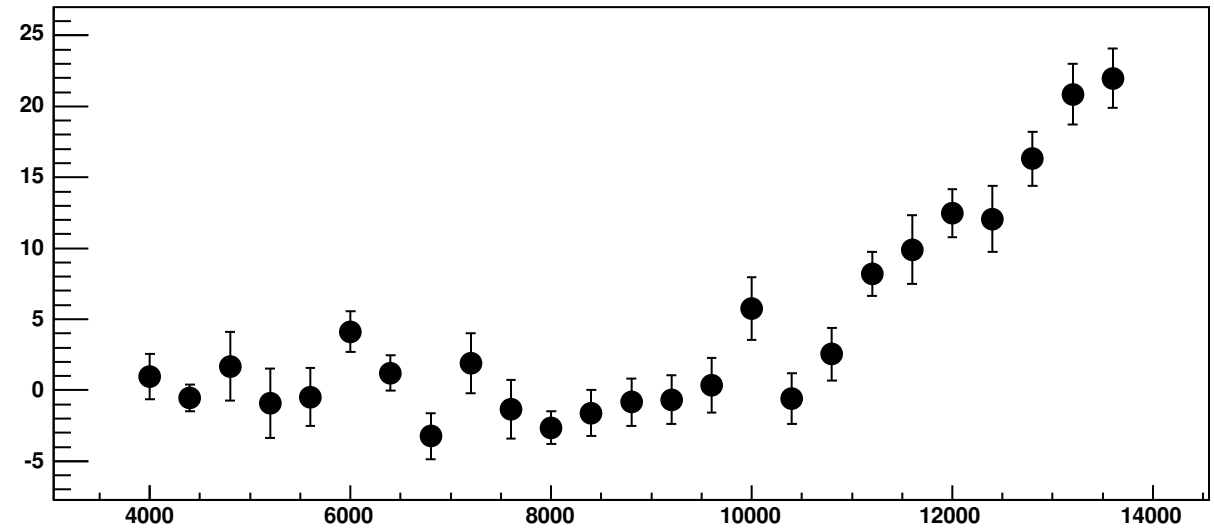
Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

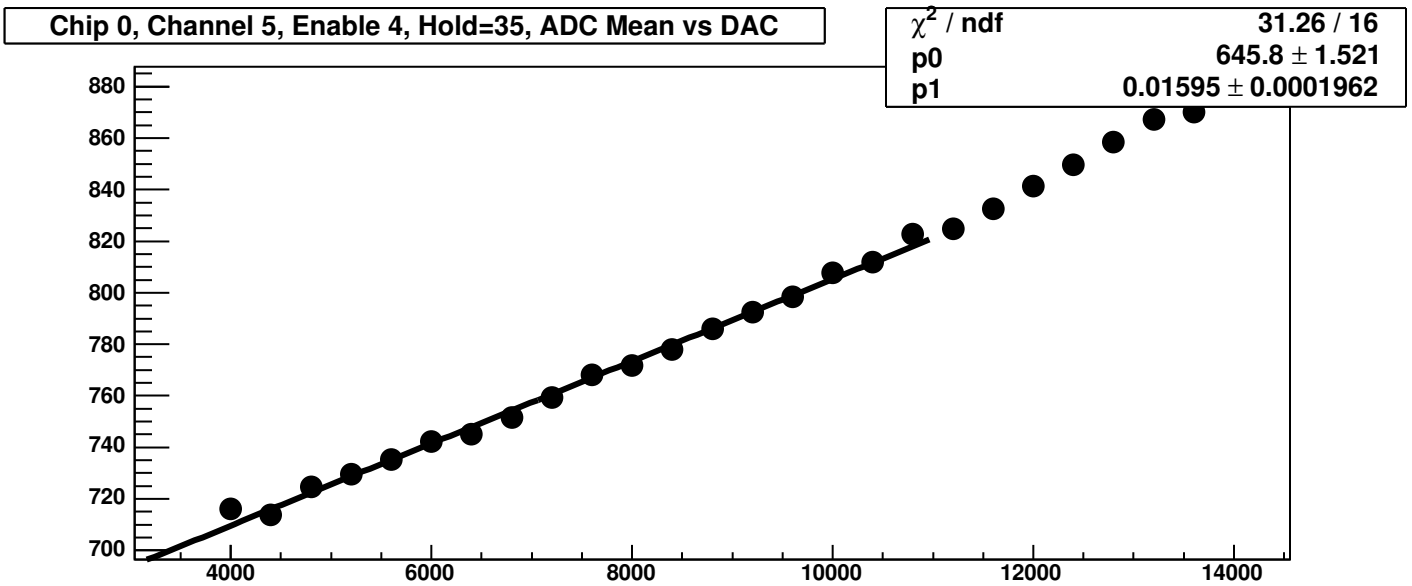


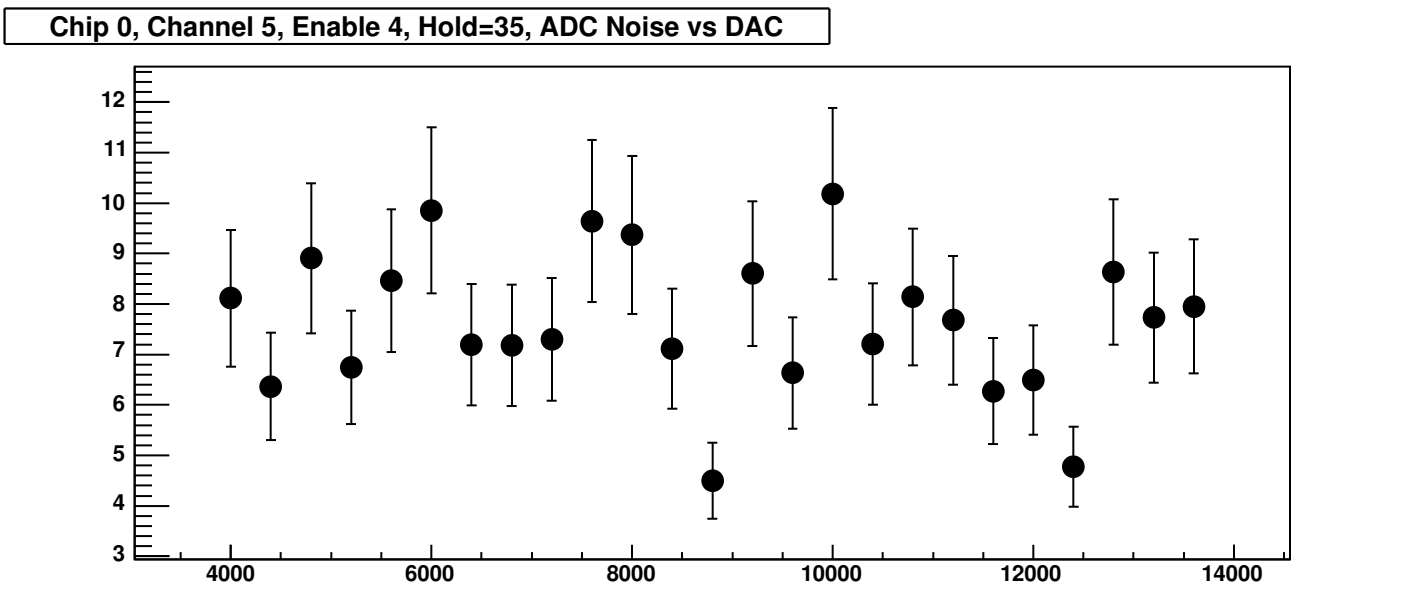
Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

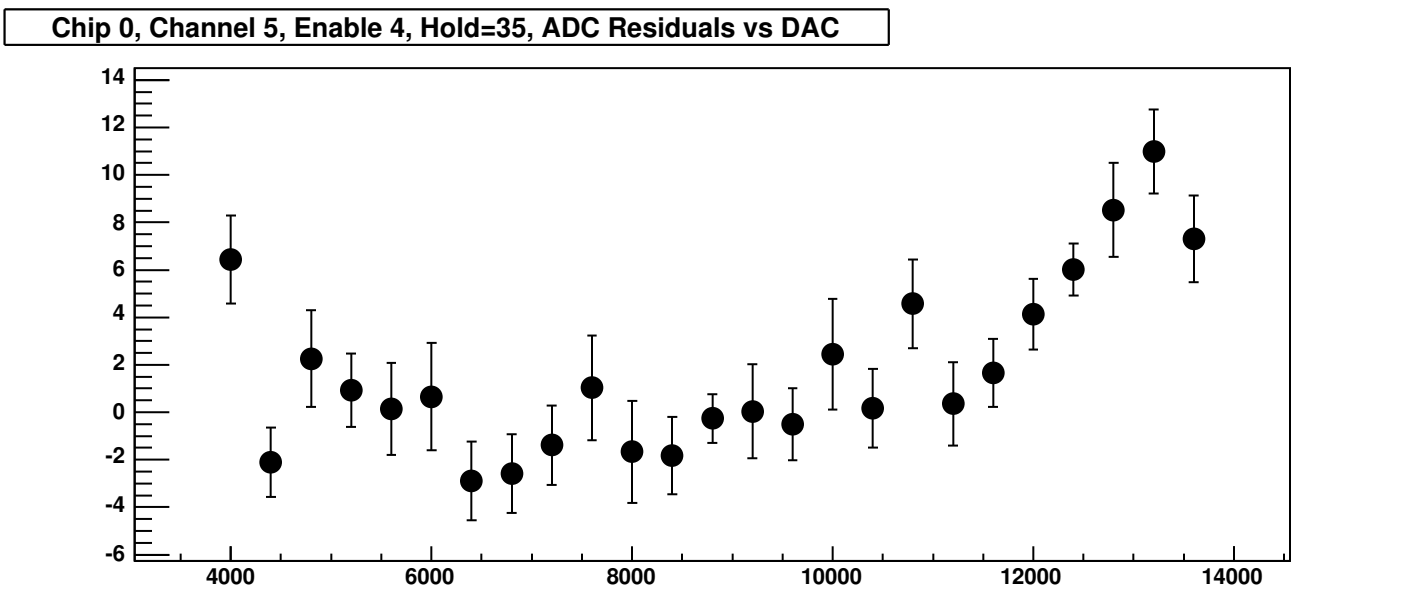


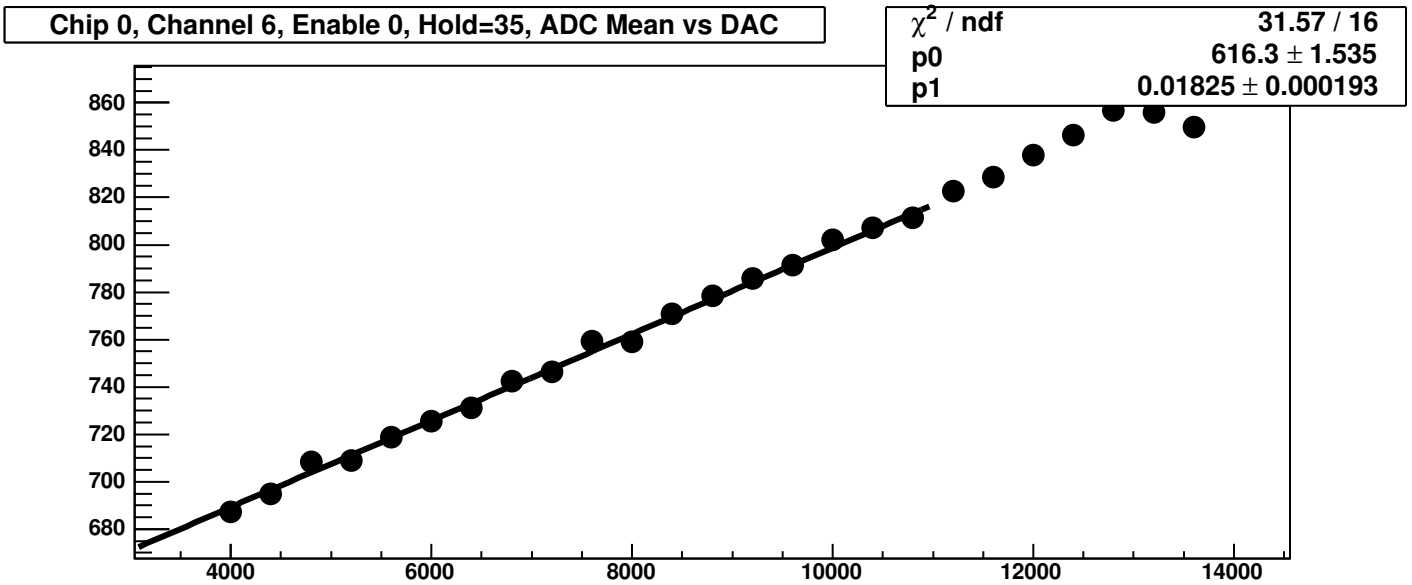
Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

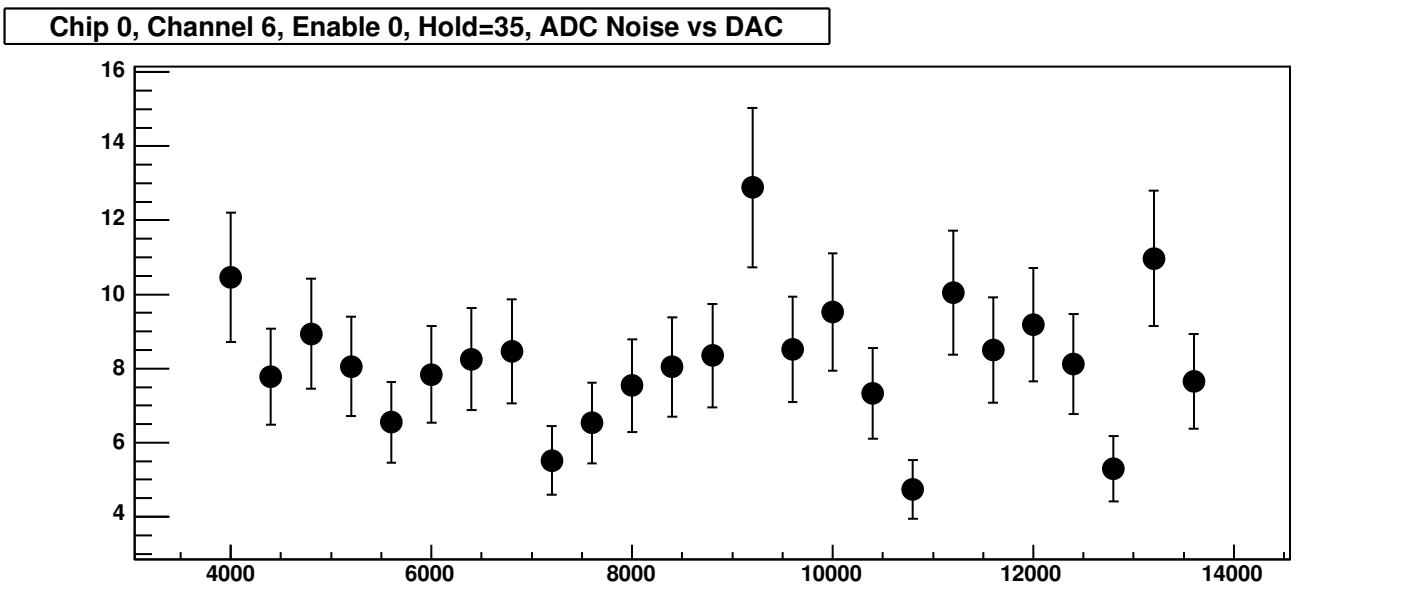


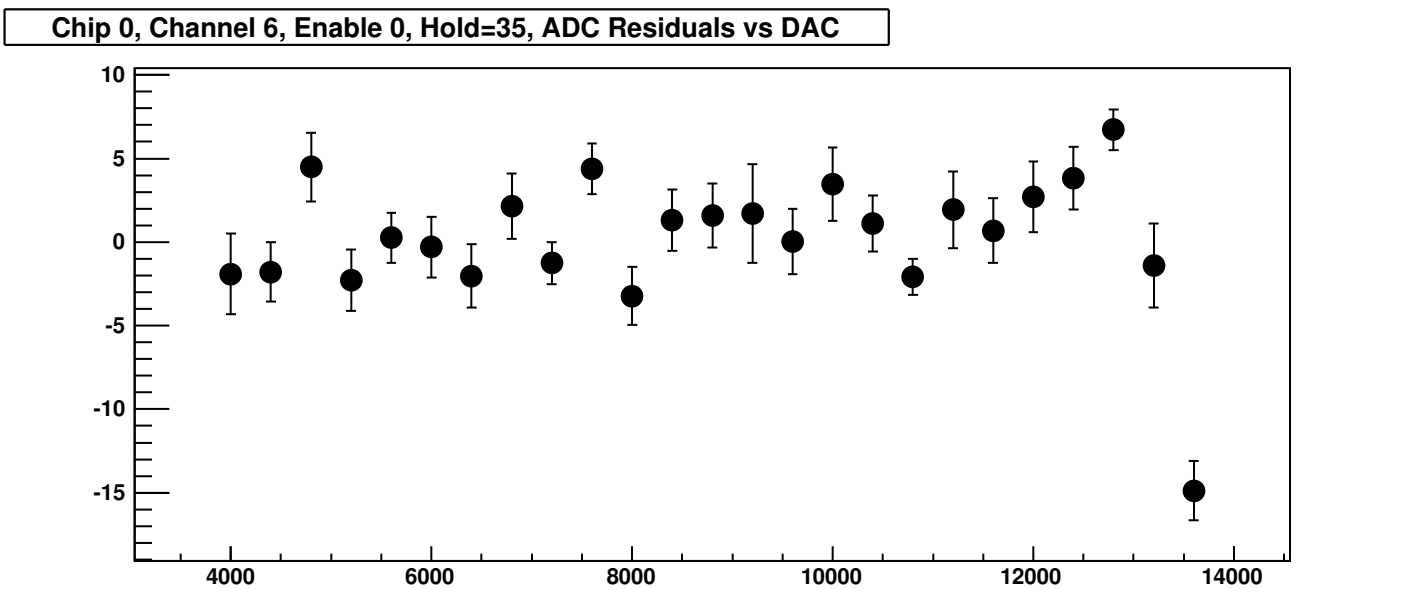

Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.26 / 16
p0 645.8 ± 1.521
p1 0.01595 ± 0.0001962
880
860
840
820
800
780
760
740
720
700
4000
6000
8000
10000
12000
14000

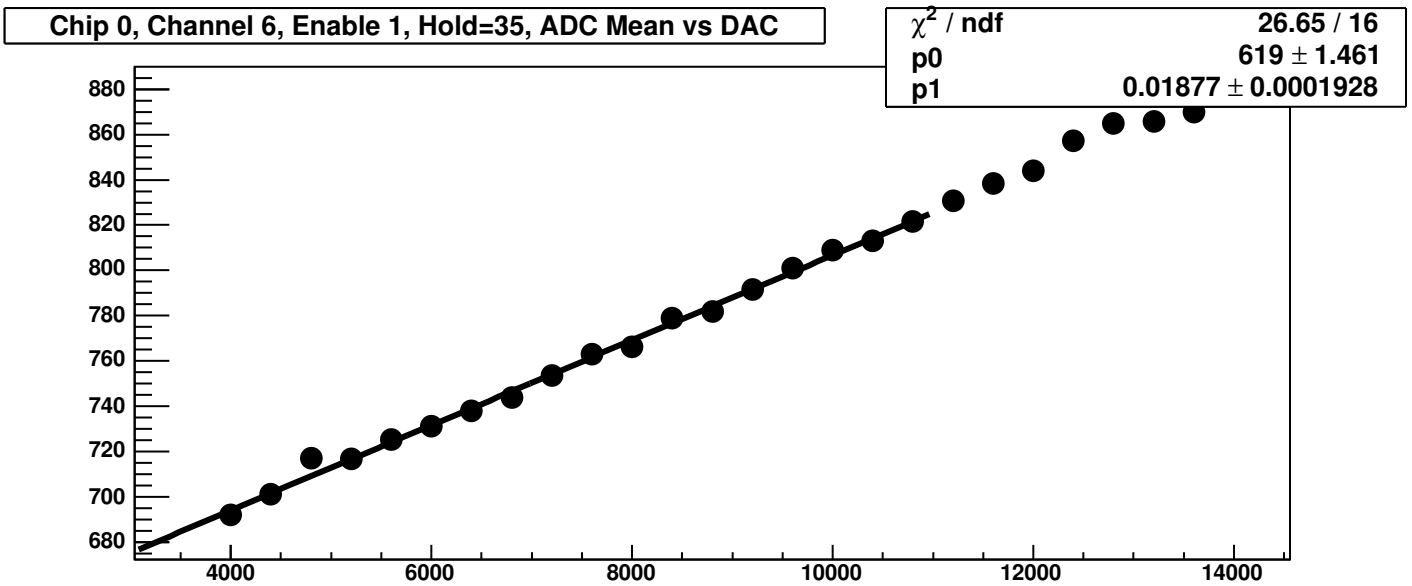

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

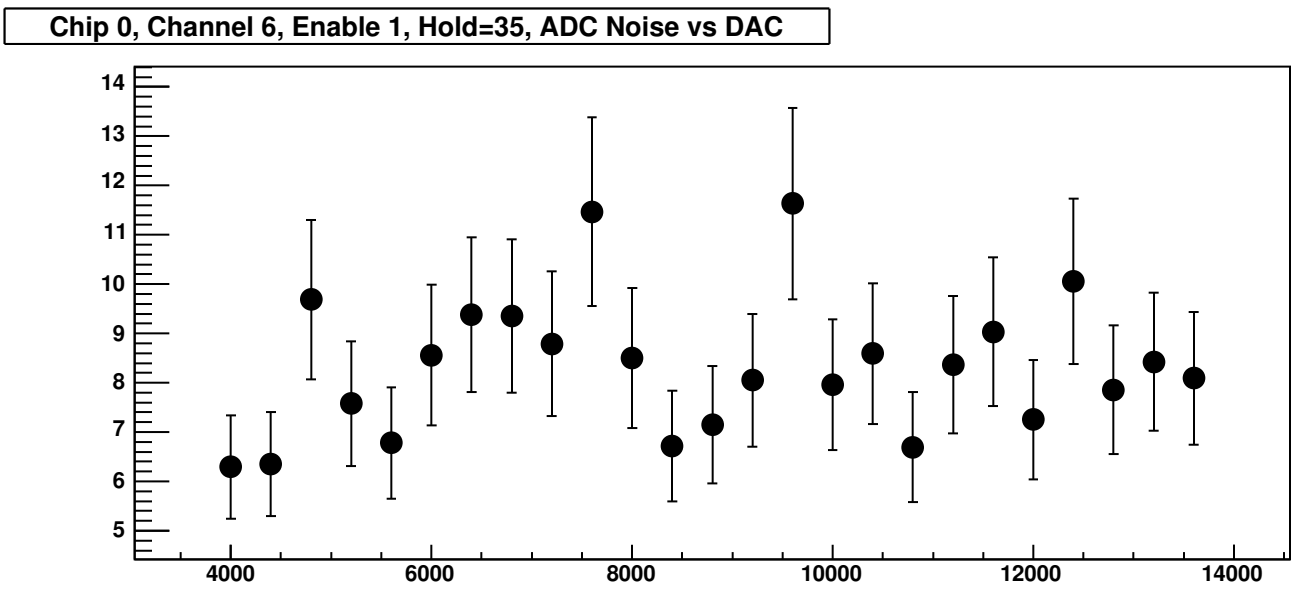

Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

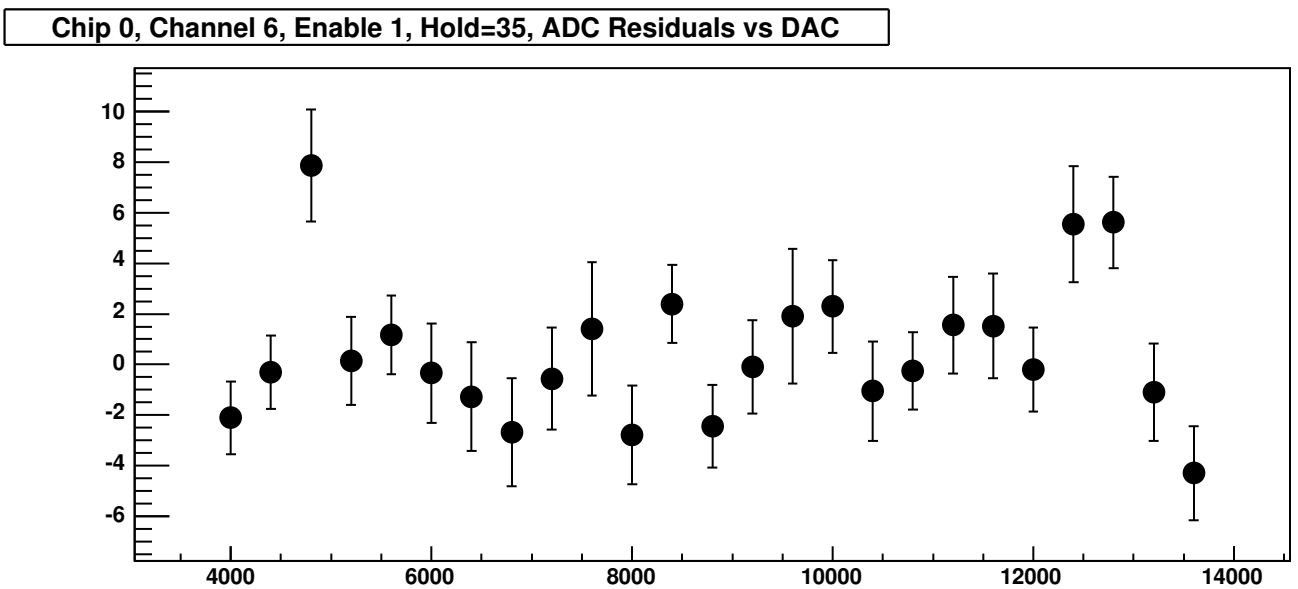

Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 31.57 / 16
p0 616.3 ± 1.535
p1 0.01825 ± 0.000193
860
840
820
800
780
760
740
720
700
680
4000
6000
8000
10000
12000
14000

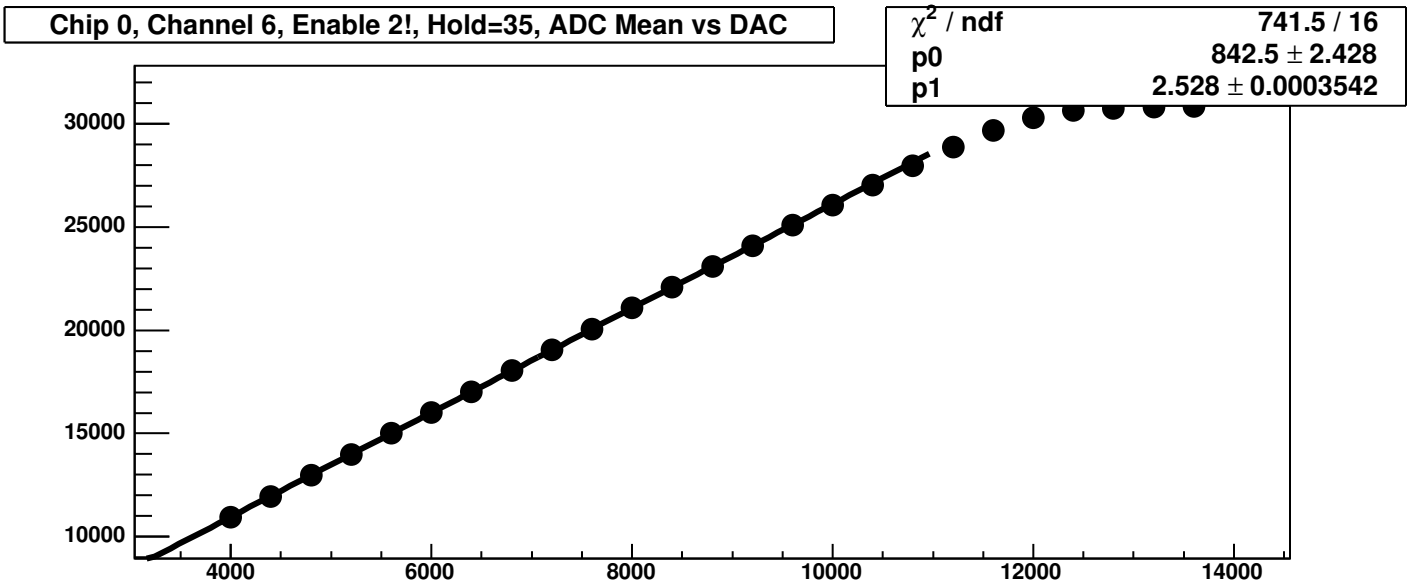

Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

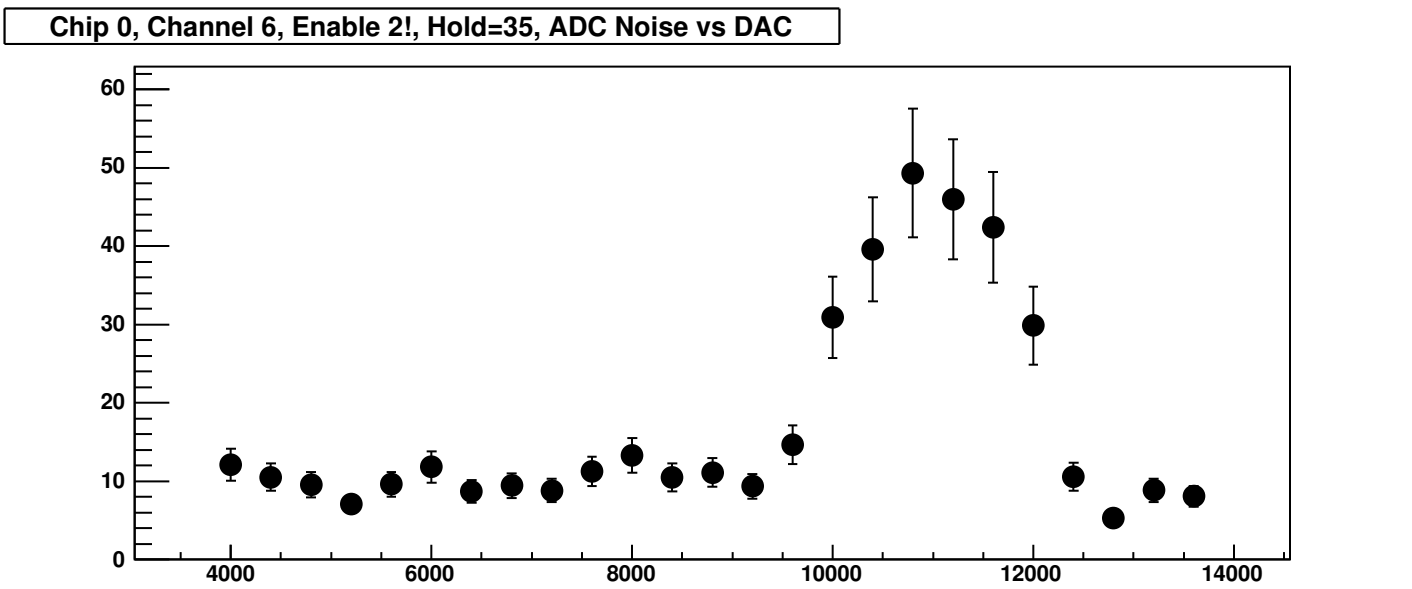

Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

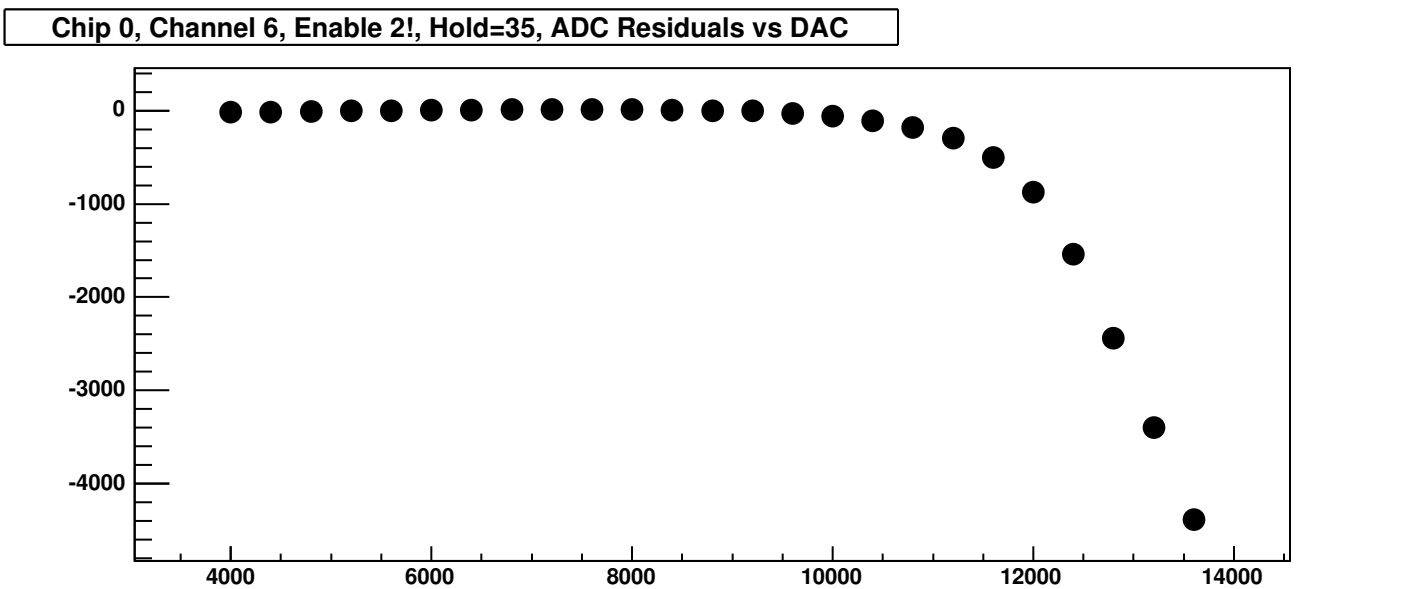

Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.65 / 16
p0 619 ± 1.461
p1 0.01877 ± 0.0001928
880
860
840
820
800
780
760
740
720
700
680
4000
6000
8000
10000
12000
14000

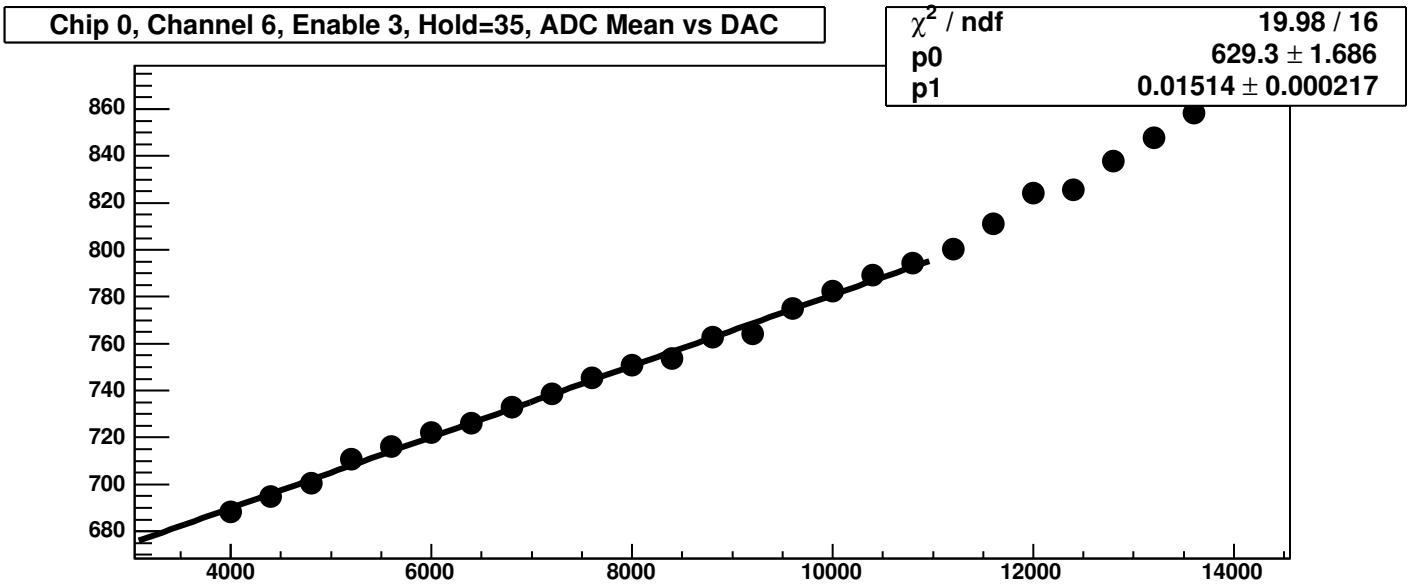

Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

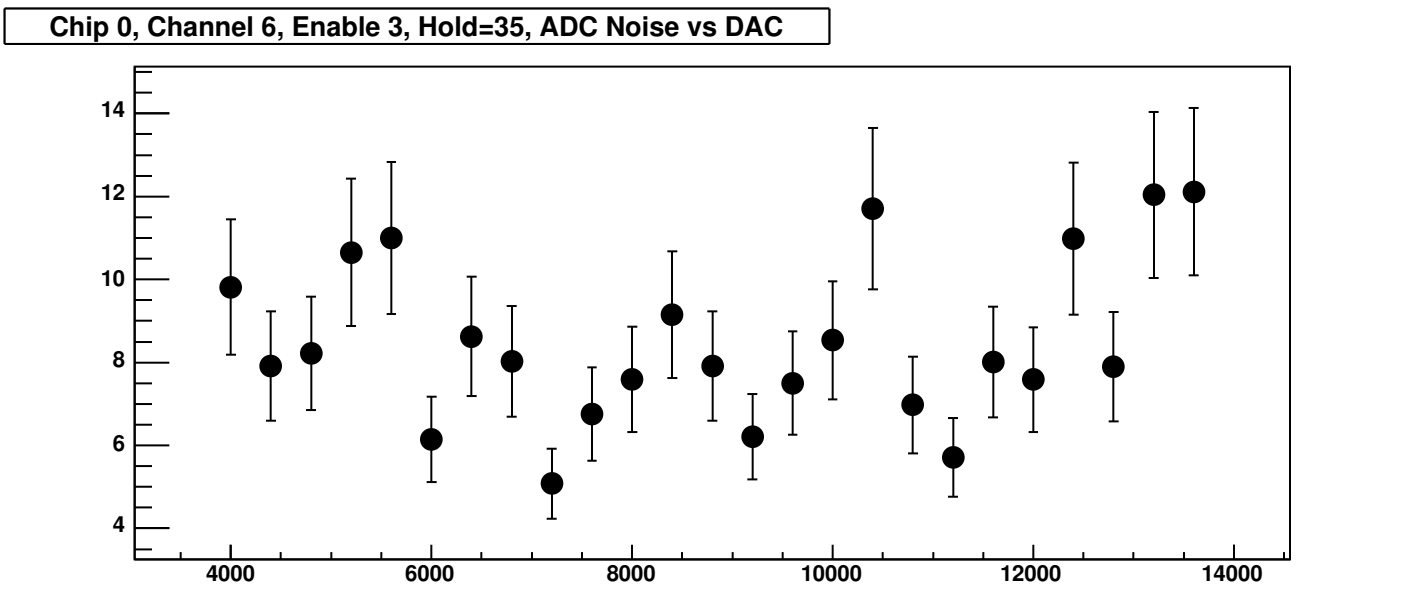

Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

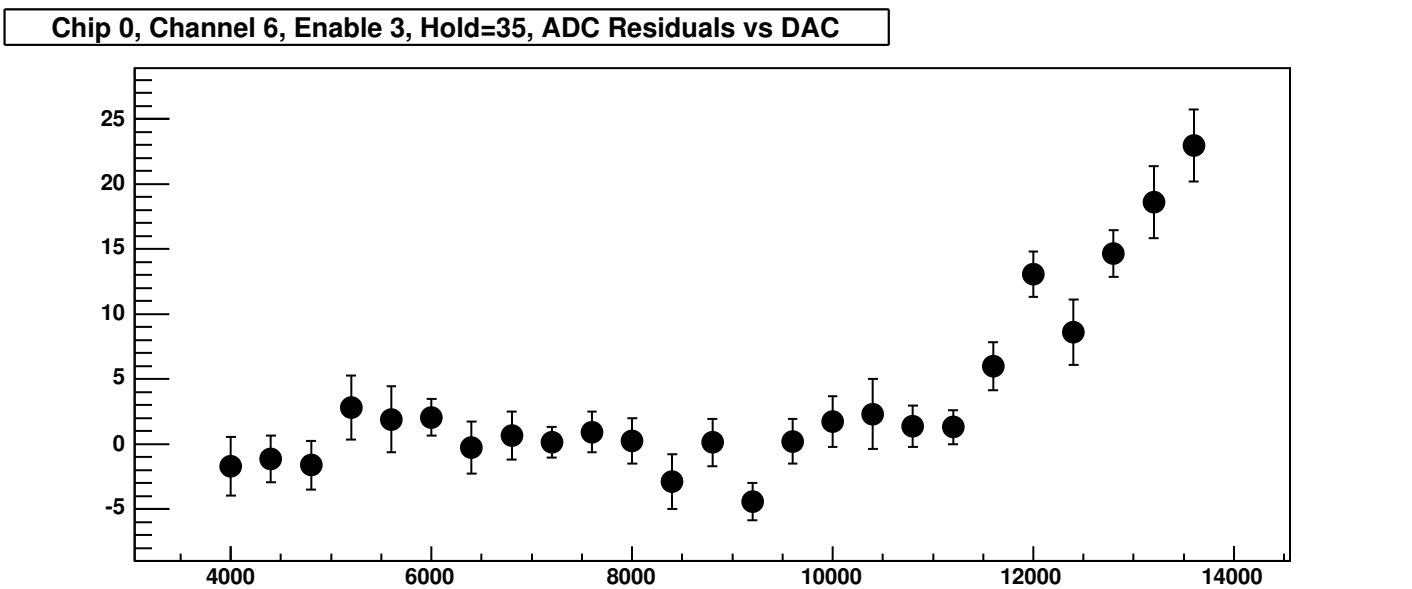

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 741.5 / 16
p0 842.5 ± 2.428
p1 2.528 ± 0.0003542
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

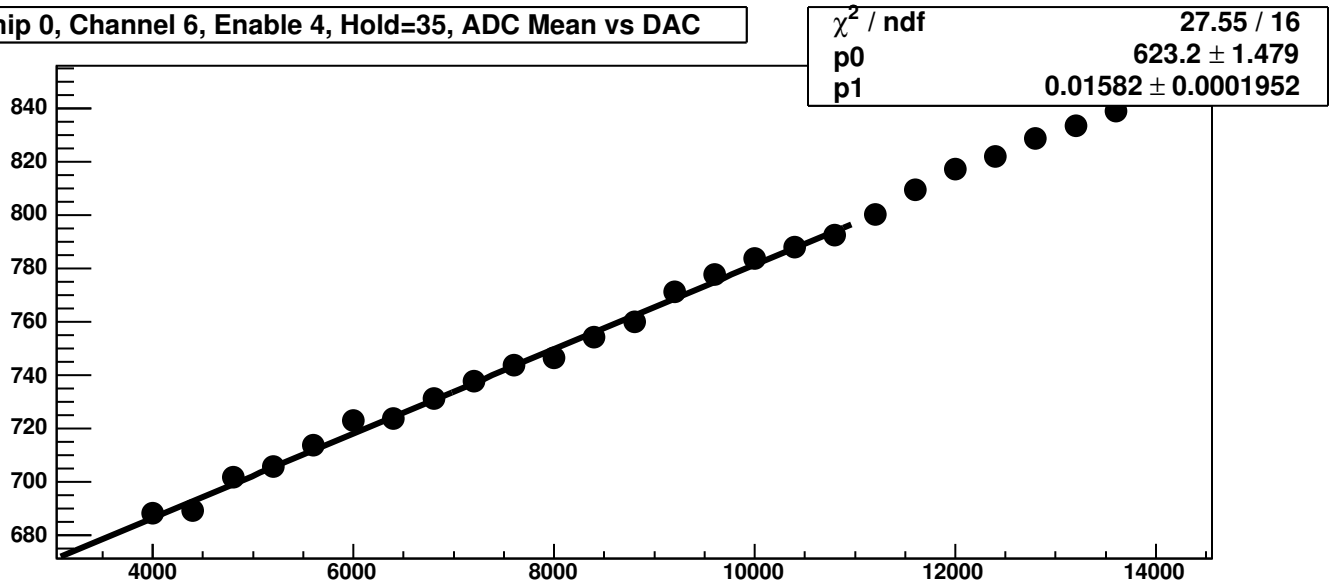

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.98 / 16
p0 629.3 ± 1.686
p1 0.01514 ± 0.000217
860
840
820
800
780
760
740
720
700
680
4000
6000
8000
10000
12000
14000

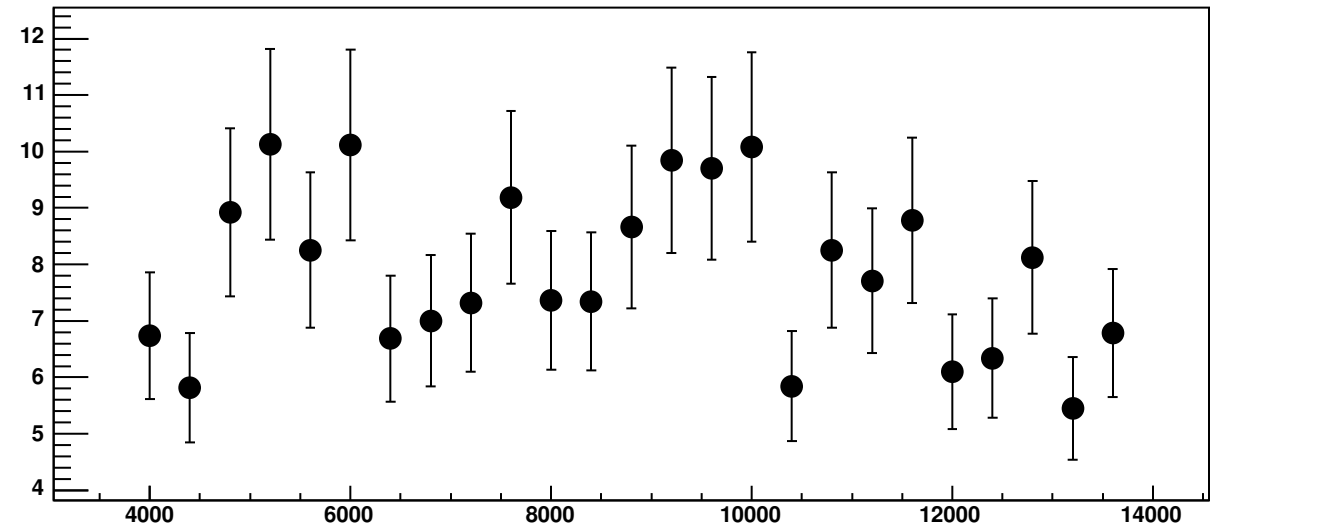

Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

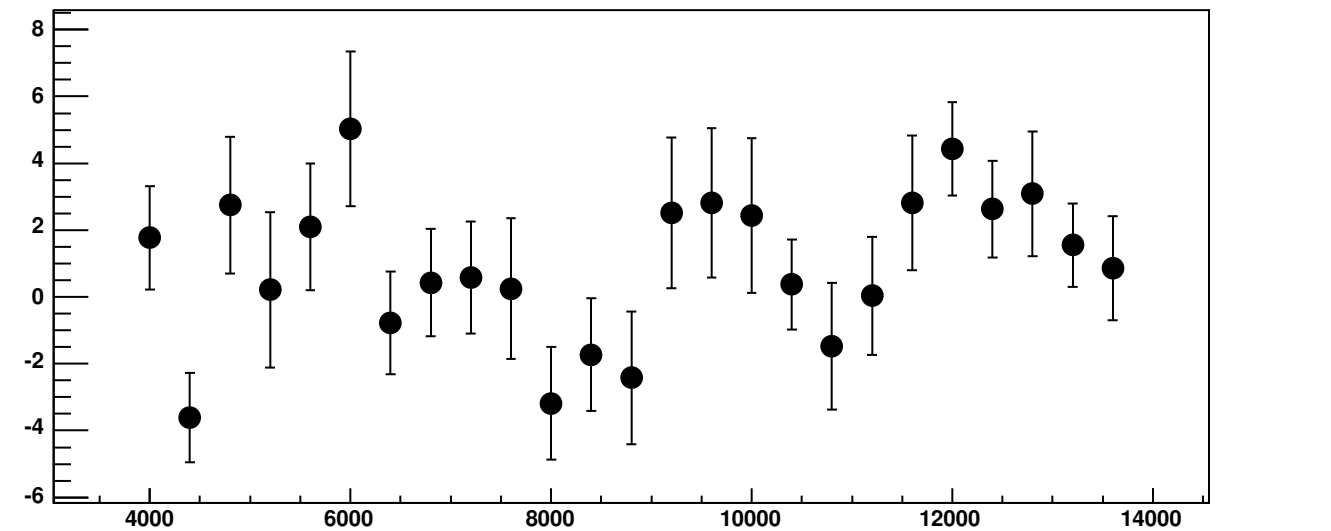
Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

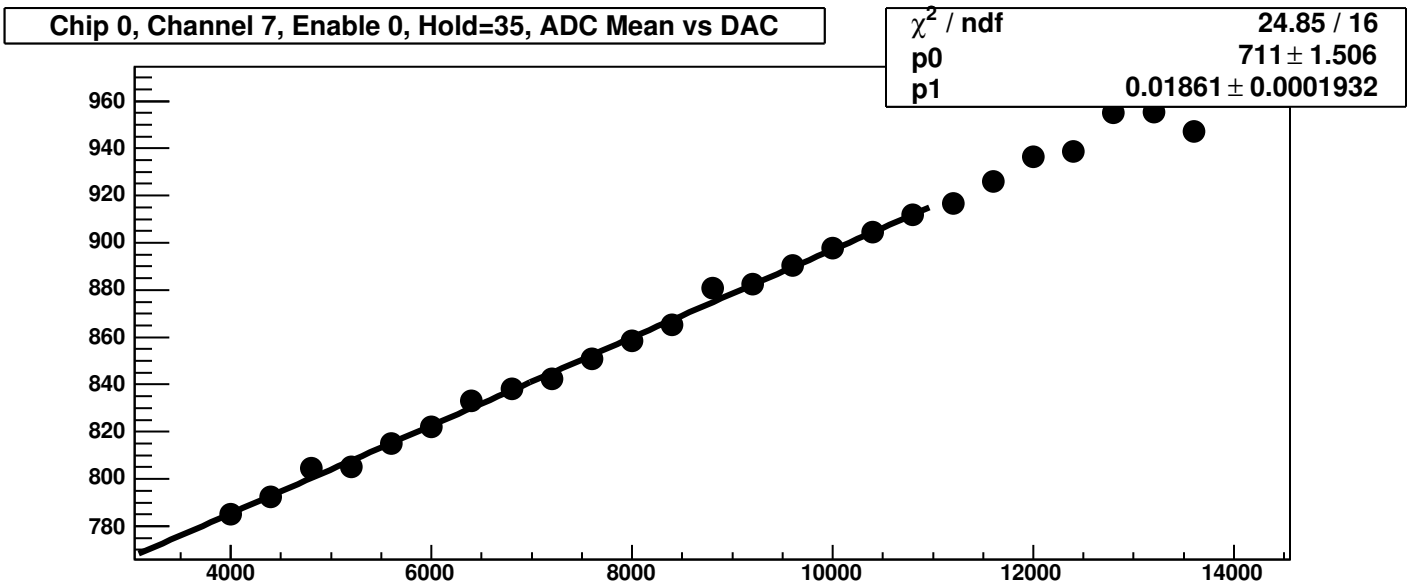


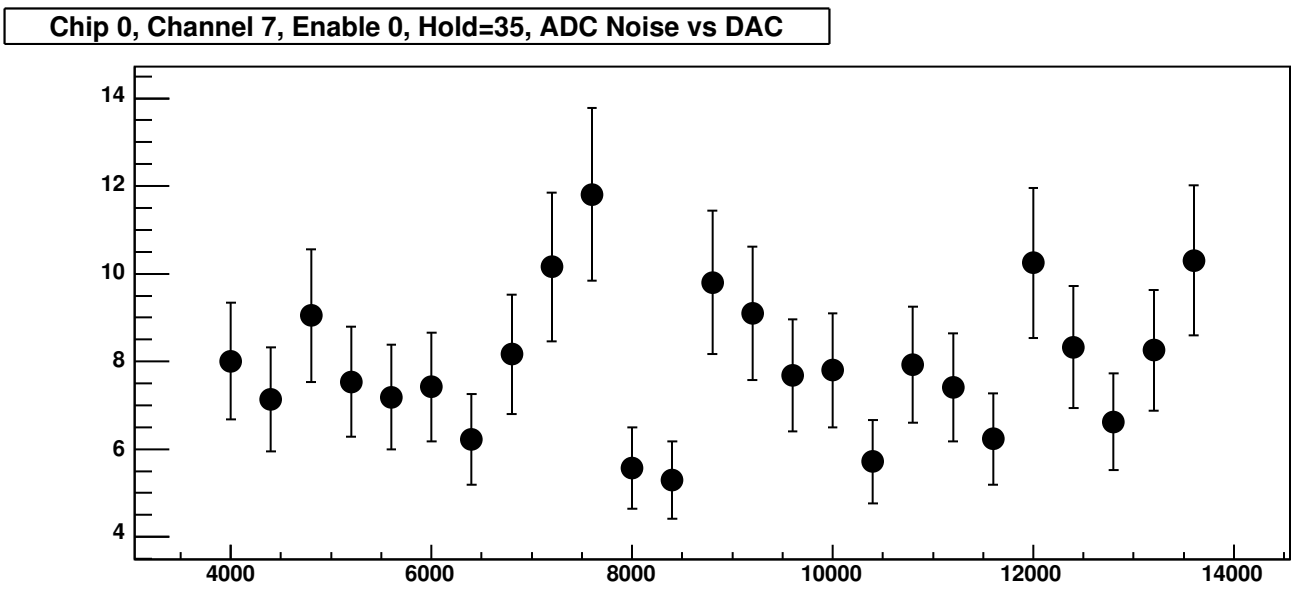
Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

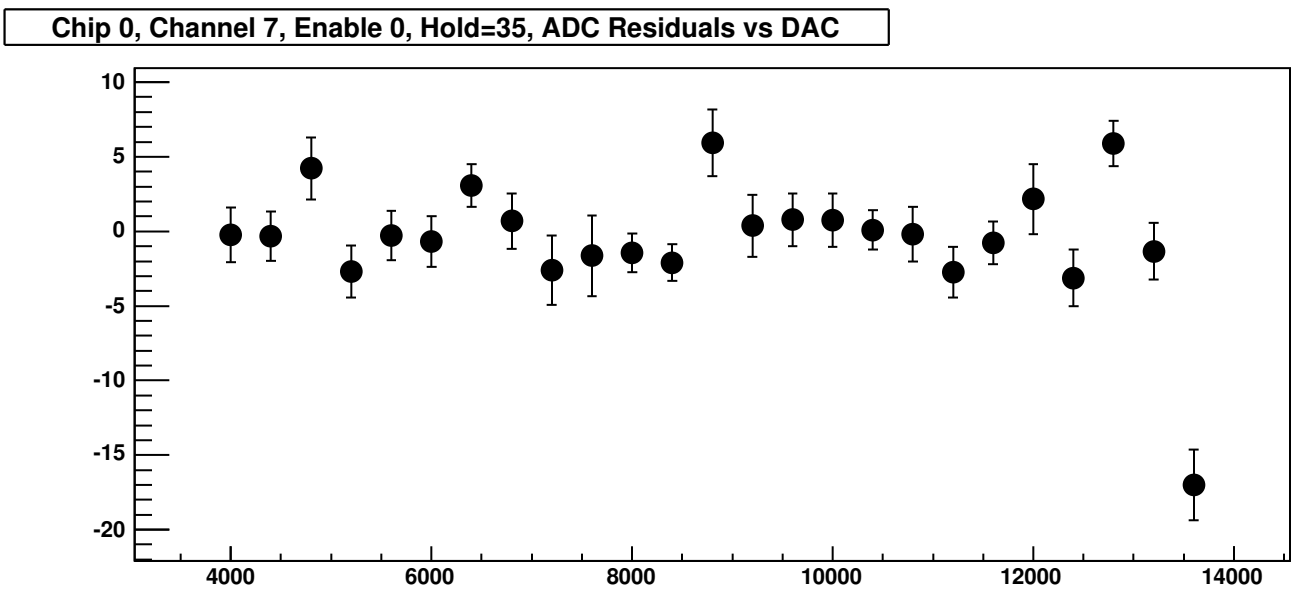


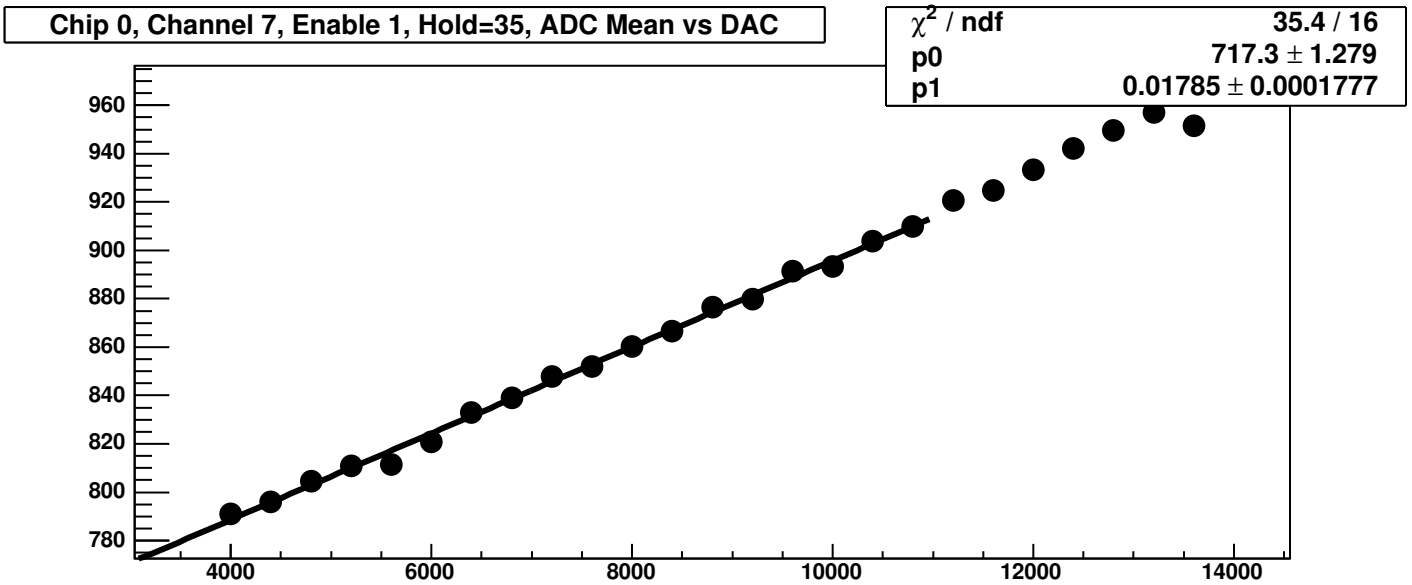
Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

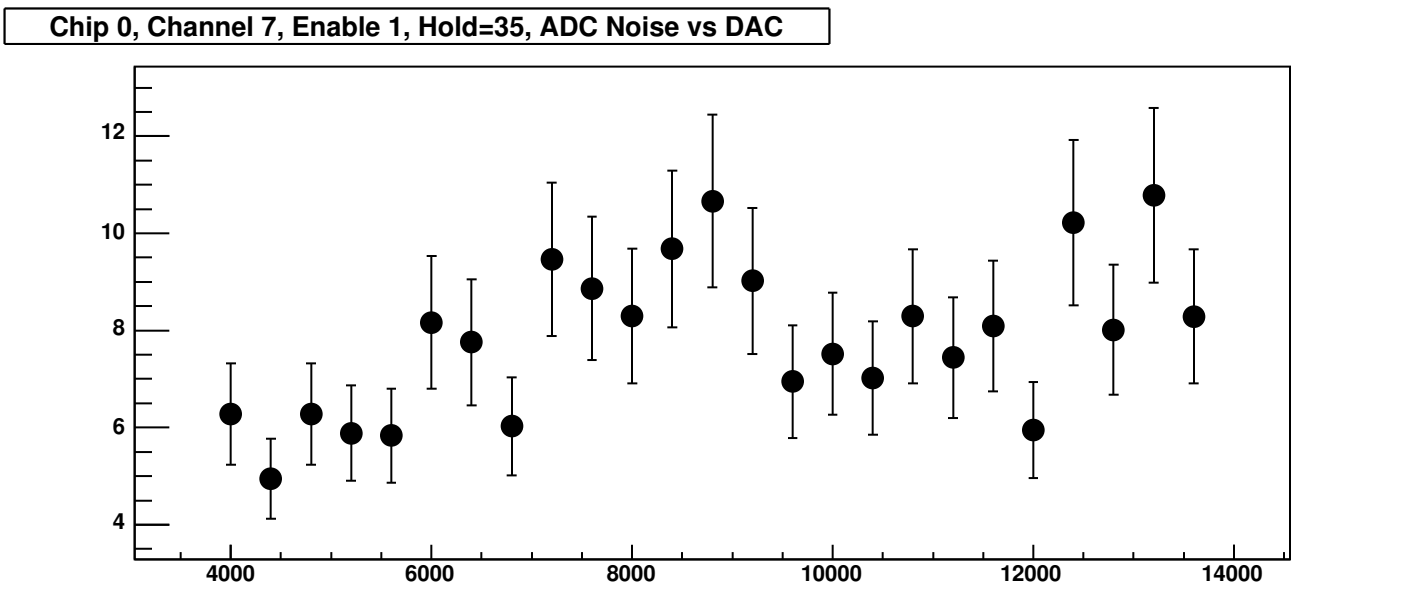


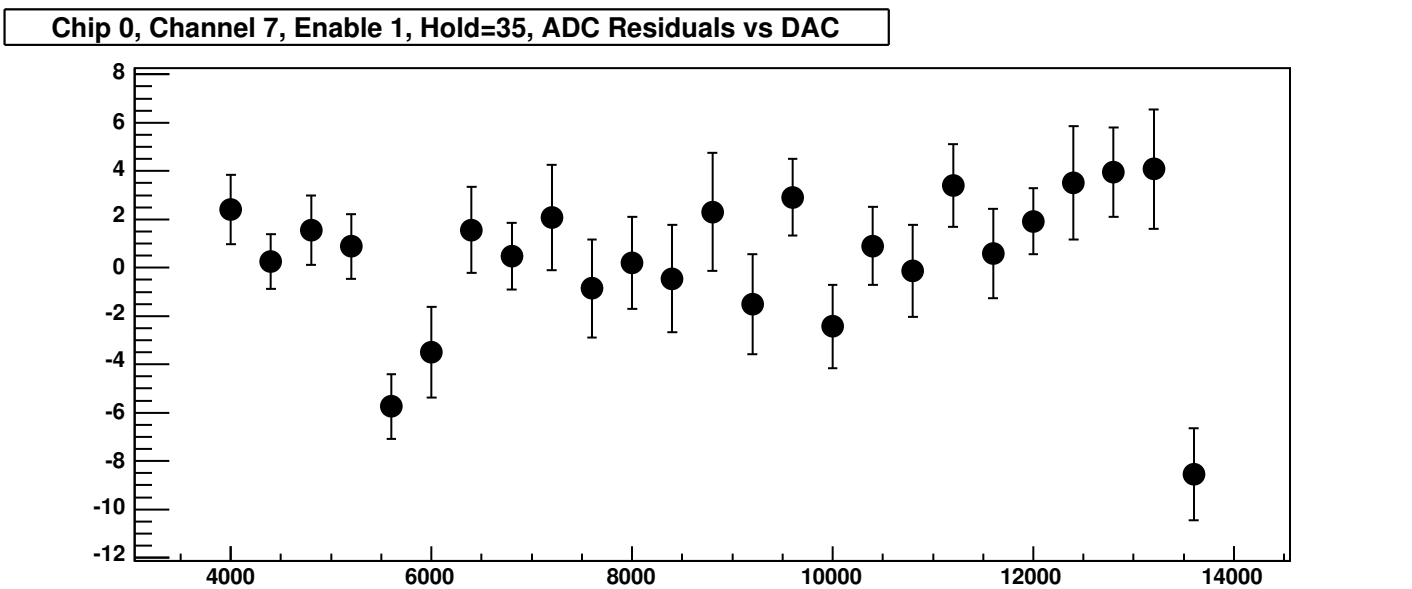

Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 24.85 / 16
p0 711 ± 1.506
p1 0.01861 ± 0.0001932
960
940
920
900
880
860
840
820
800
780
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

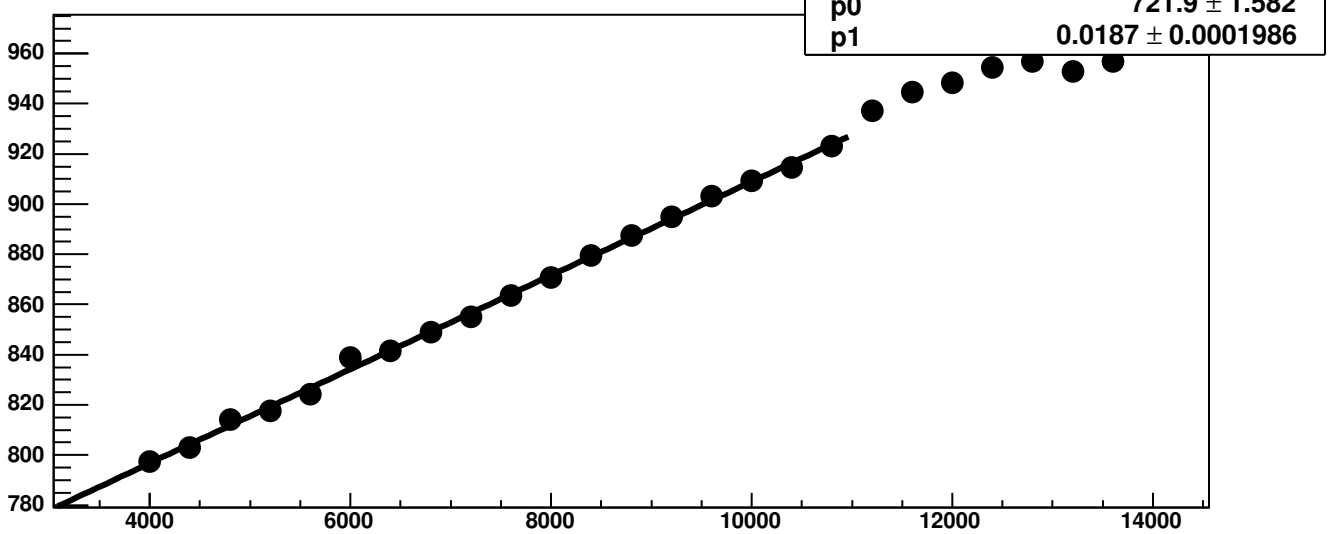

Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.4 / 16
p0 717.3 ± 1.279
p1 0.01785 ± 0.0001777
960
940
920
900
880
860
840
820
800
780
4000
6000
8000
10000
12000
14000

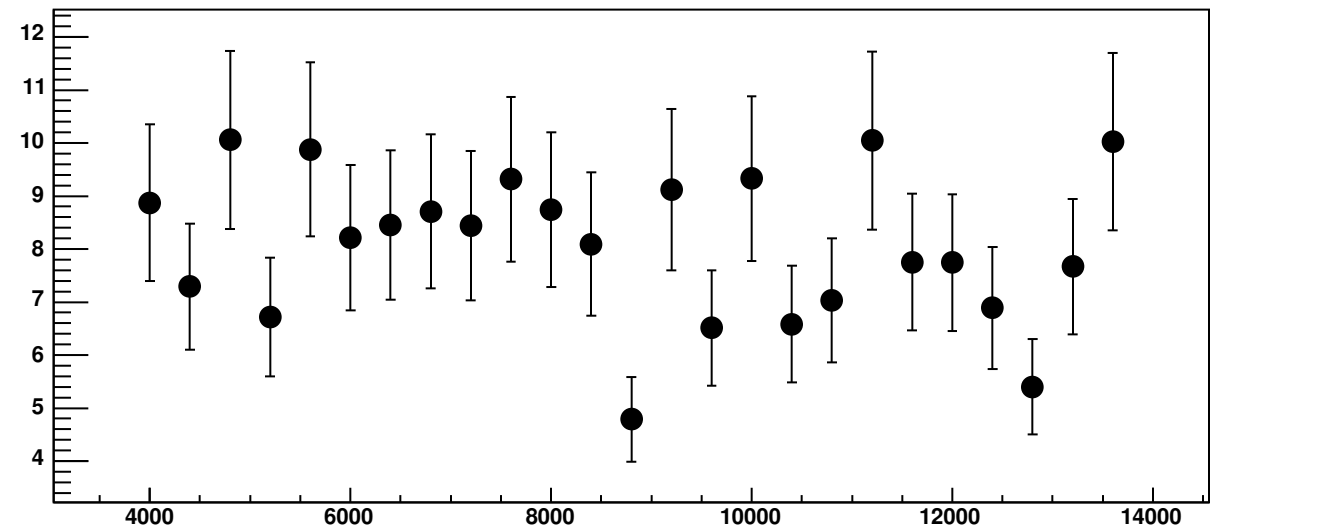

Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000

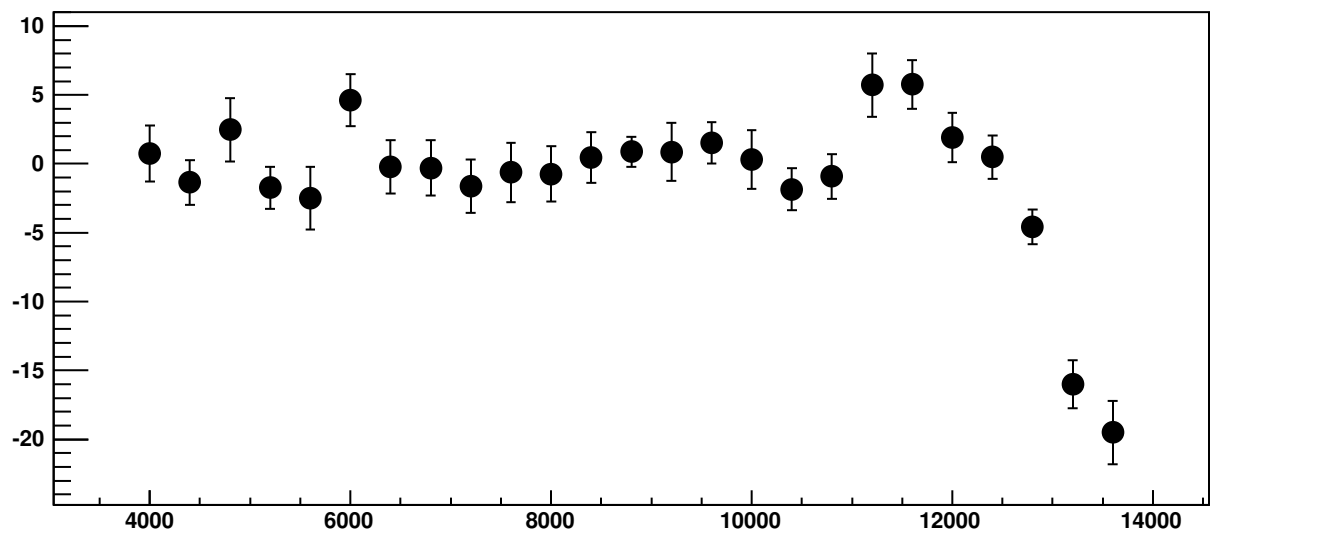
Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

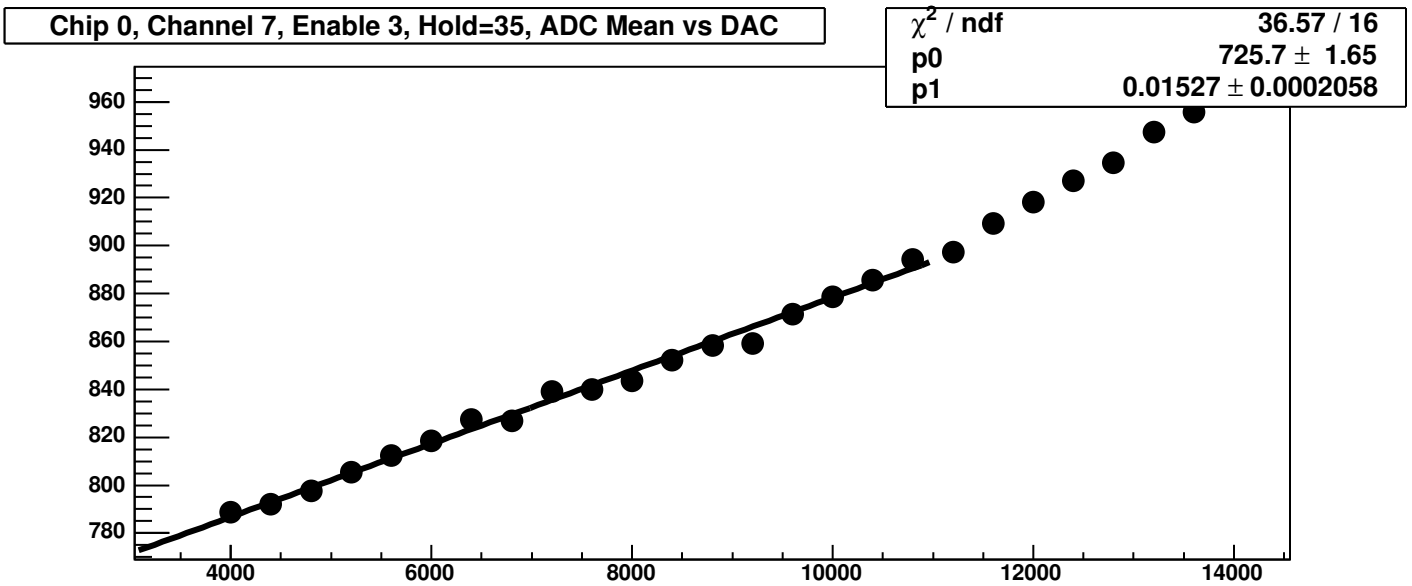


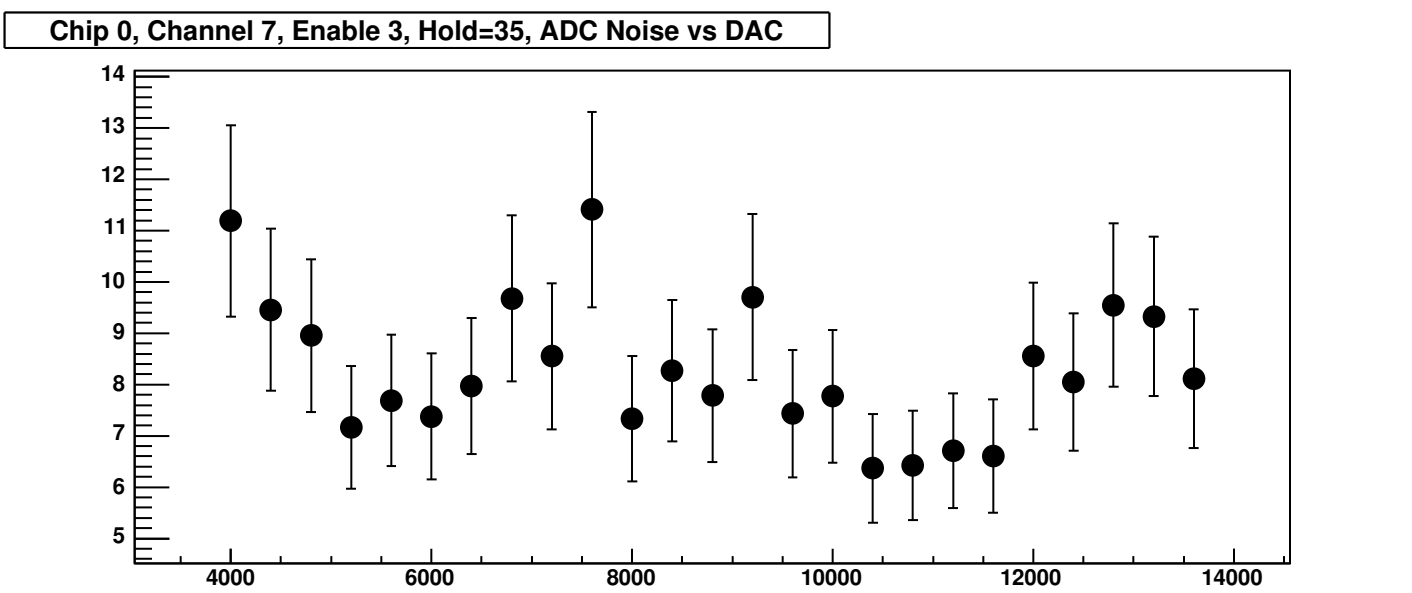
Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC

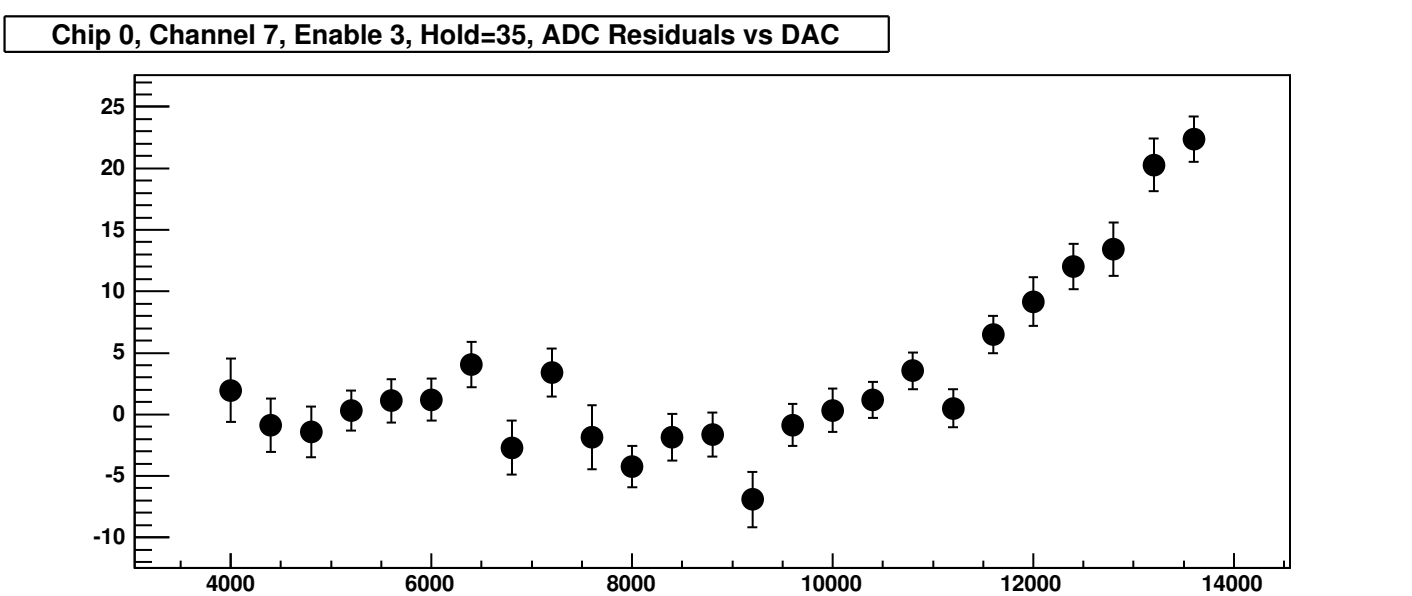


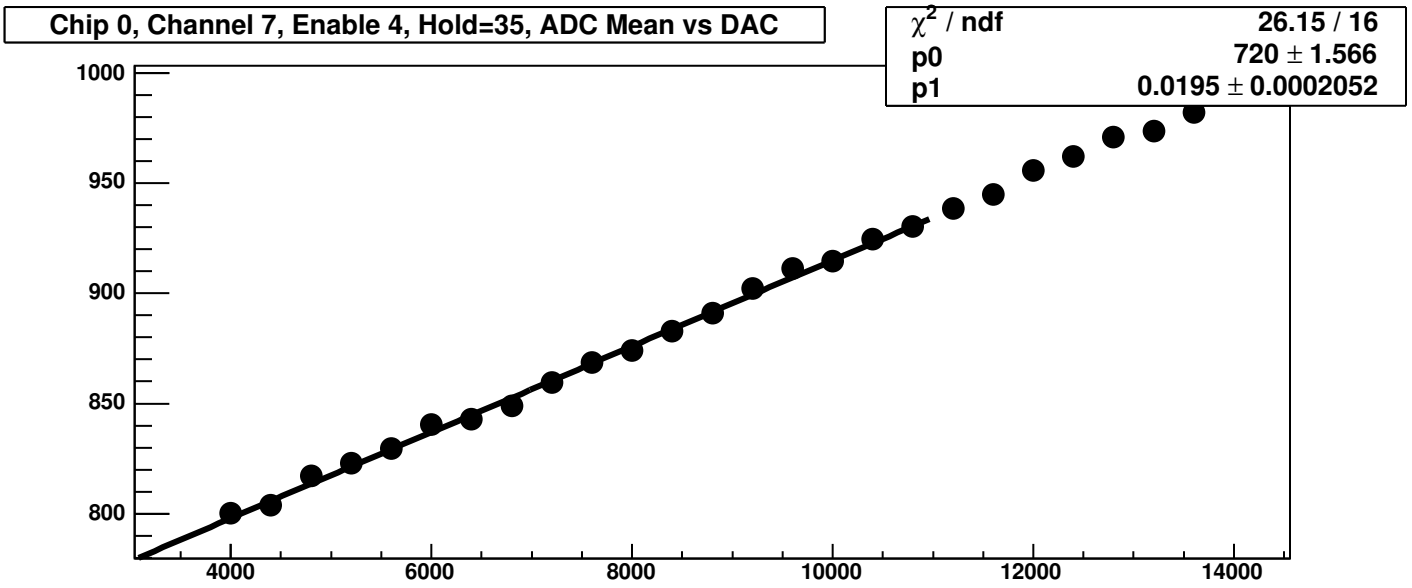
Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

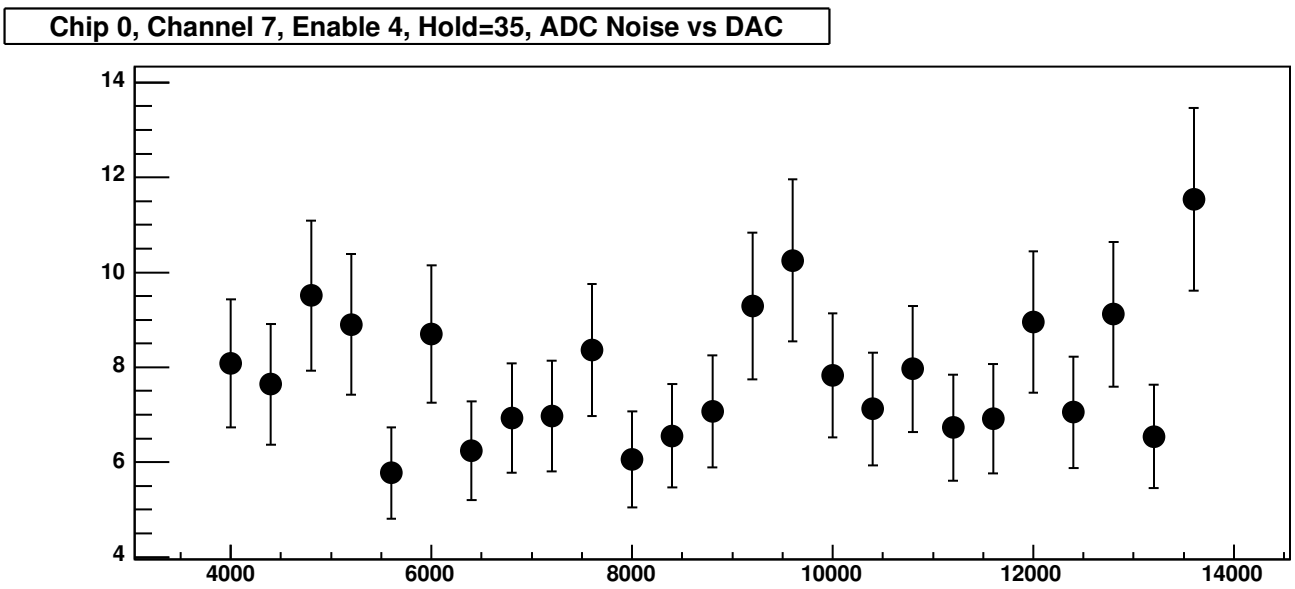


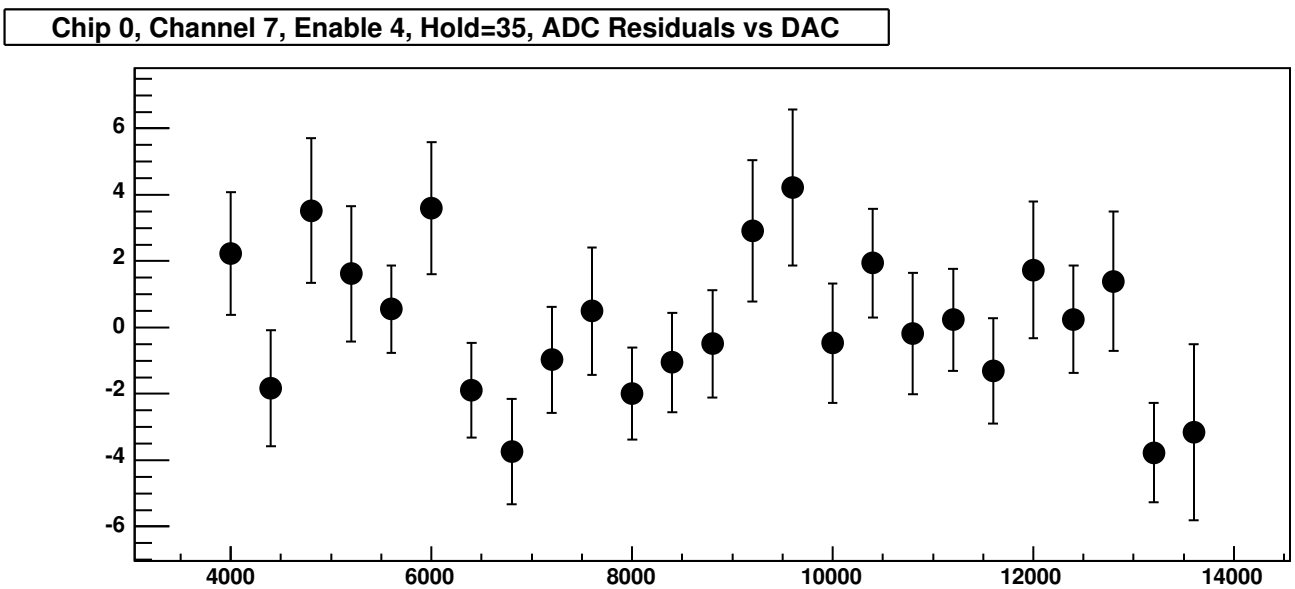

Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.57 / 16
p0 725.7 ± 1.65
p1 0.01527 ± 0.0002058
960
940
920
900
880
860
840
820
800
780
4000
6000
8000
10000
12000
14000

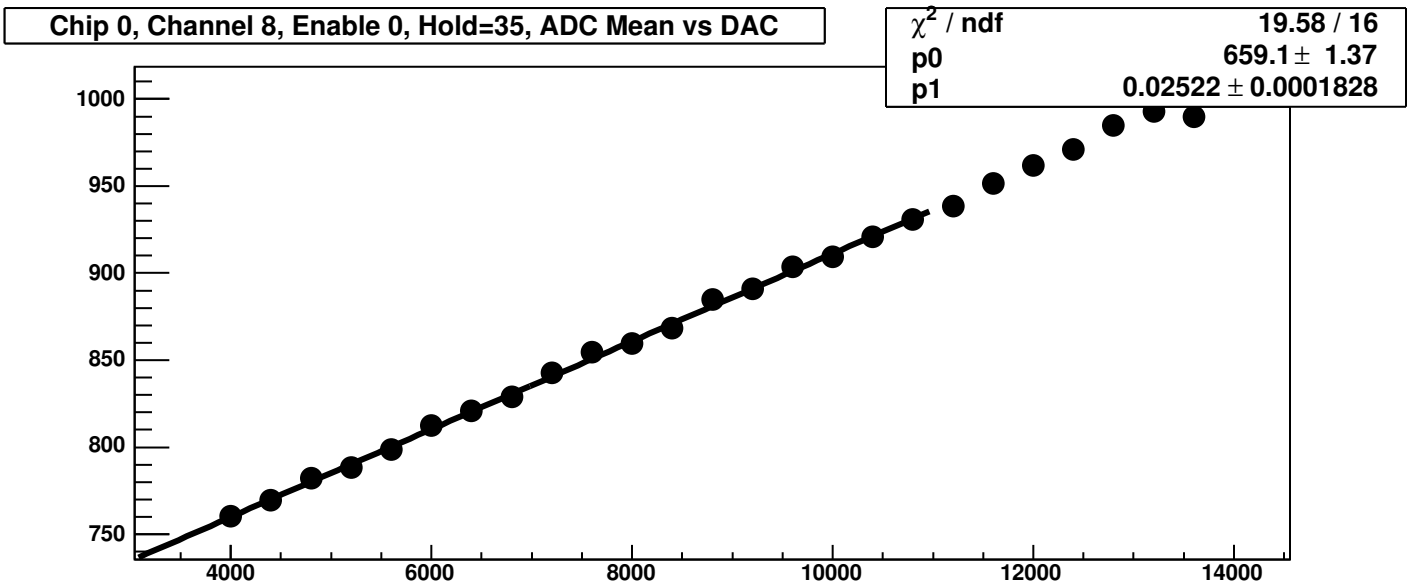

Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

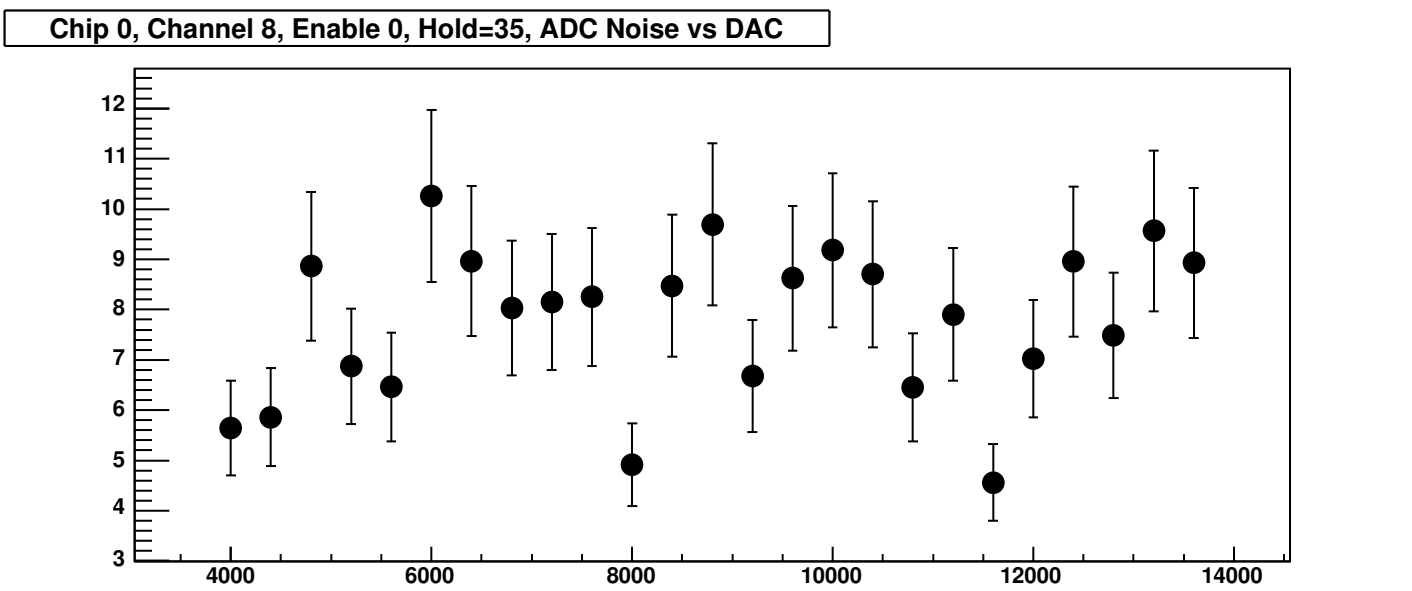

Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

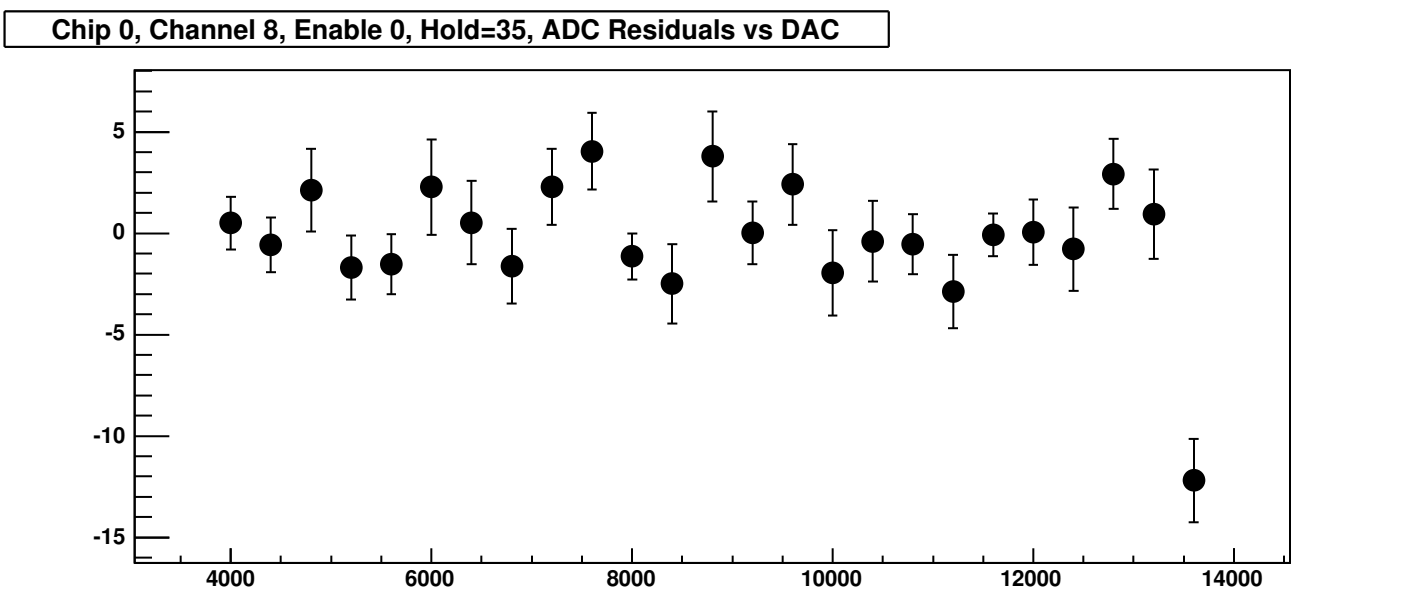

Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.15 / 16
p0 720 ± 1.566
p1 0.0195 ± 0.0002052
1000
950
900
850
800
4000
6000
8000
10000
12000
14000

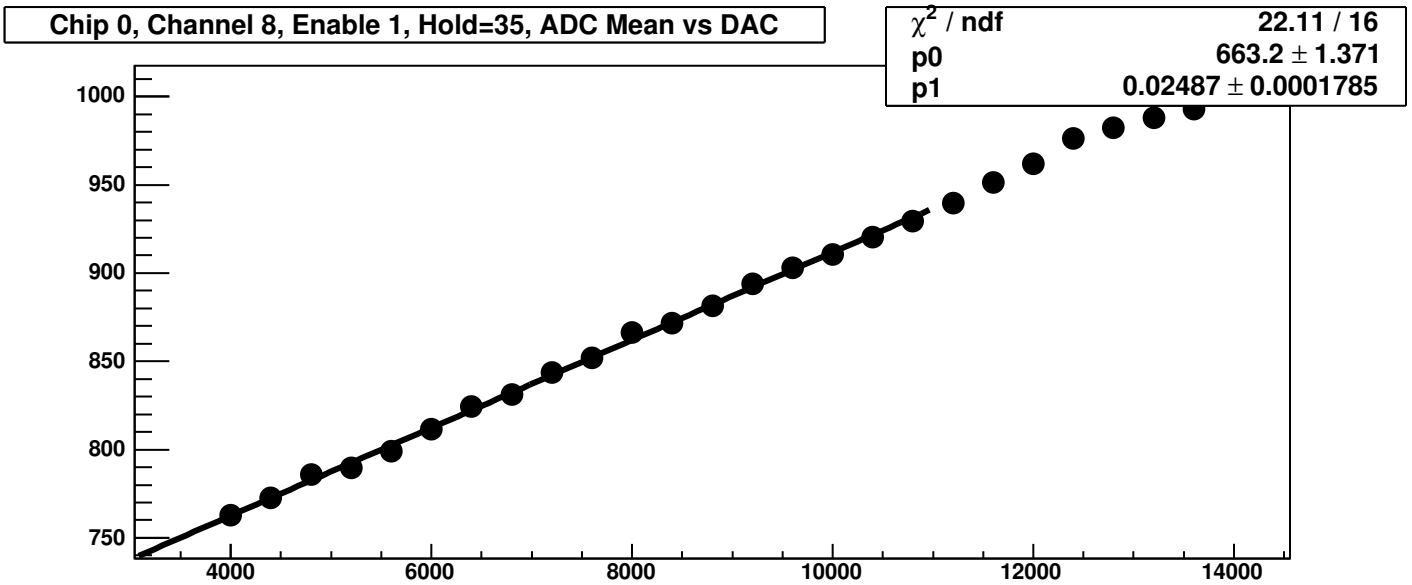

Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

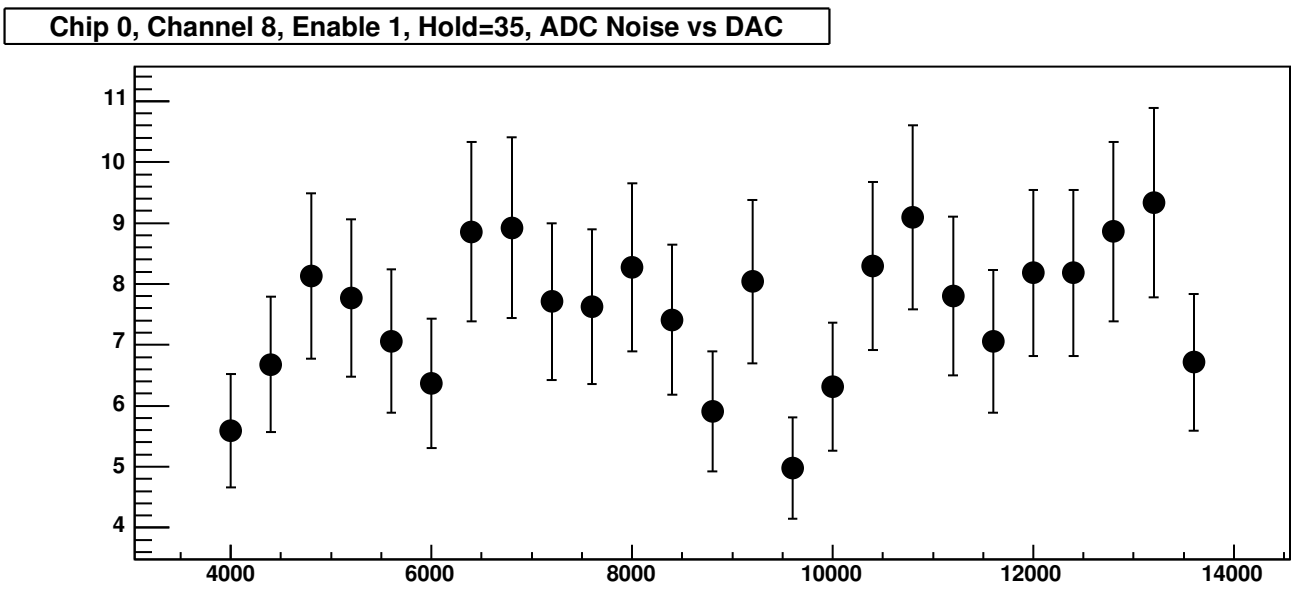

Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

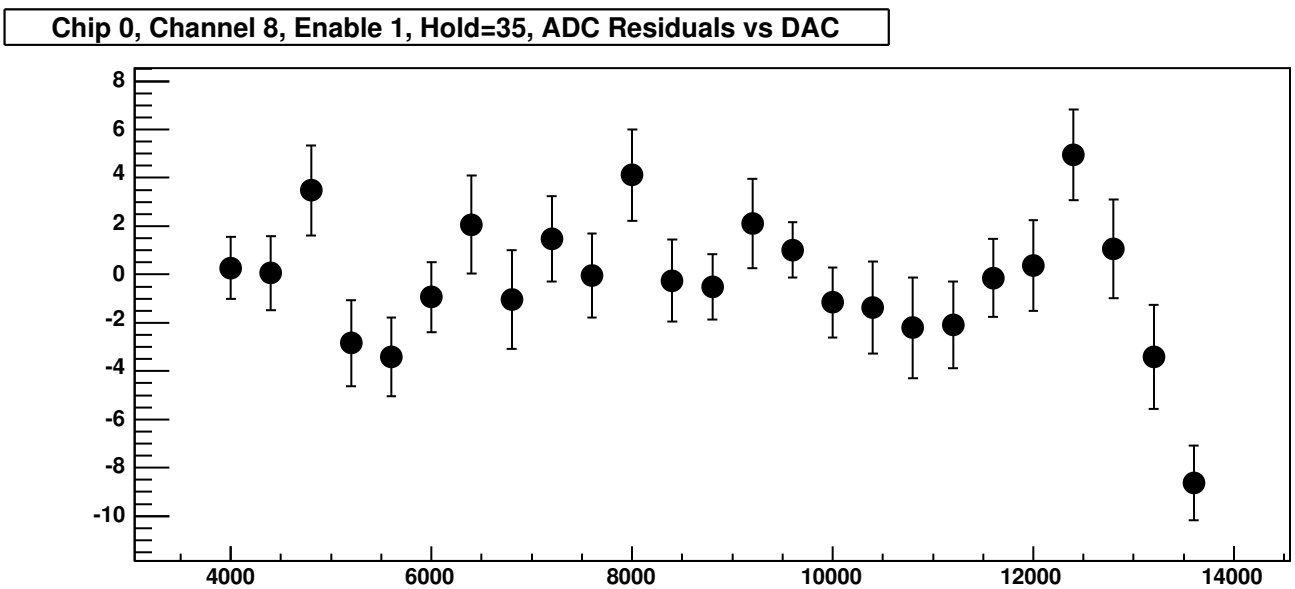

Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 19.58 / 16
p0 659.1 ± 1.37
p1 0.02522 ± 0.0001828
1000
950
900
850
800
750
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

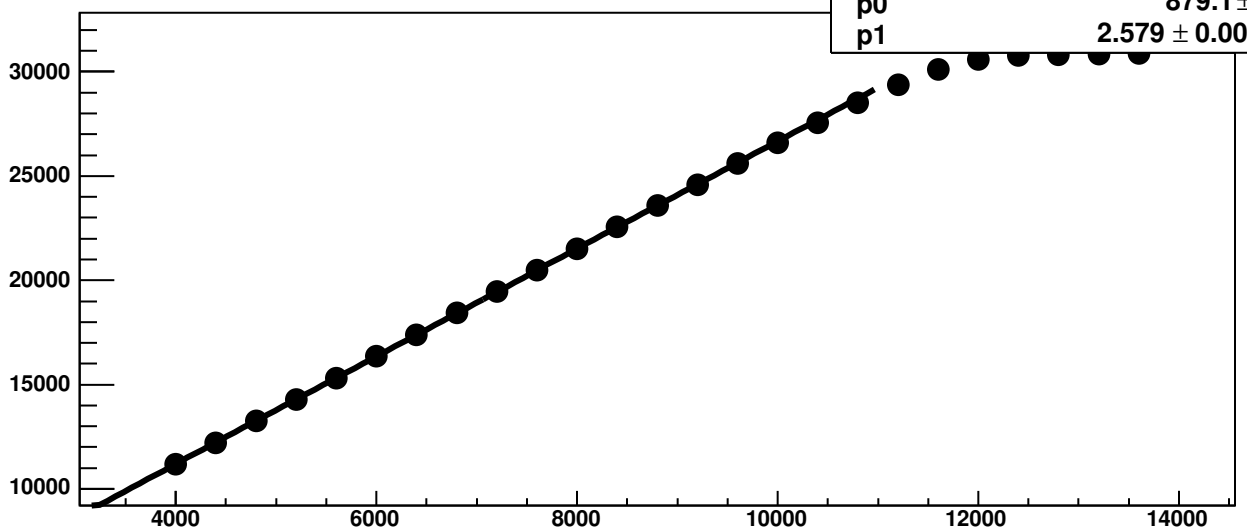

Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.11 / 16
p0 663.2 ± 1.371
p1 0.02487 ± 0.0001785
1000
950
900
850
800
750
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

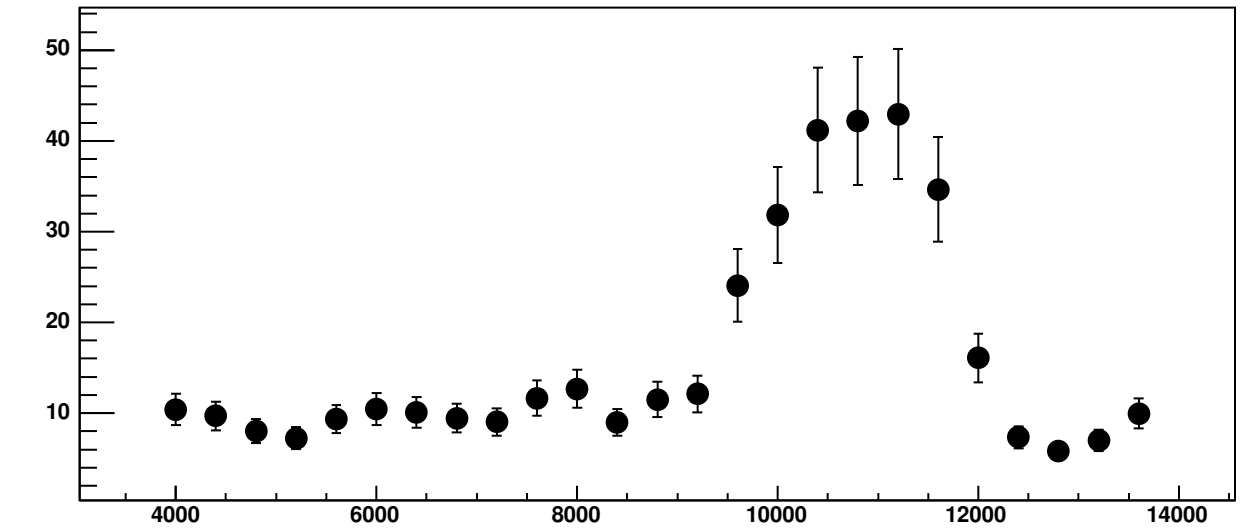

Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

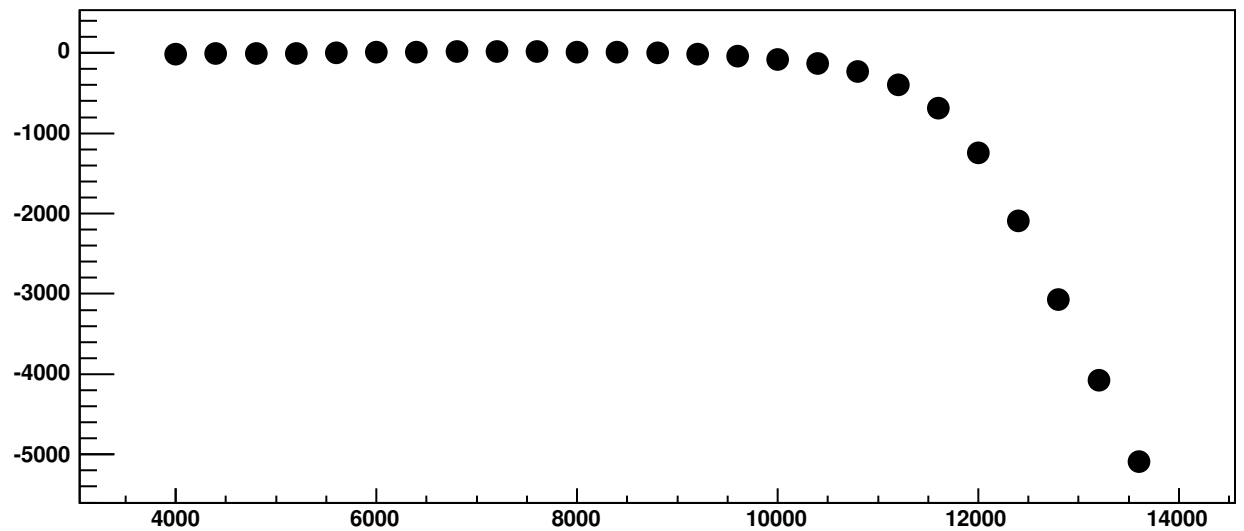
χ^2 / ndf	1291 / 16
p0	879.1 ± 2.381
p1	2.579 ± 0.0003593

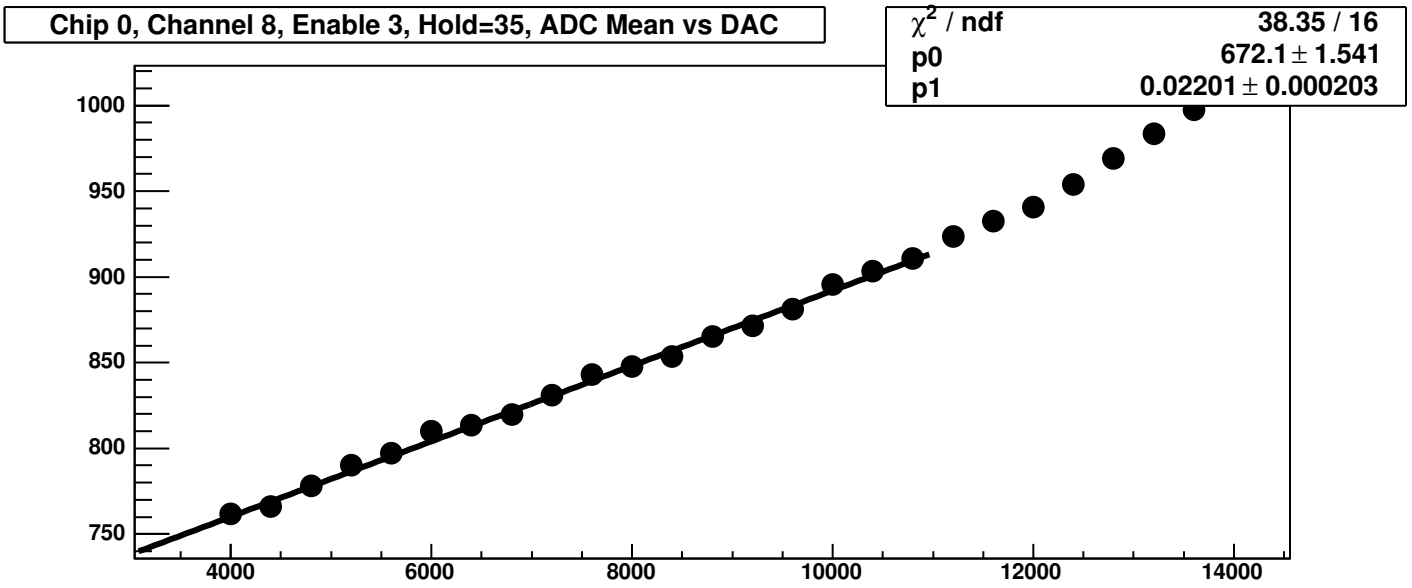


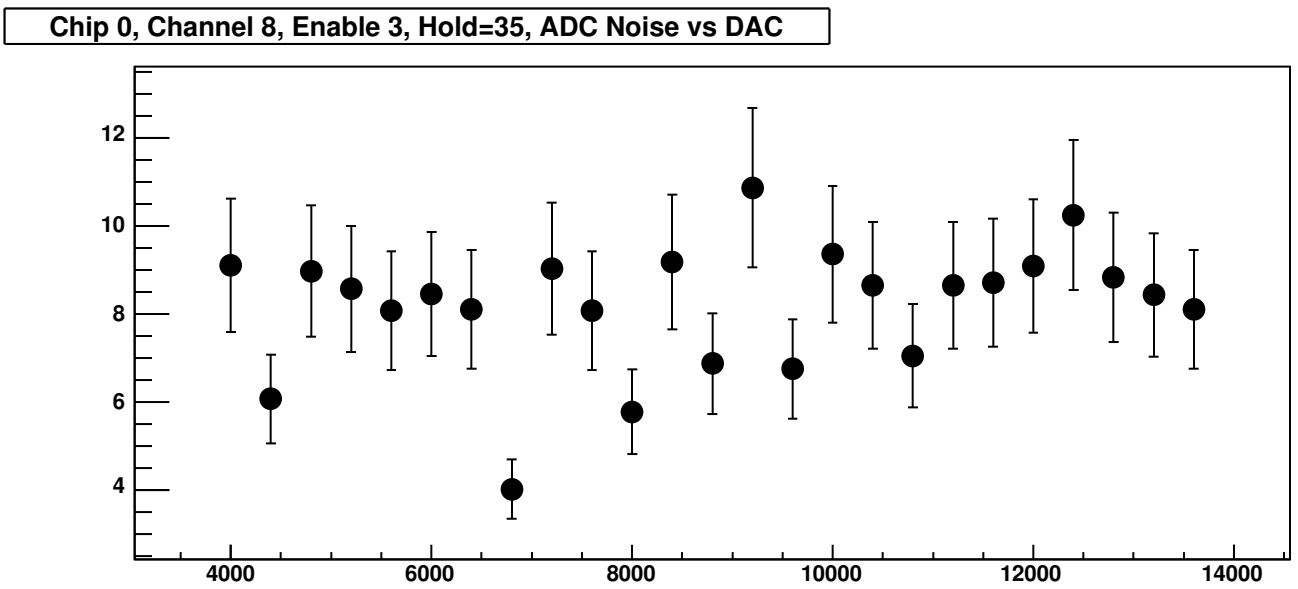
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC

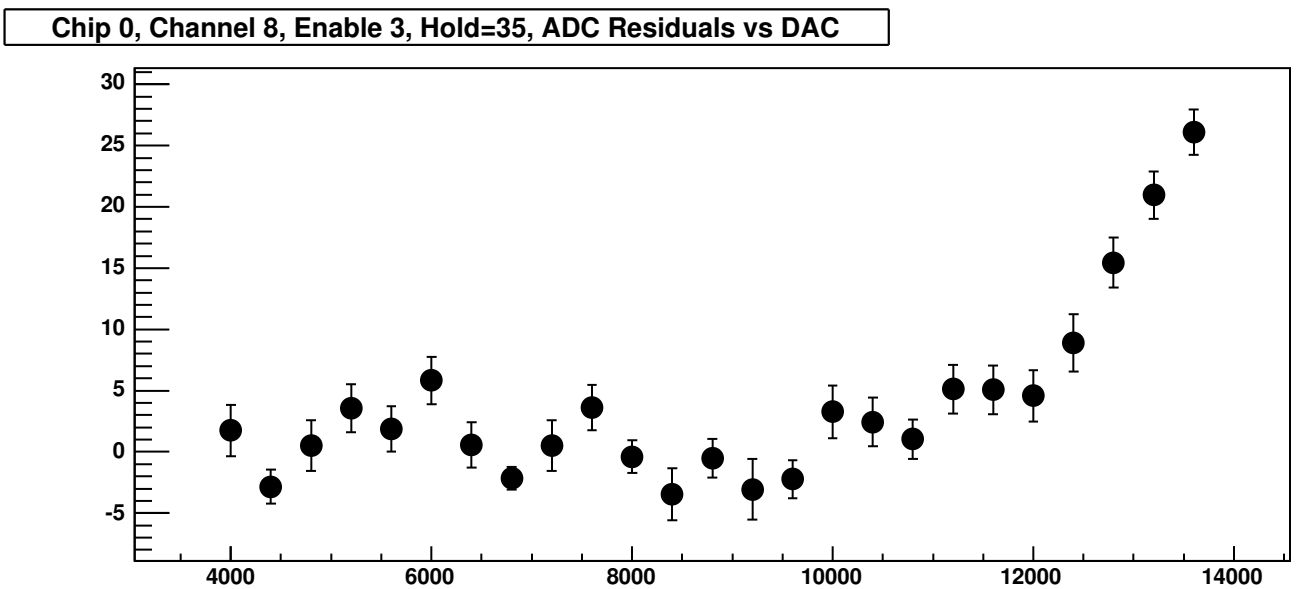


Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

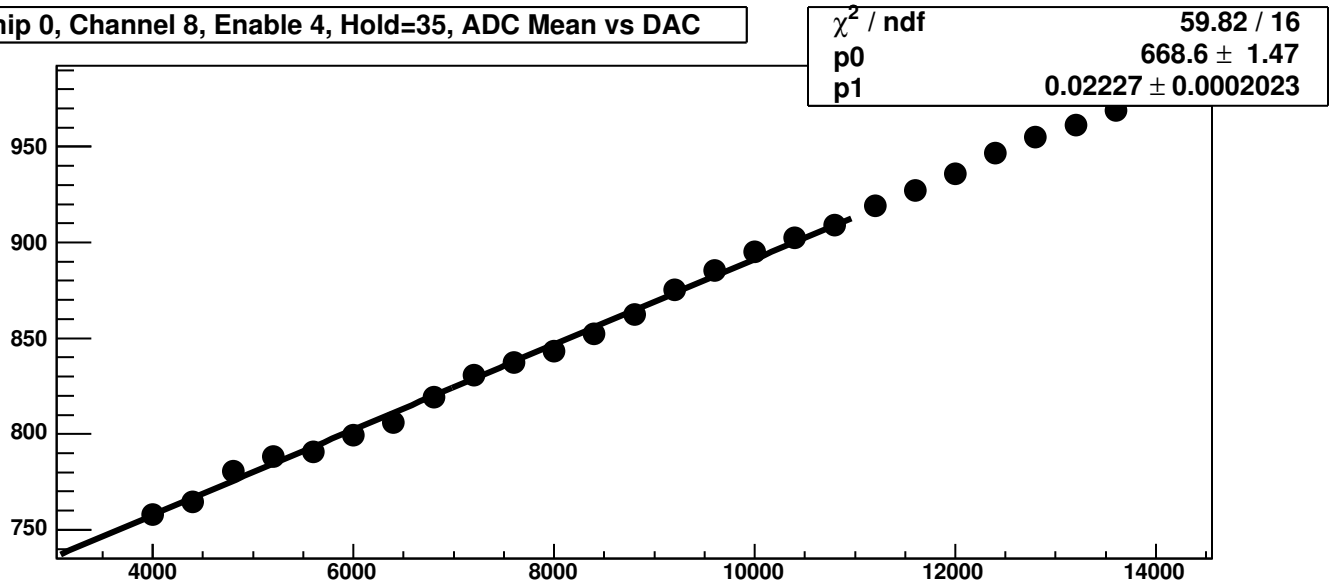



Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.35 / 16
p0 672.1 ± 1.541
p1 0.02201 ± 0.000203
1000
950
900
850
800
750
4000
6000
8000
10000
12000
14000

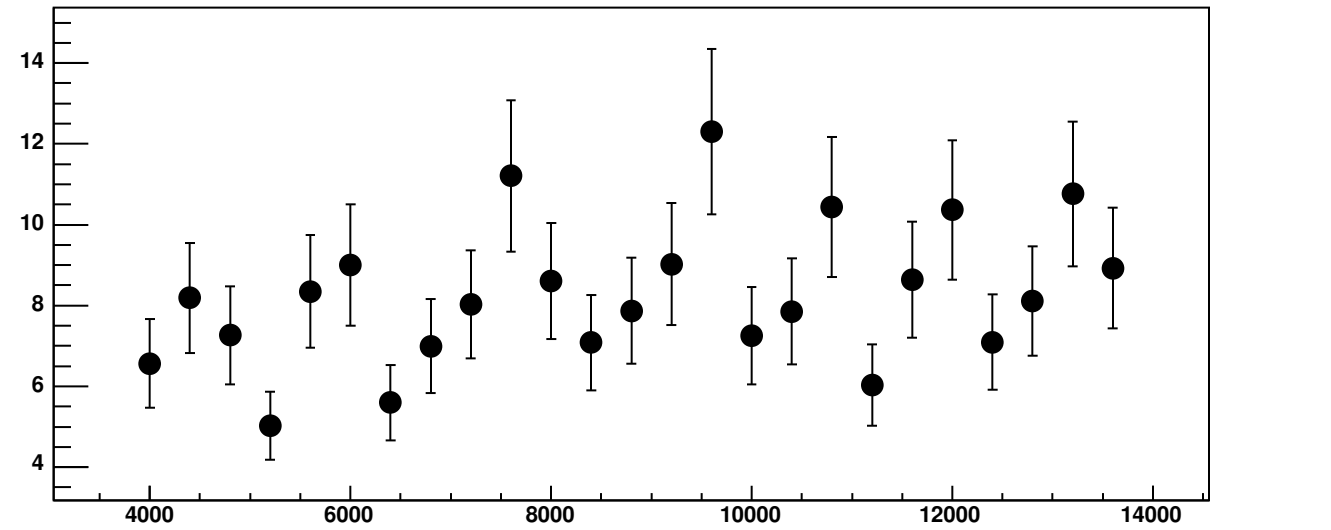

Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

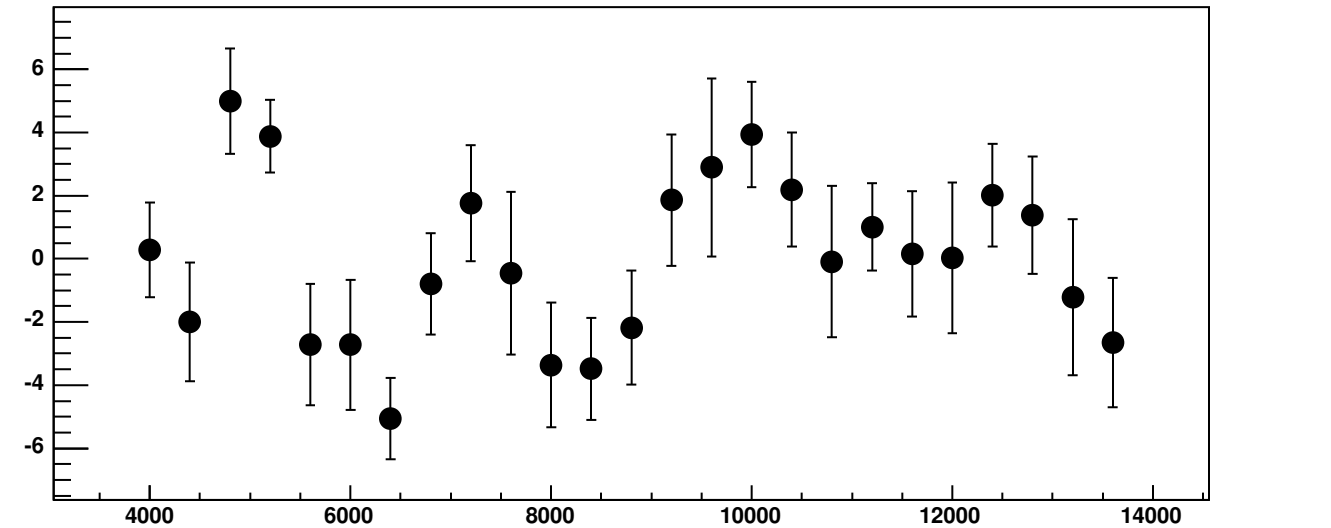
Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

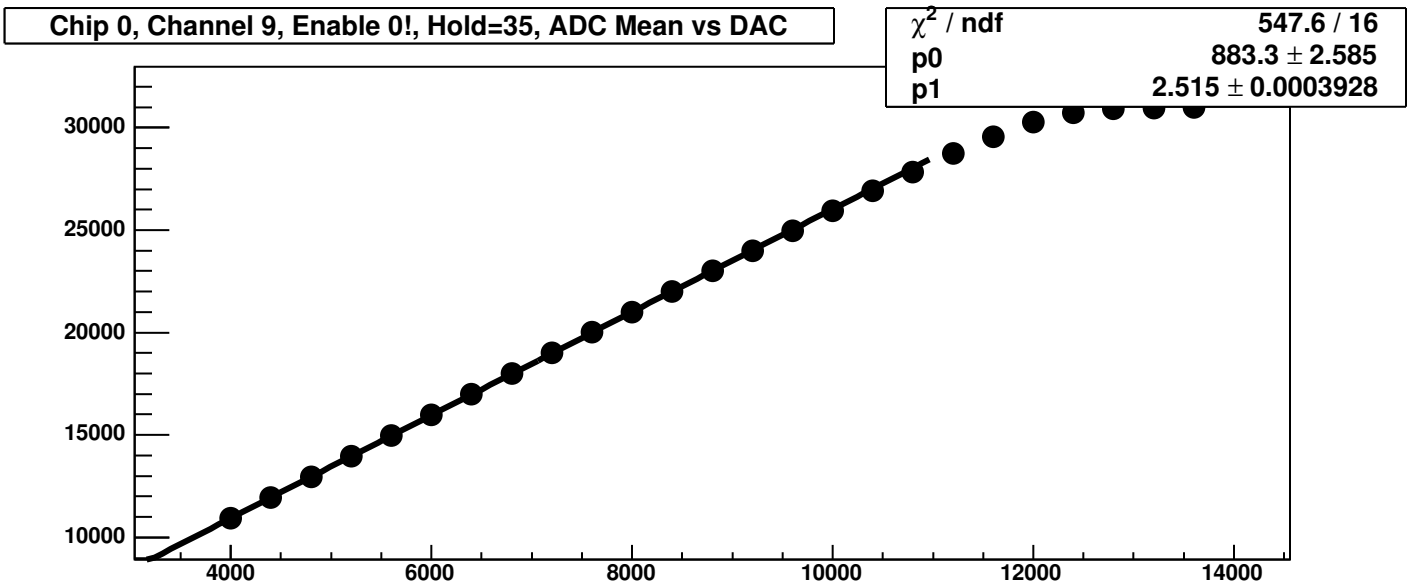


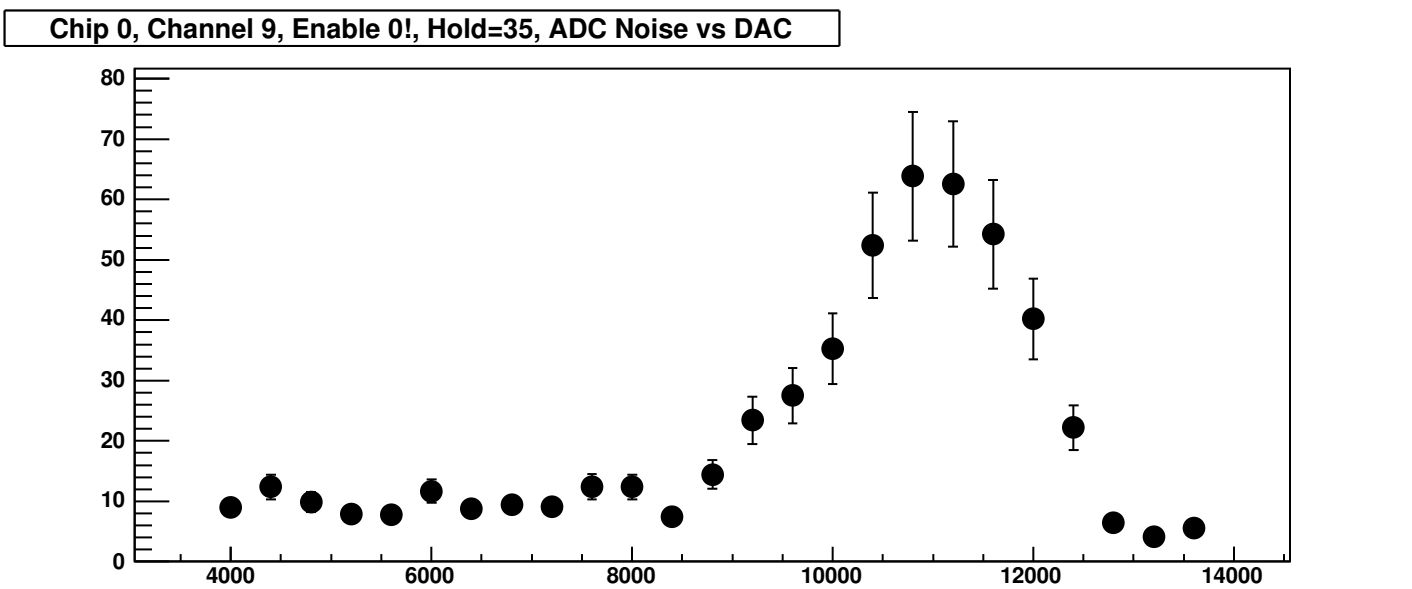
Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

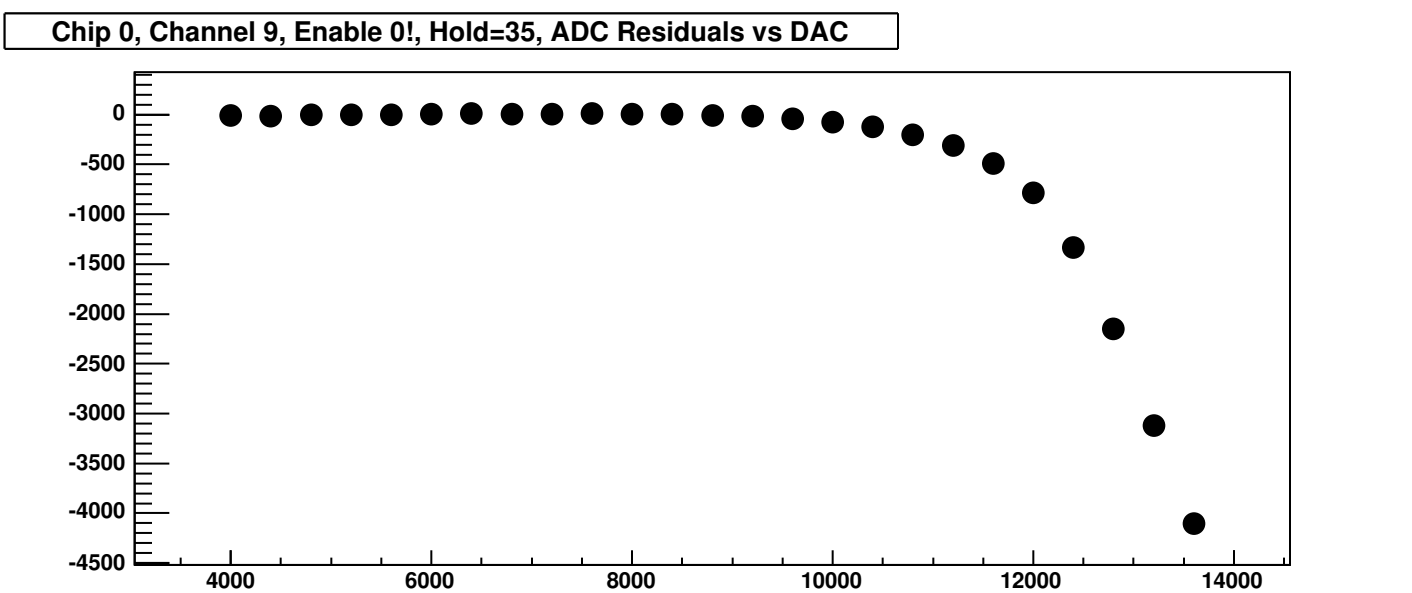


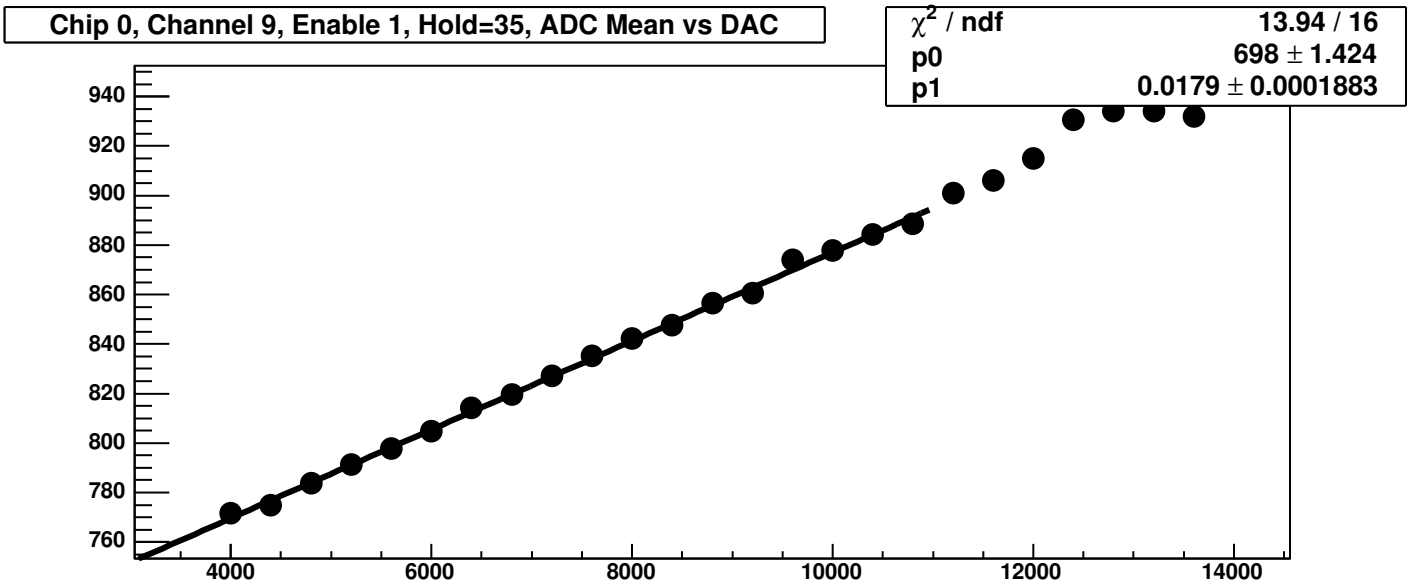
Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

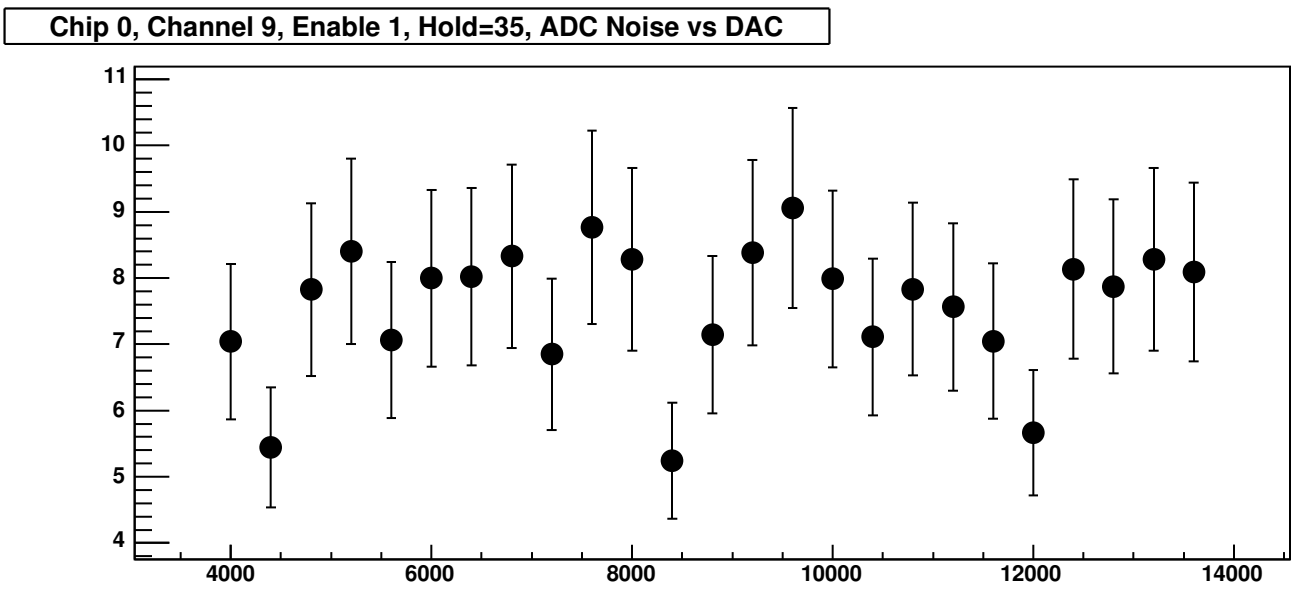


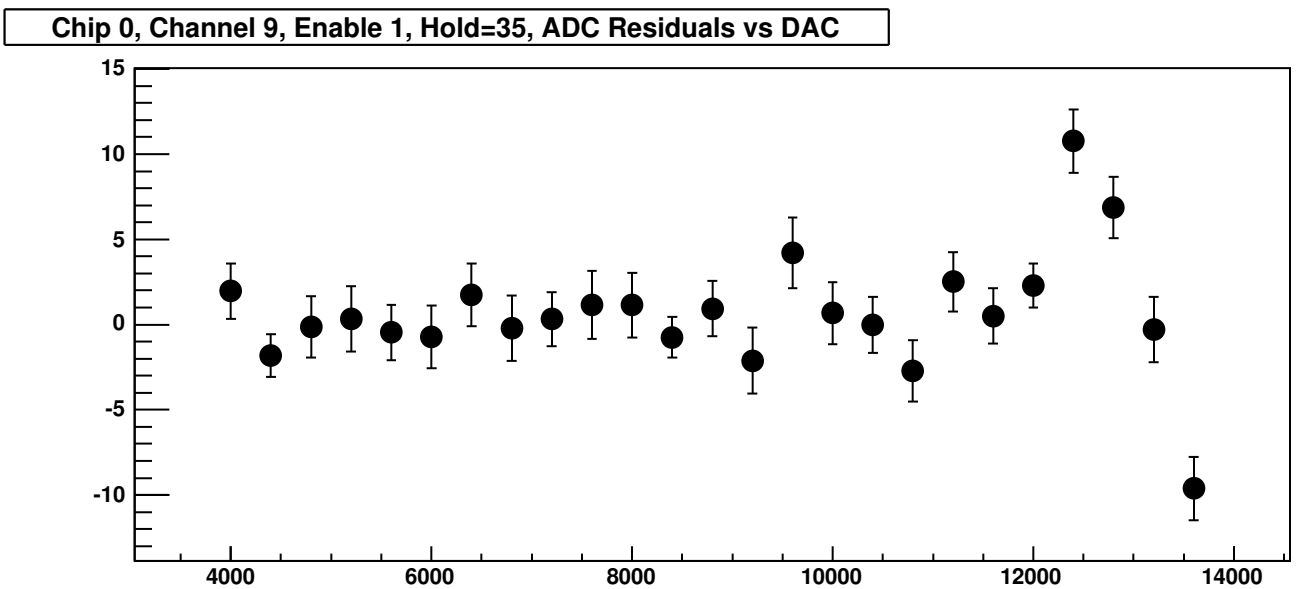

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 547.6 / 16
p0 883.3 ± 2.585
p1 2.515 ± 0.0003928
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

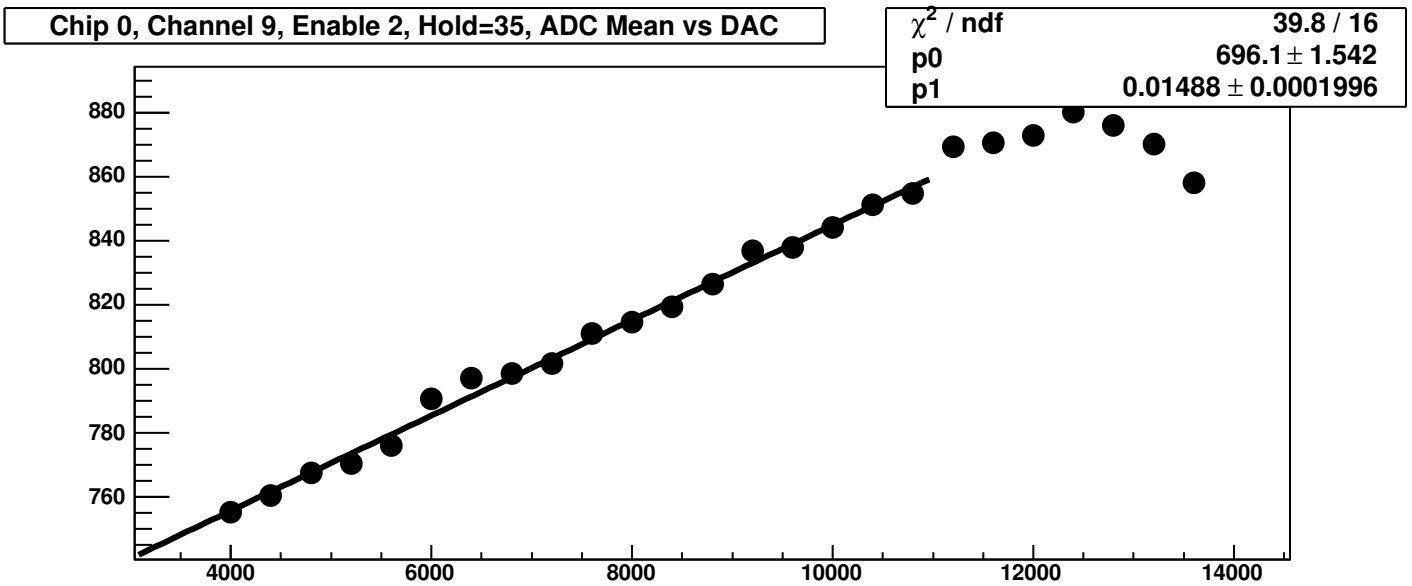

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

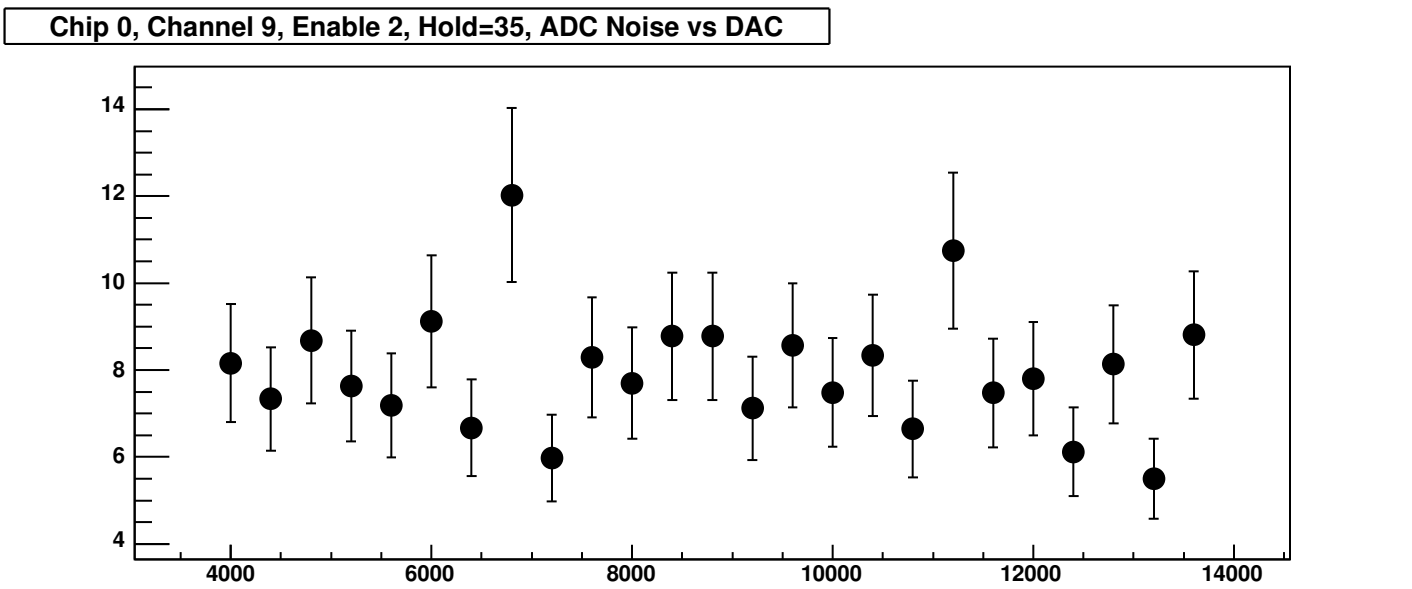

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000

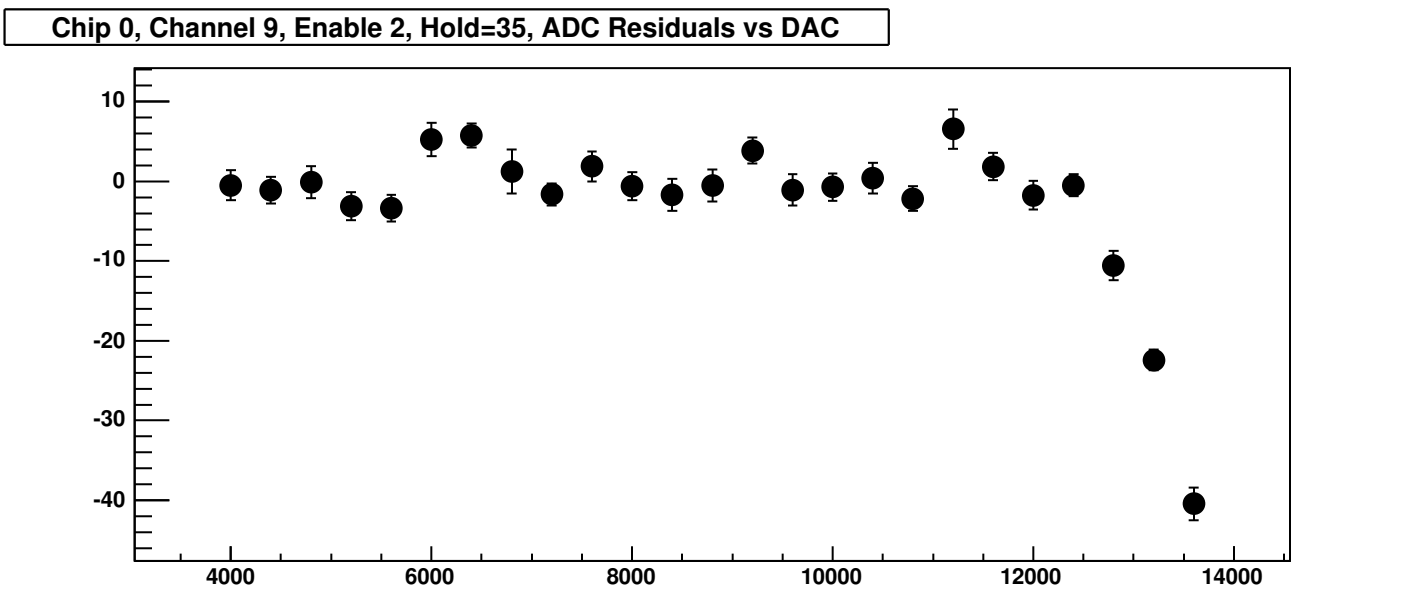

Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 13.94 / 16
p0 698 ± 1.424
p1 0.0179 ± 0.0001883
940
920
900
880
860
840
820
800
780
760
4000
6000
8000
10000
12000
14000

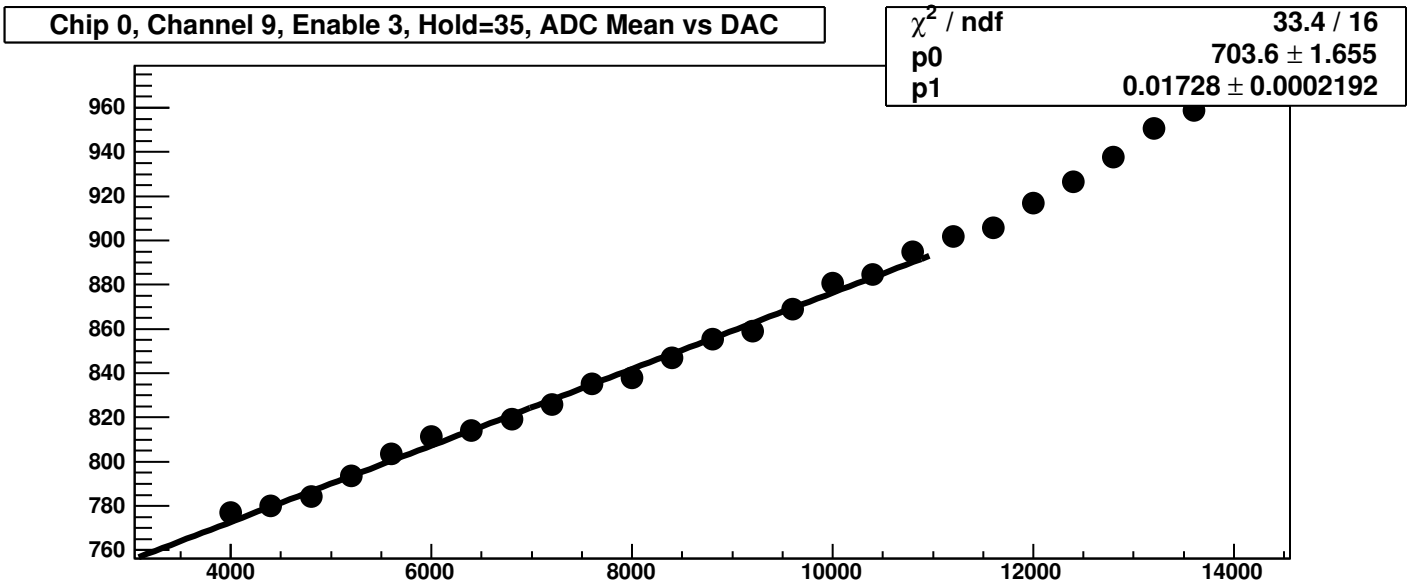

Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

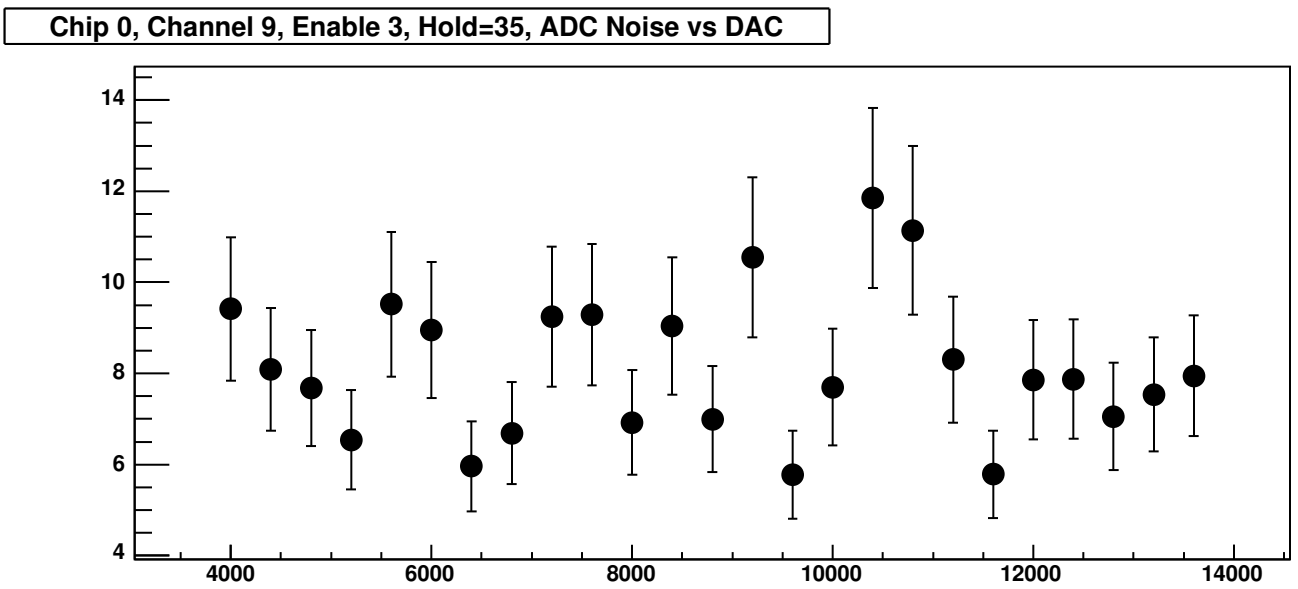

Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

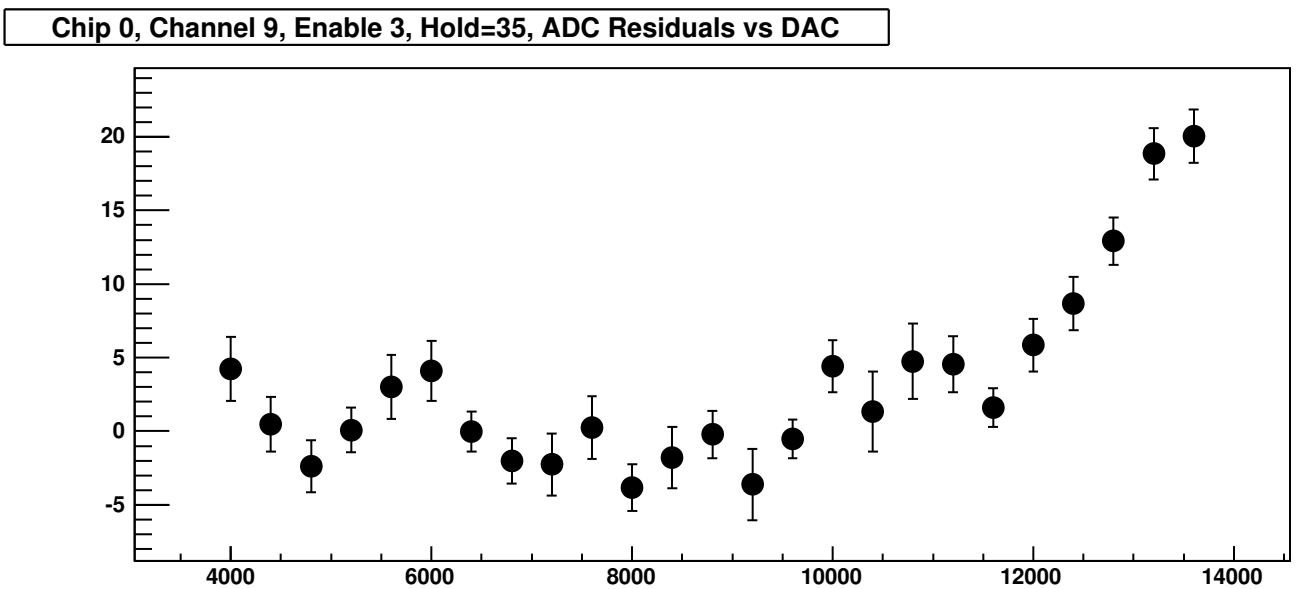

Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.8 / 16
p0 696.1 ± 1.542
p1 0.01488 ± 0.0001996
880
860
840
820
800
780
760
4000
6000
8000
10000
12000
14000

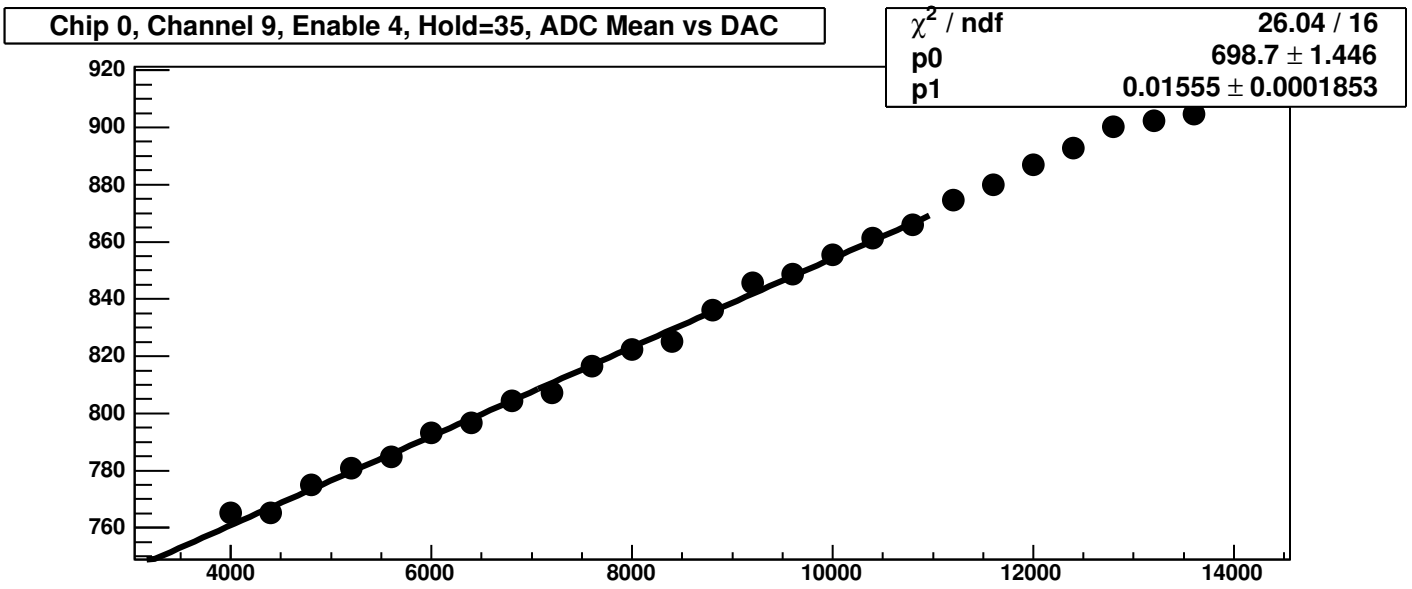

Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

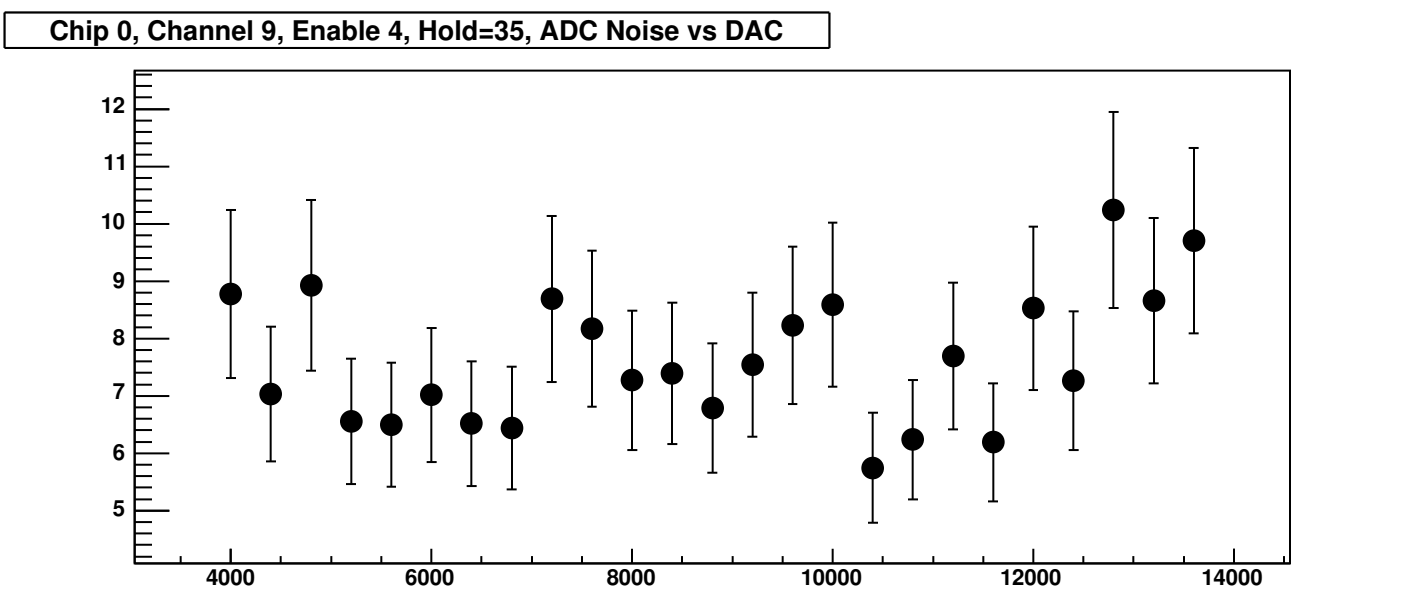

Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

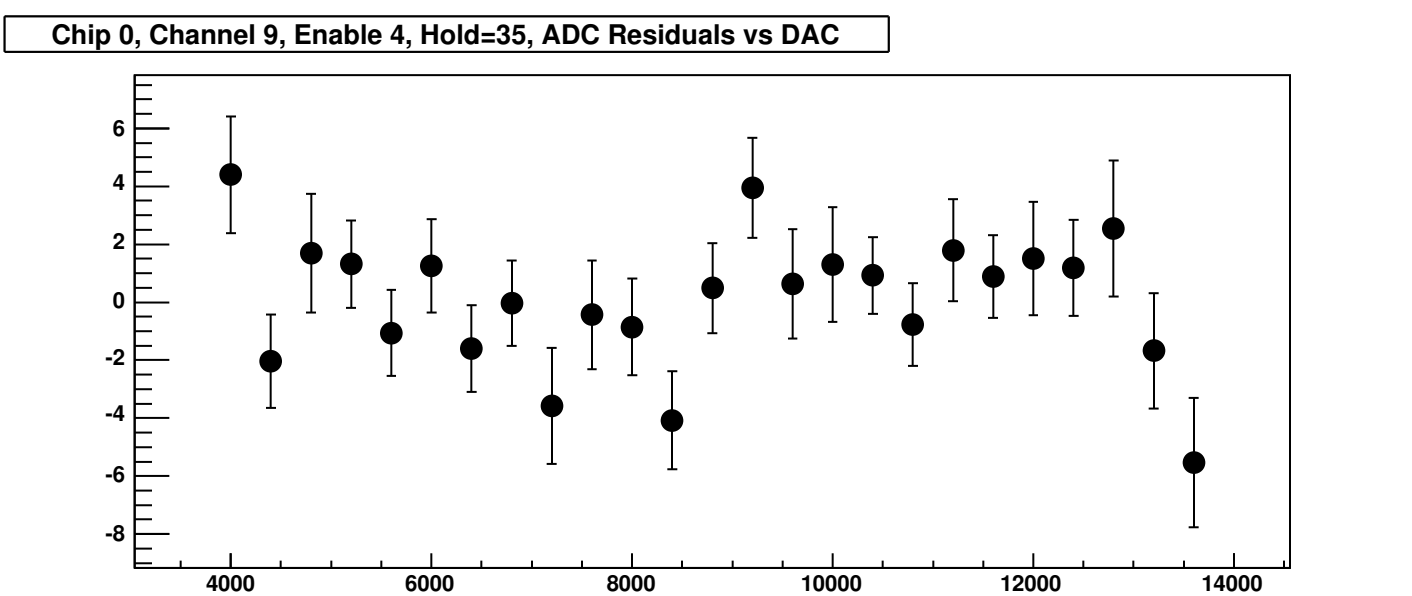

Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.4 / 16
p0 703.6 ± 1.655
p1 0.01728 ± 0.0002192
960
940
920
900
880
860
840
820
800
780
760
4000
6000
8000
10000
12000
14000

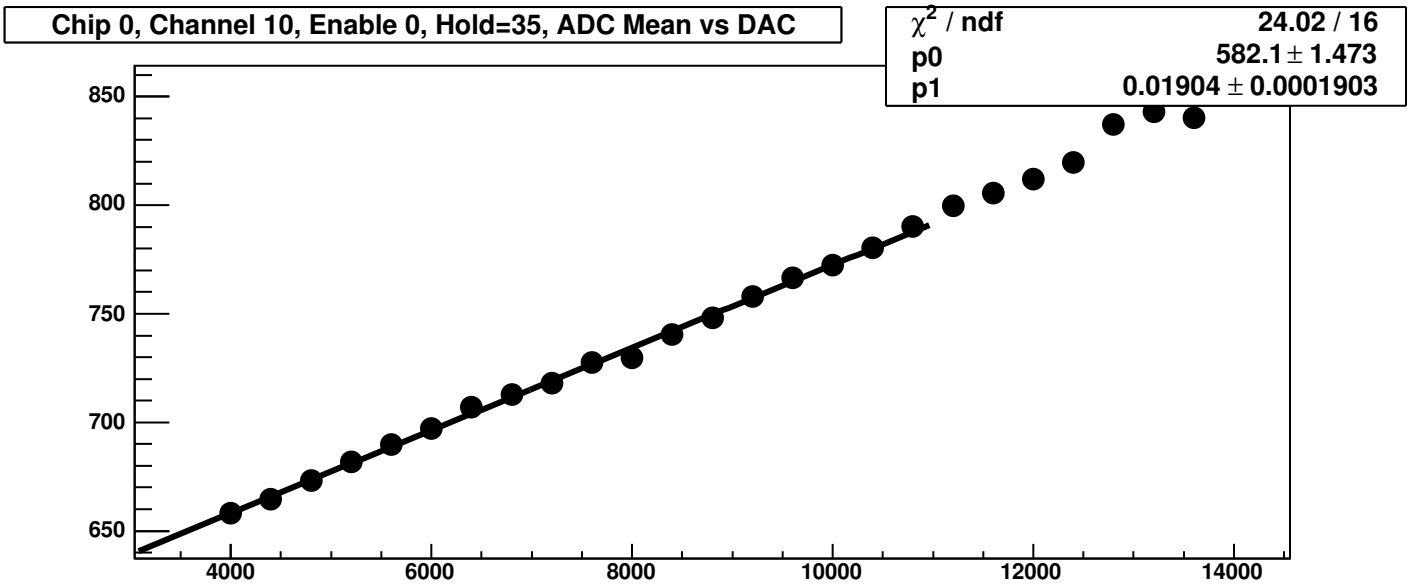

Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

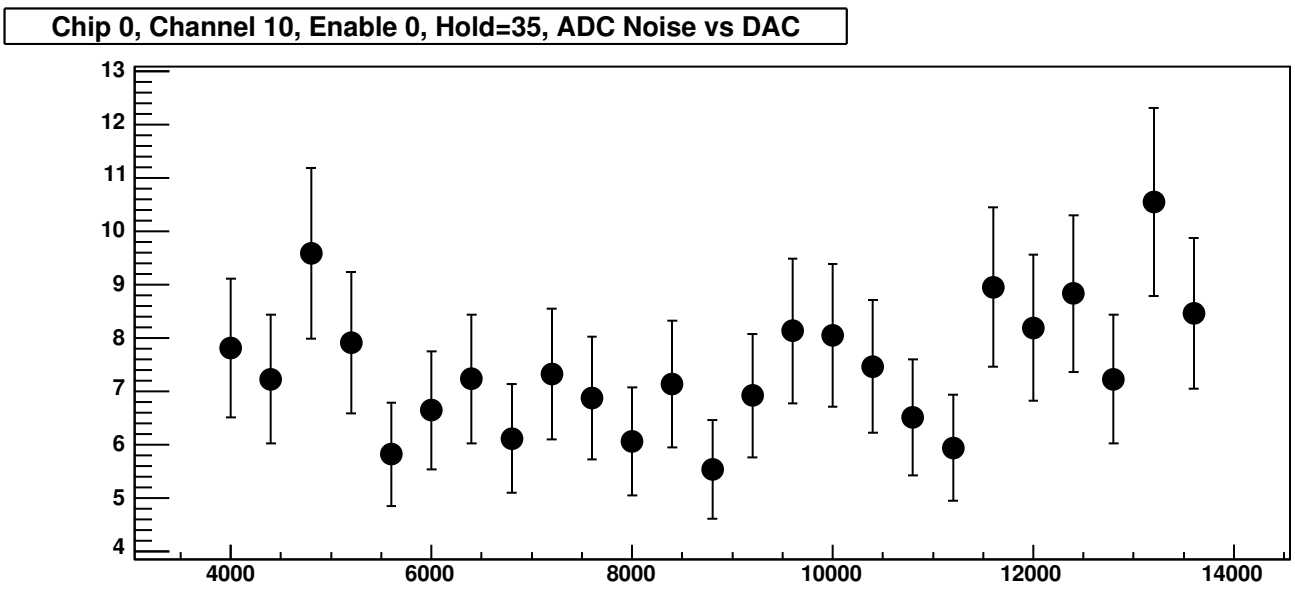

Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

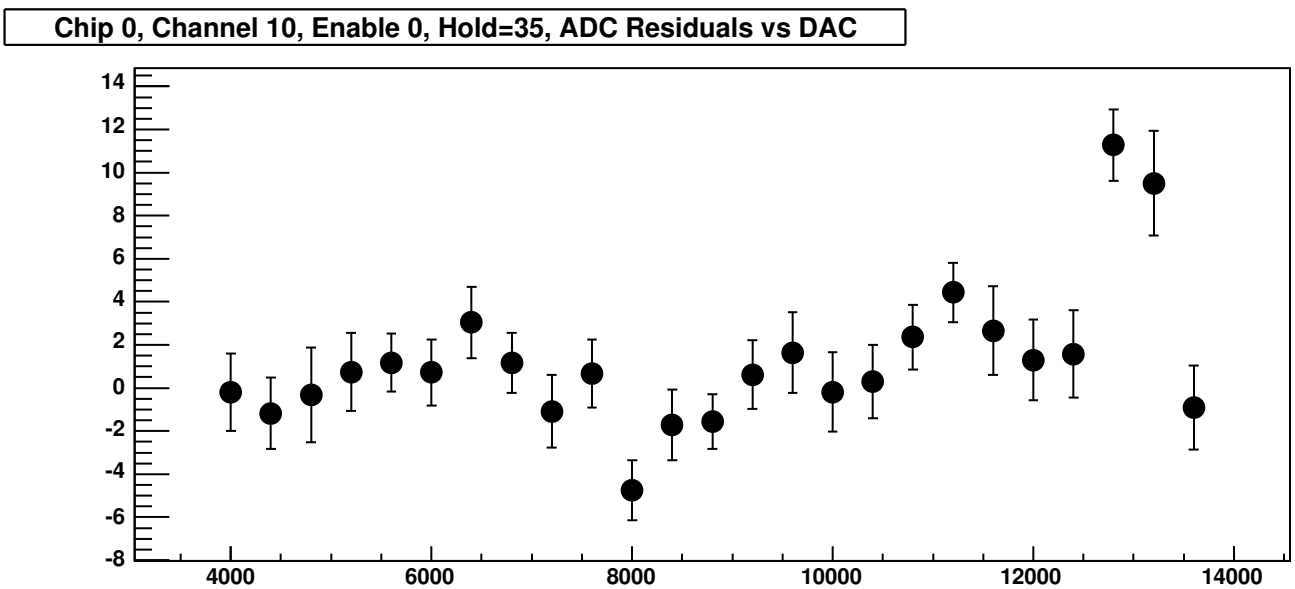

Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.04 / 16
p0 698.7 ± 1.446
p1 0.01555 ± 0.0001853
920
900
880
860
840
820
800
780
760
4000
6000
8000
10000
12000
14000

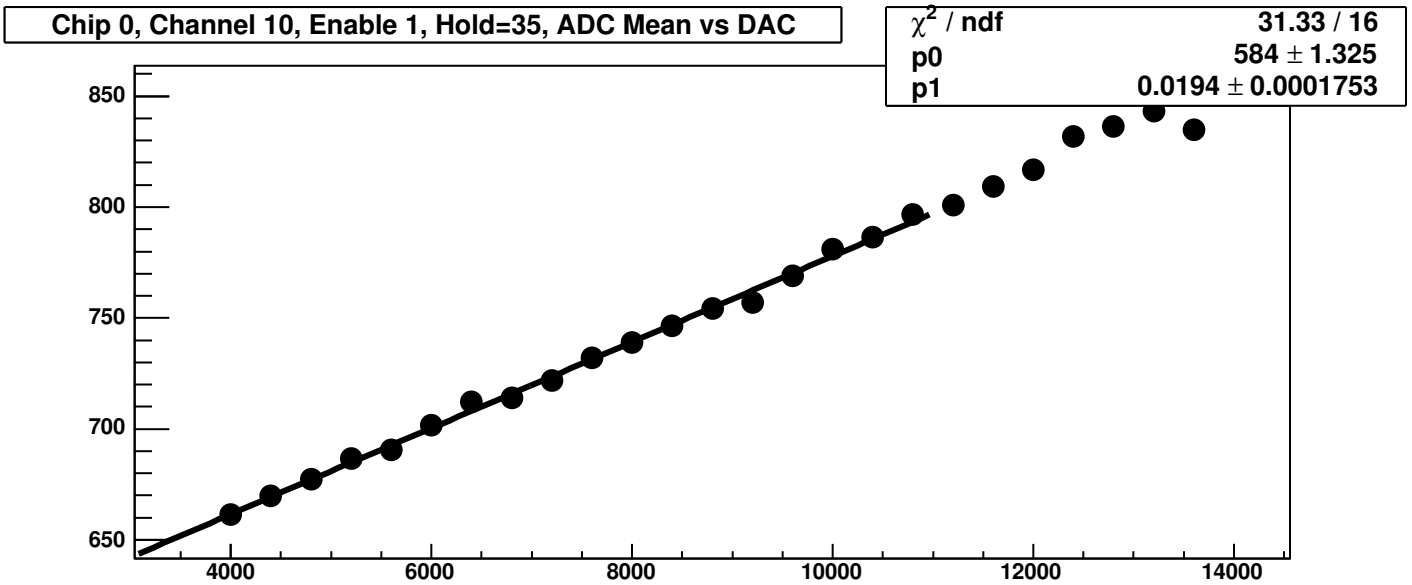

Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

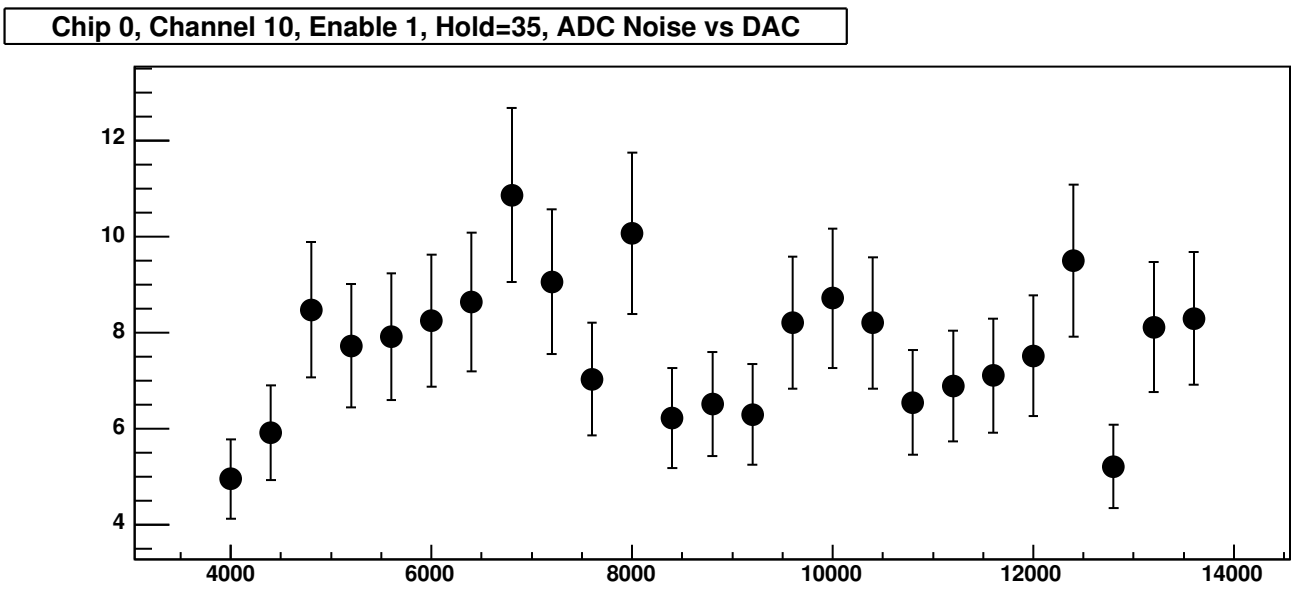

Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

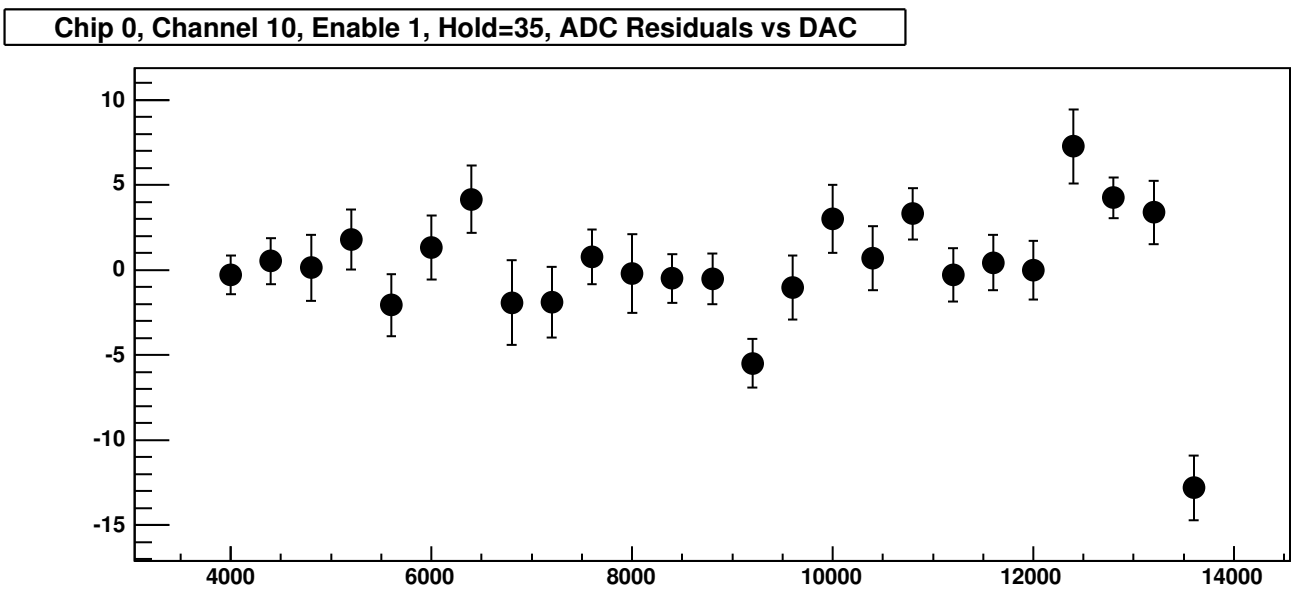

Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 24.02 / 16
p0 582.1 ± 1.473
p1 0.01904 ± 0.0001903
850
800
750
700
650
4000
6000
8000
10000
12000
14000

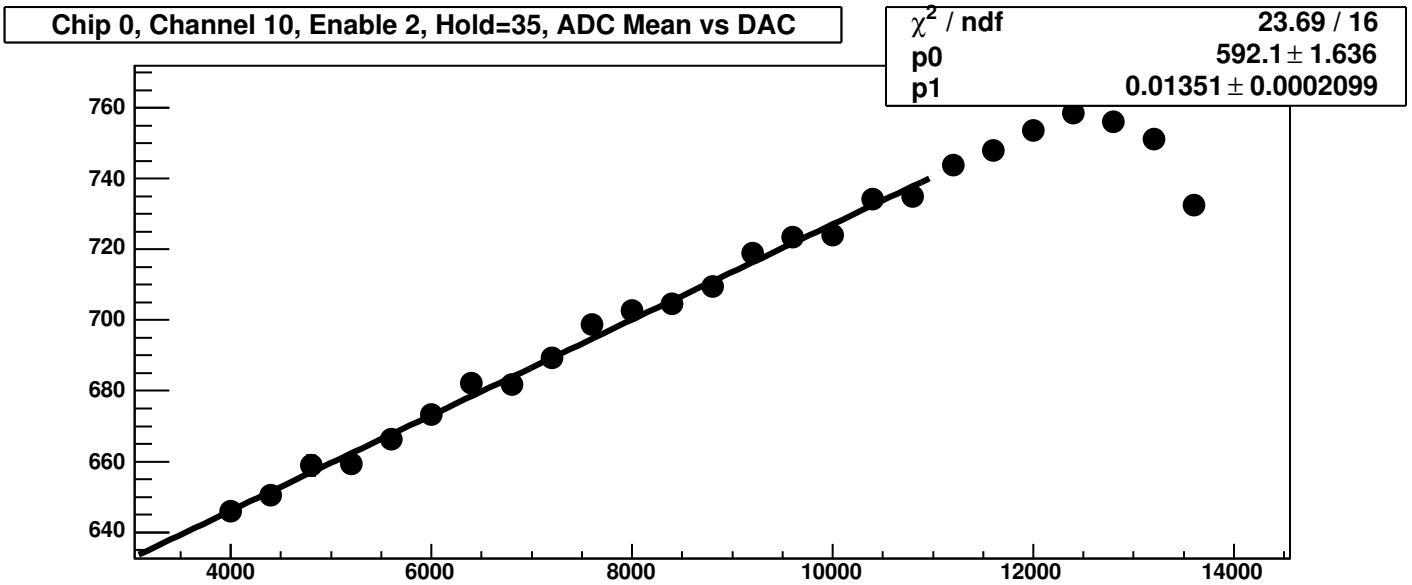

Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

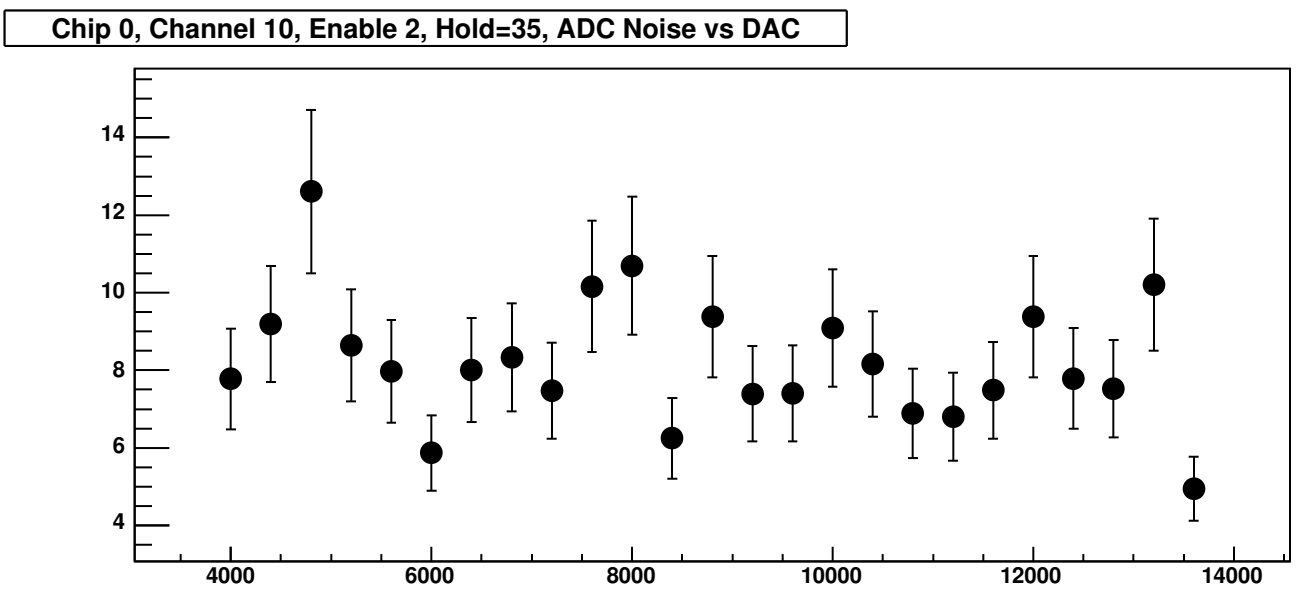

Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

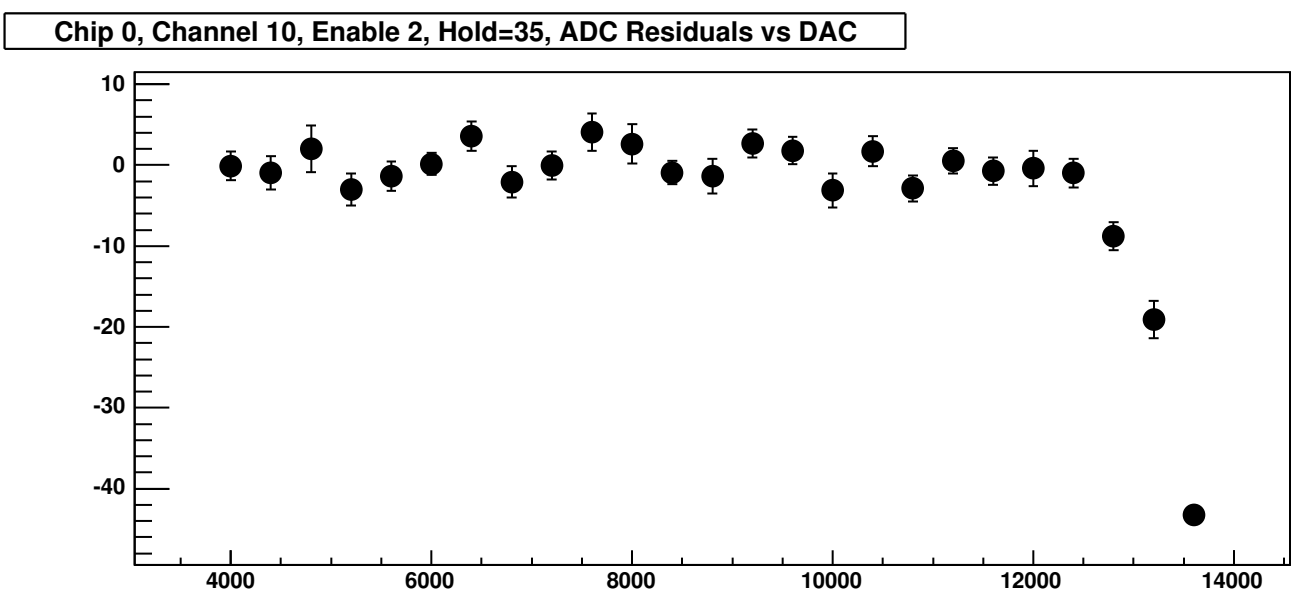

Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.33 / 16
p0 584 ± 1.325
p1 0.0194 ± 0.0001753
850
800
750
700
650
4000
6000
8000
10000
12000
14000

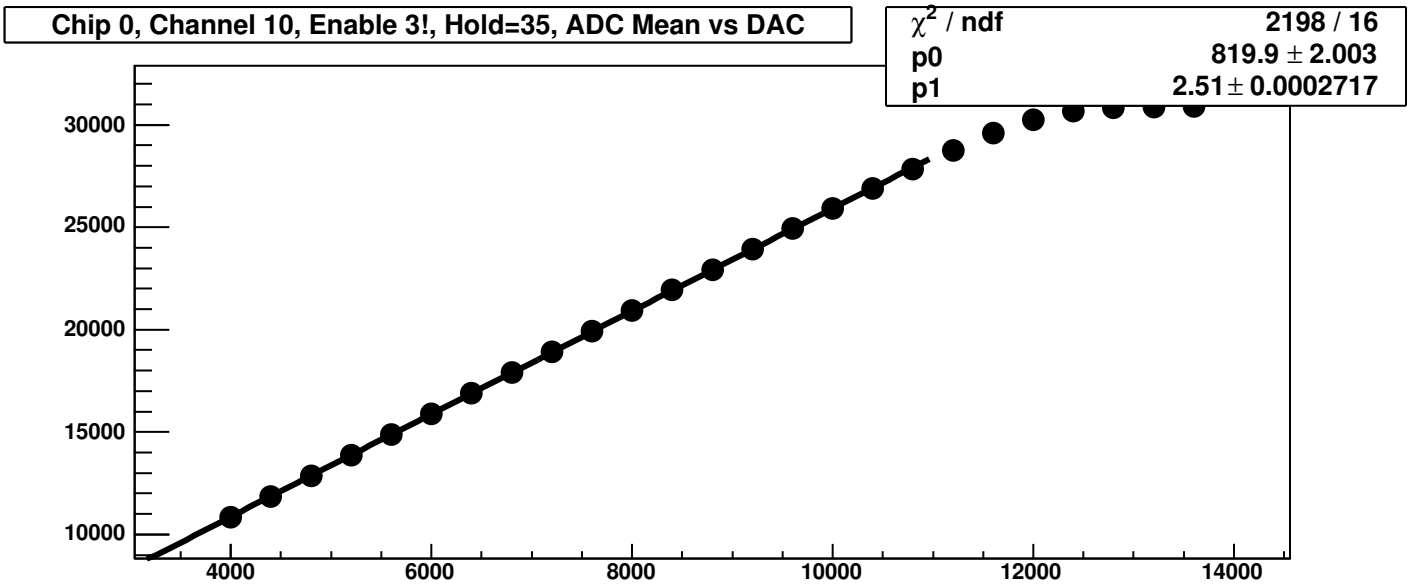

Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

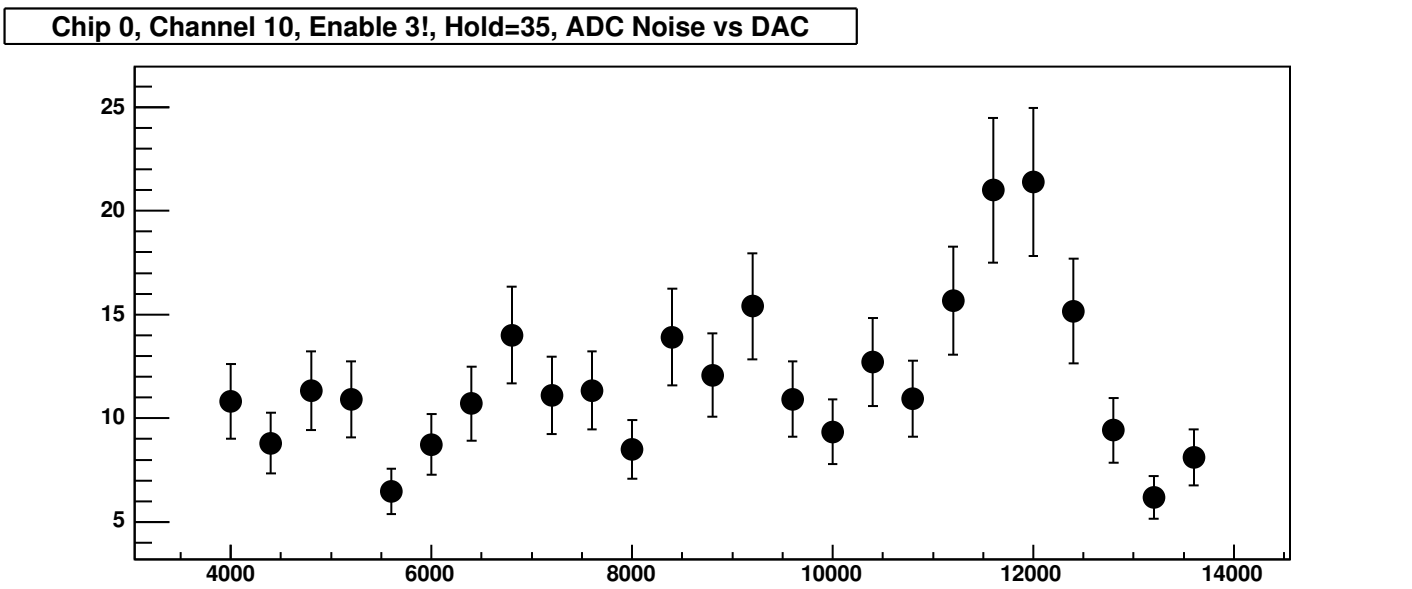

Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

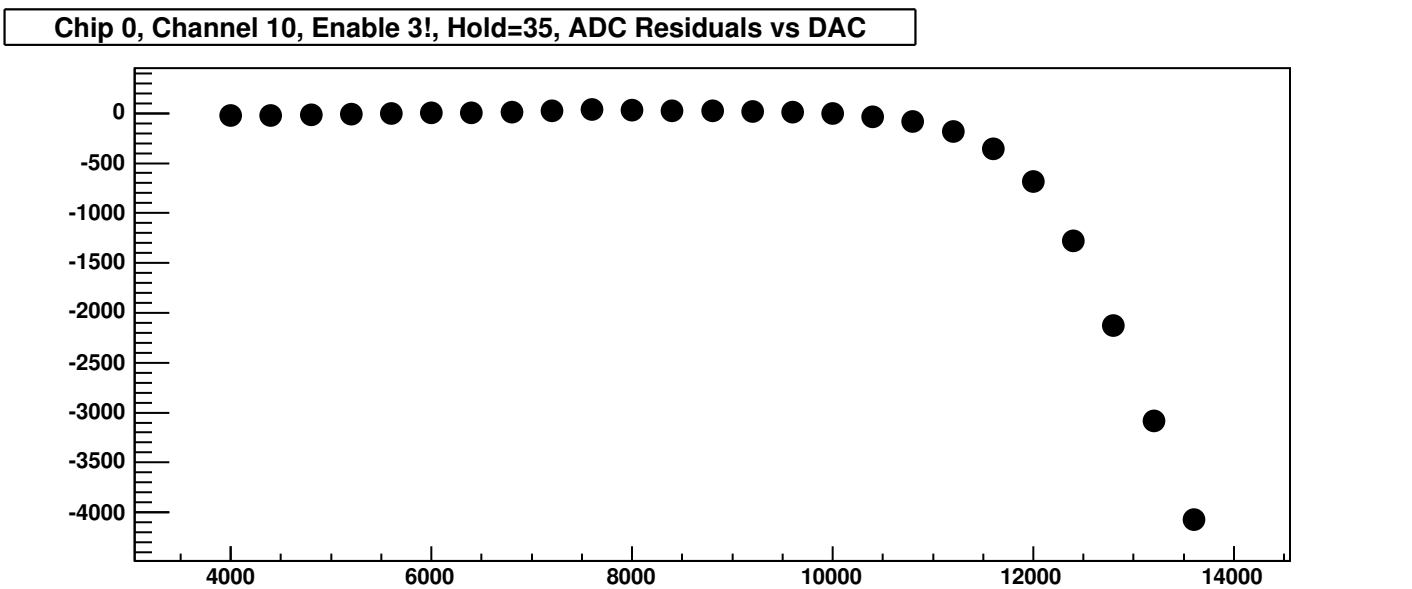

Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.69 / 16
p0 592.1 ± 1.636
p1 0.01351 ± 0.0002099
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

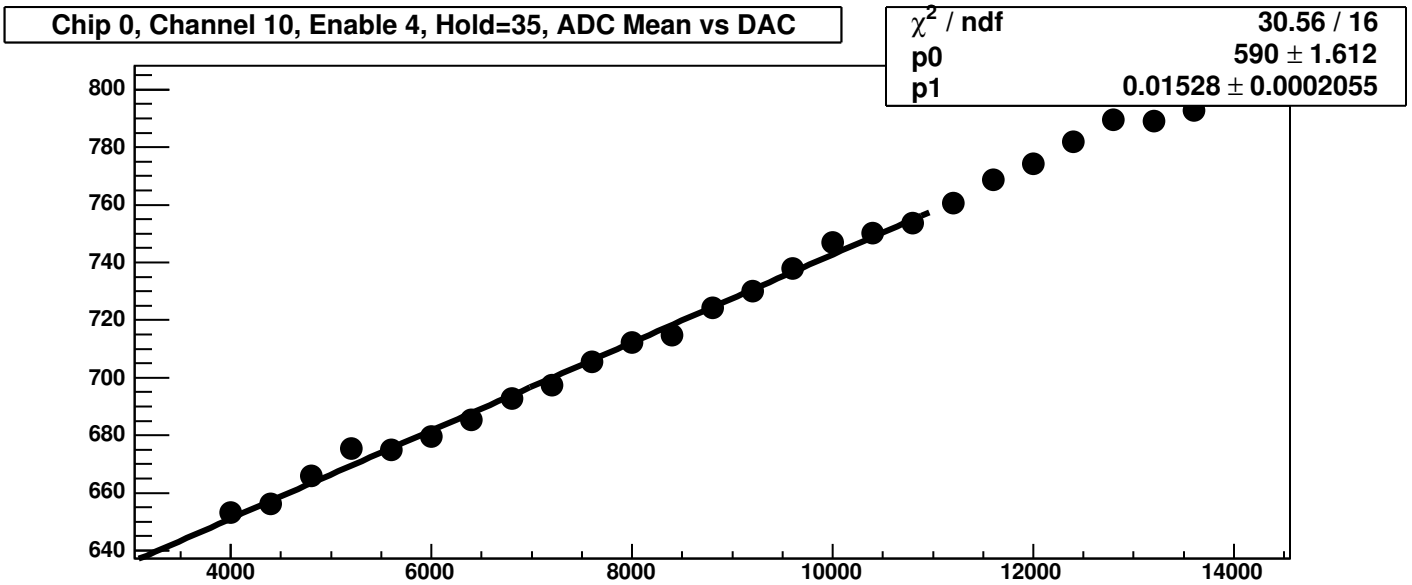

Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

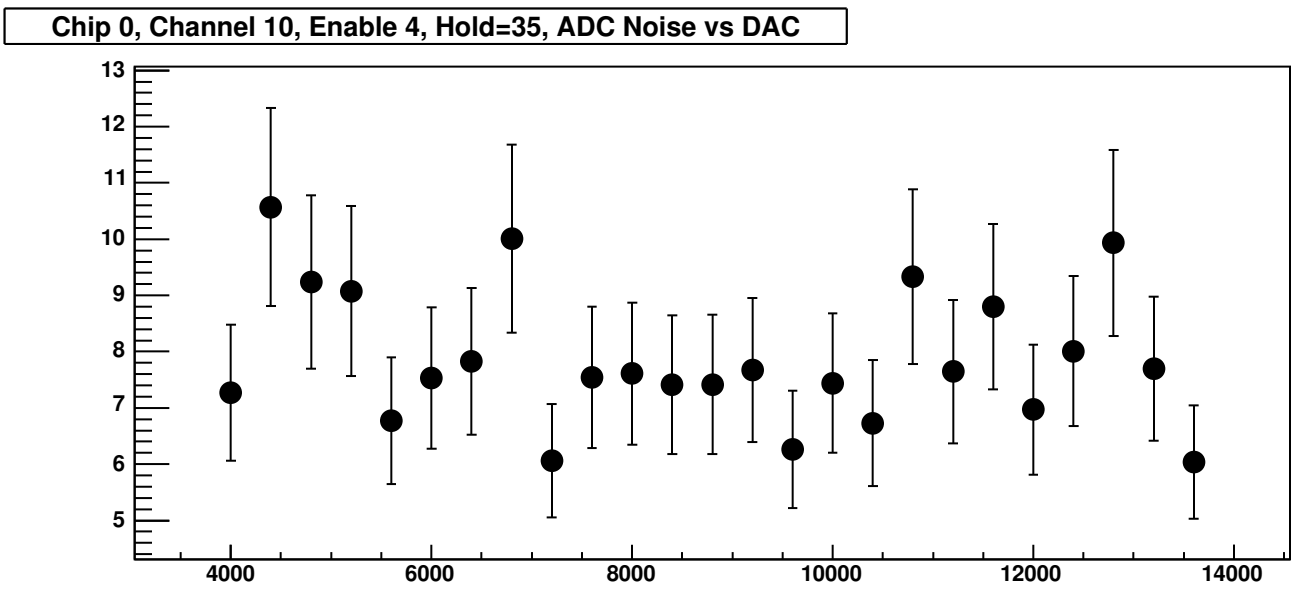

Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

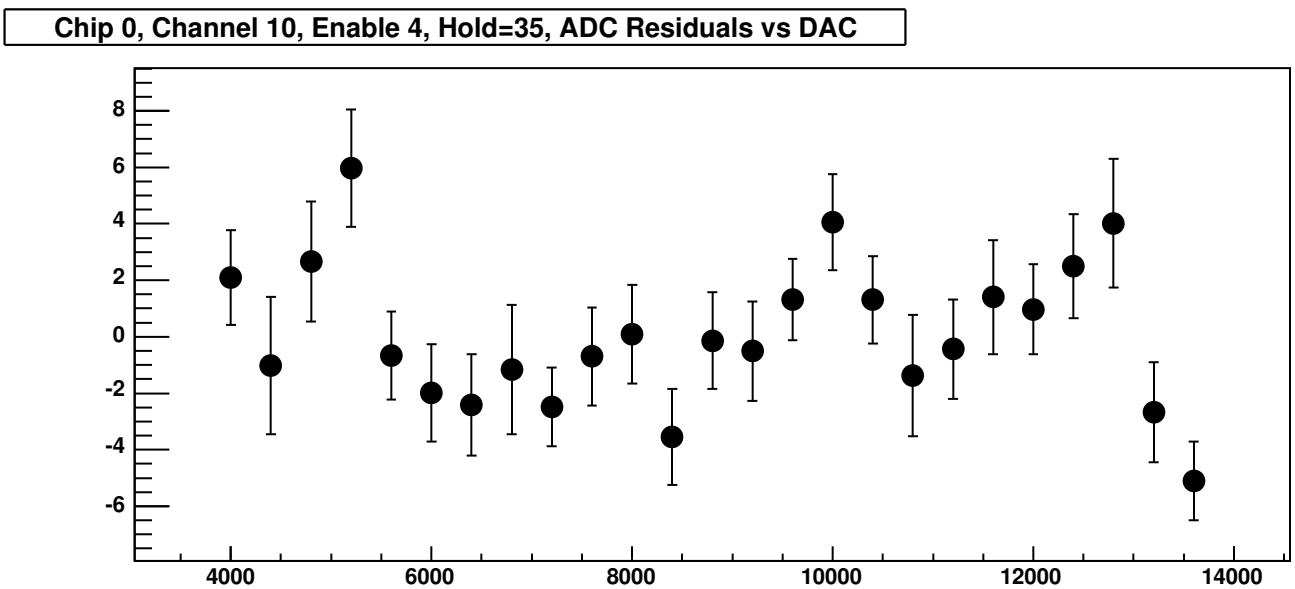

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 2198 / 16
p0 819.9 ± 2.003
p1 2.51 ± 0.0002717
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

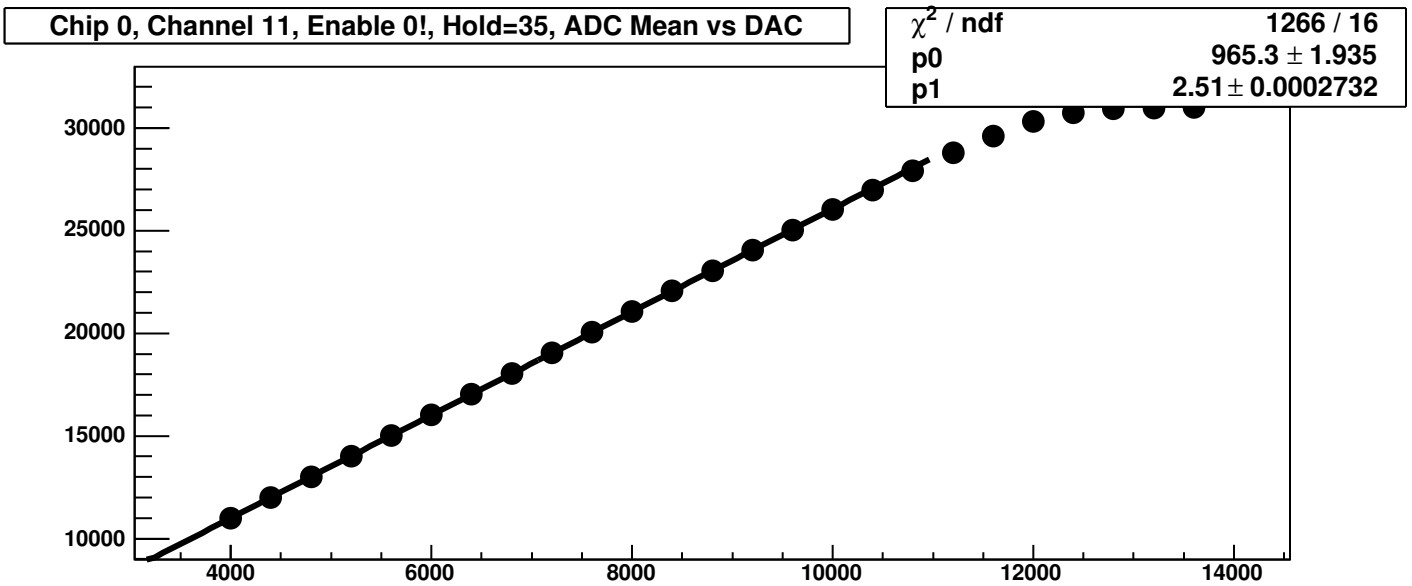

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
4000
6000
8000
10000
12000
14000

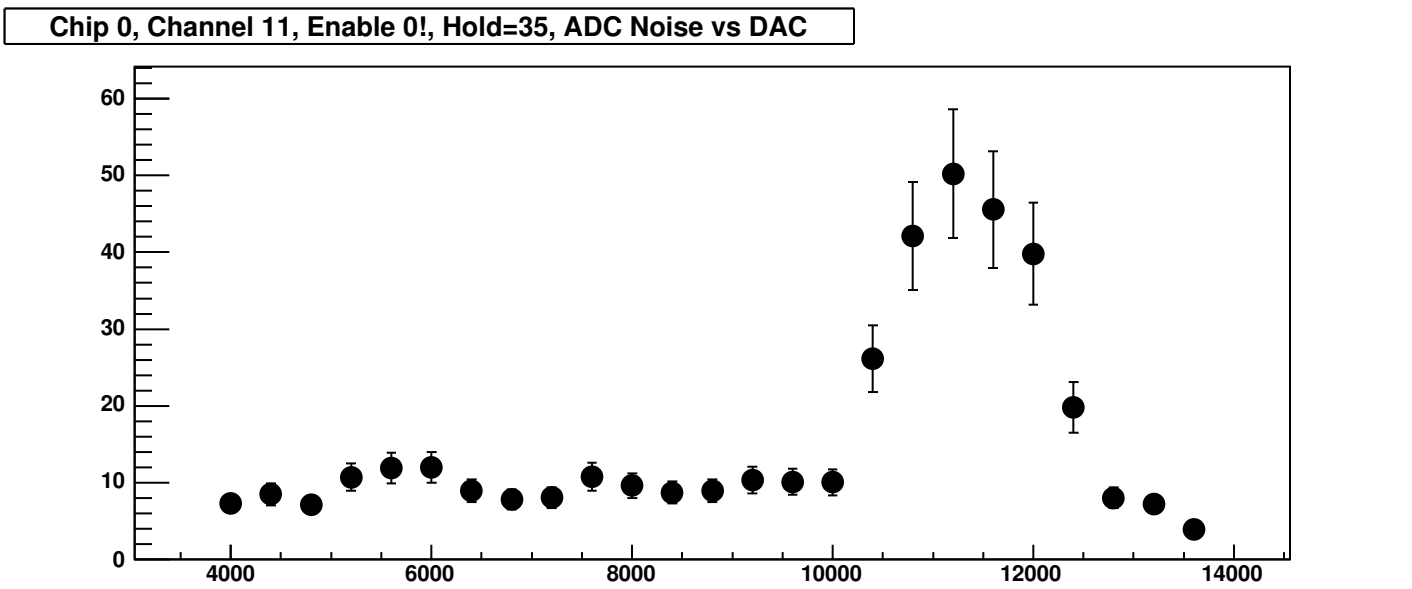

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

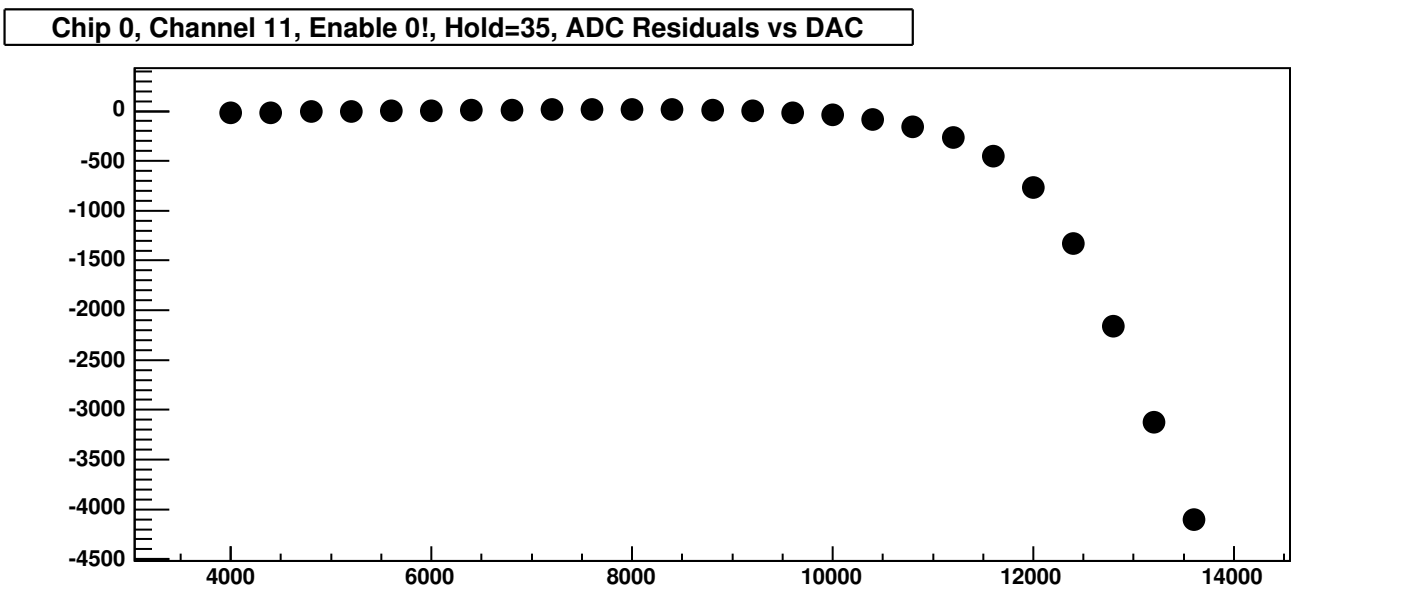

Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.56 / 16
p0 590 ± 1.612
p1 0.01528 ± 0.0002055
800
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

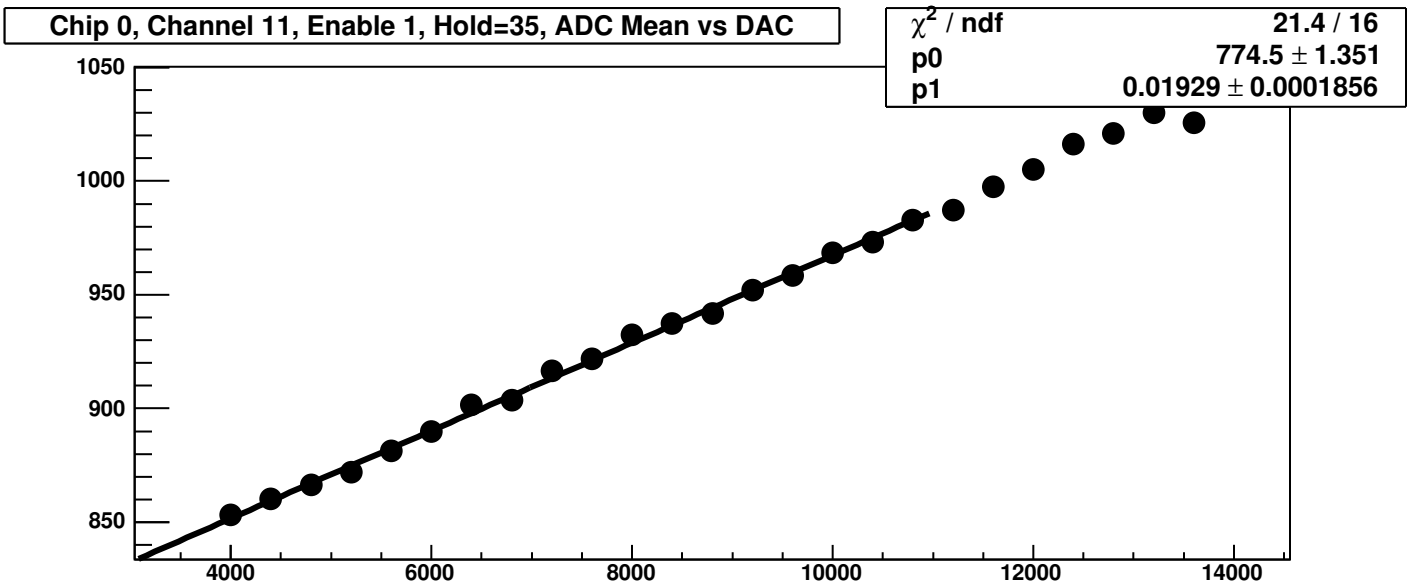

Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

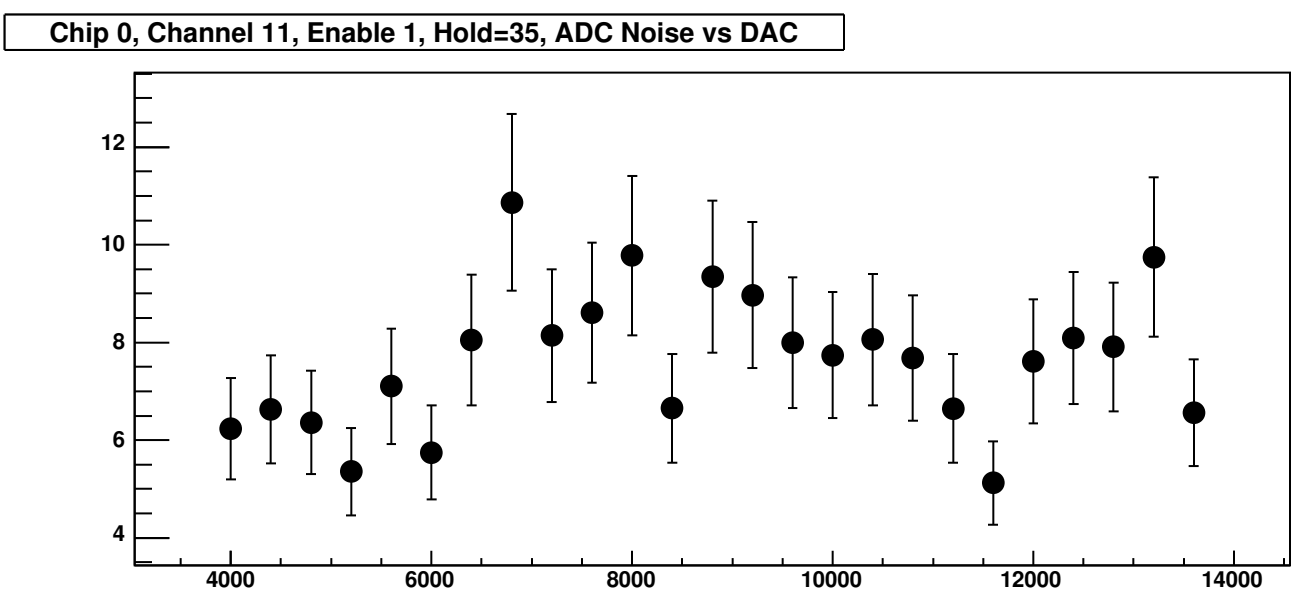

Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

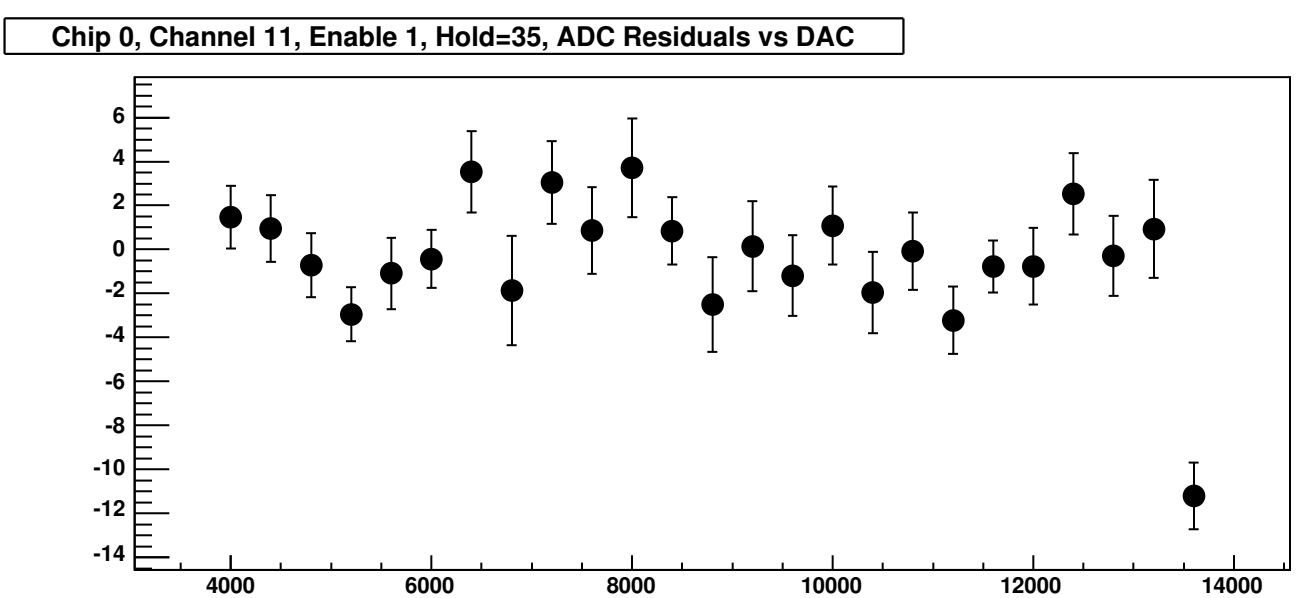

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1266 / 16
p0 965.3 ± 1.935
p1 2.51 ± 0.0002732
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

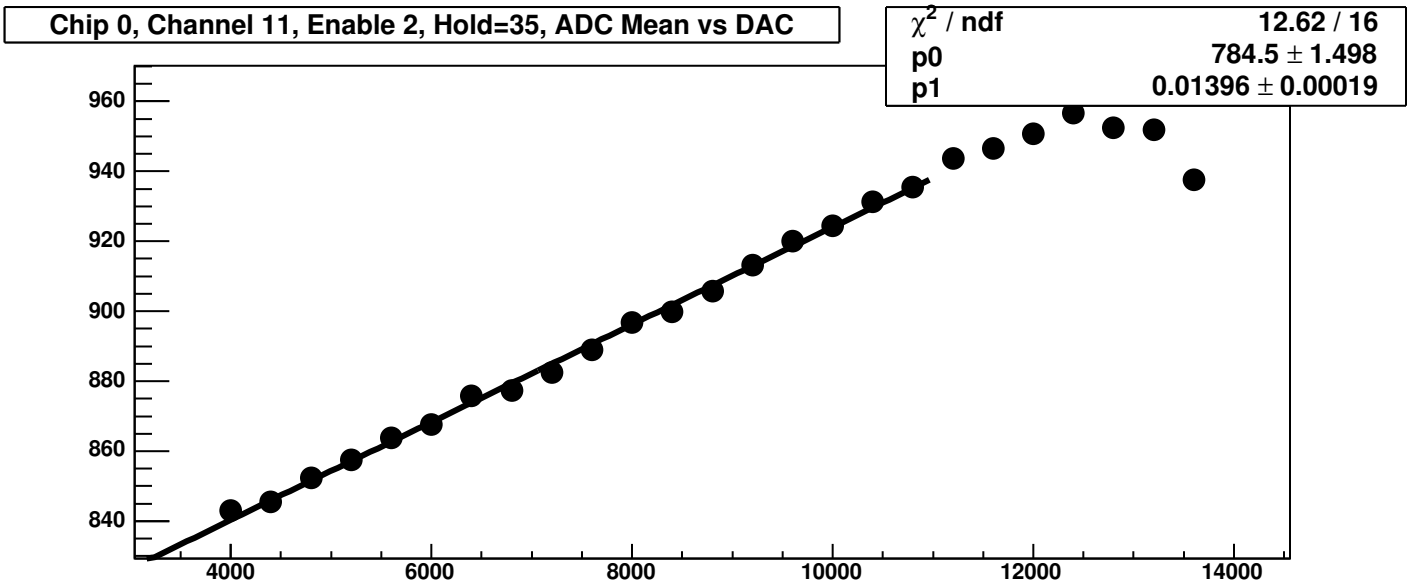

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

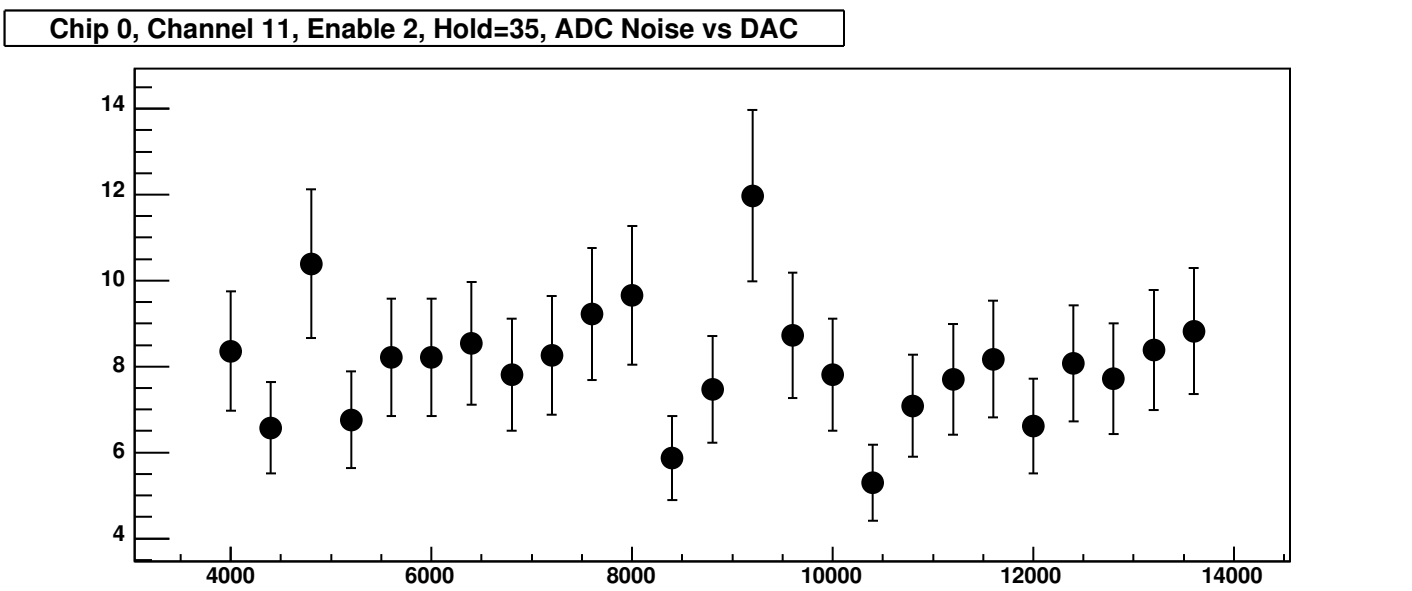

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000

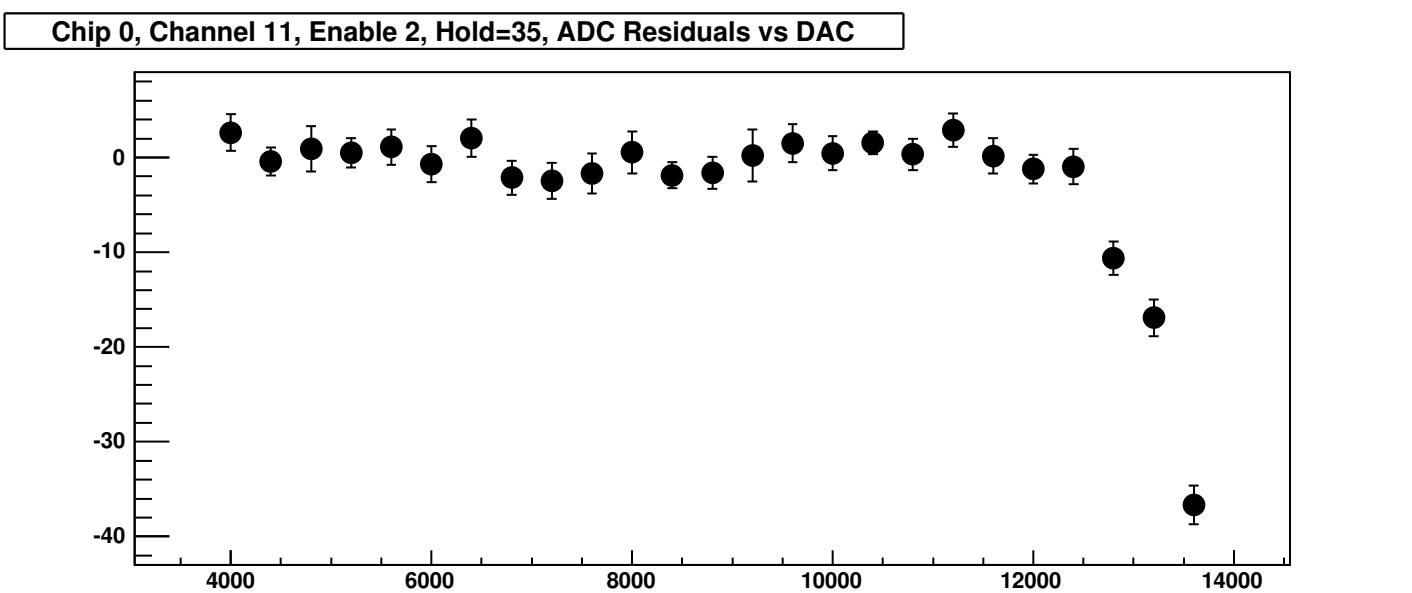

Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.4 / 16
p0 774.5 ± 1.351
p1 0.01929 ± 0.0001856
1050
1000
950
900
850
4000
6000
8000
10000
12000
14000

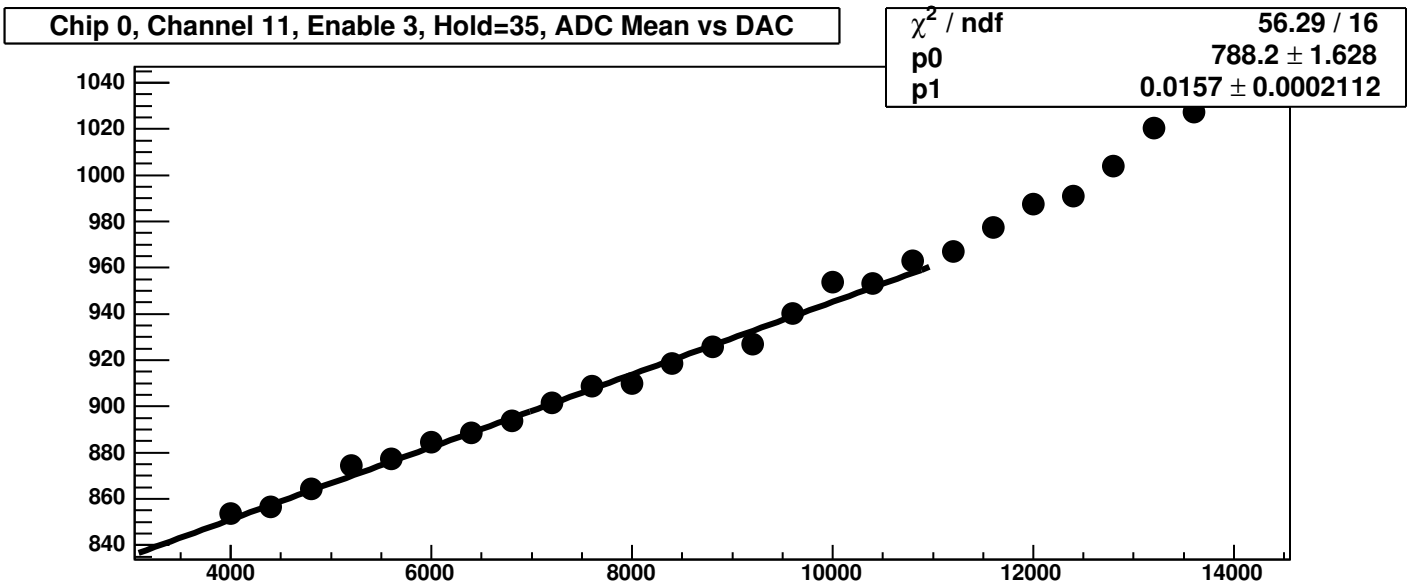

Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

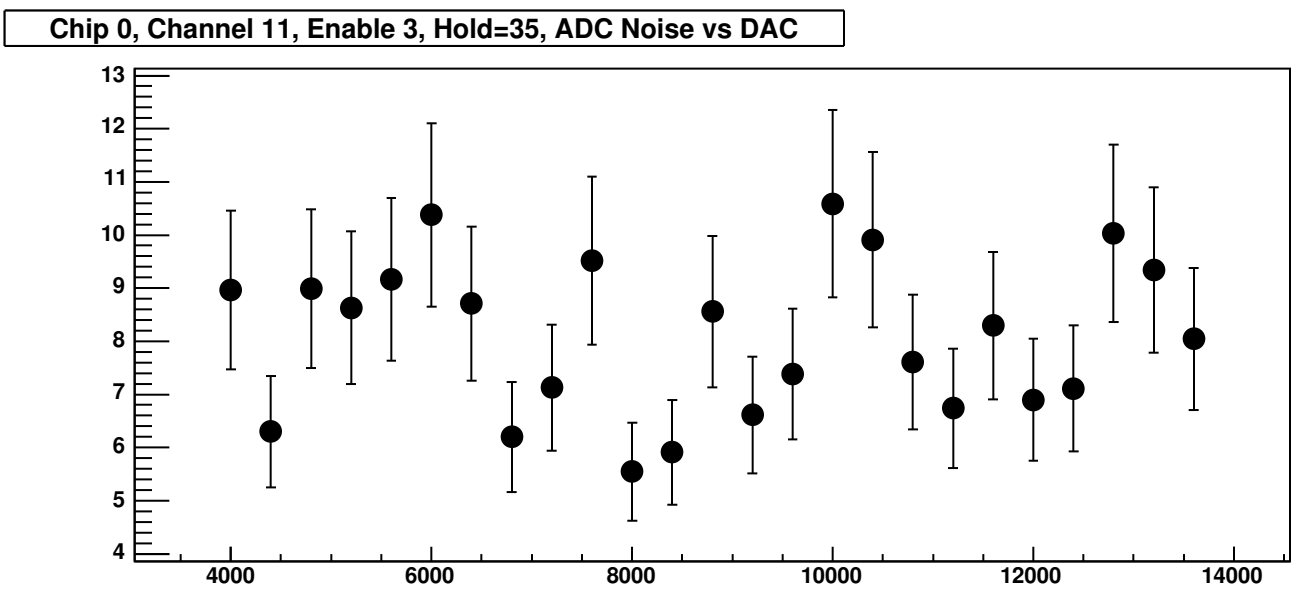

Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
4000
6000
8000
10000
12000
14000

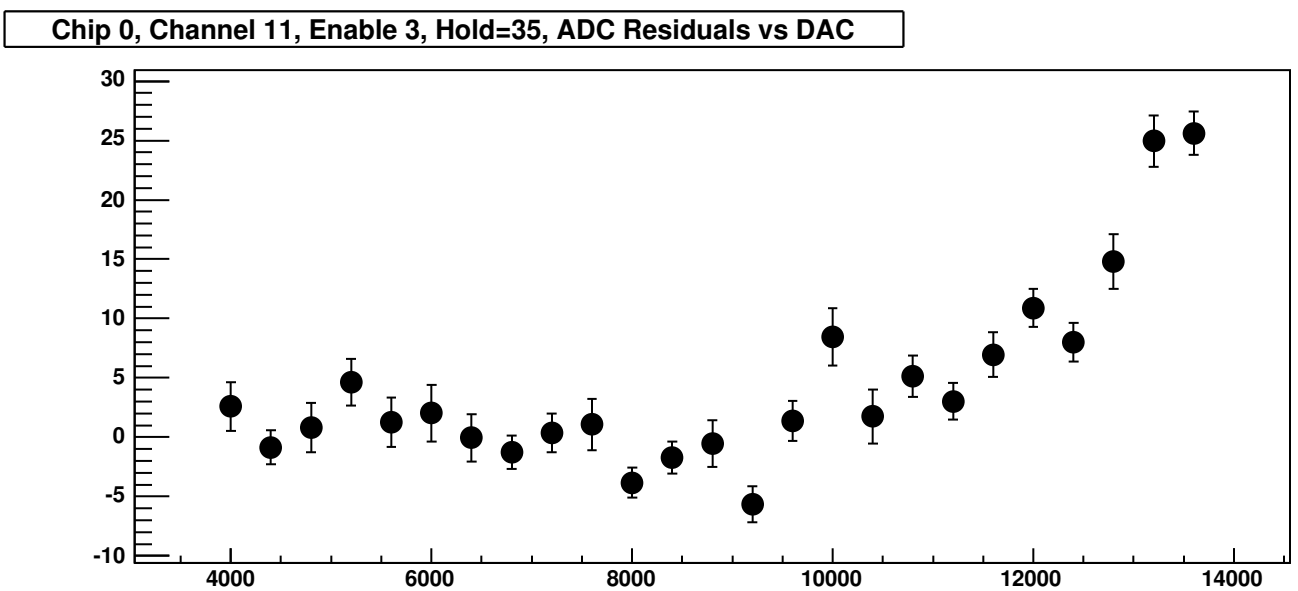

Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 12.62 / 16
p0 784.5 ± 1.498
p1 0.01396 ± 0.00019
960
940
920
900
880
860
840
4000
6000
8000
10000
12000
14000

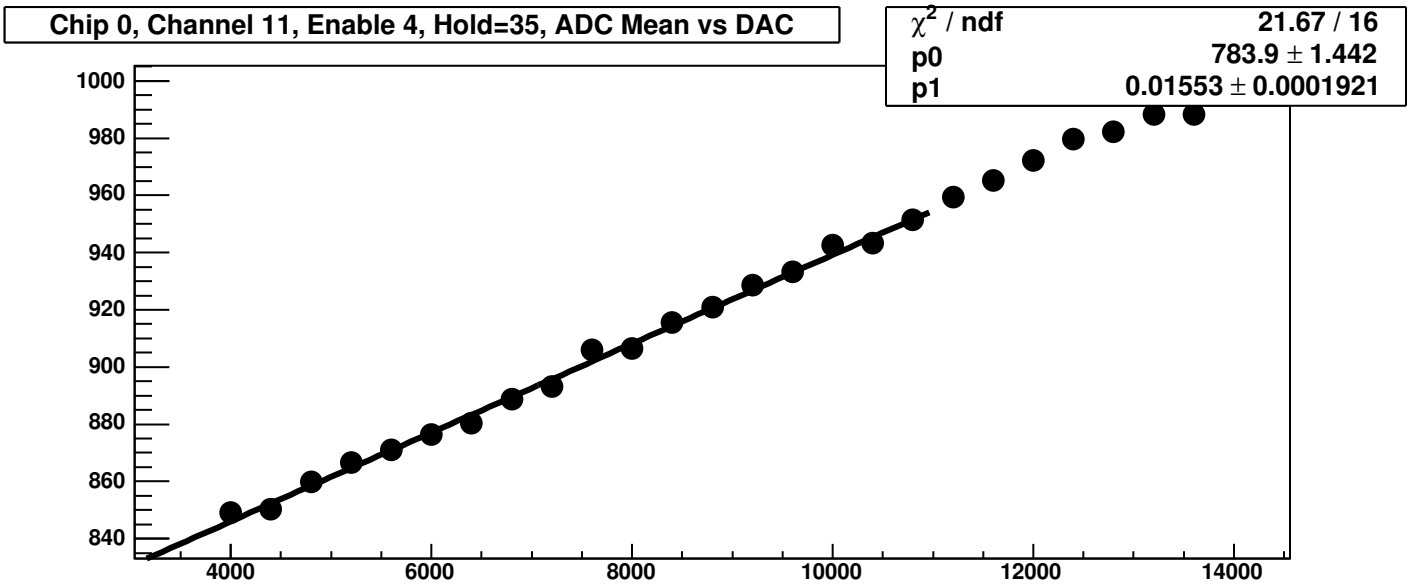

Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

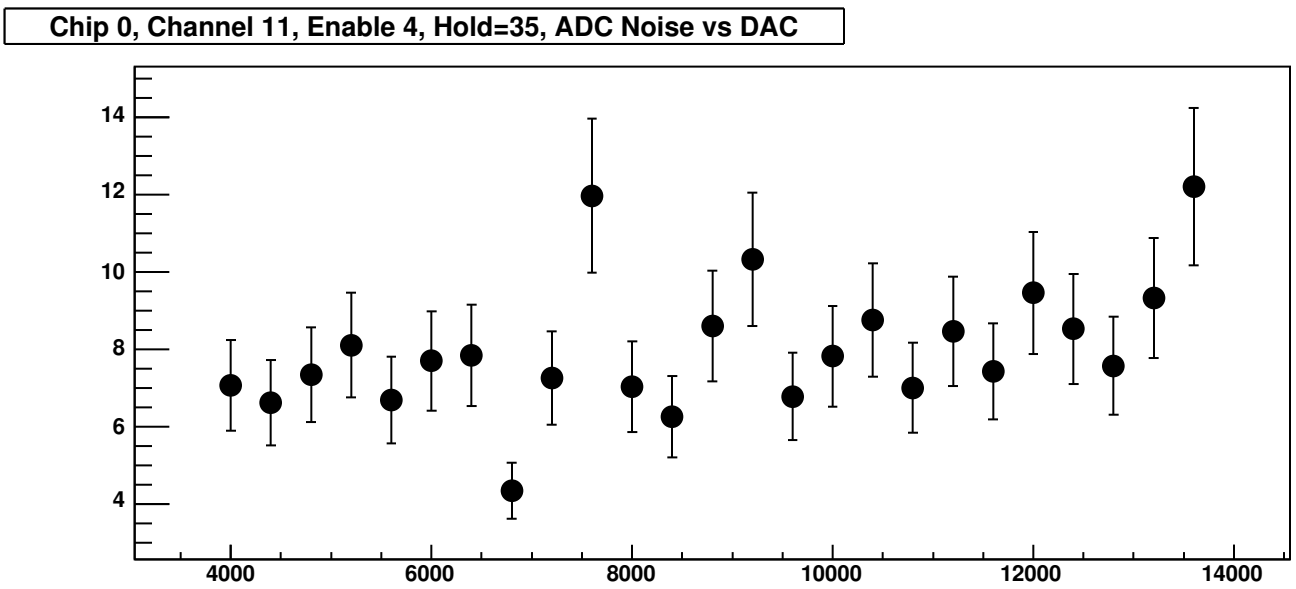

Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

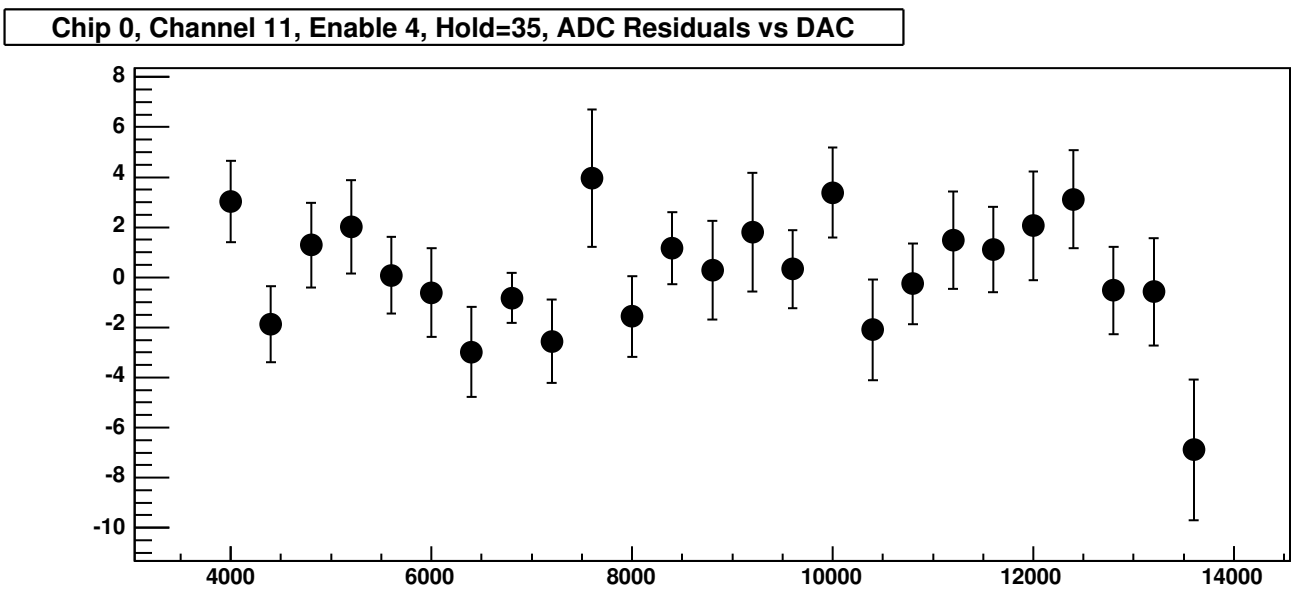

Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 56.29 / 16
p0 788.2 ± 1.628
p1 0.0157 ± 0.0002112
1040
1020
1000
980
960
940
920
900
880
860
840
4000
6000
8000
10000
12000
14000

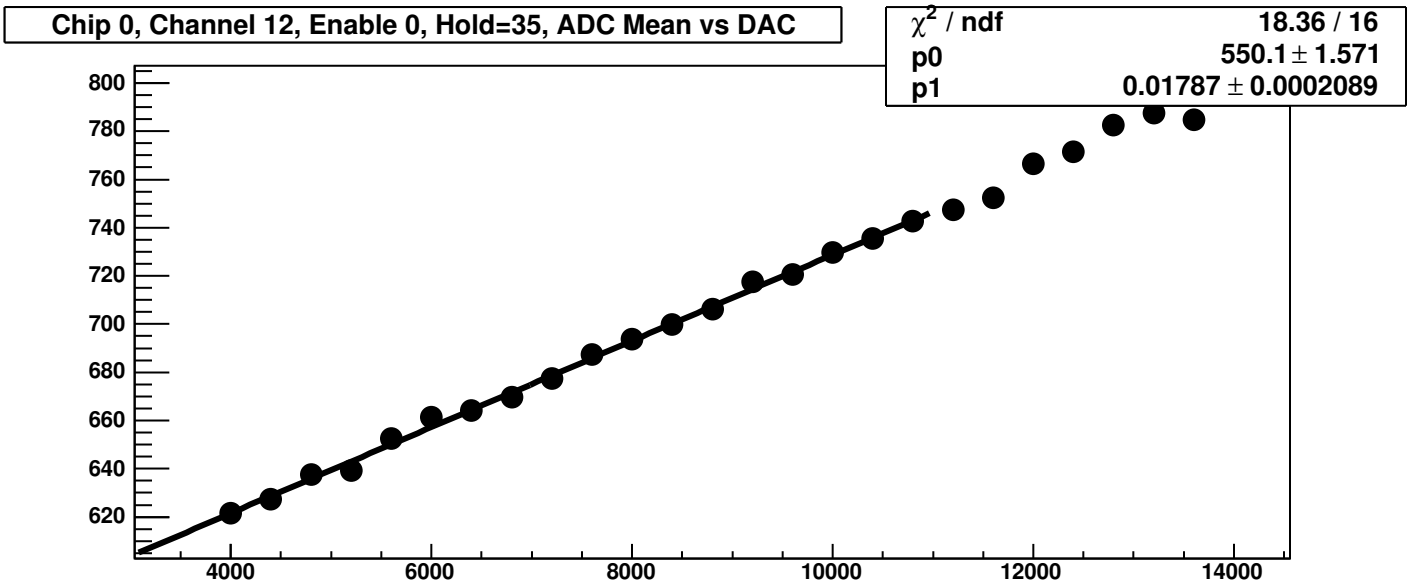

Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

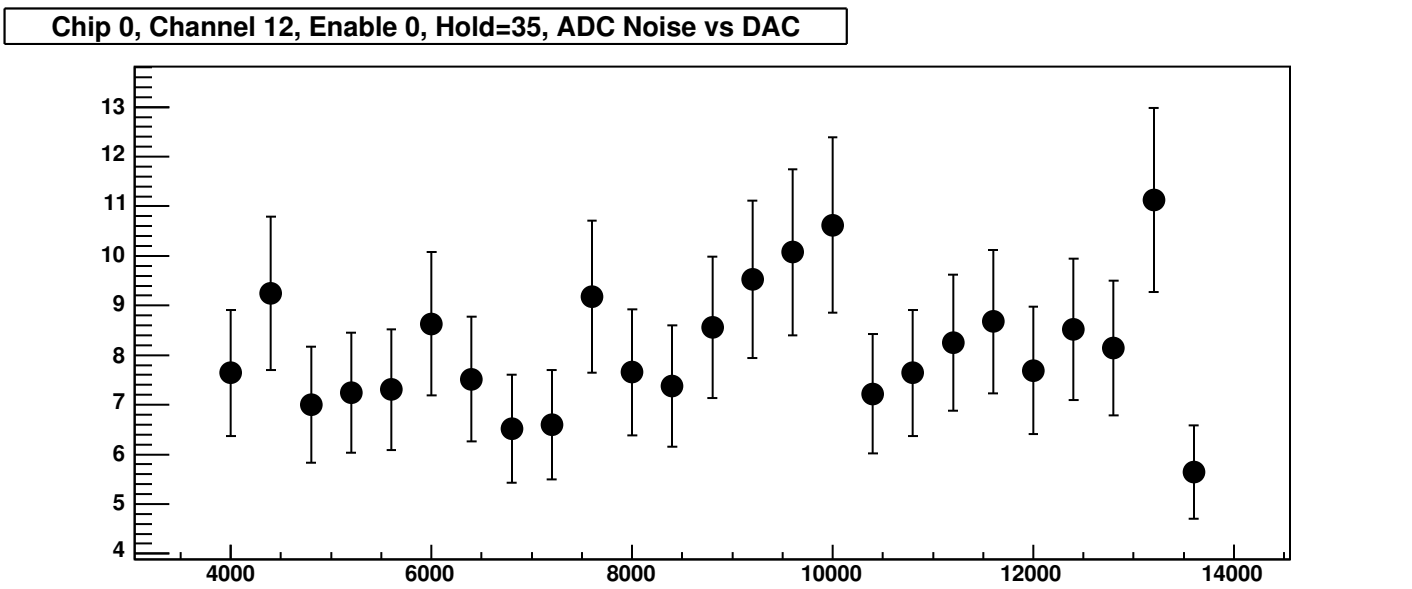

Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

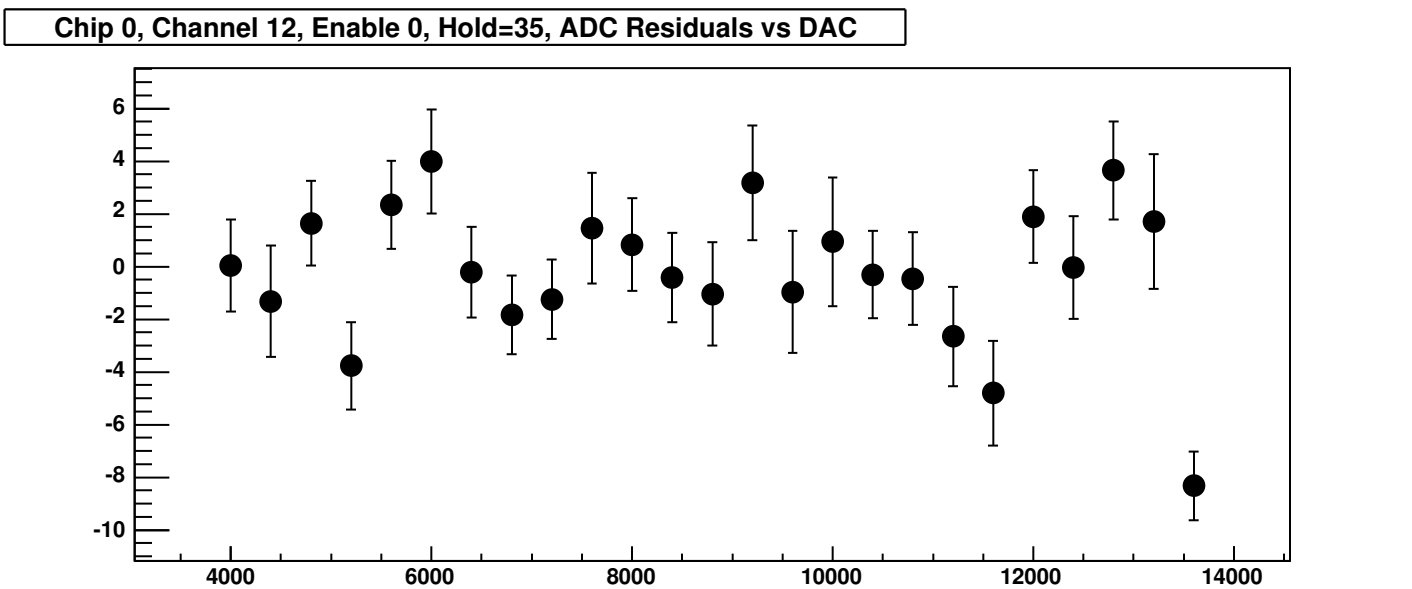

Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.67 / 16
p0 783.9 ± 1.442
p1 0.01553 ± 0.0001921
1000
980
960
940
920
900
880
860
840
4000
6000
8000
10000
12000
14000

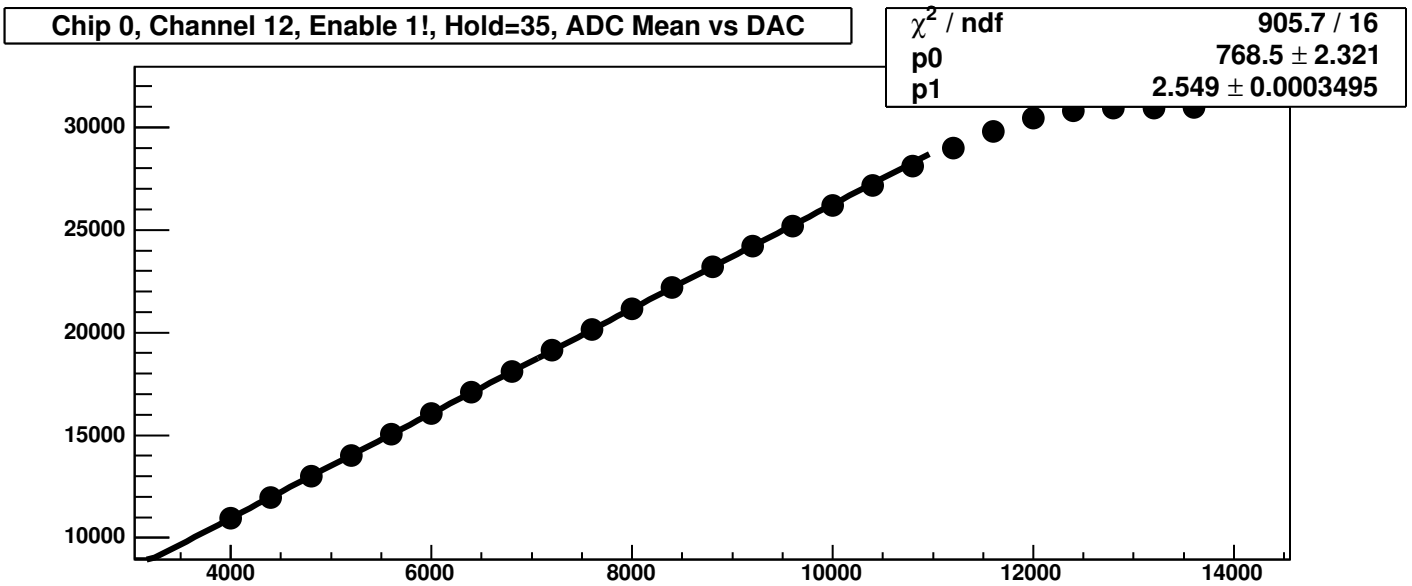

Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

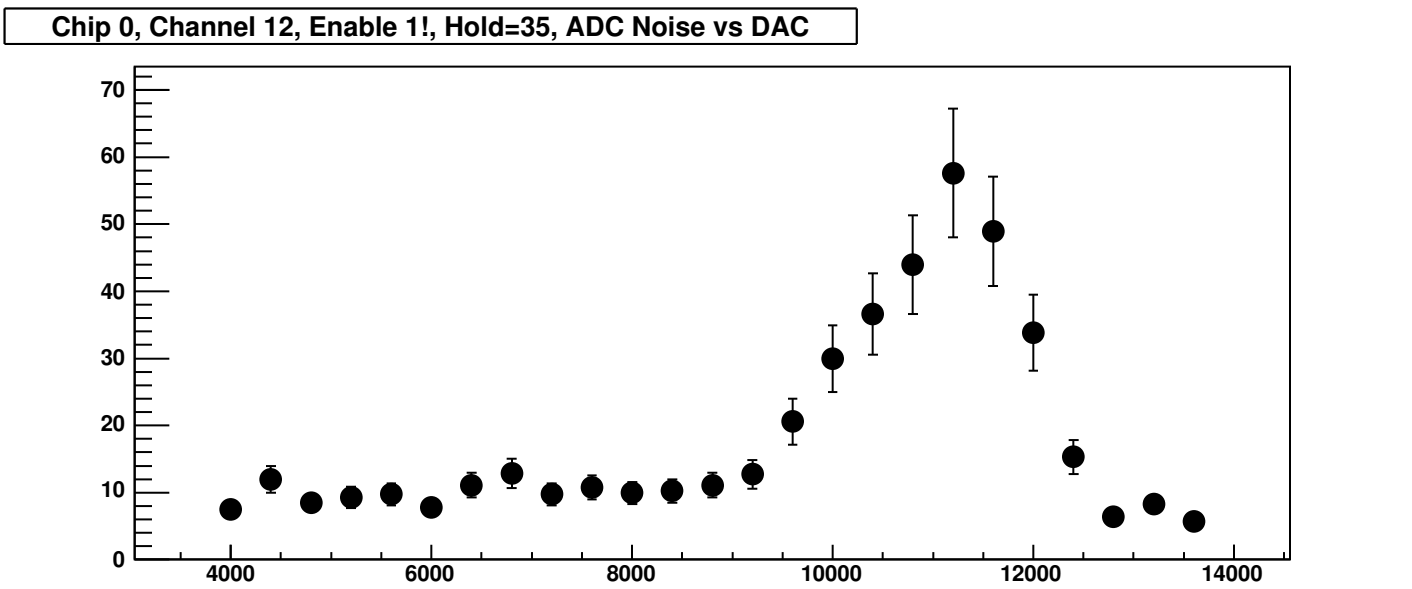

Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

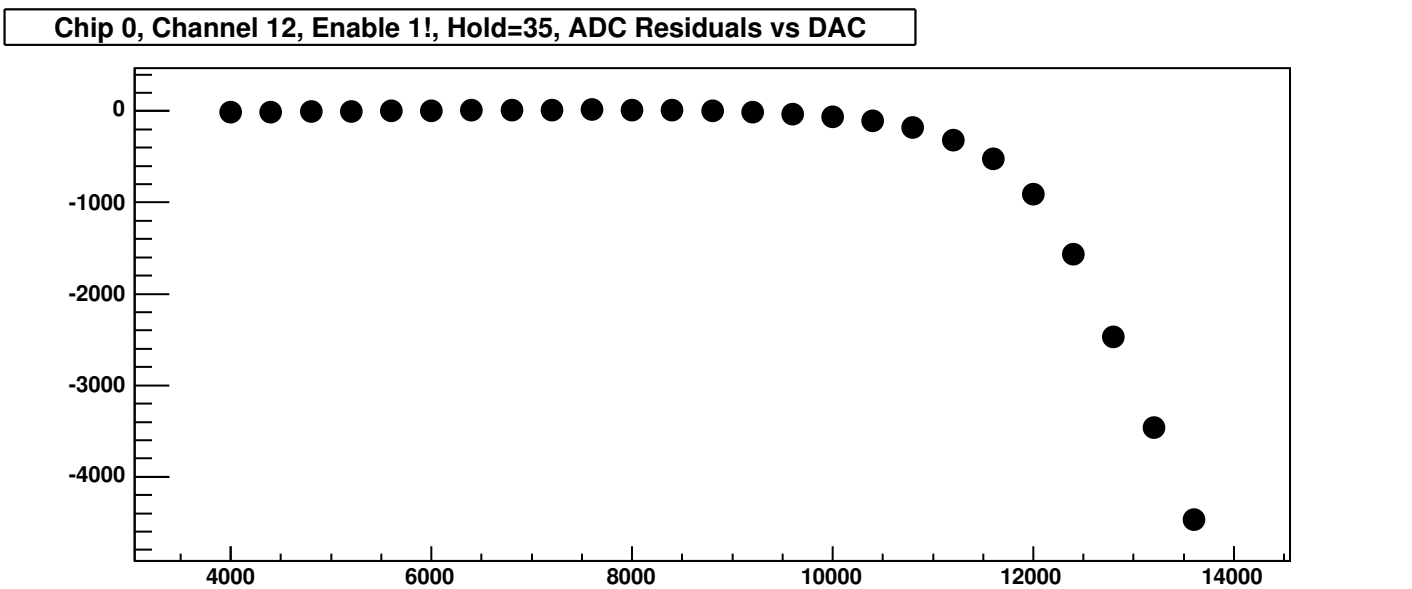

Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18.36 / 16
p0 550.1 ± 1.571
p1 0.01787 ± 0.0002089
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

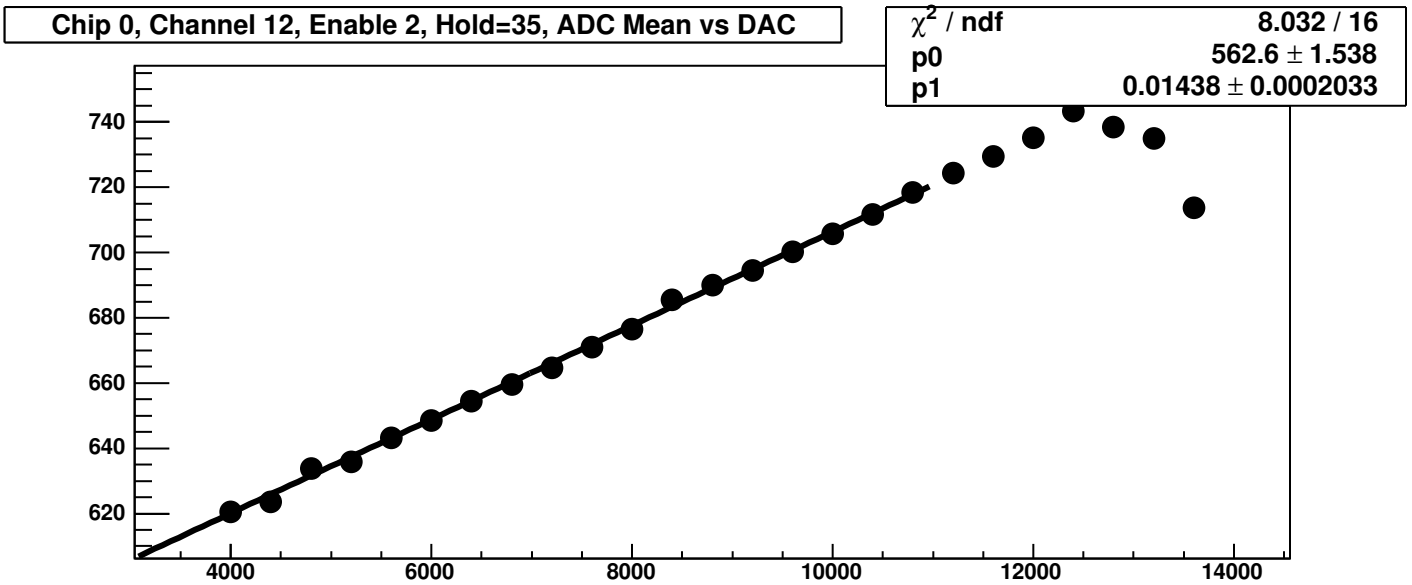

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

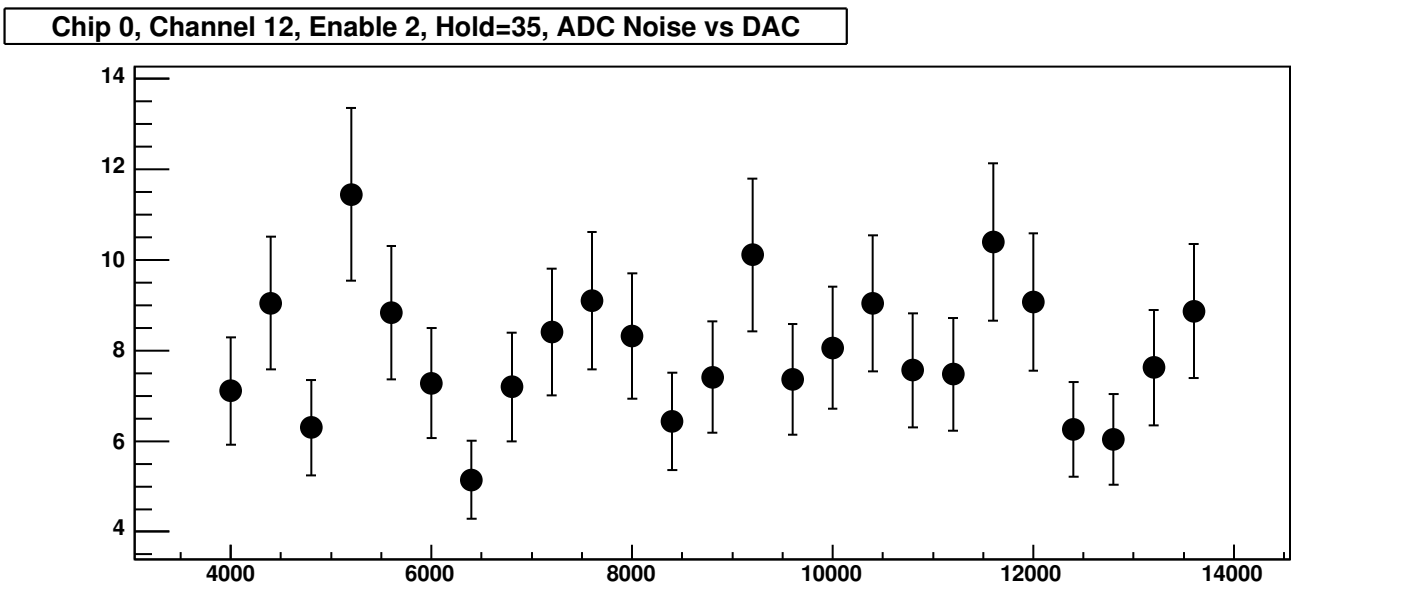

Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

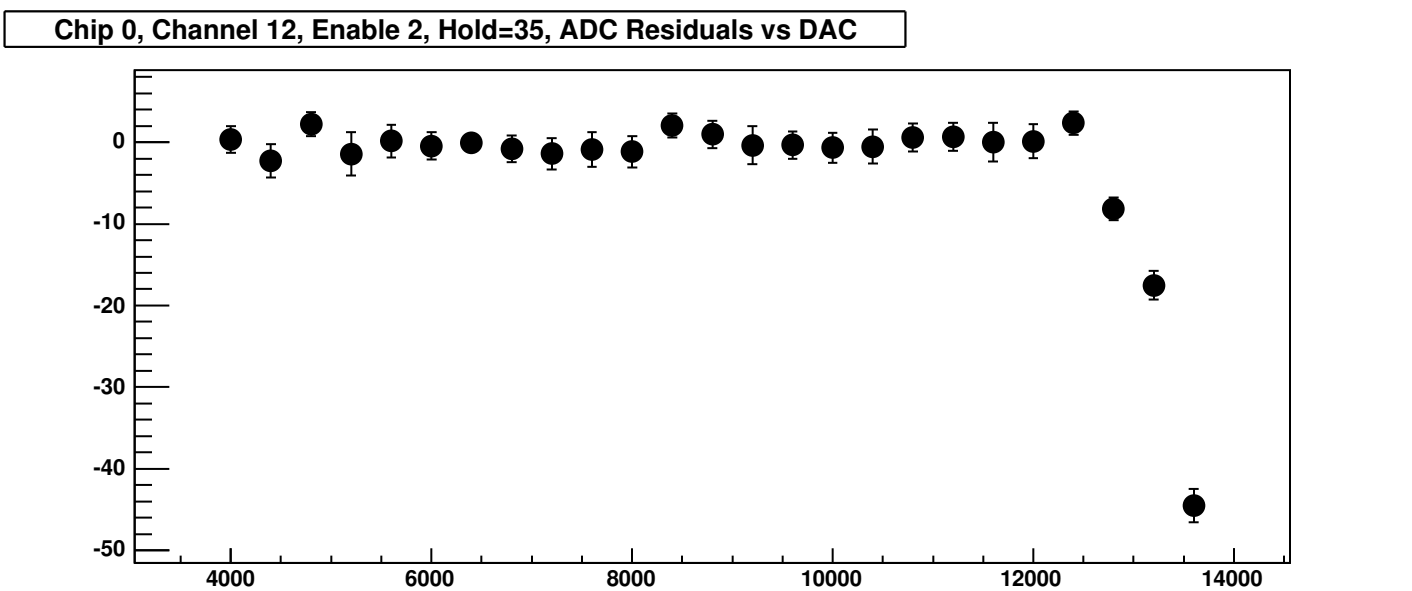

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 905.7 / 16
p0 768.5 ± 2.321
p1 2.549 ± 0.0003495
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

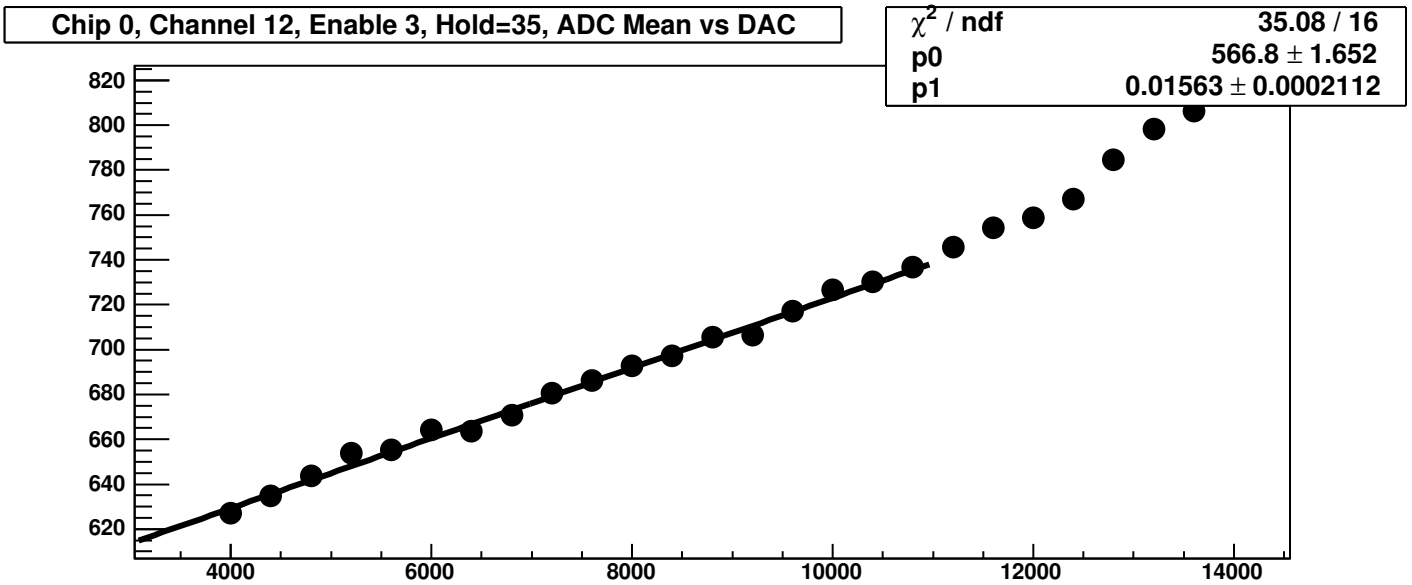

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

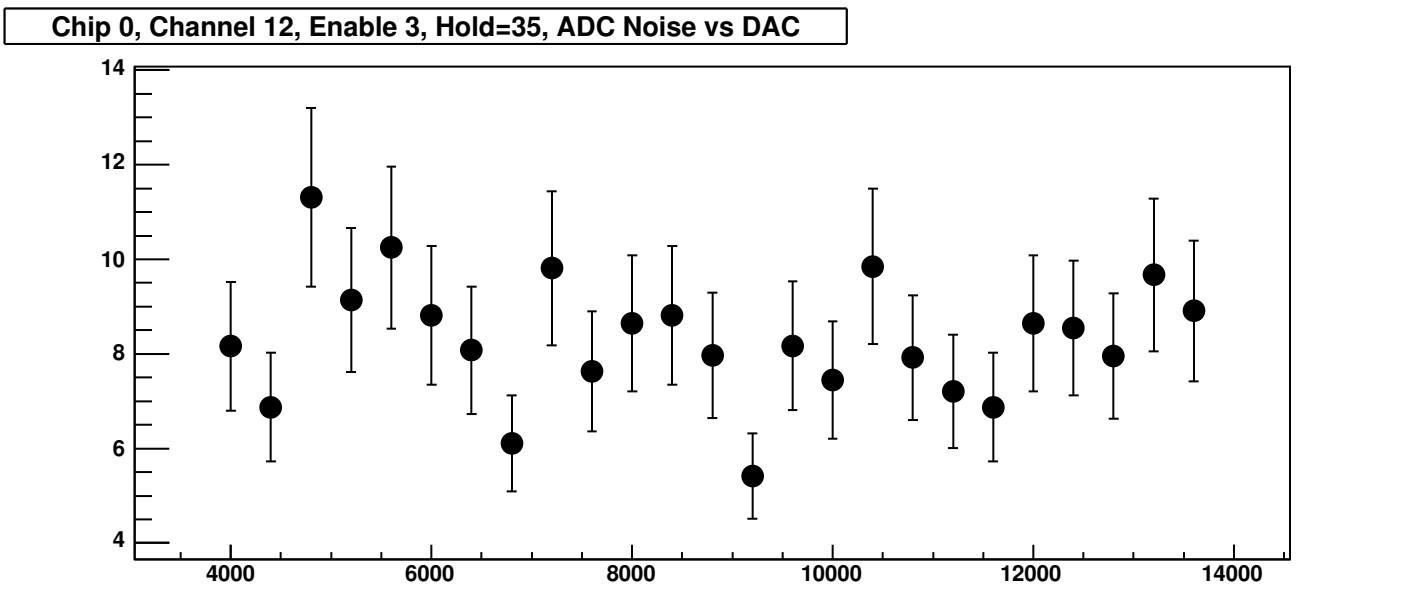

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

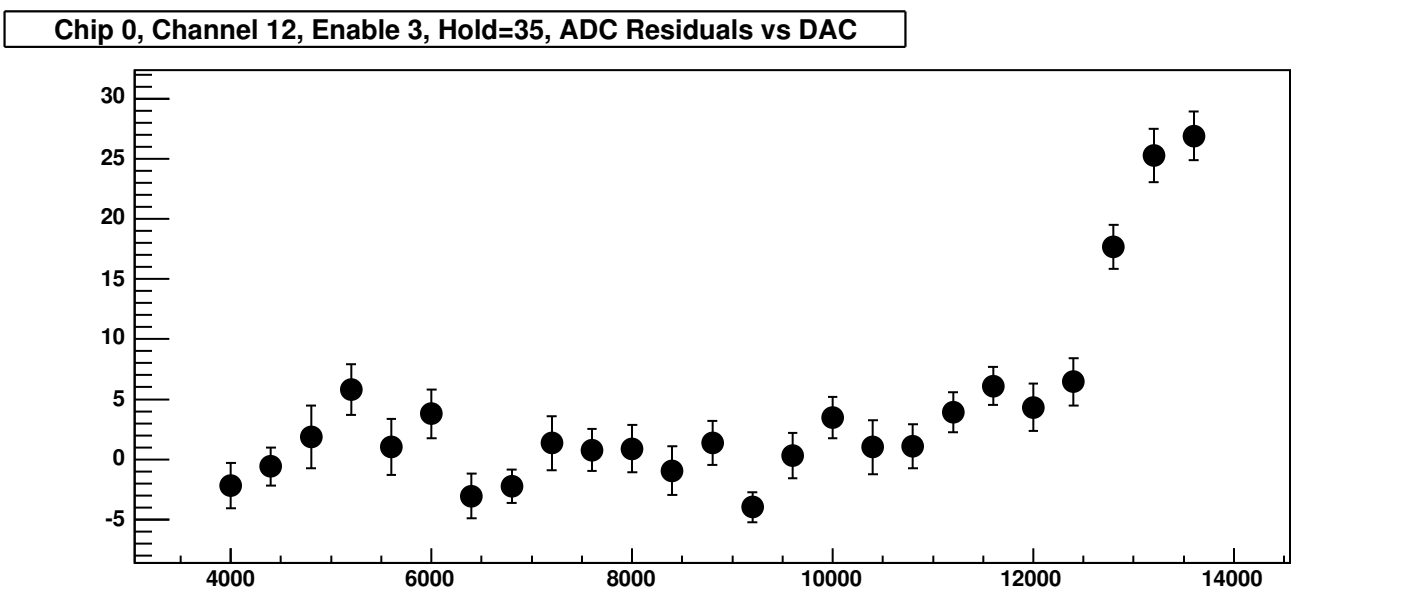

Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 8.032 / 16
p0 562.6 ± 1.538
p1 0.01438 ± 0.0002033
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

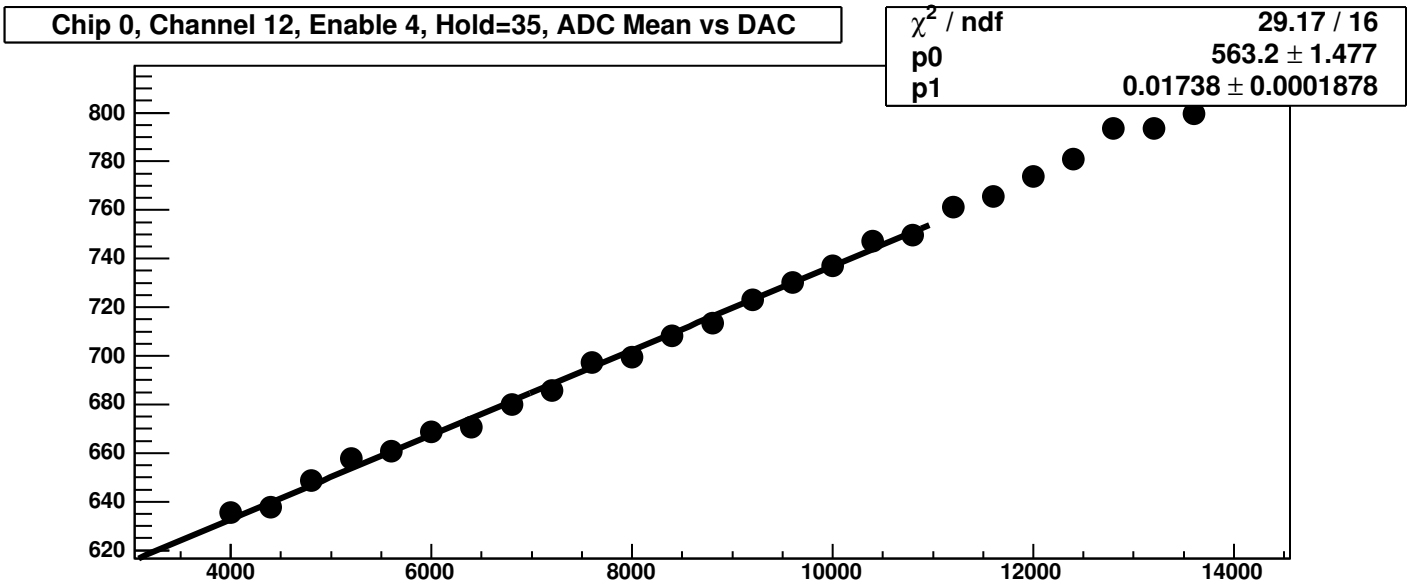

Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

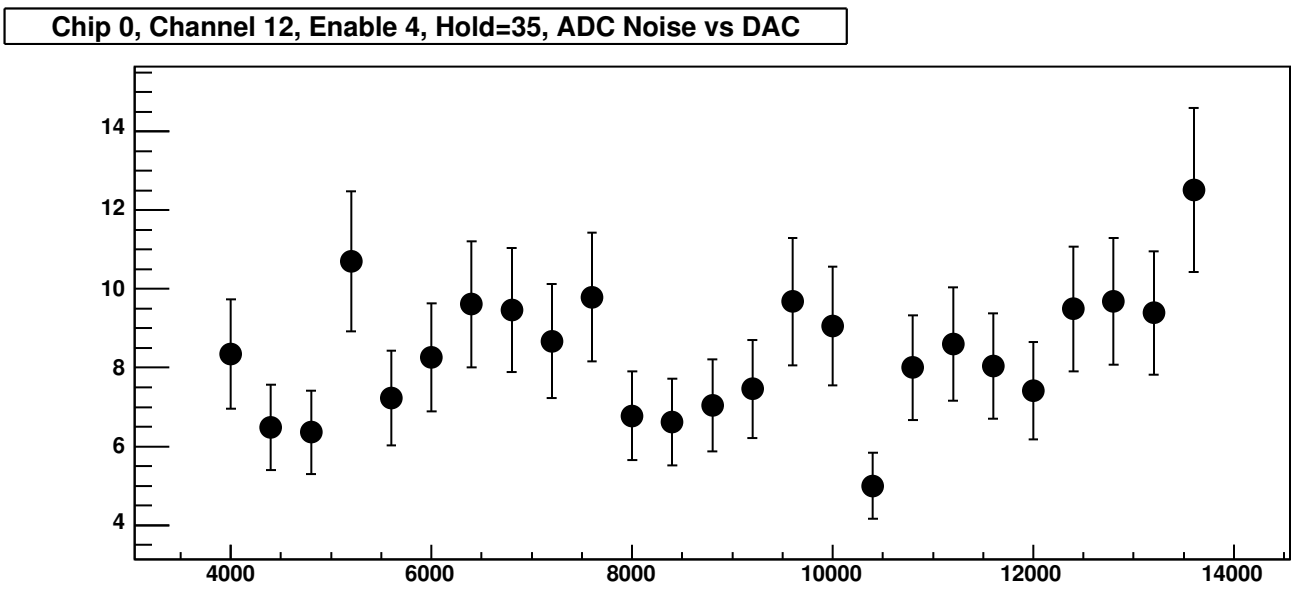

Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

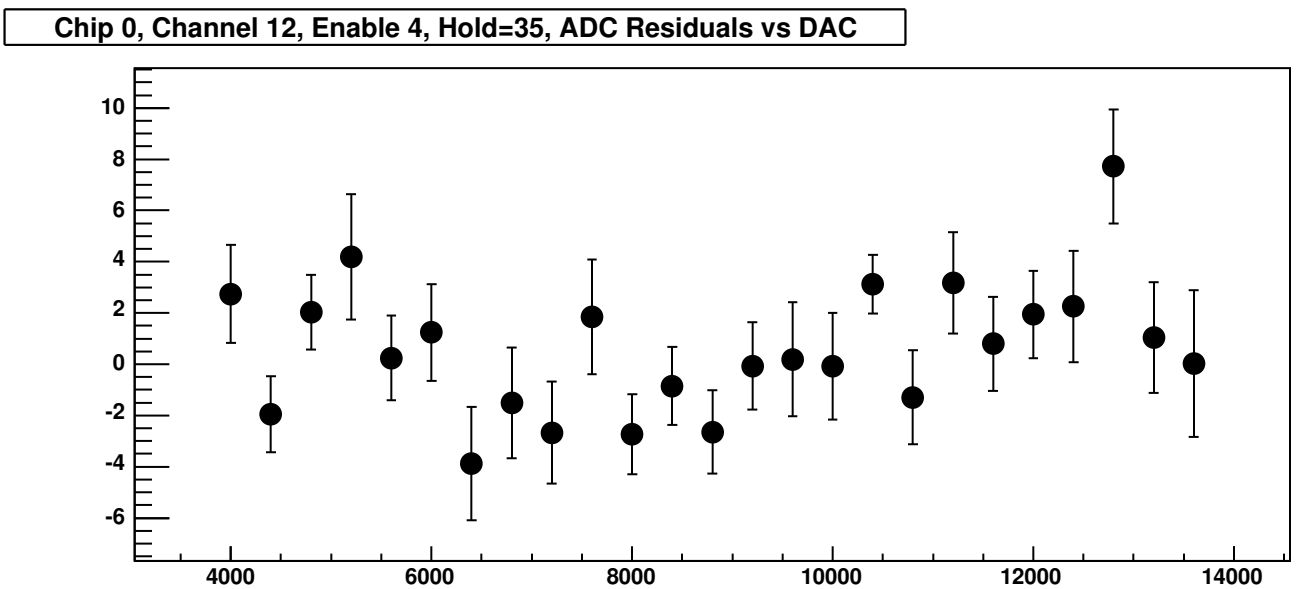

Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.08 / 16
p0 566.8 ± 1.652
p1 0.01563 ± 0.0002112
820
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

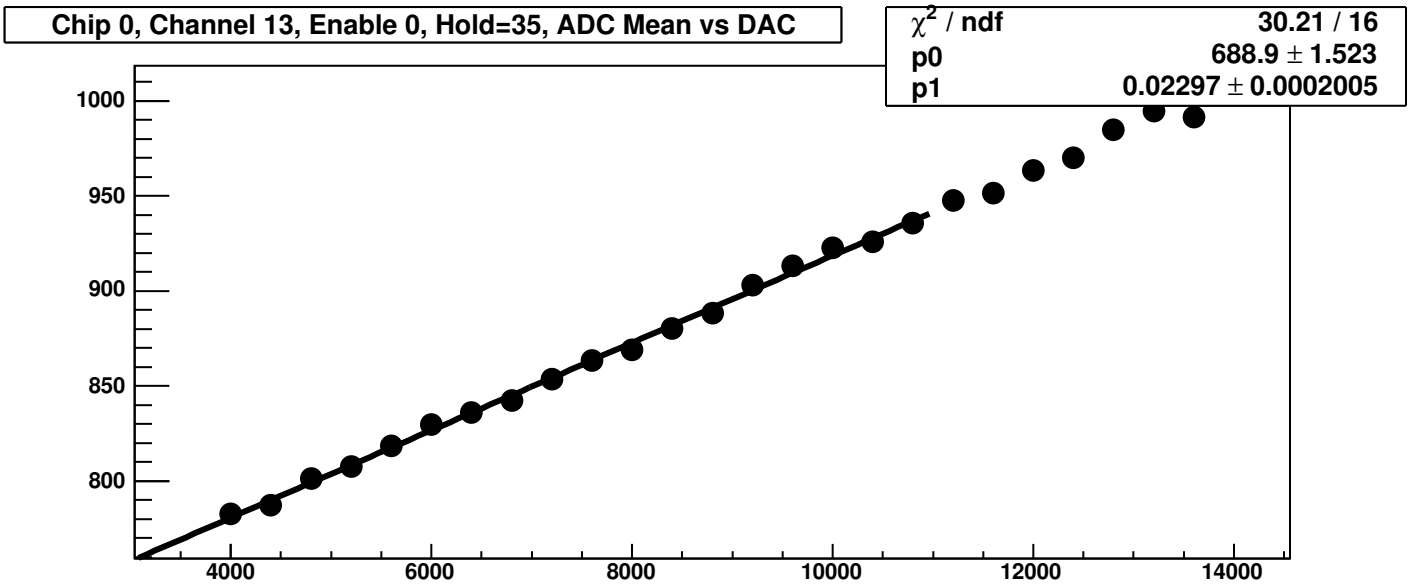

Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

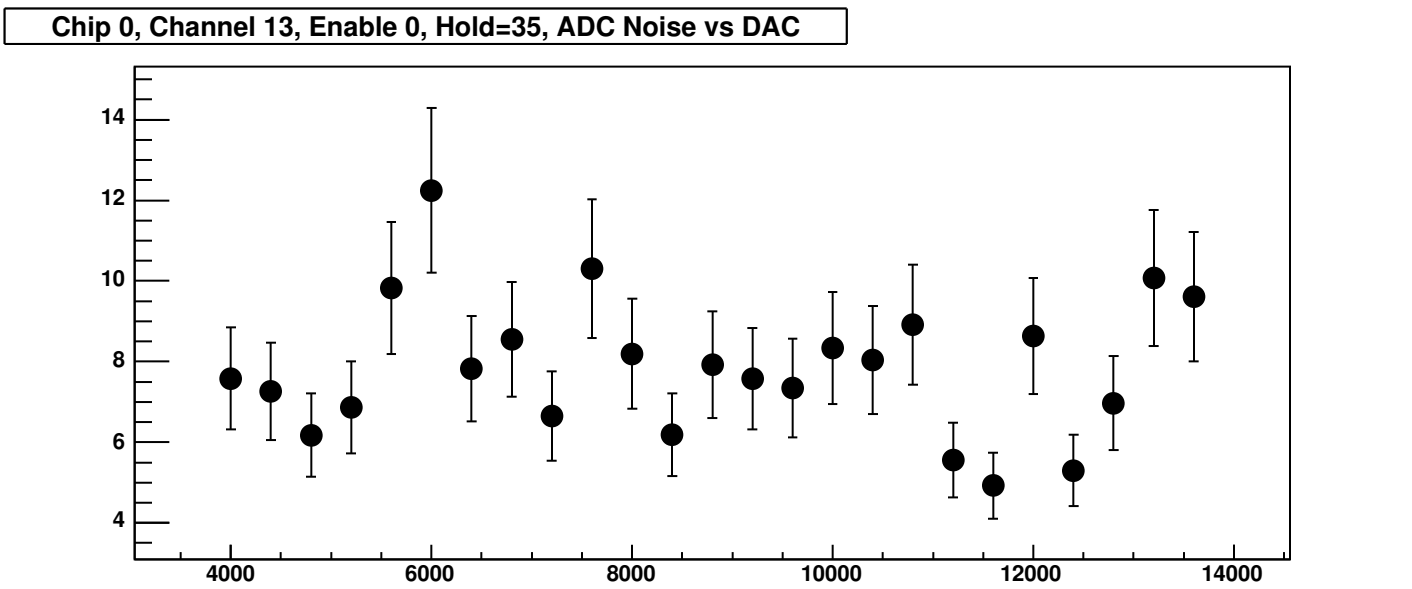

Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

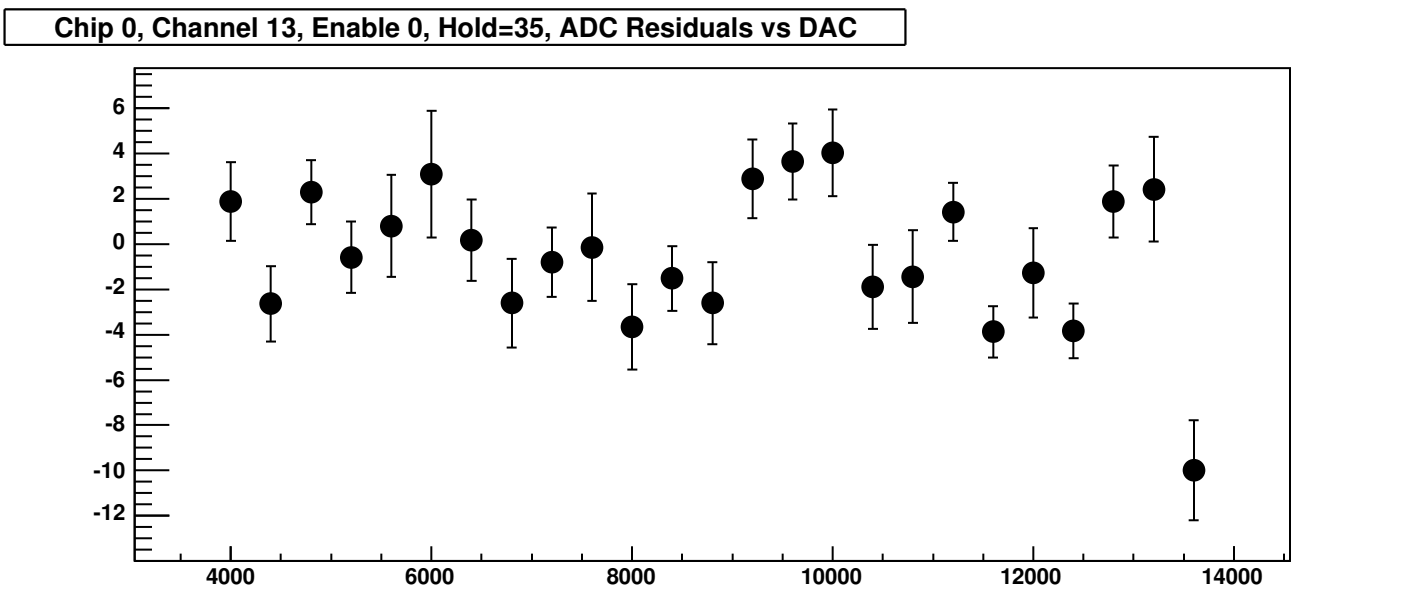

Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.17 / 16
p0 563.2 ± 1.477
p1 0.01738 ± 0.0001878
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

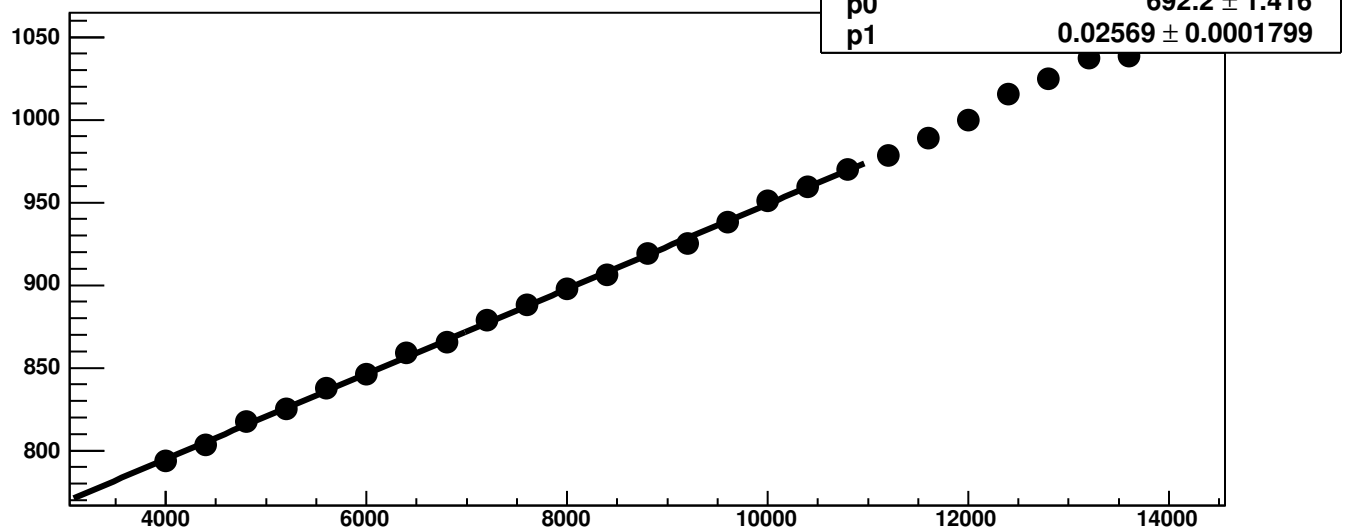

Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 30.21 / 16
p0 688.9 ± 1.523
p1 0.02297 ± 0.0002005
1000
950
900
850
800
4000
6000
8000
10000
12000
14000

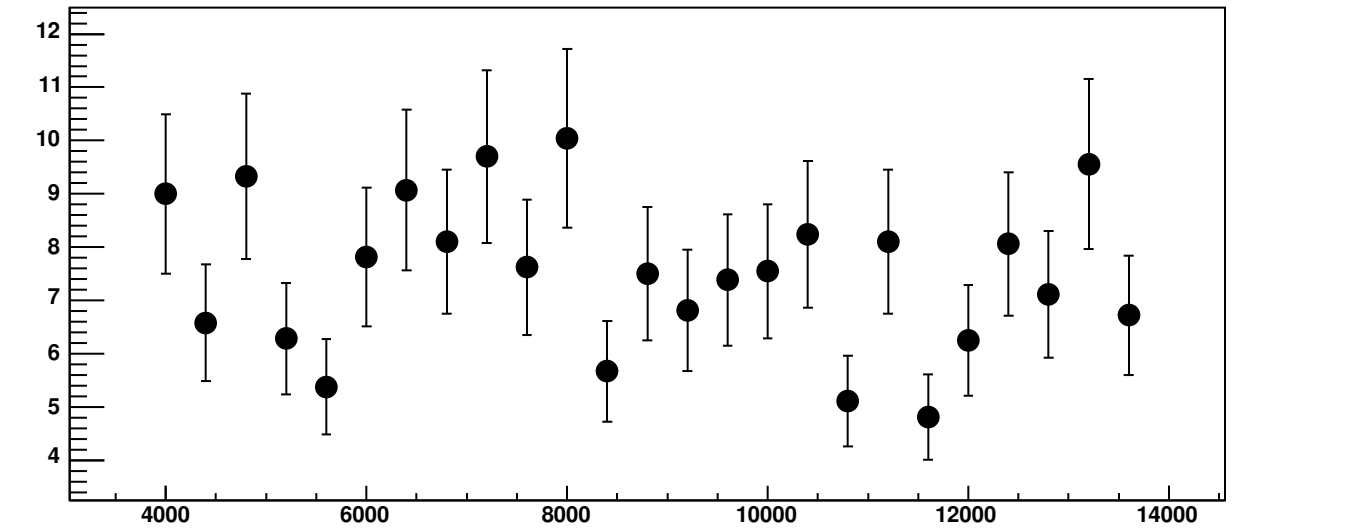

Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000

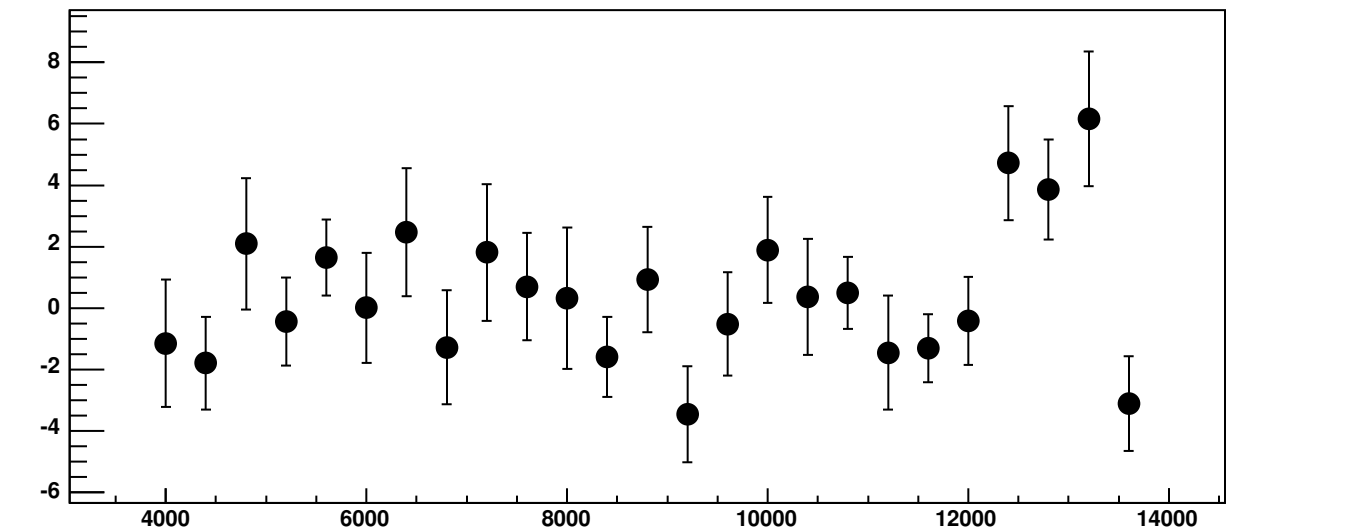
Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

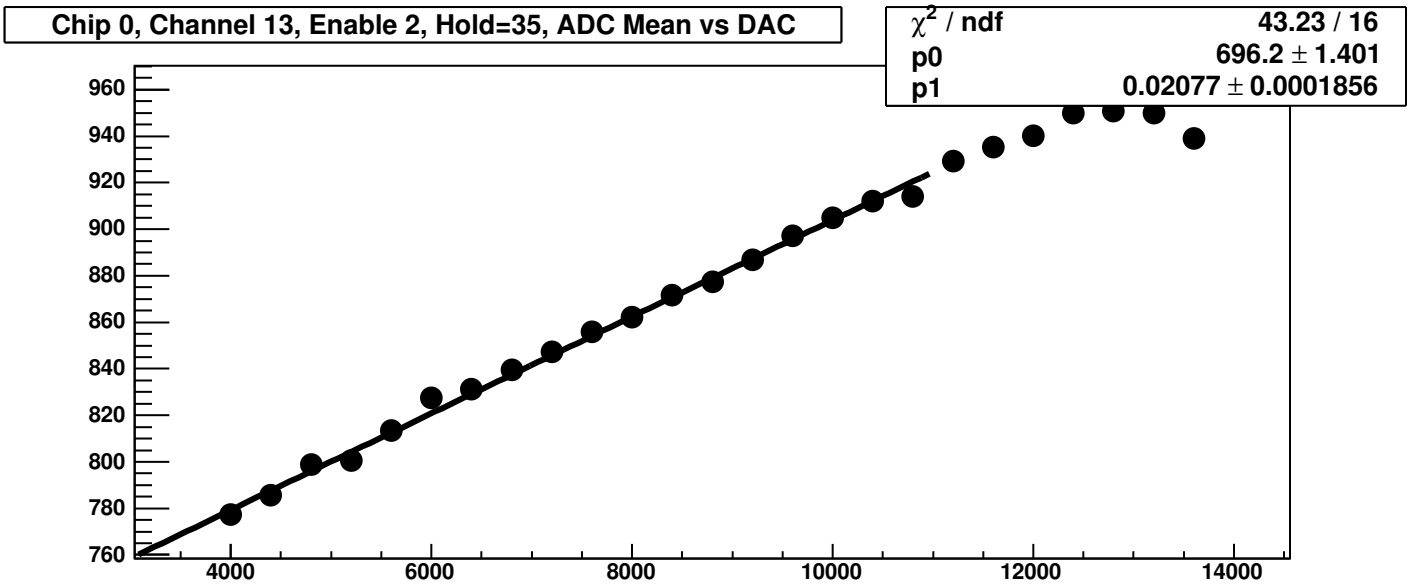


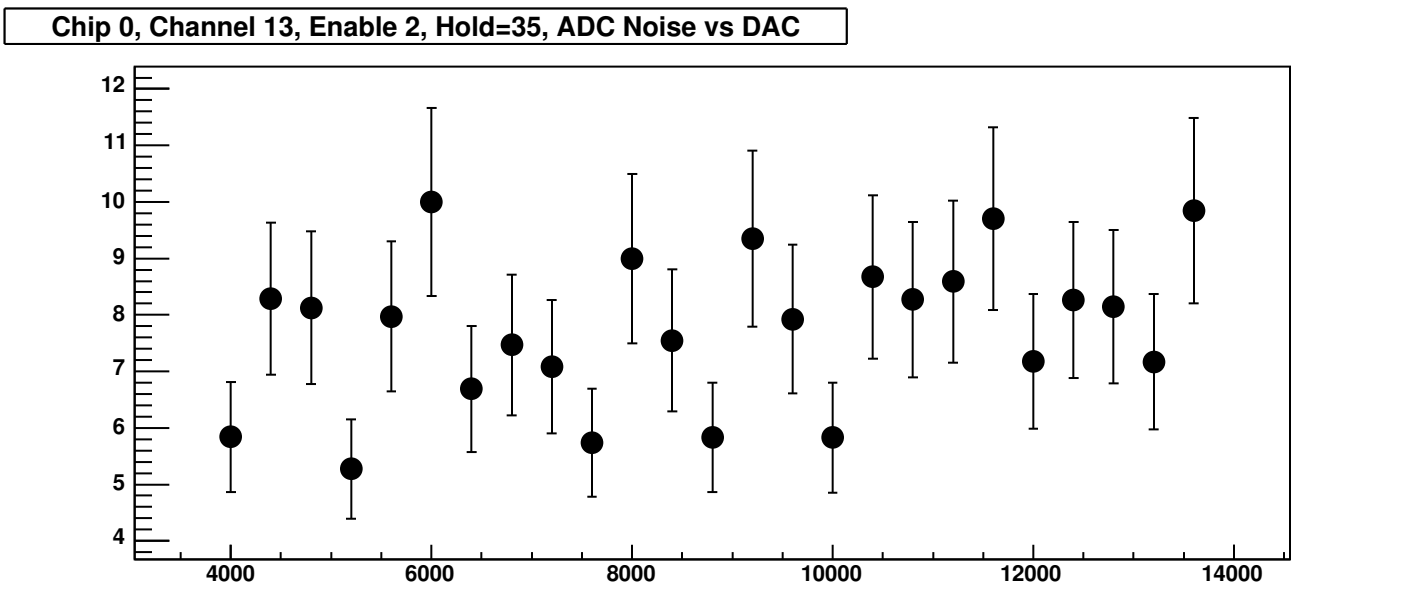
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

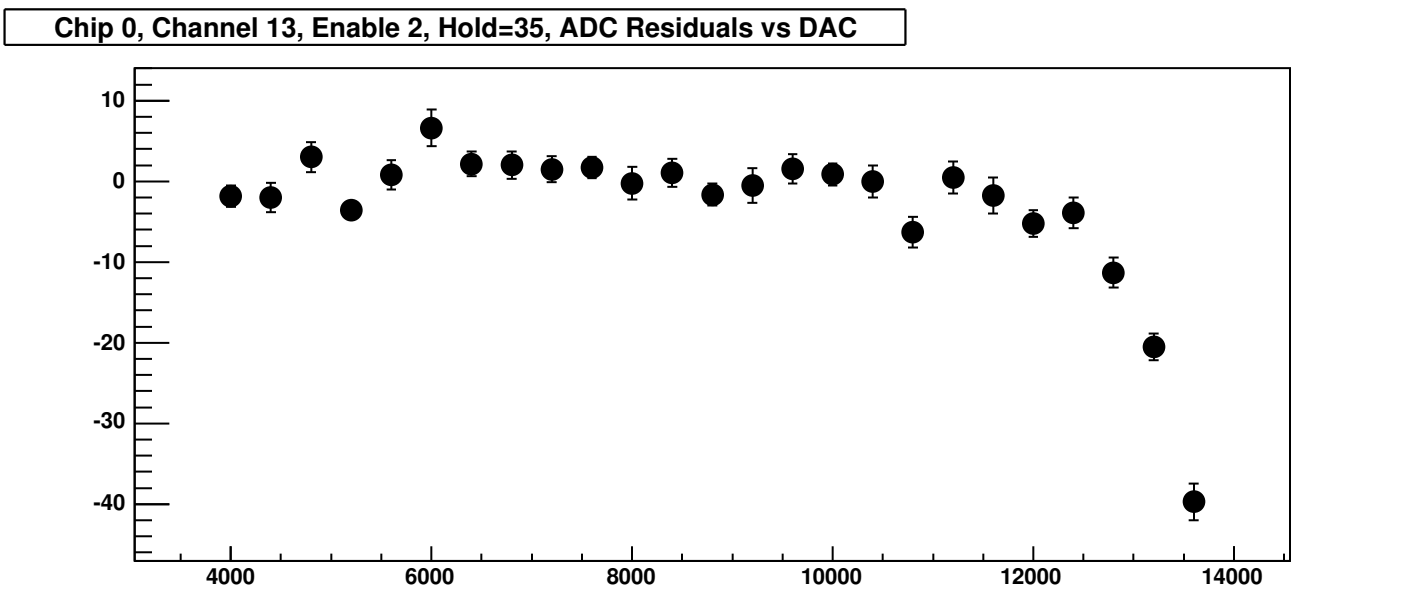


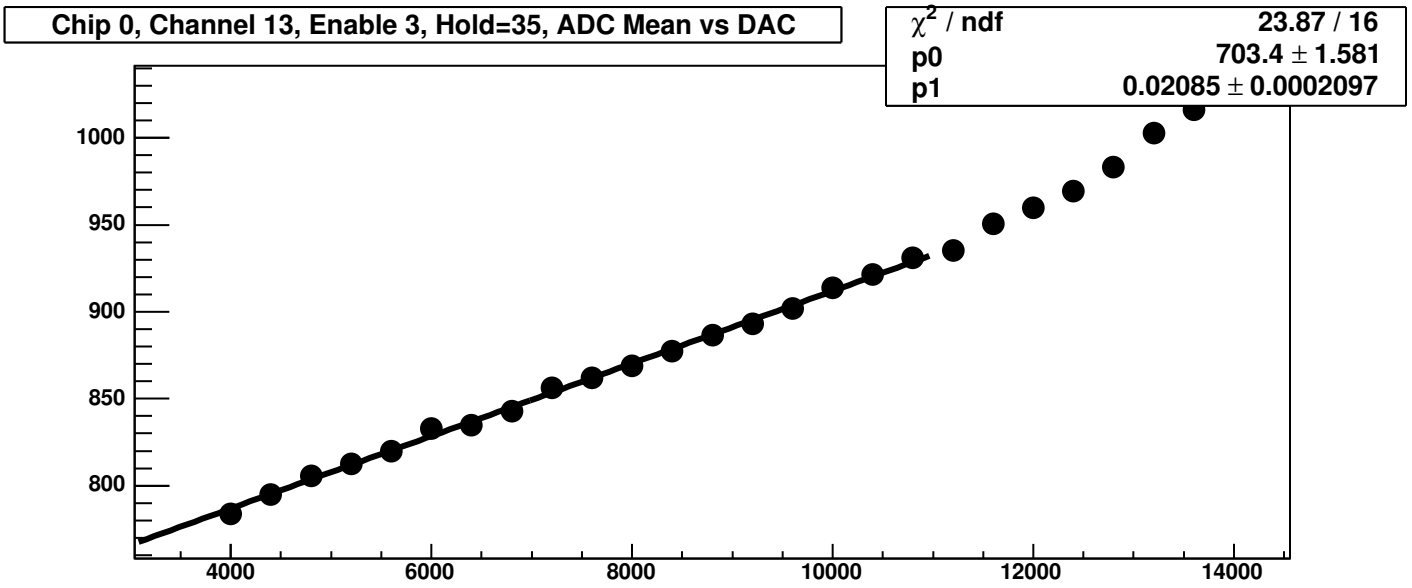
Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

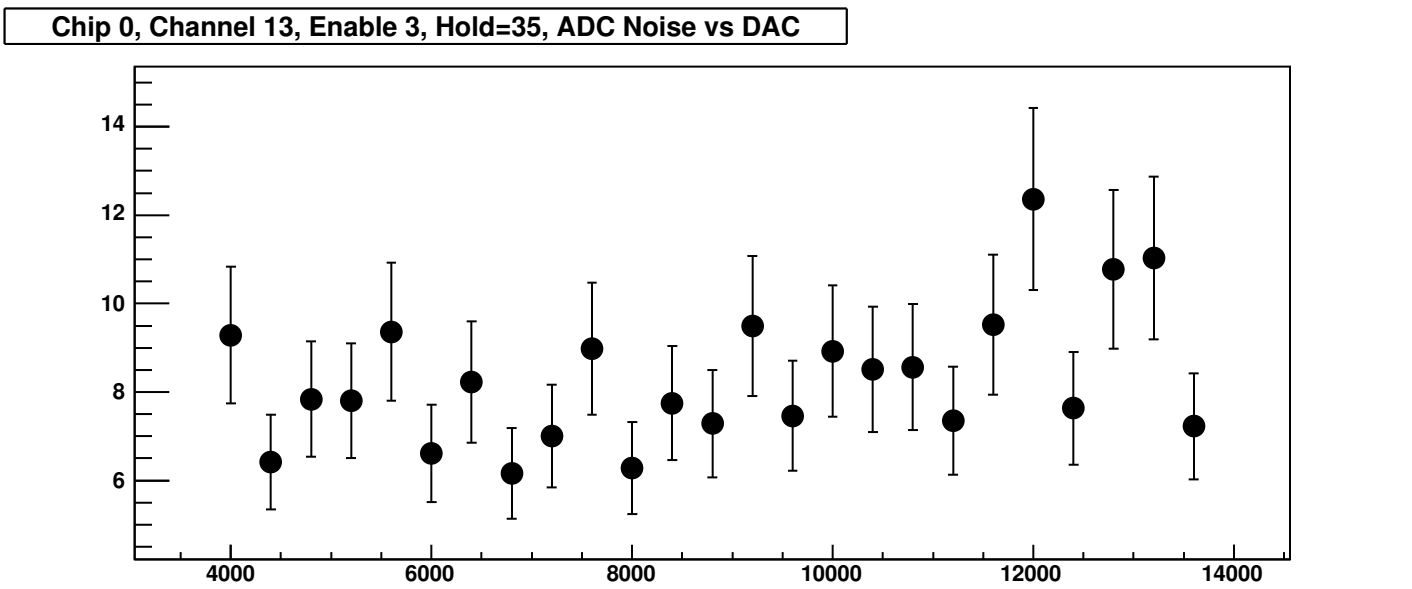


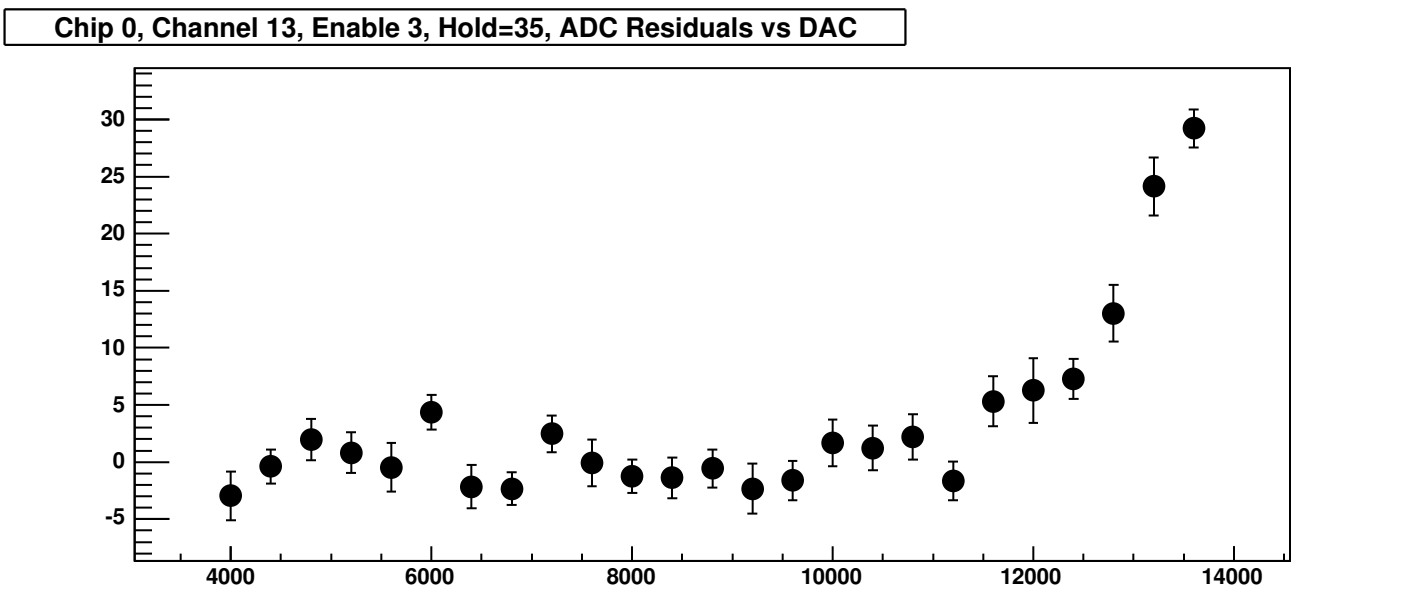

Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.23 / 16
p0 696.2 ± 1.401
p1 0.02077 ± 0.0001856
960
940
920
900
880
860
840
820
800
780
760
4000
6000
8000
10000
12000
14000

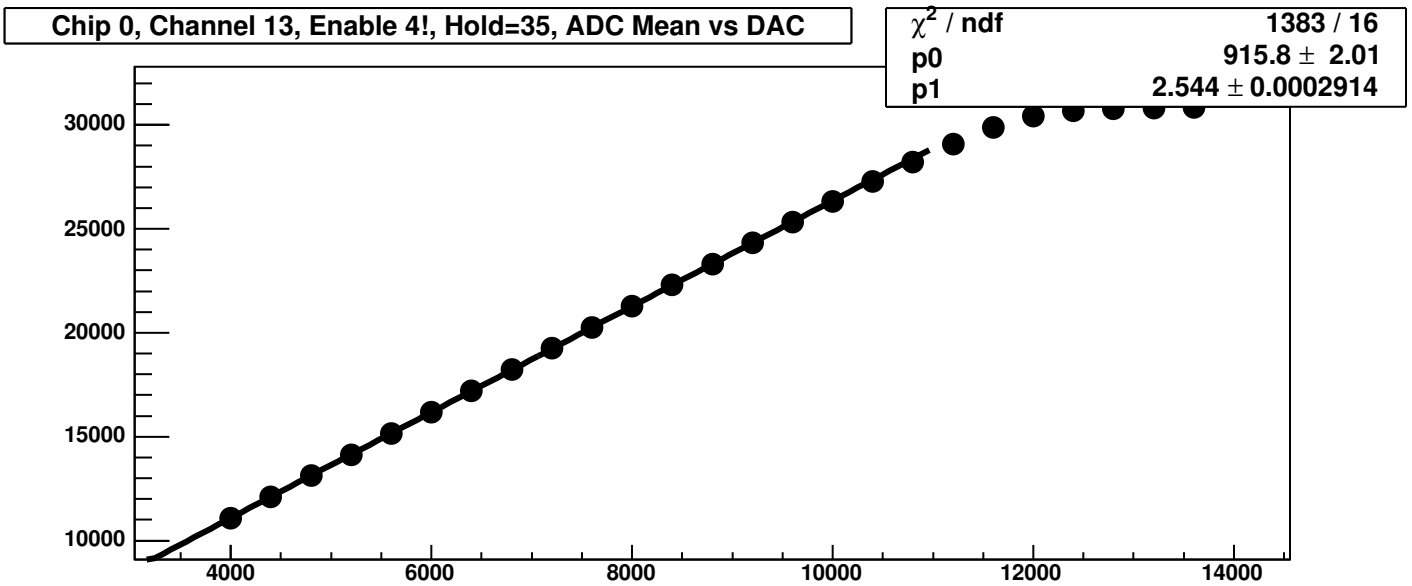

Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

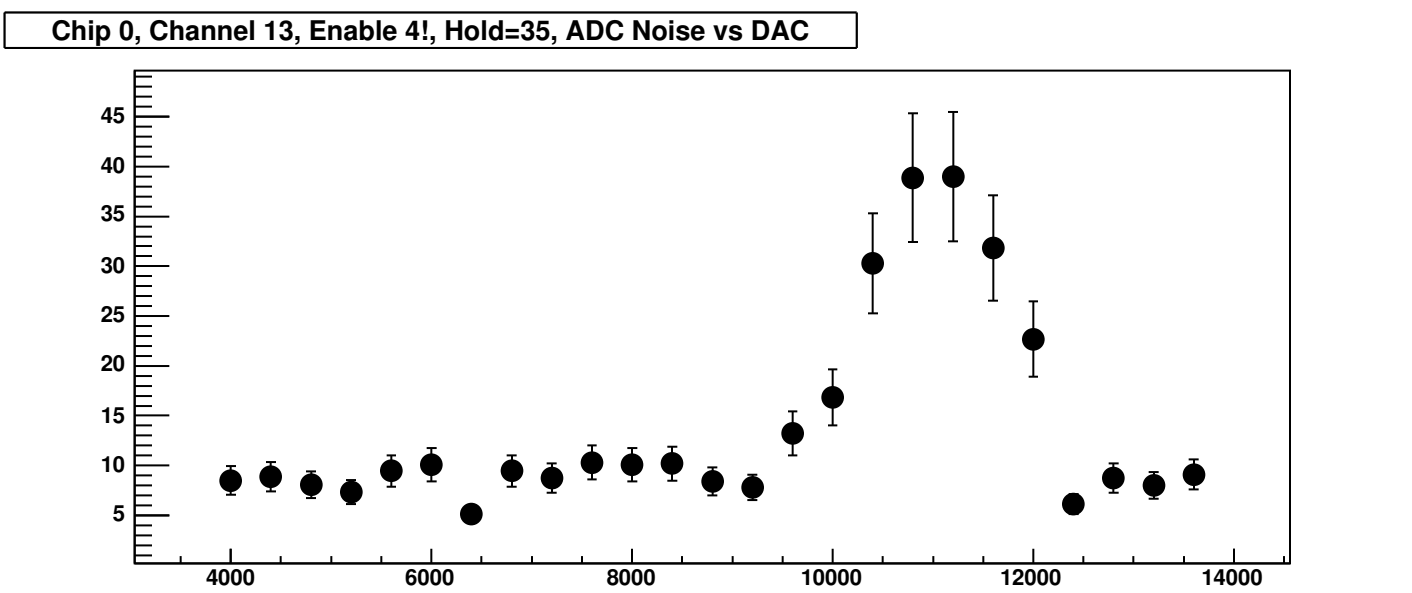

Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

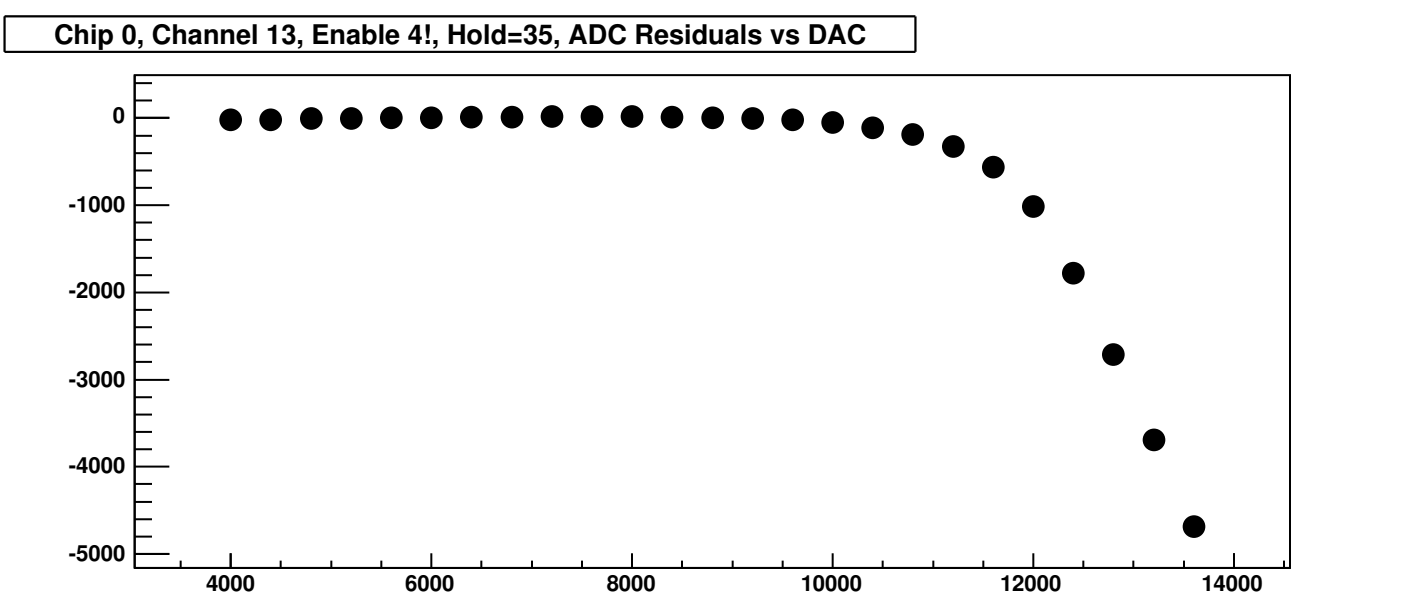

Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.87 / 16
p0 703.4 ± 1.581
p1 0.02085 ± 0.0002097
1000
950
900
850
800
4000
6000
8000
10000
12000
14000

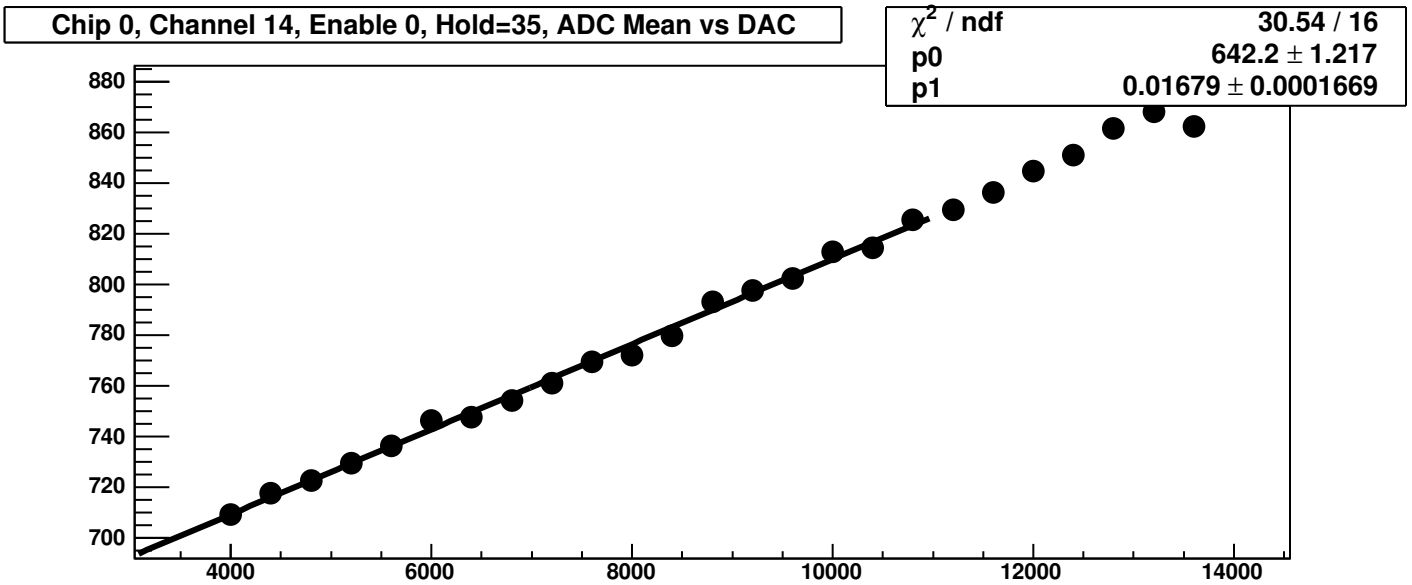

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4000
6000
8000
10000
12000
14000

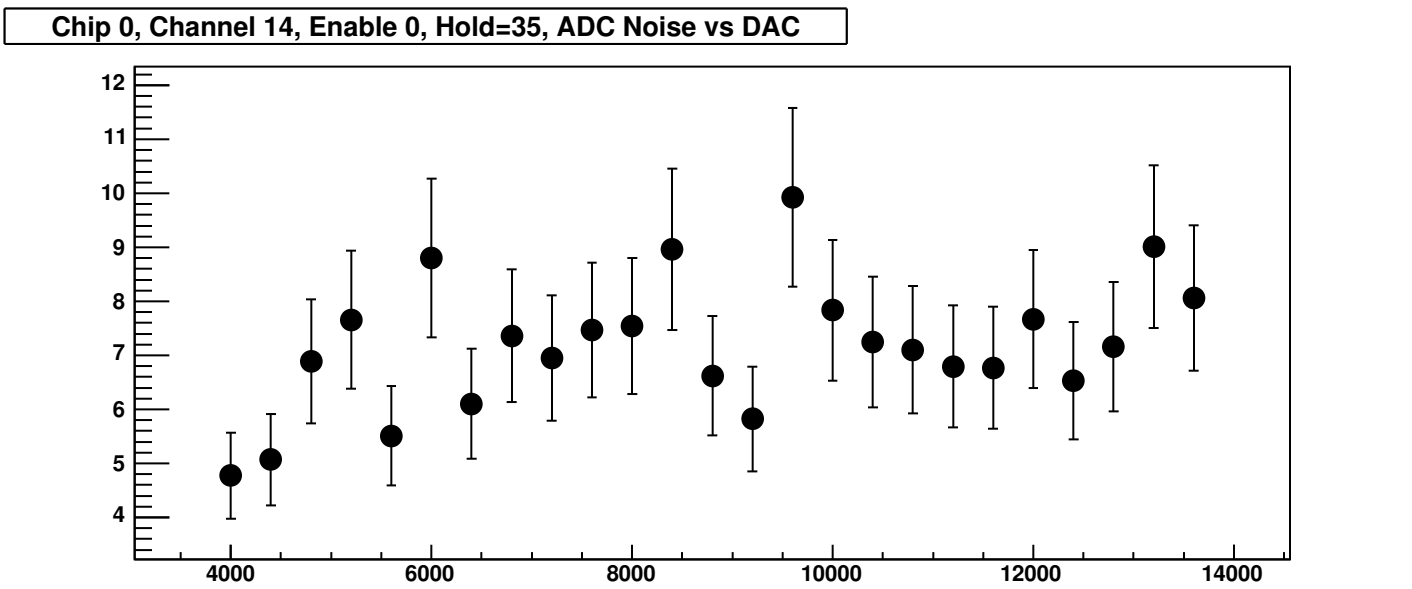

Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

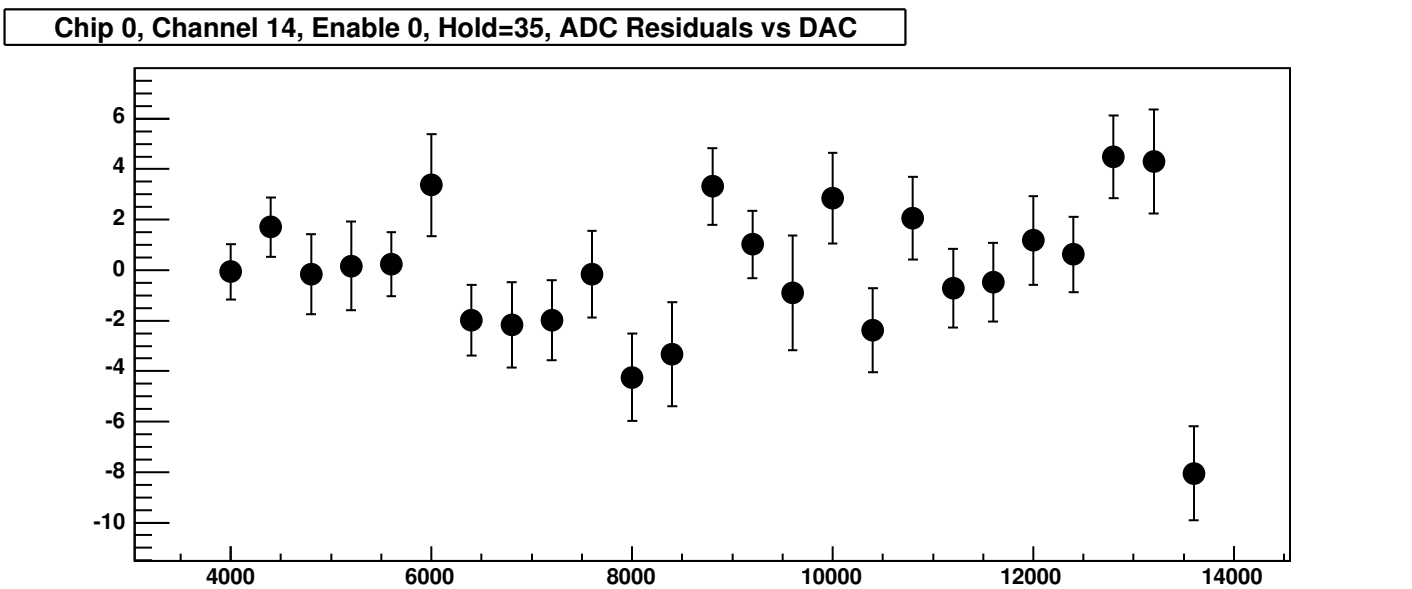

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1383 / 16
p0 915.8 ± 2.01
p1 2.544 ± 0.0002914
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

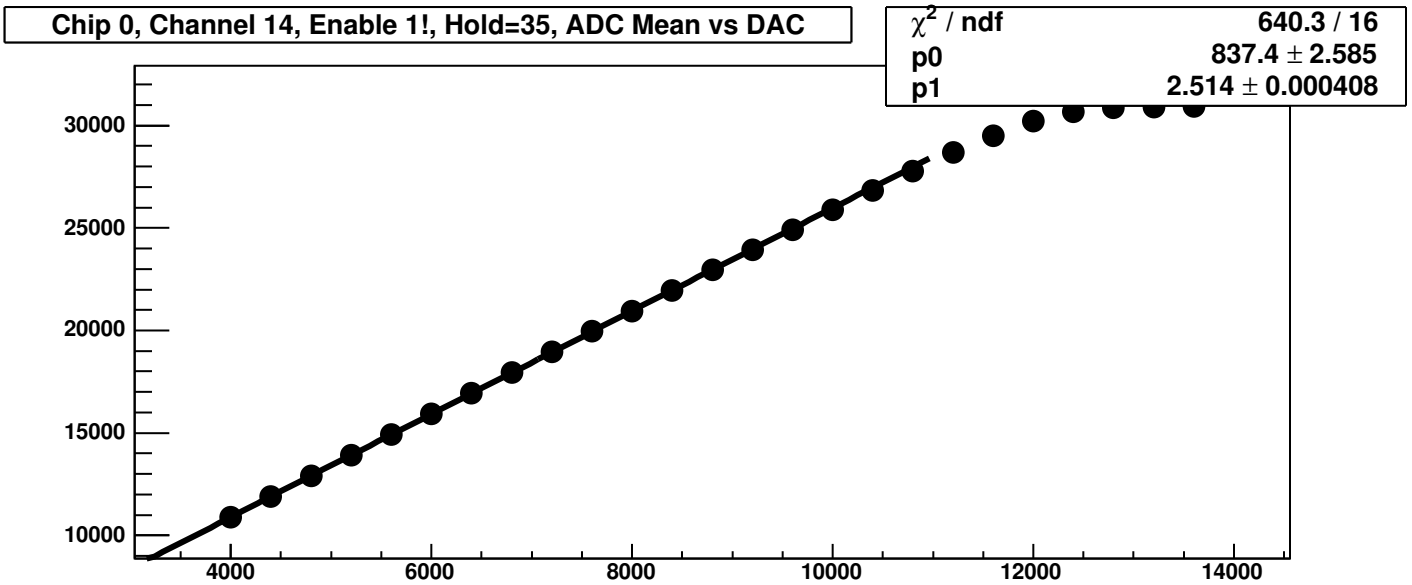

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

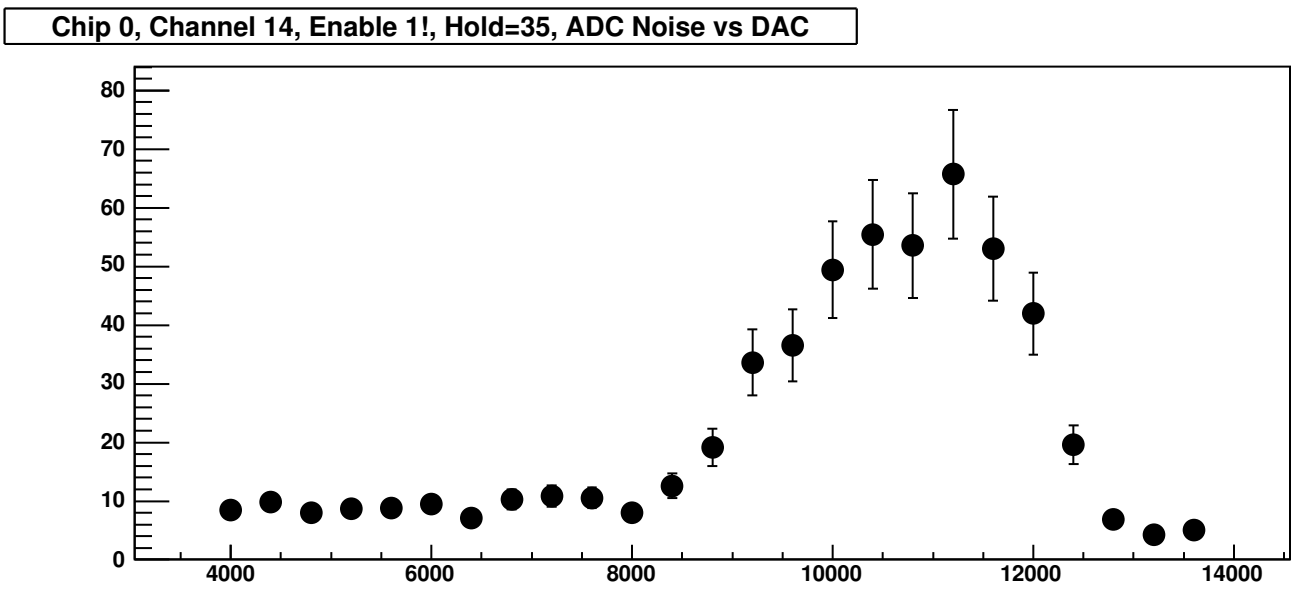

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

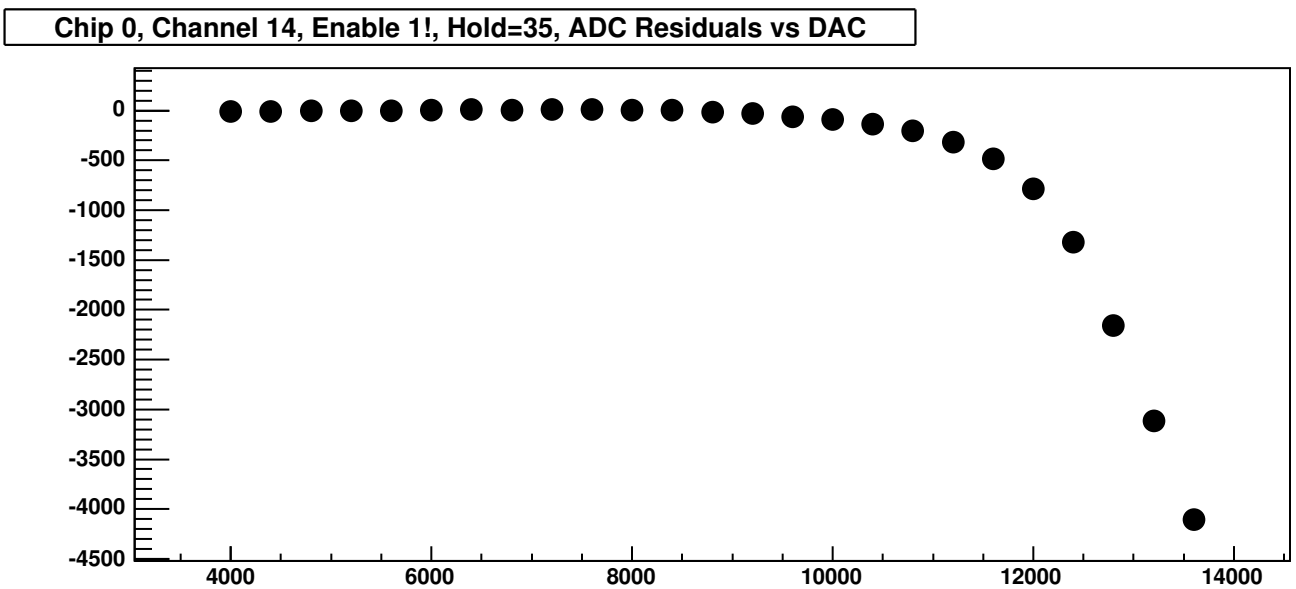

Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 30.54 / 16
p0 642.2 ± 1.217
p1 0.01679 ± 0.0001669
880
860
840
820
800
780
760
740
720
700
4000
6000
8000
10000
12000
14000

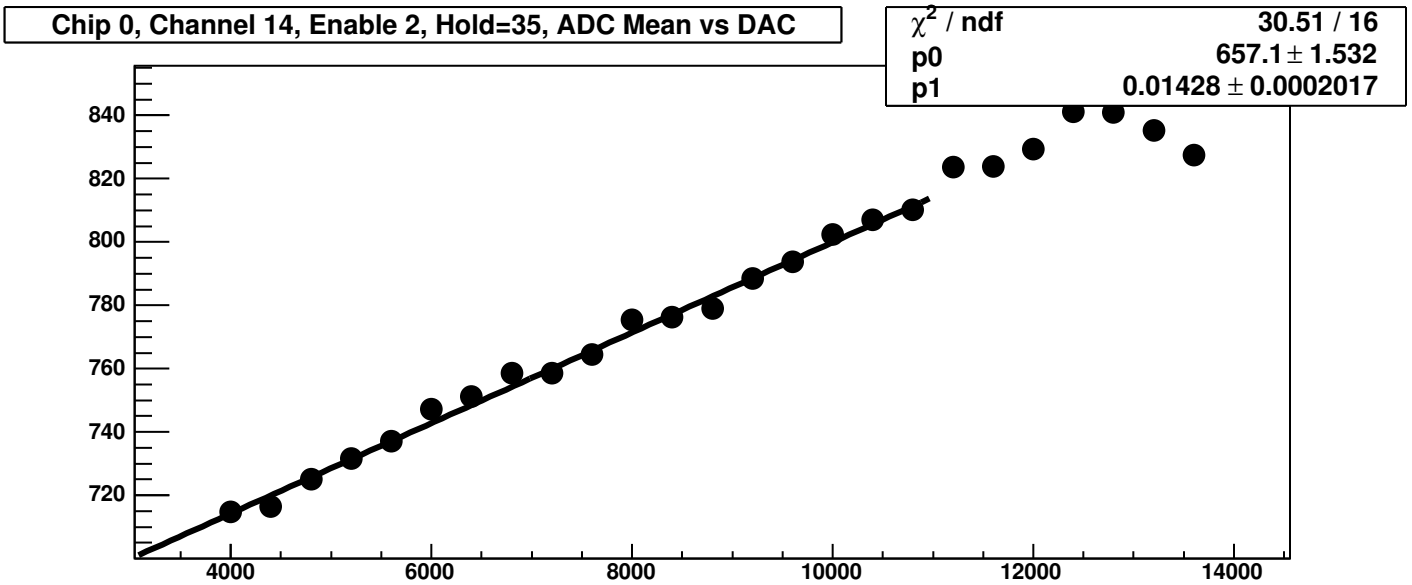

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

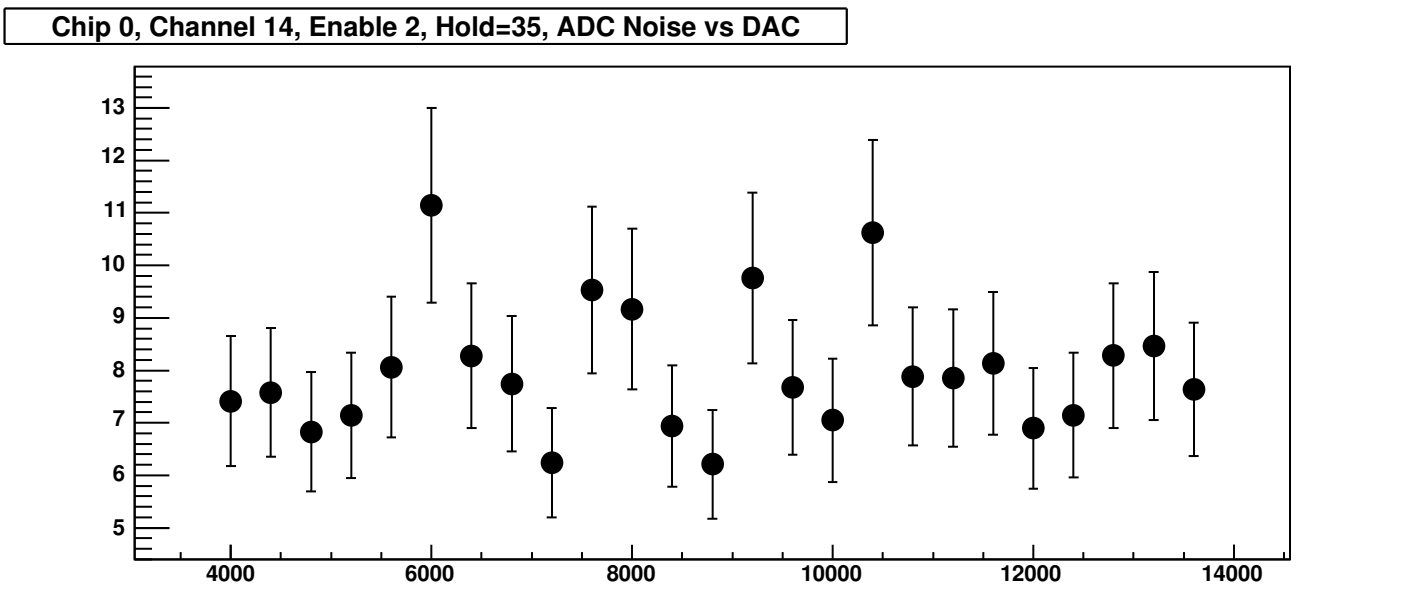

Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

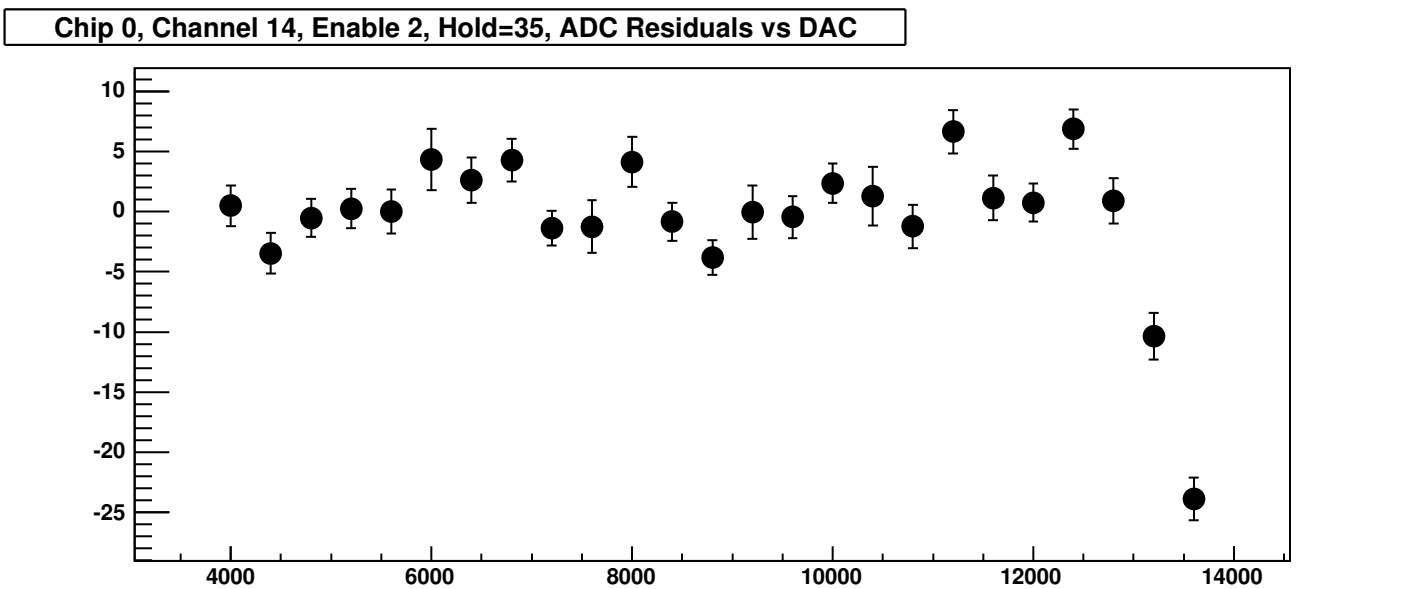

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 640.3 / 16
p0 837.4 ± 2.585
p1 2.514 ± 0.000408
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

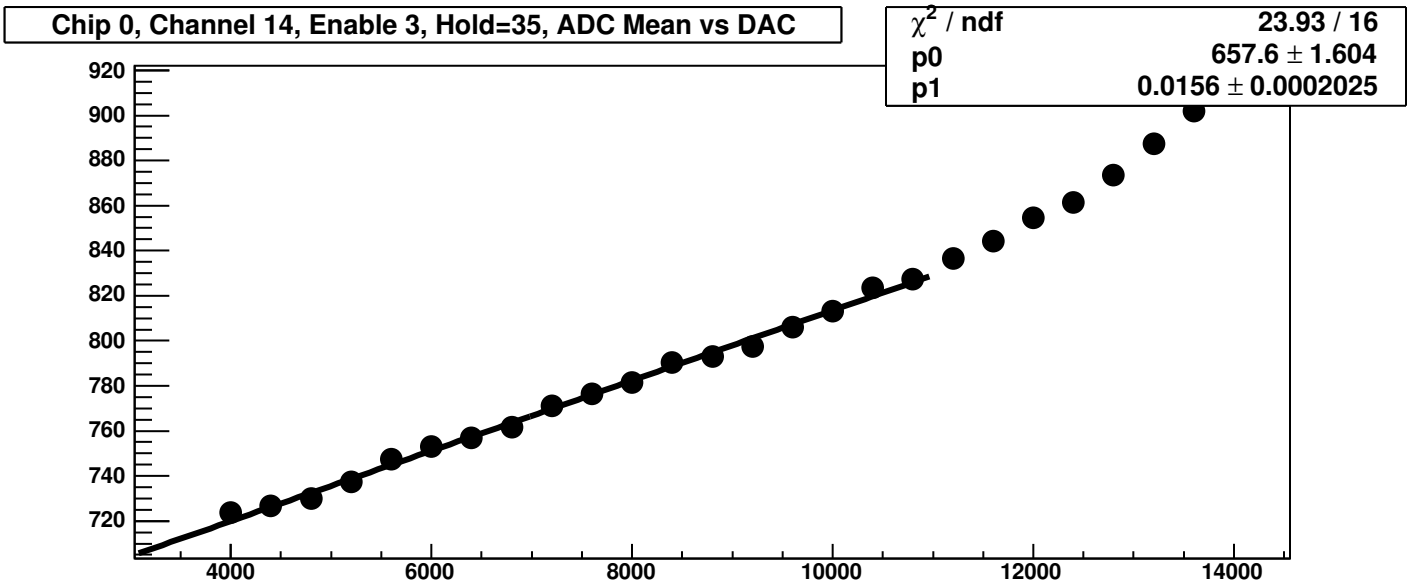

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

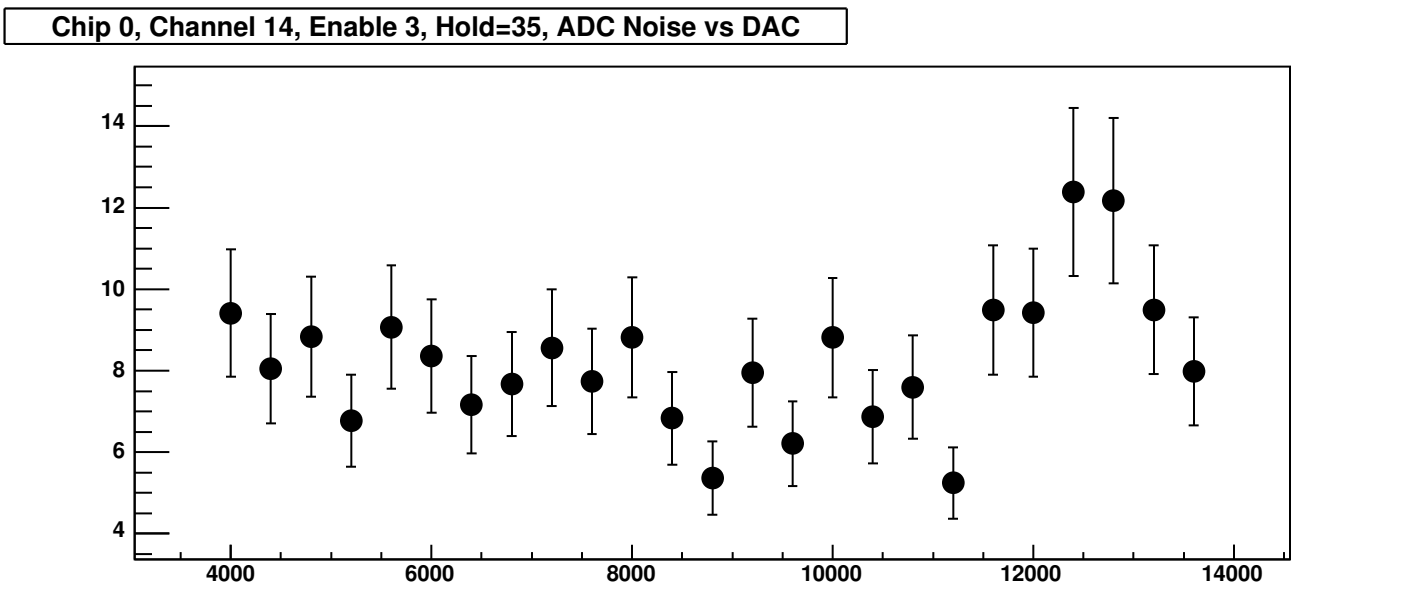

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000

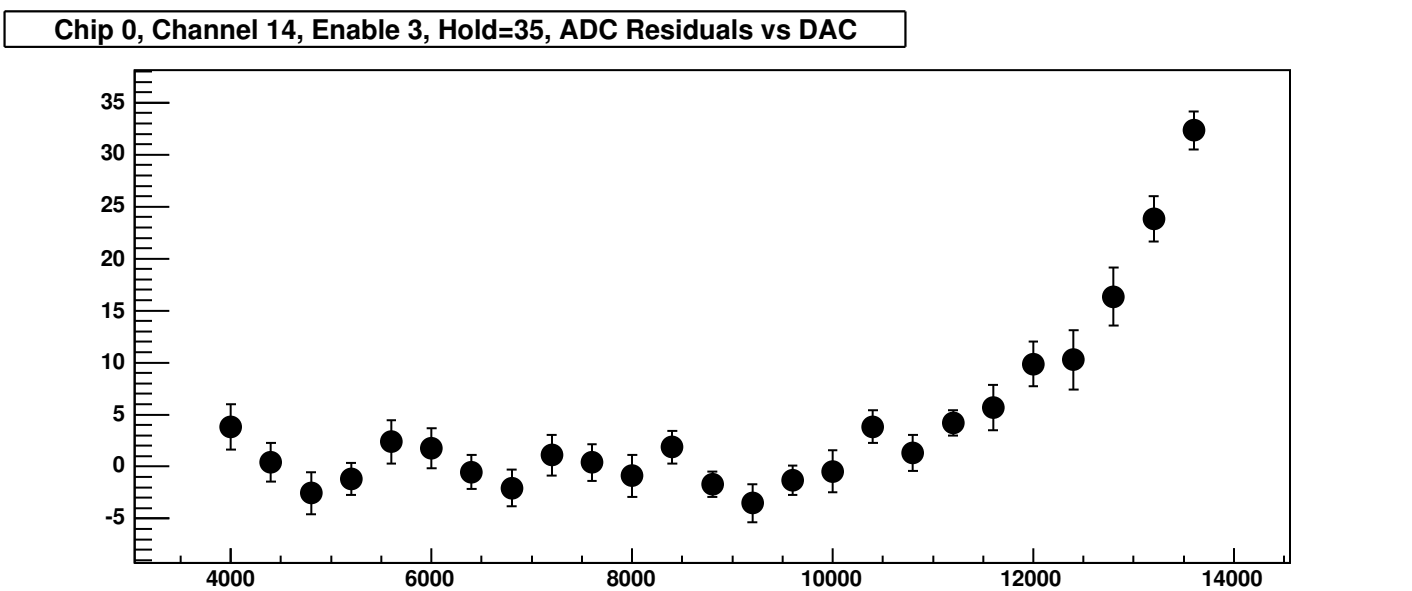

Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.51 / 16
p0 657.1 ± 1.532
p1 0.01428 ± 0.0002017
840
820
800
780
760
740
720
4000
6000
8000
10000
12000
14000

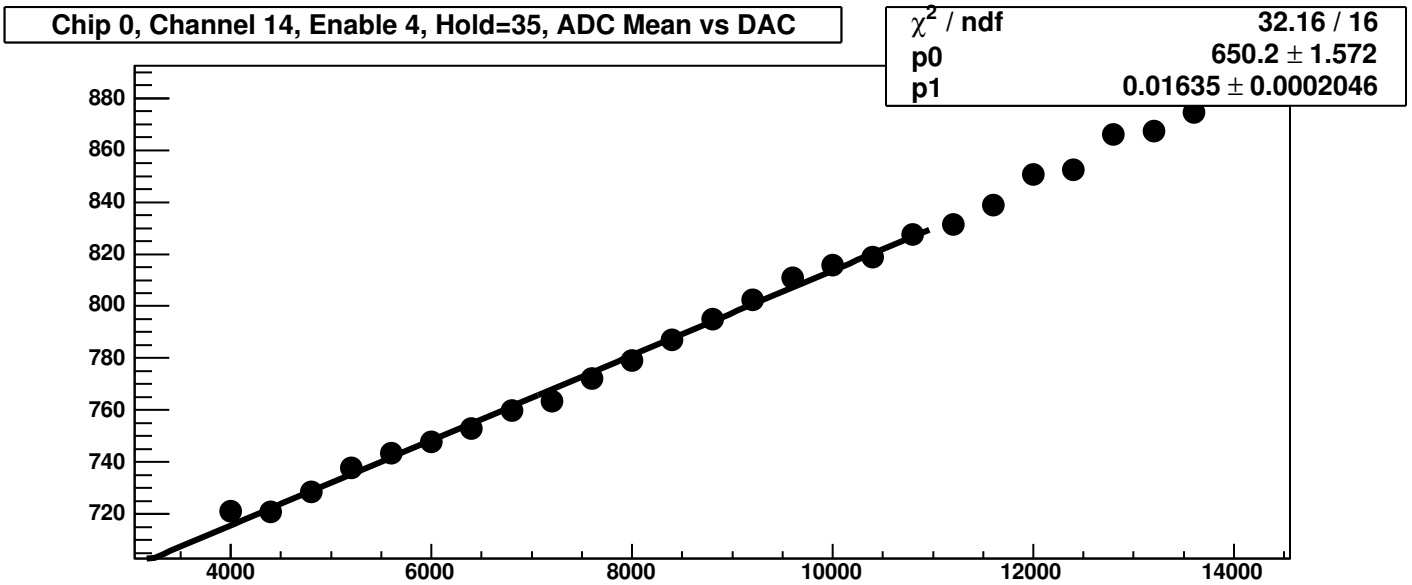

Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

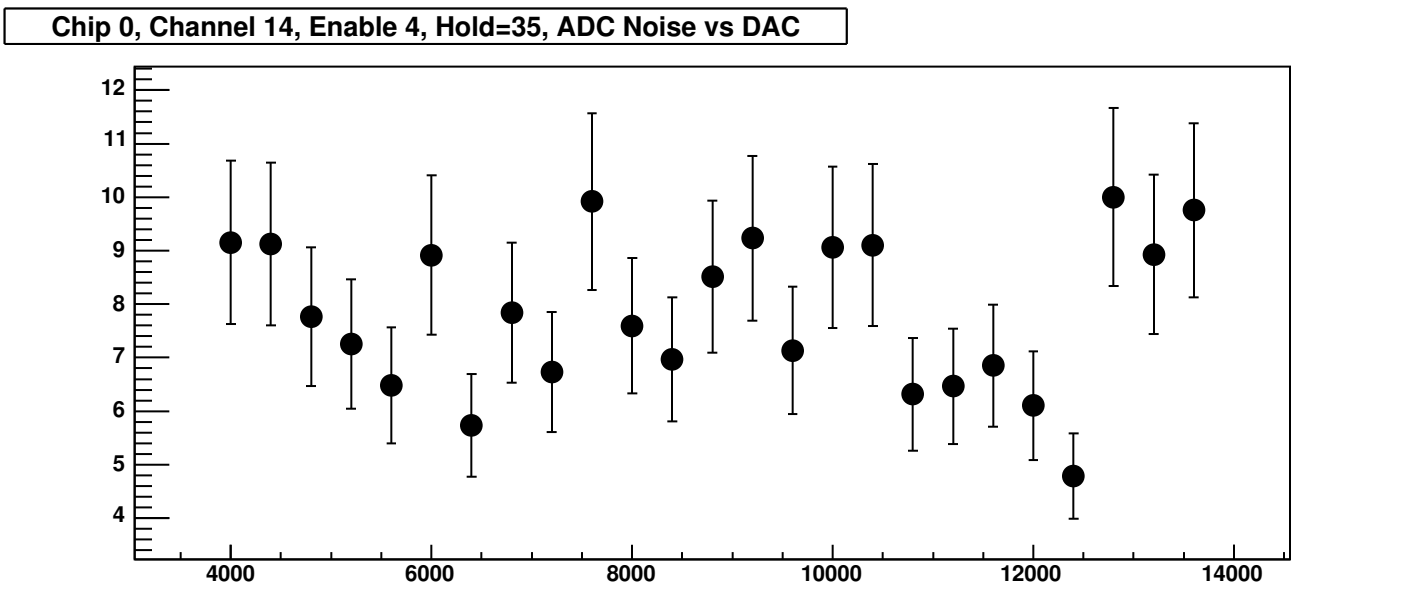

Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

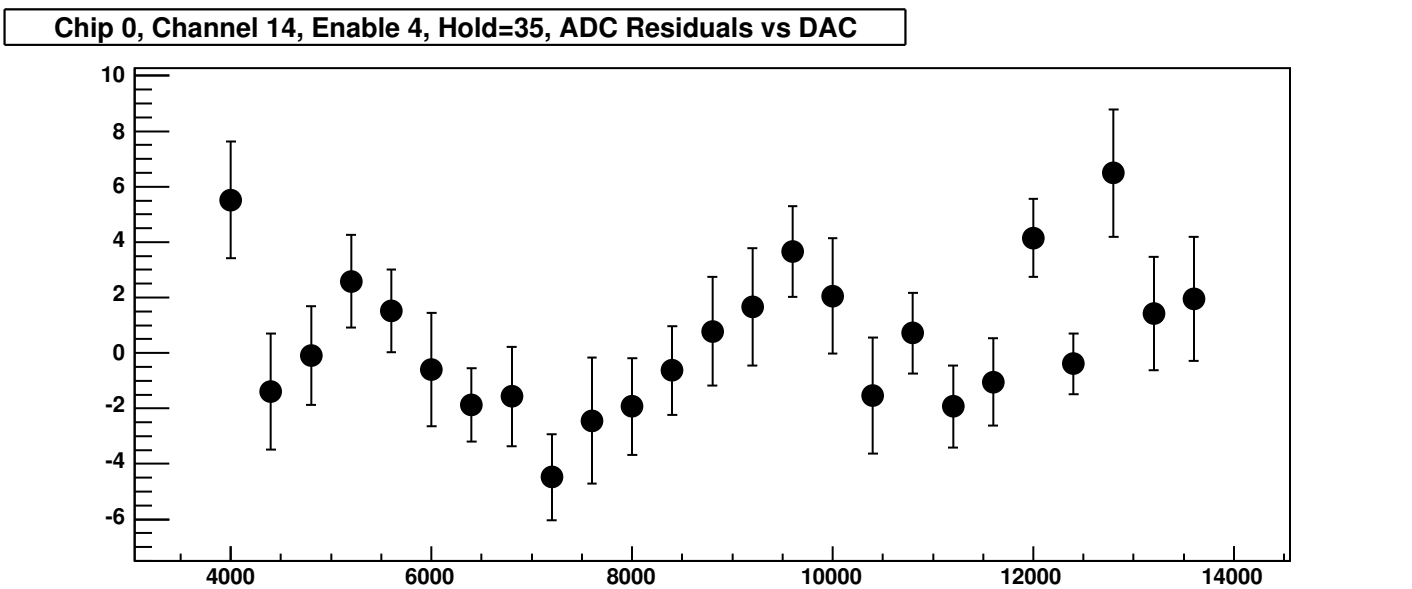

Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.93 / 16
p0 657.6 ± 1.604
p1 0.0156 ± 0.0002025
920
900
880
860
840
820
800
780
760
740
720
4000
6000
8000
10000
12000
14000

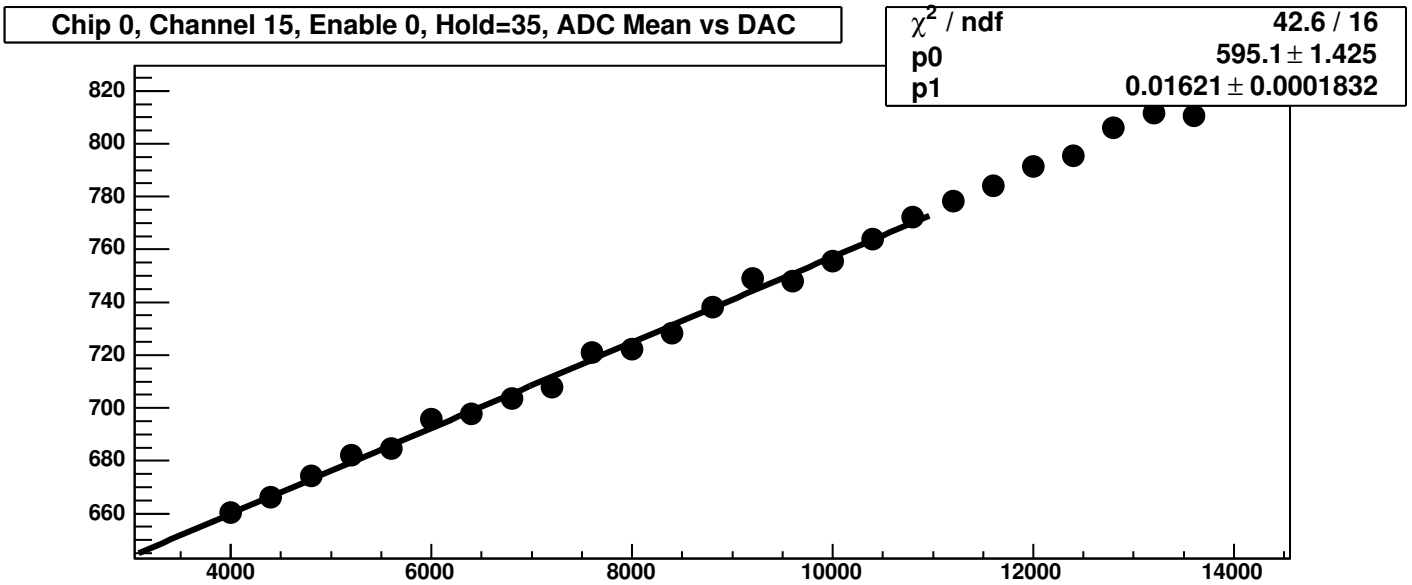

Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

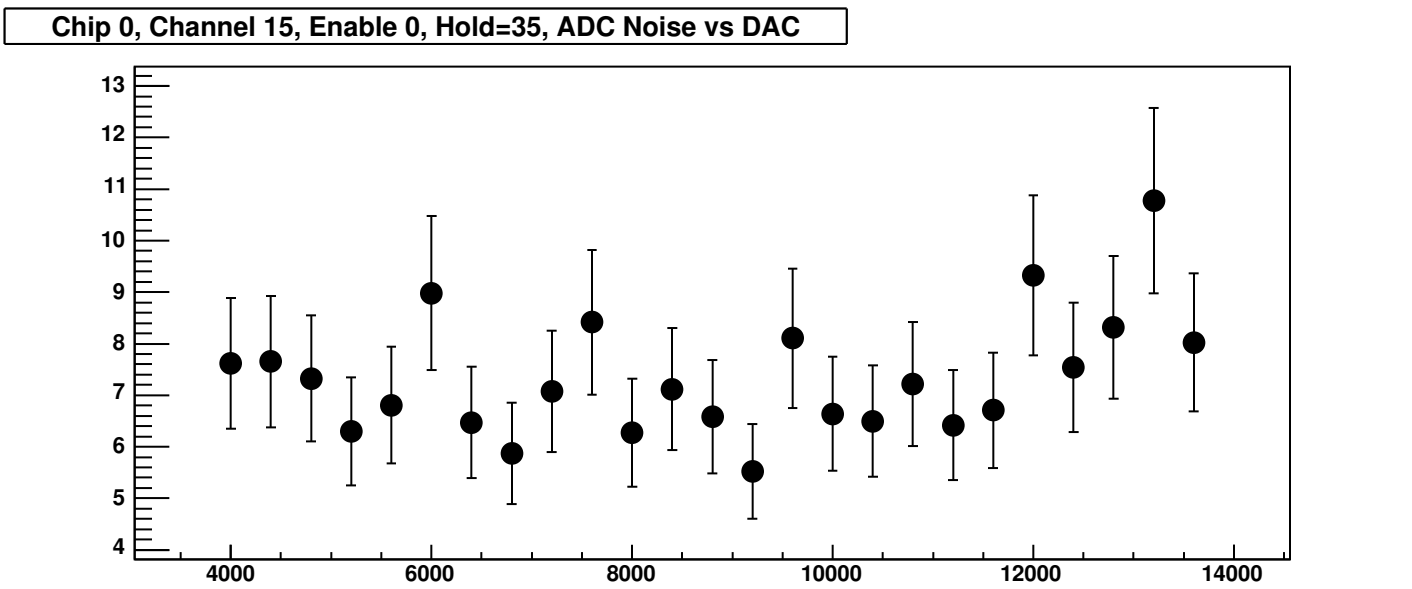

Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

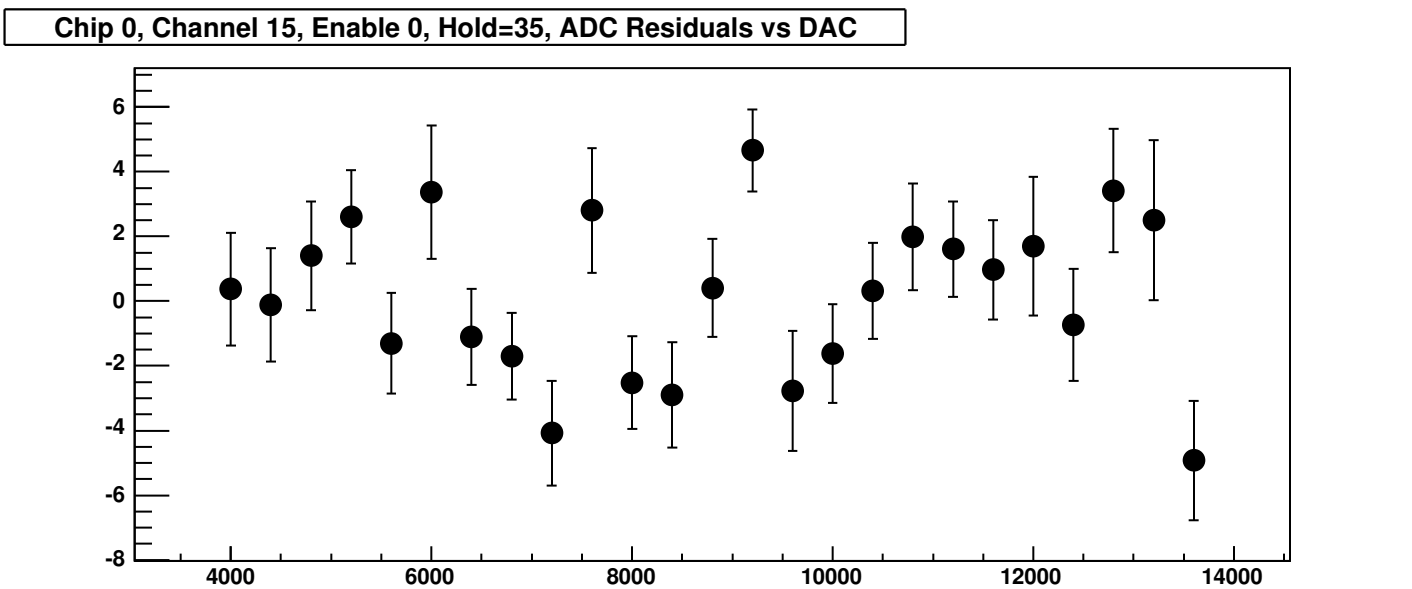

Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.16 / 16
p0 650.2 ± 1.572
p1 0.01635 ± 0.0002046
880
860
840
820
800
780
760
740
720
4000
6000
8000
10000
12000
14000

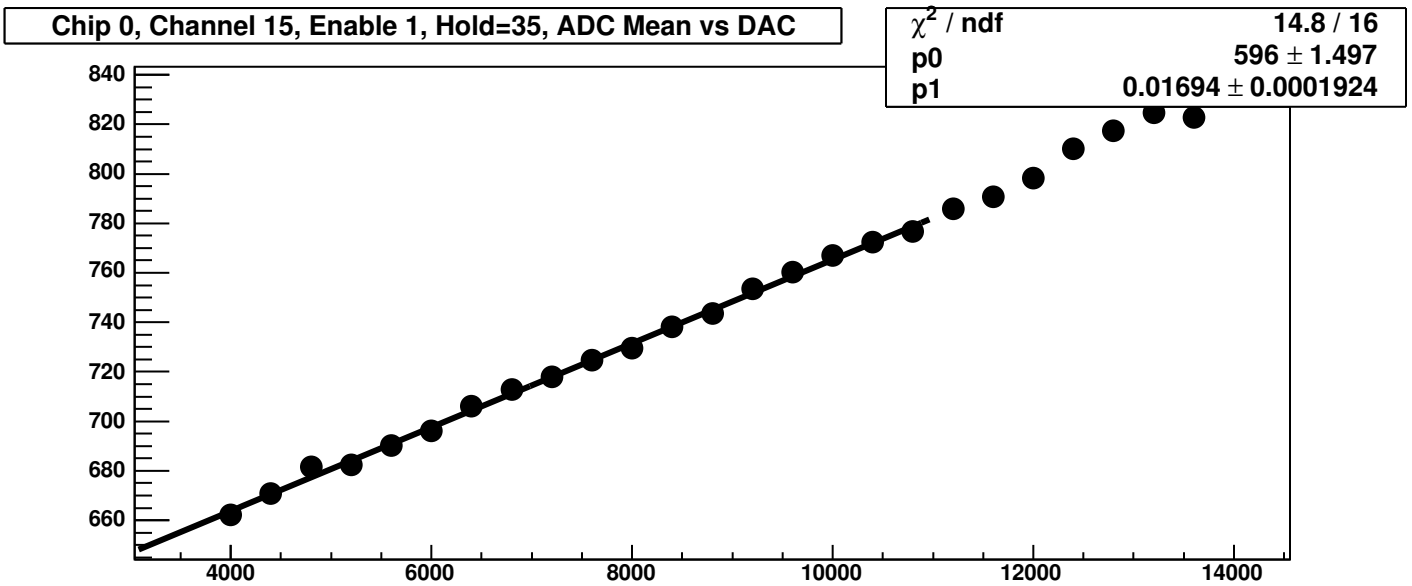

Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

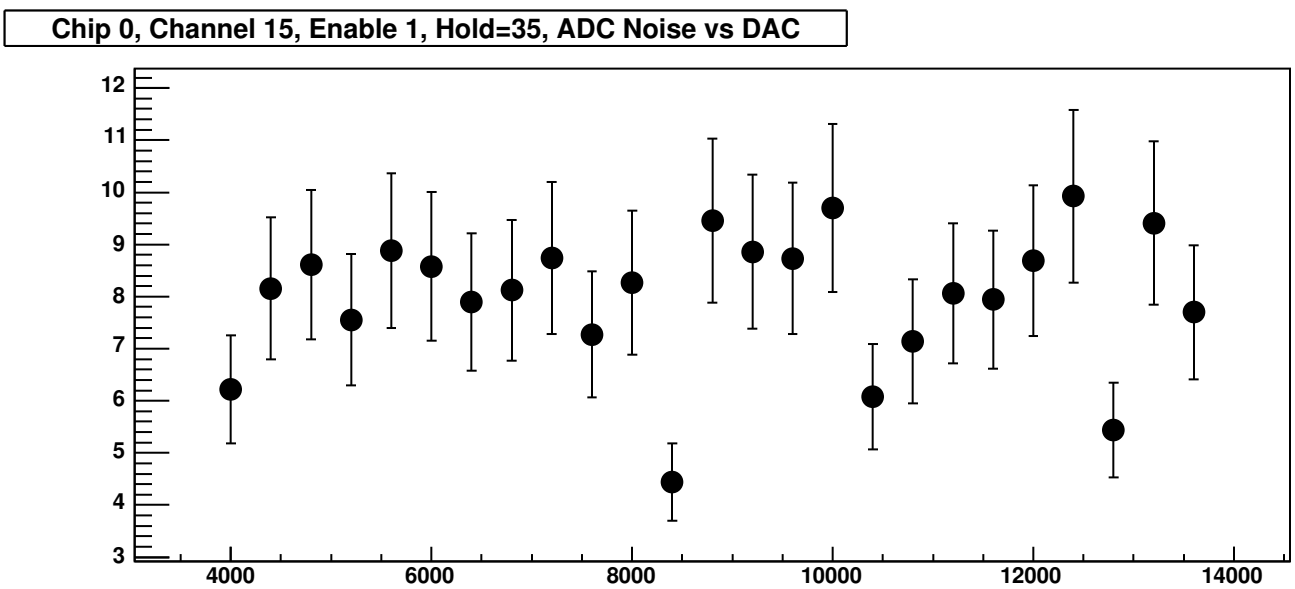

Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

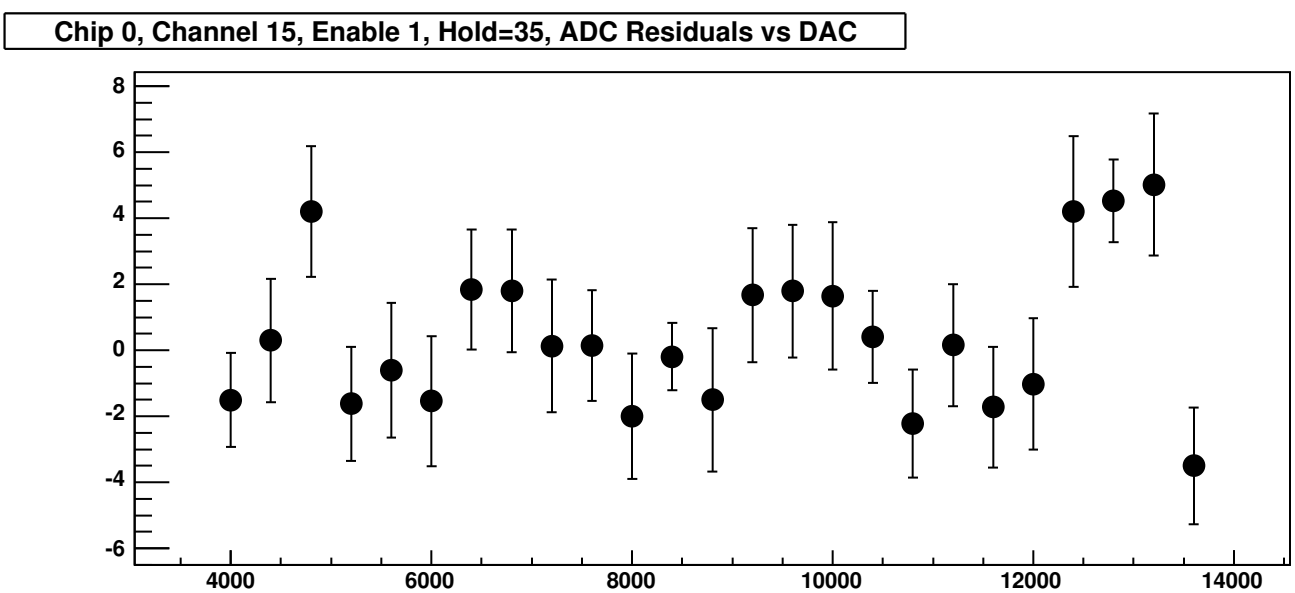

Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42.6 / 16
p0 595.1 ± 1.425
p1 0.01621 ± 0.0001832
820
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

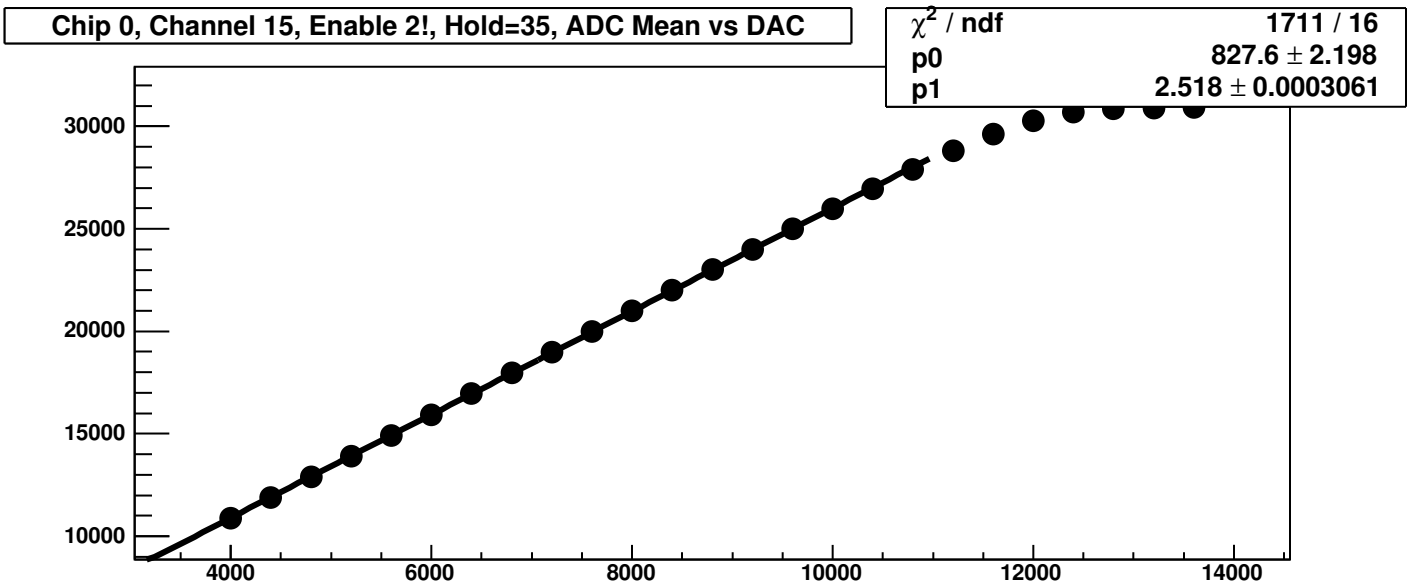

Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

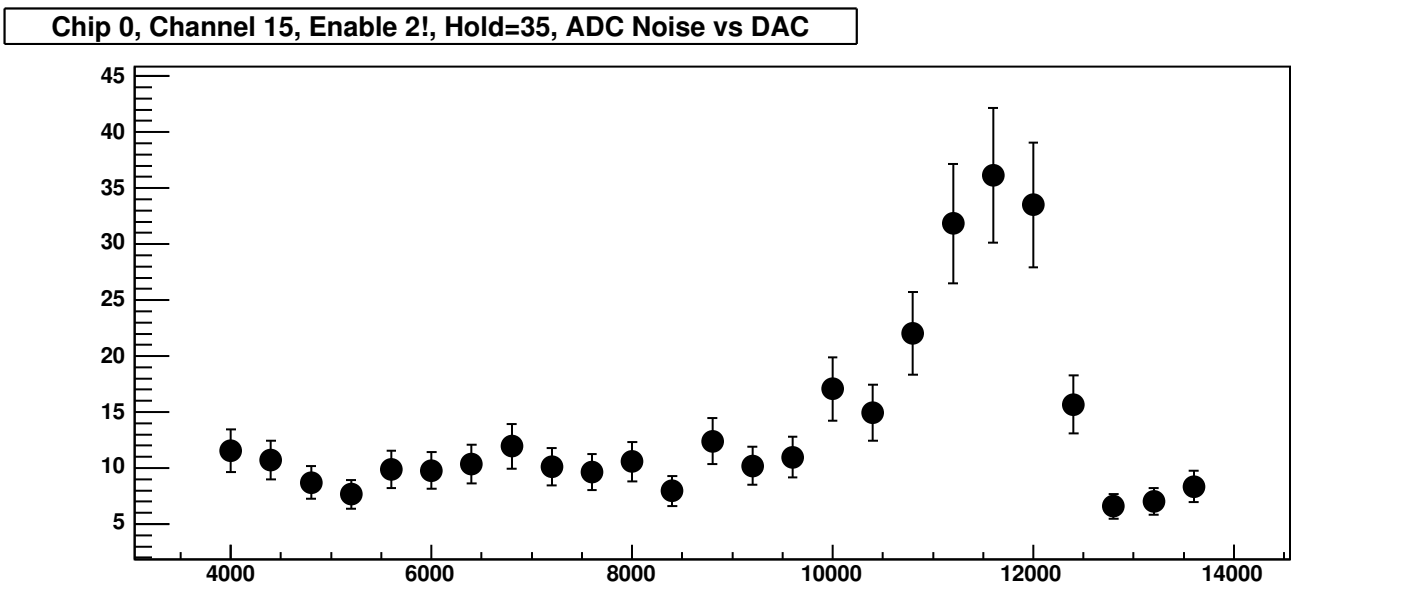

Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

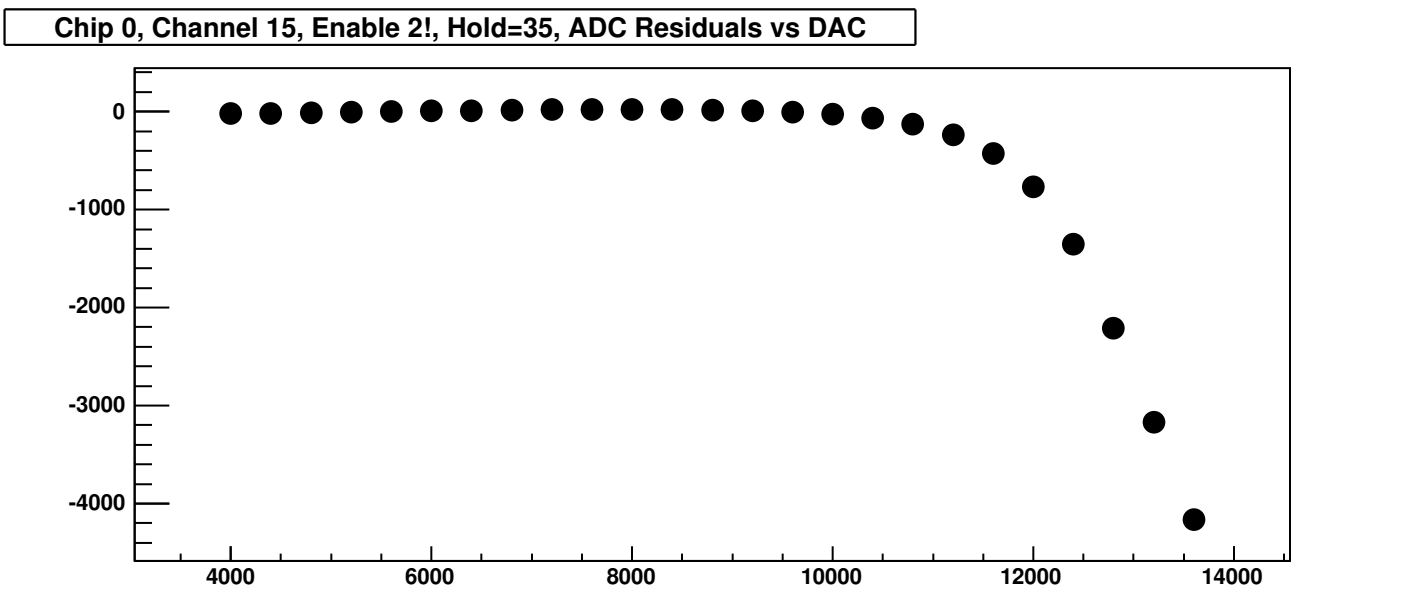

Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.8 / 16
p0 596 ± 1.497
p1 0.01694 ± 0.0001924
840
820
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

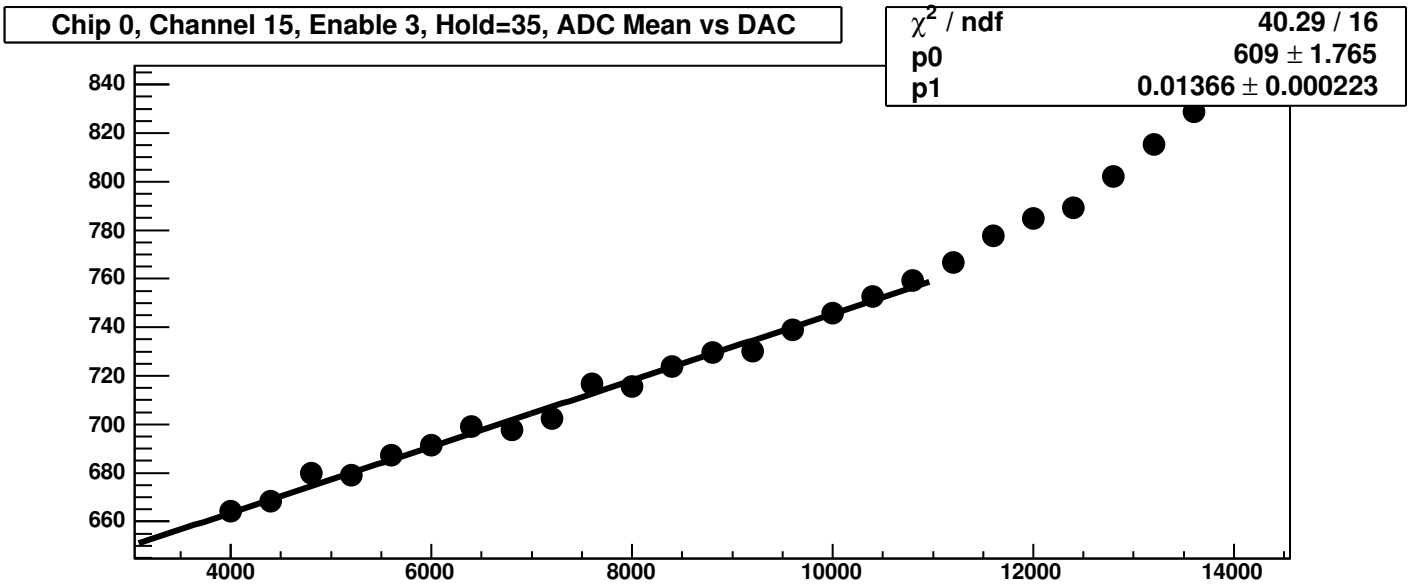

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

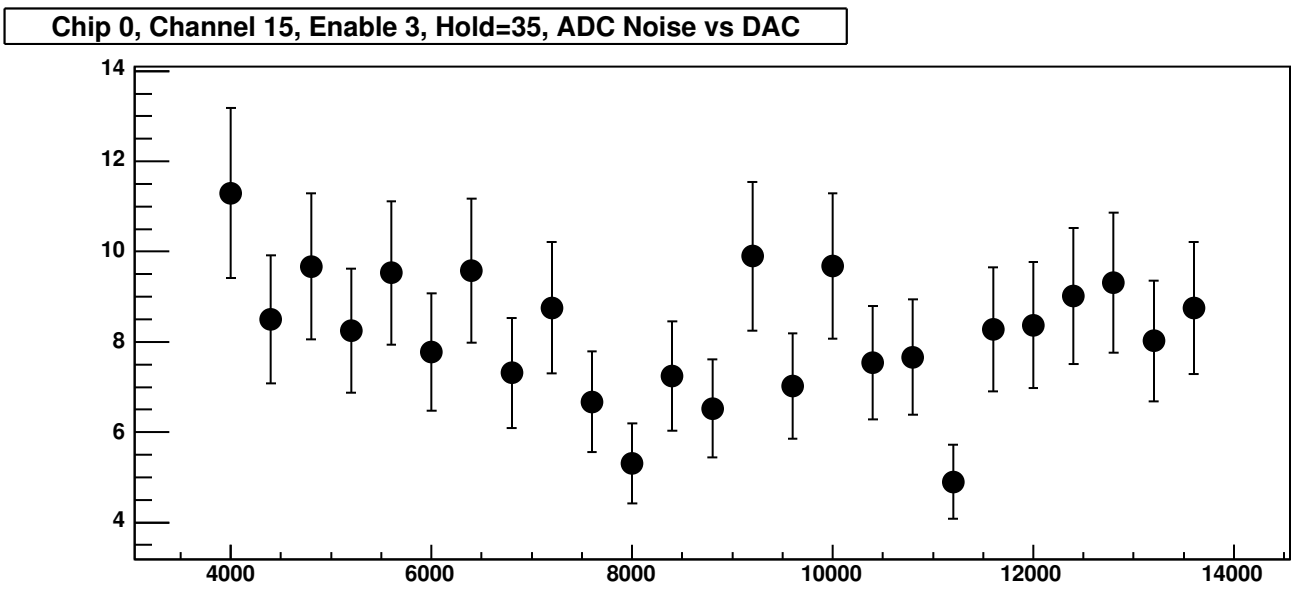

Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

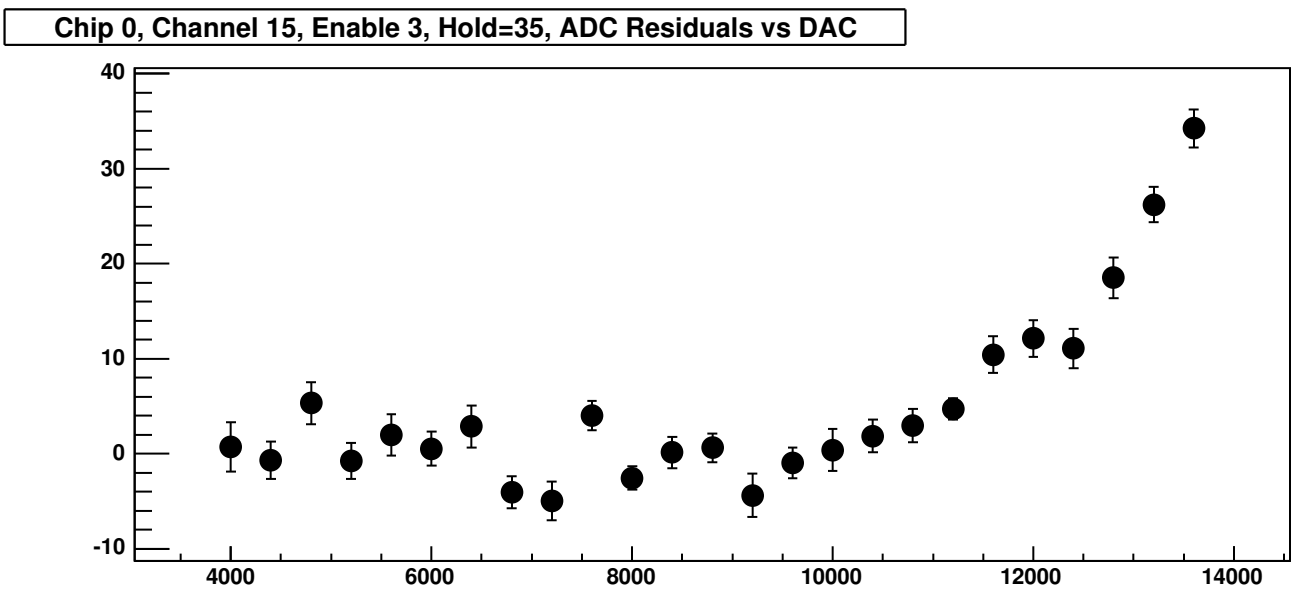

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1711 / 16
p0 827.6 ± 2.198
p1 2.518 ± 0.0003061
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

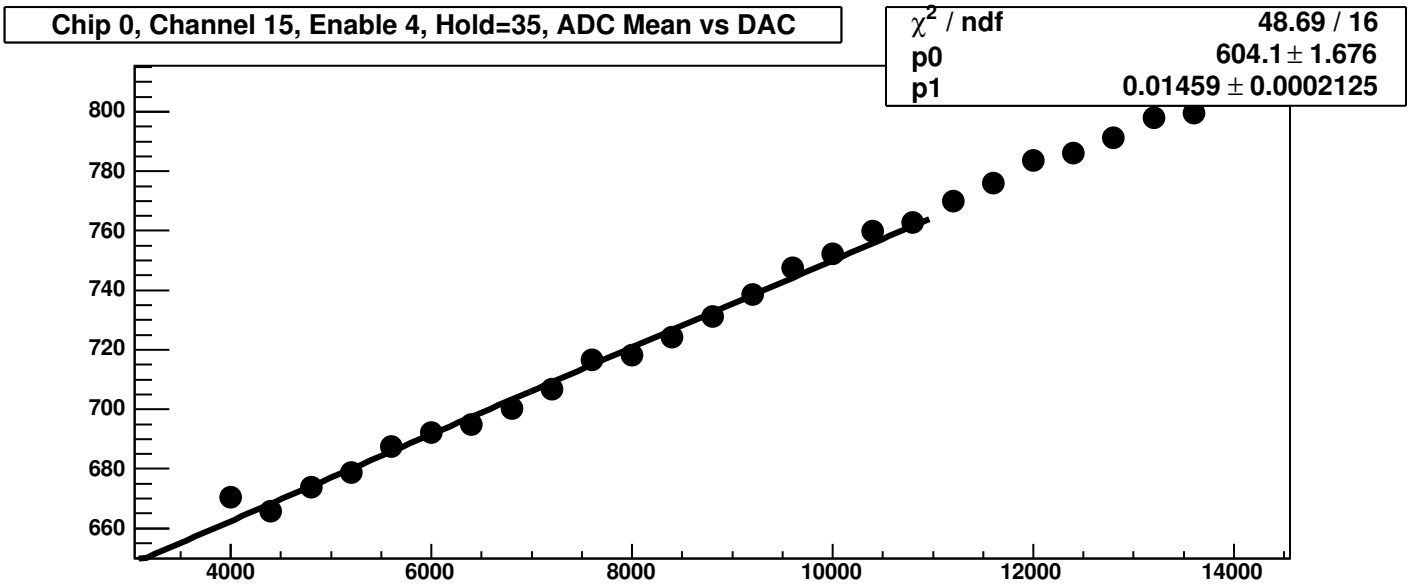

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

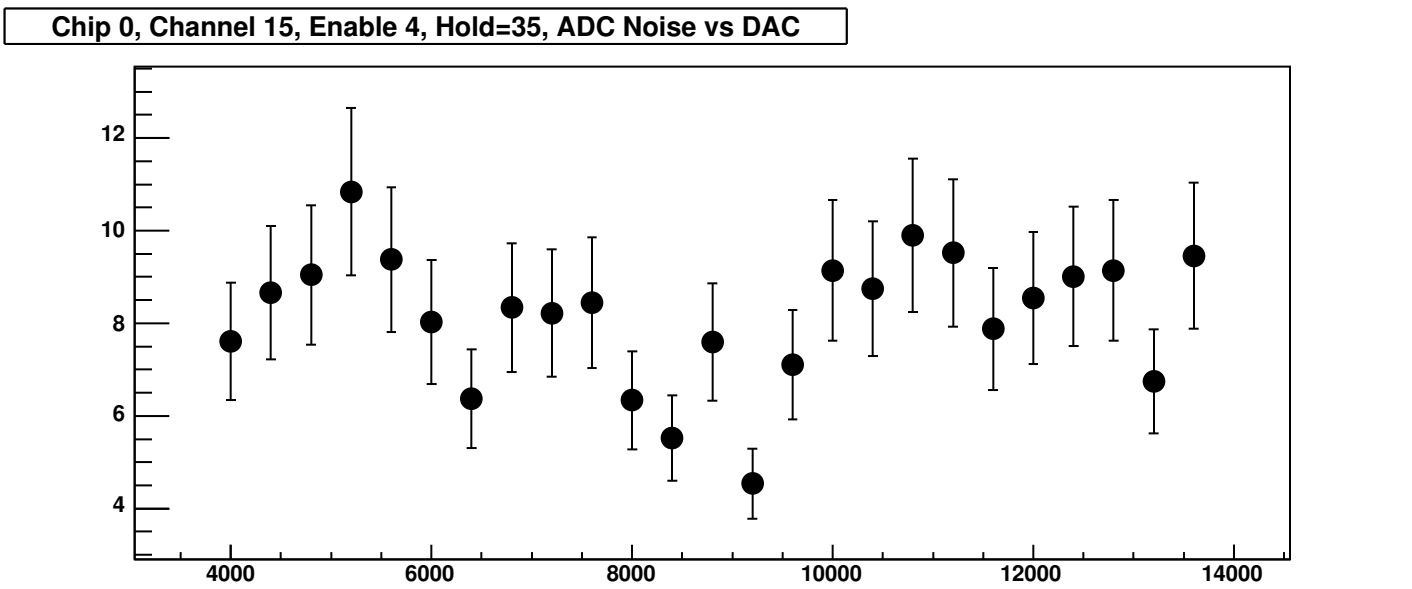

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

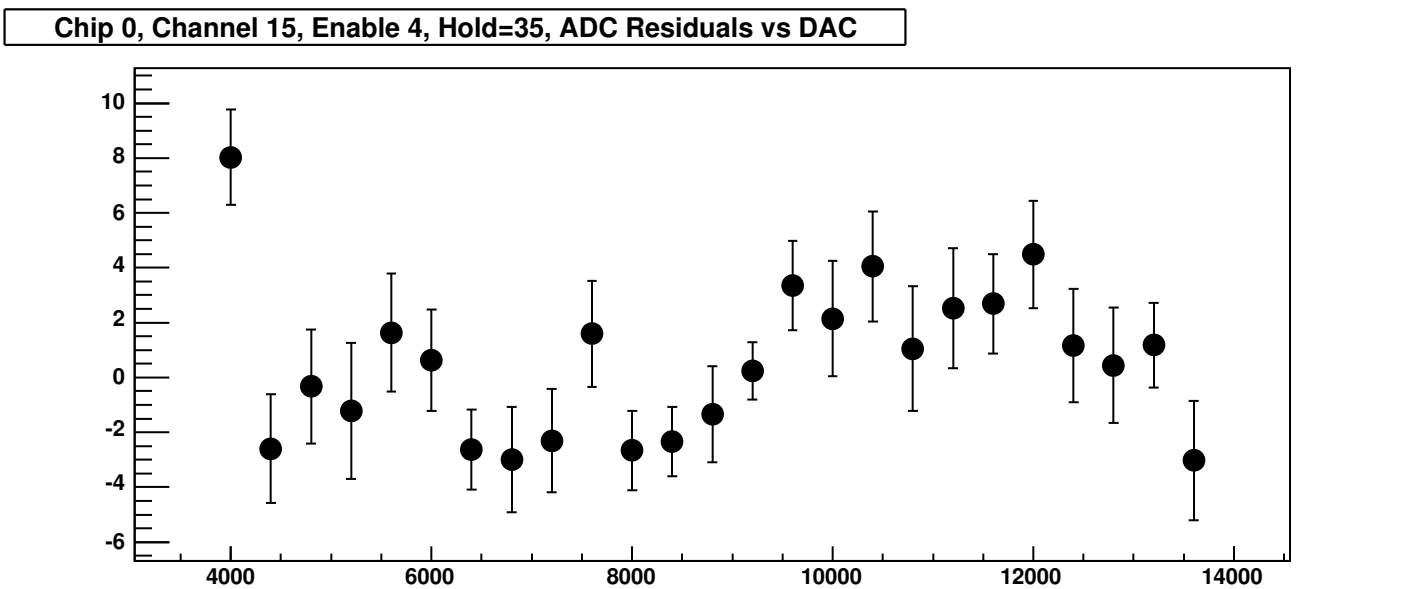

Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.29 / 16
p0 609 ± 1.765
p1 0.01366 ± 0.000223
840
820
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

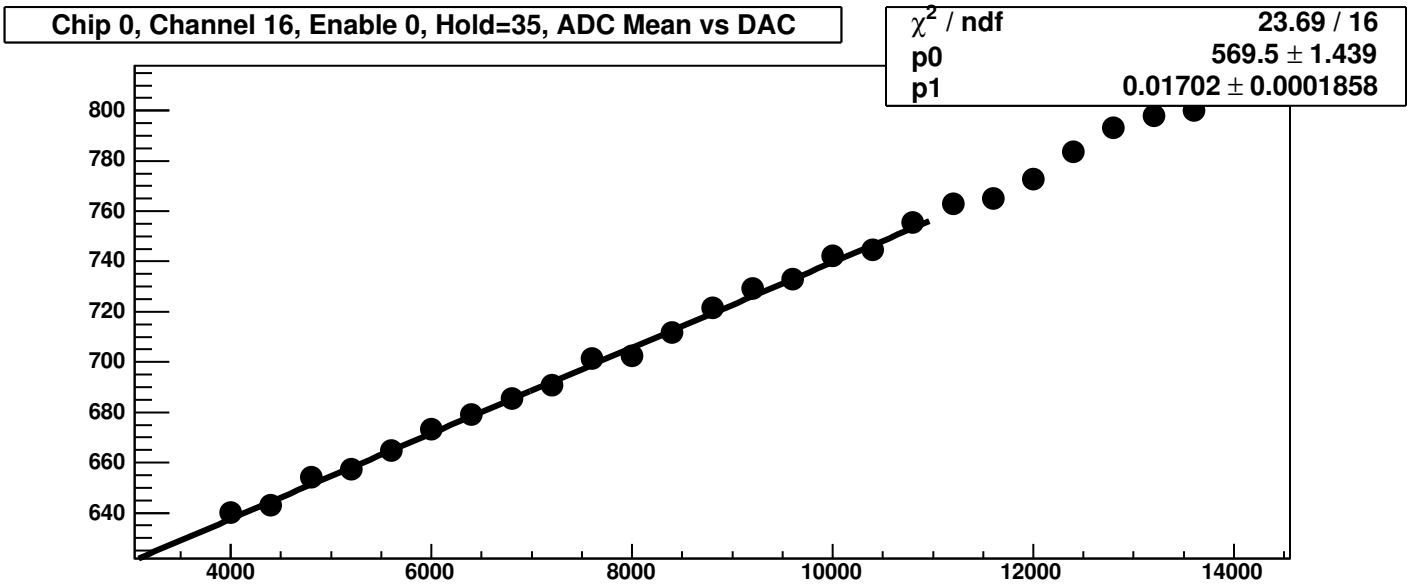

Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

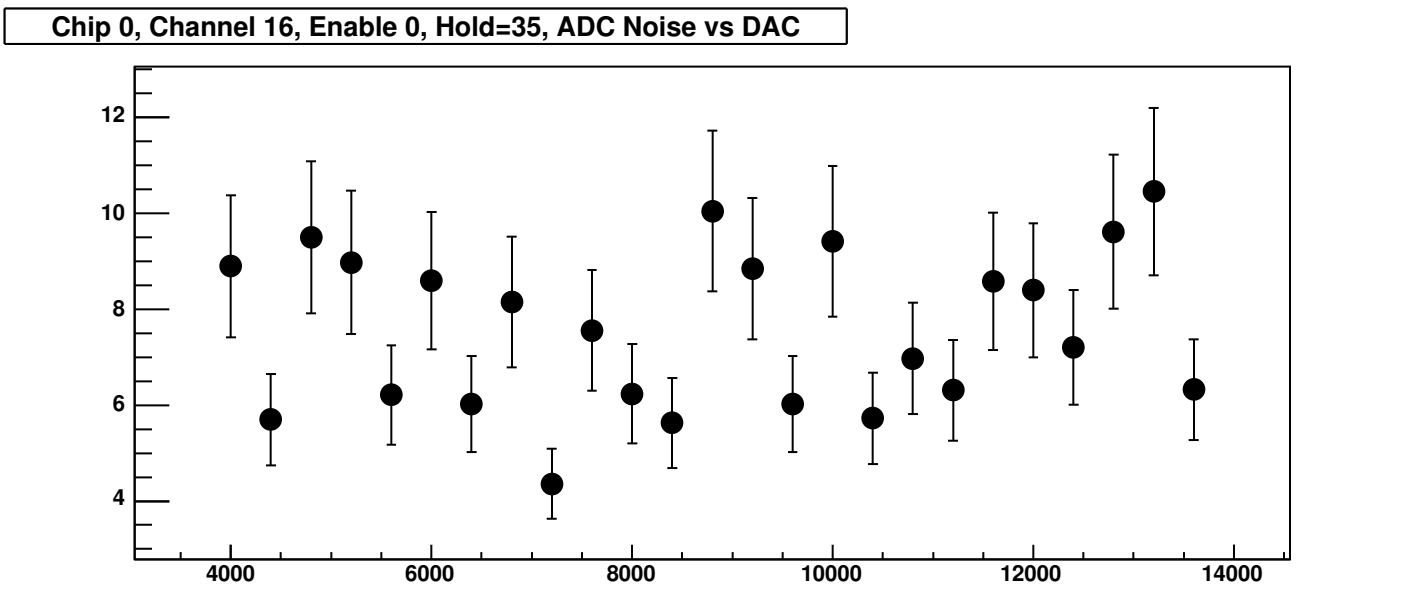

Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
4000
6000
8000
10000
12000
14000

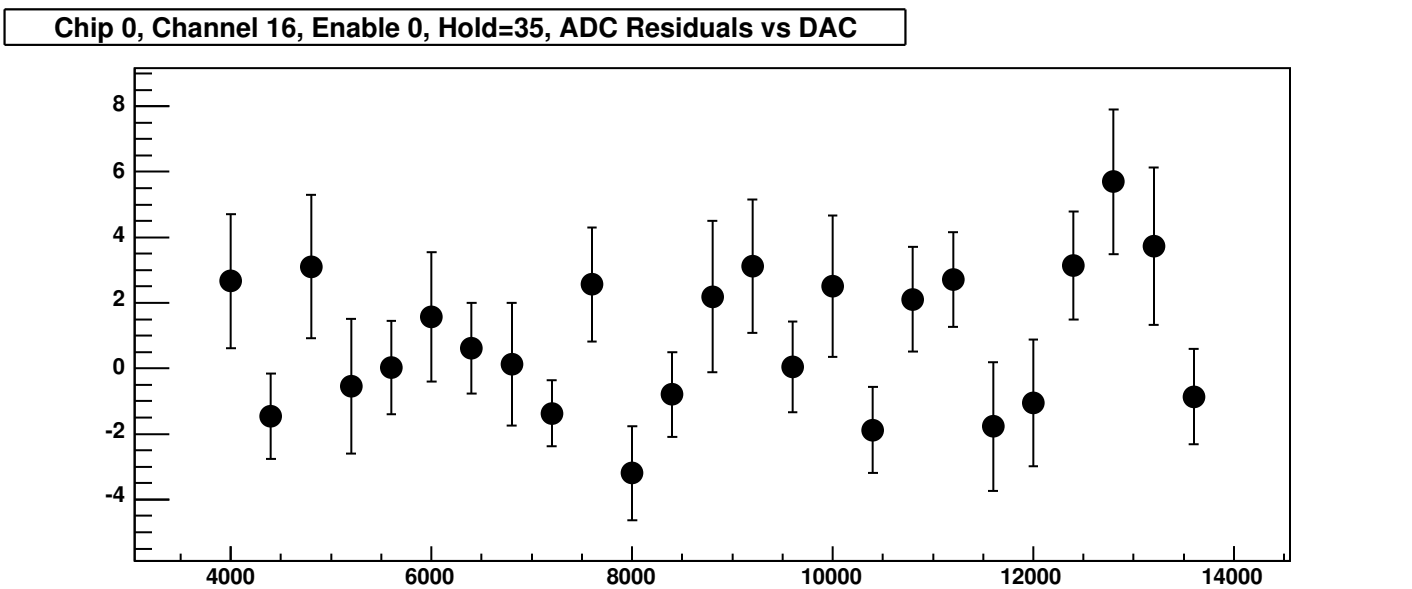

Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.69 / 16
p0 604.1 ± 1.676
p1 0.01459 ± 0.0002125
800
780
760
740
720
700
680
660
4000
6000
8000
10000
12000
14000

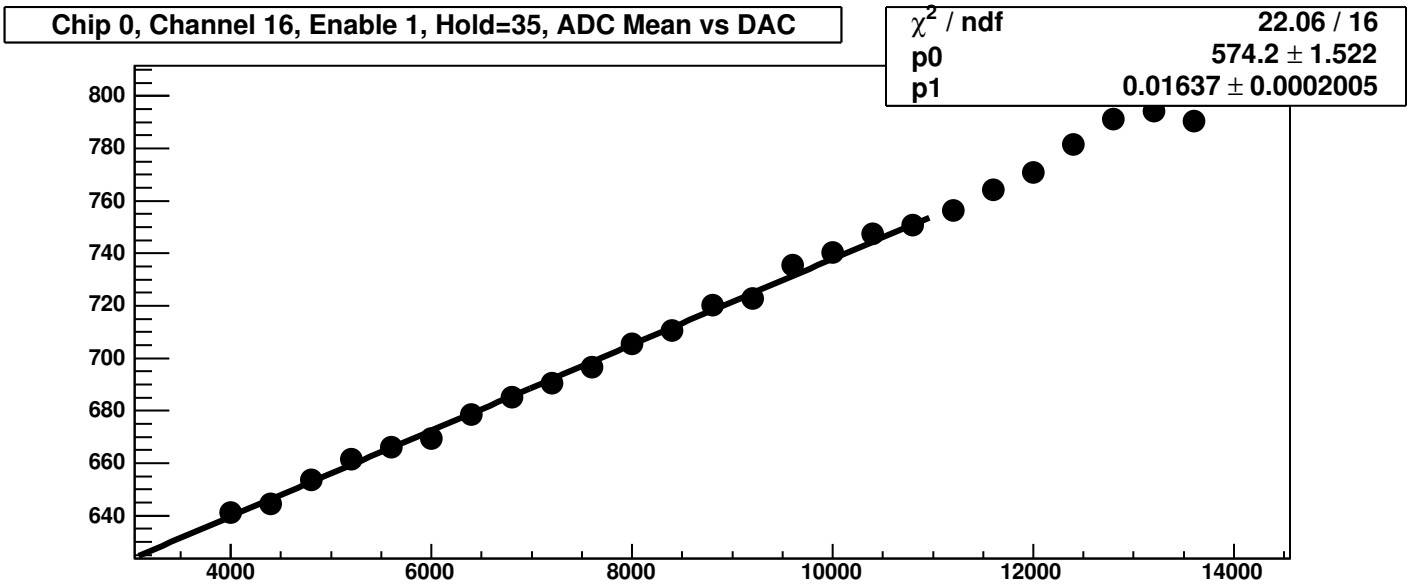

Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

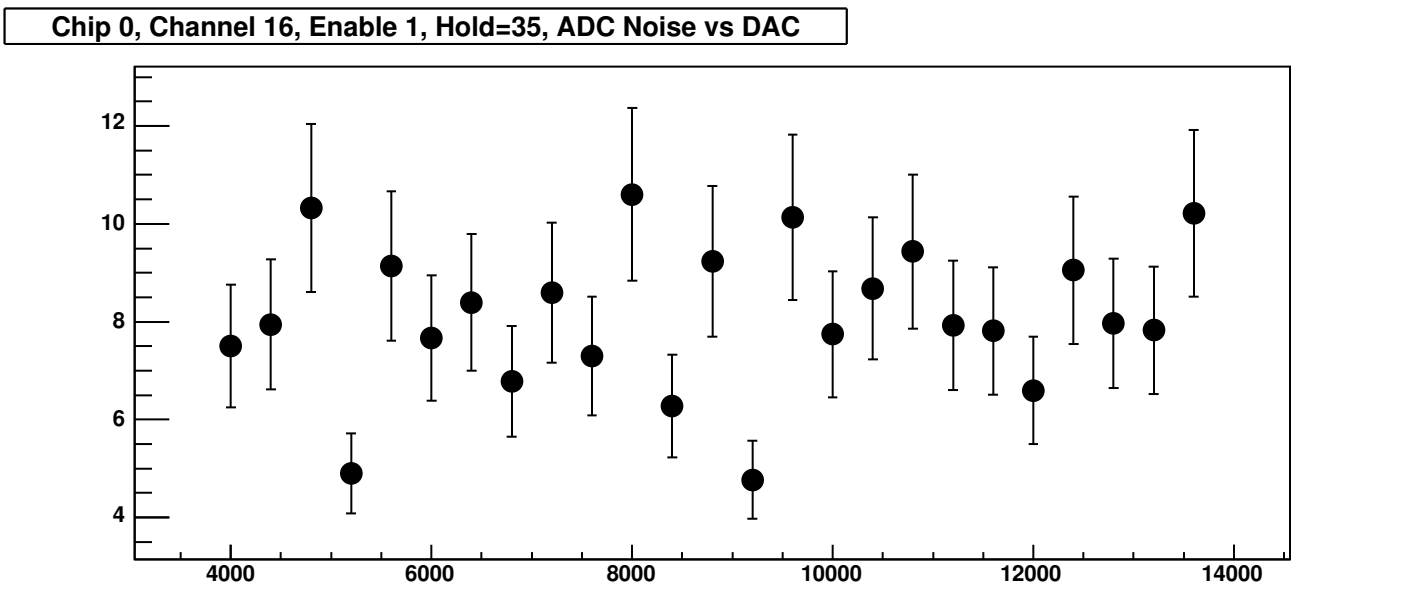

Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

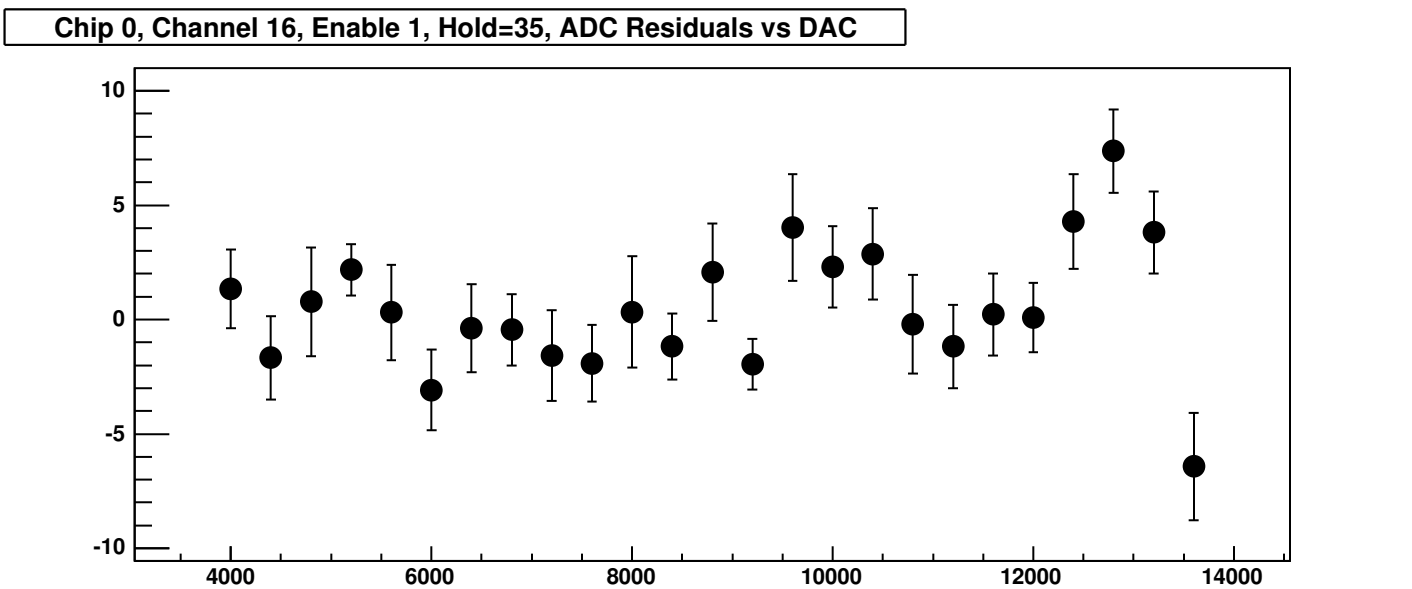

Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 23.69 / 16
p0 569.5 ± 1.439
p1 0.01702 ± 0.0001858
800
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

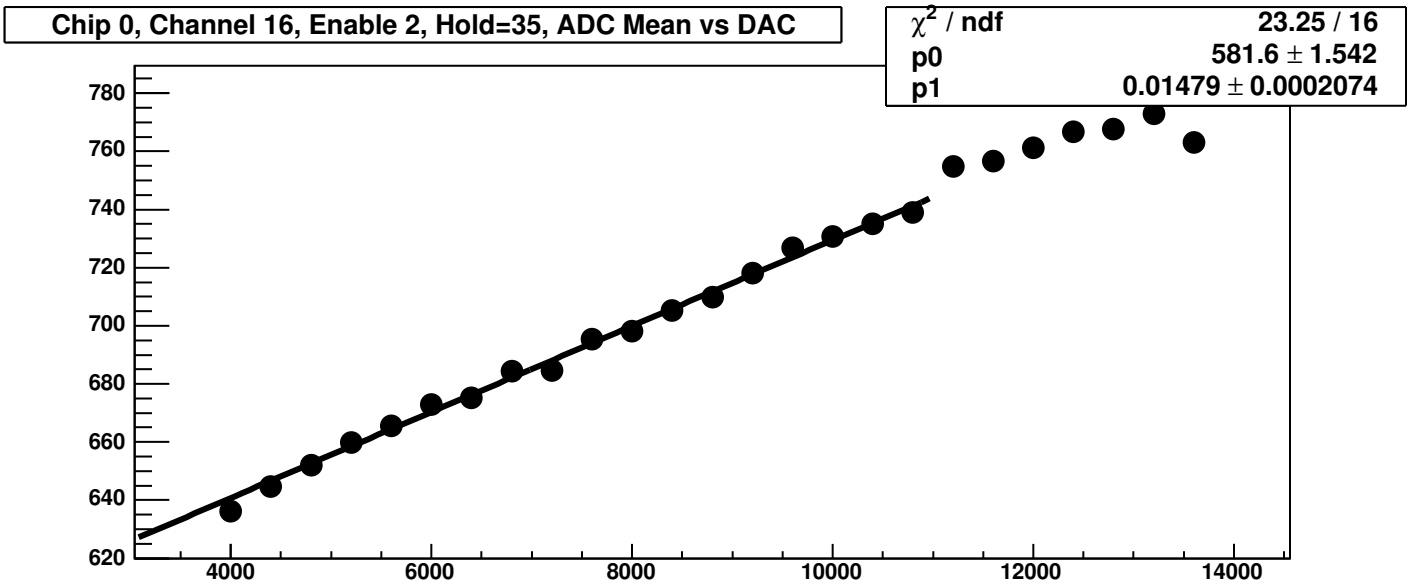

Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

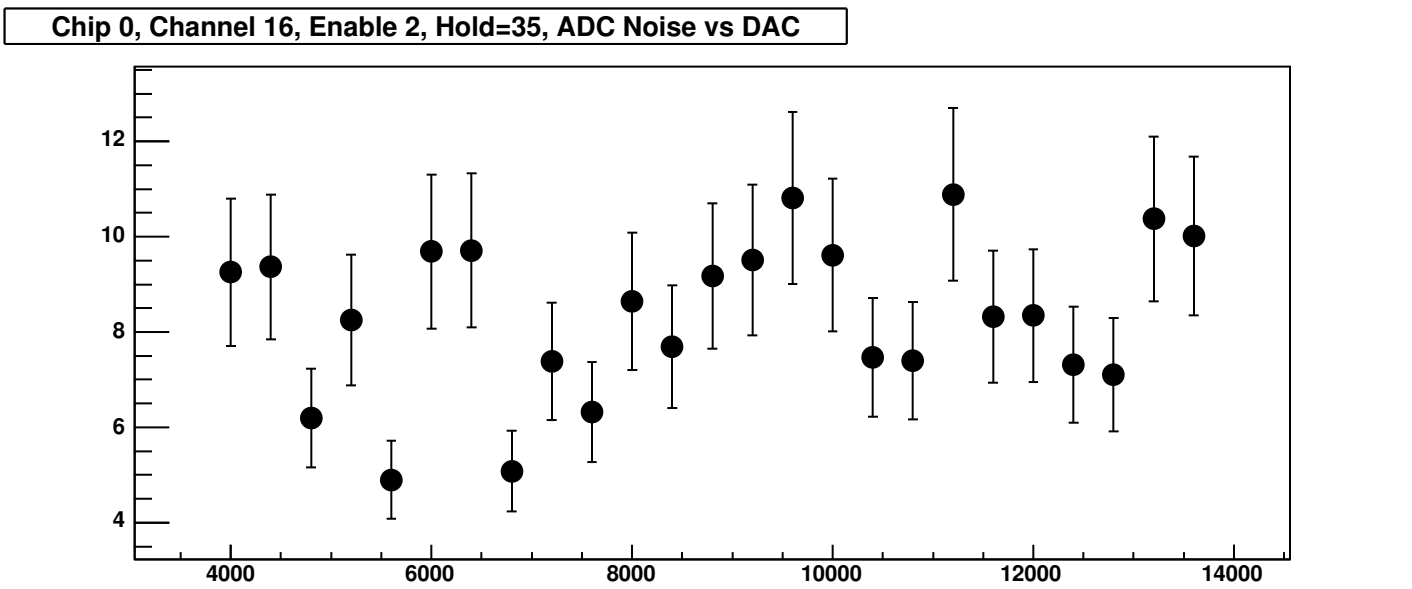

Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

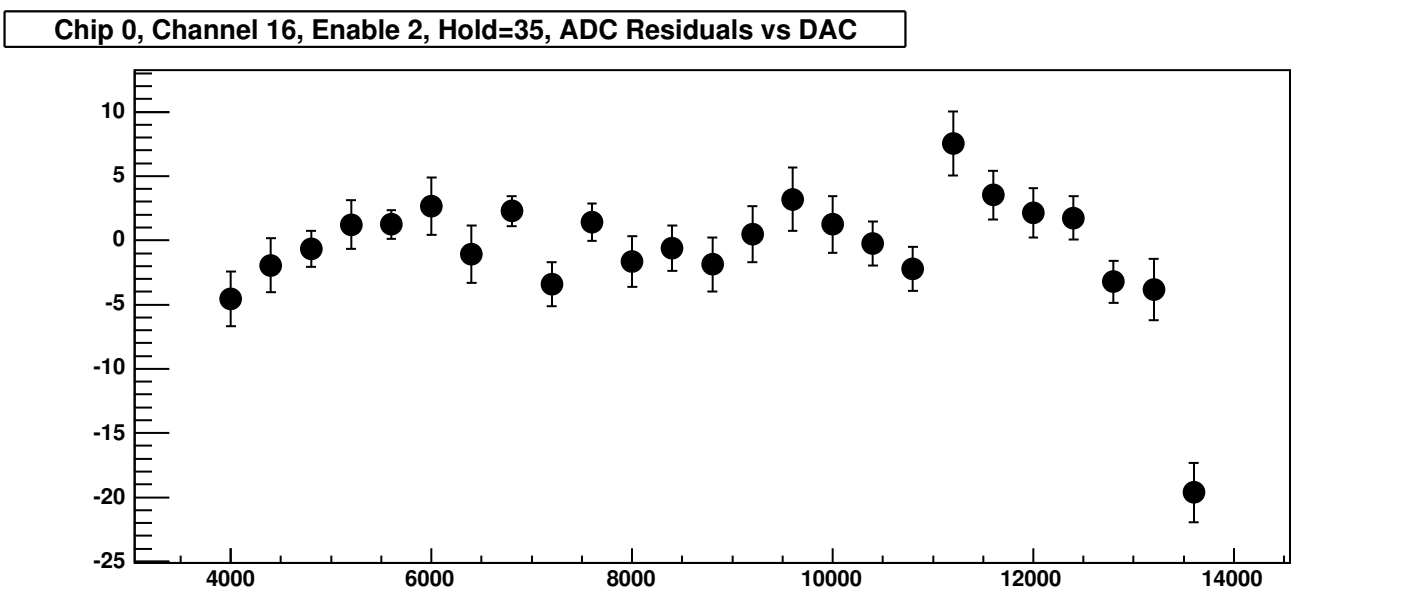

Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.06 / 16
p0 574.2 ± 1.522
p1 0.01637 ± 0.0002005
800
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

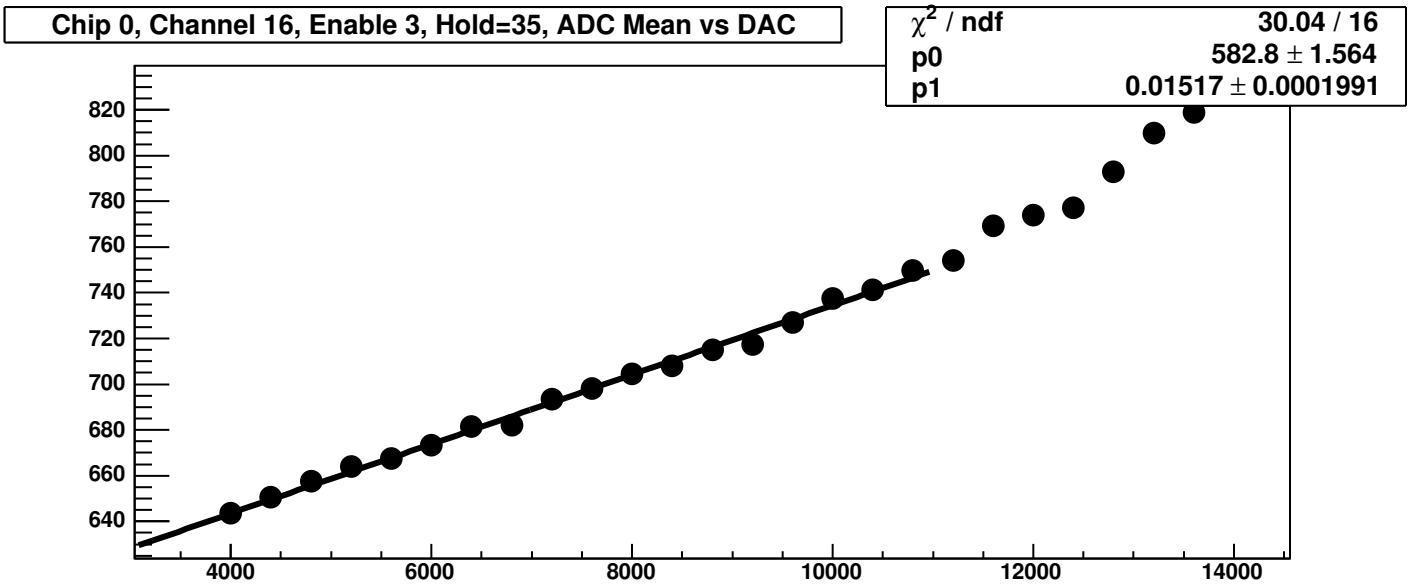

Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

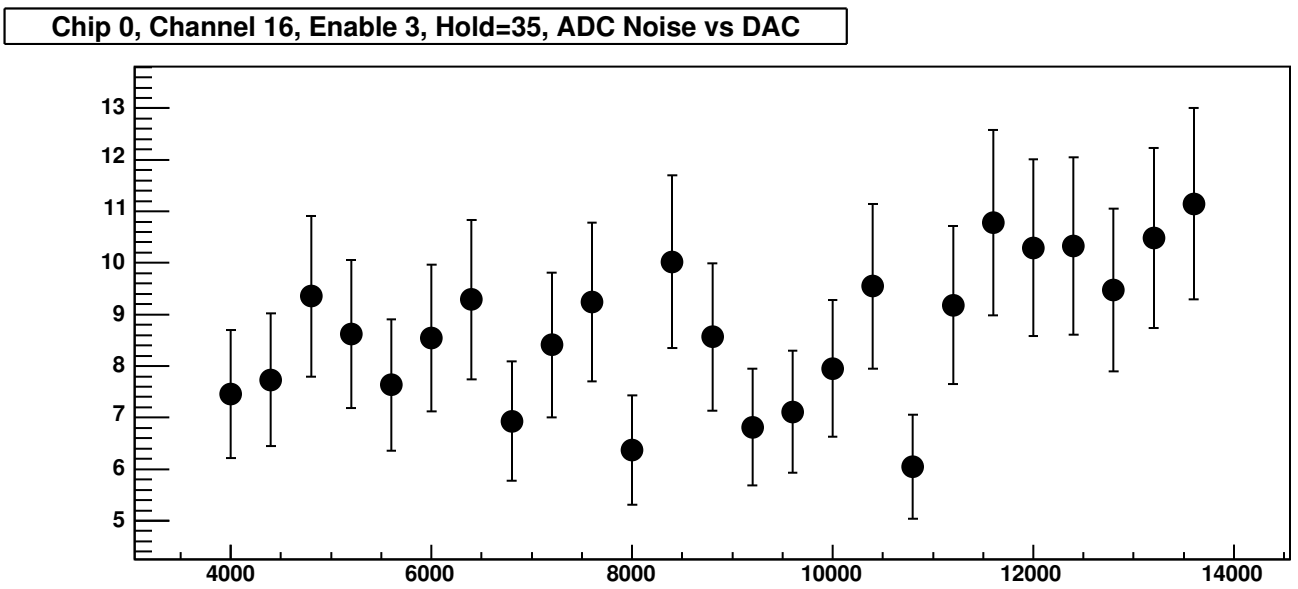

Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

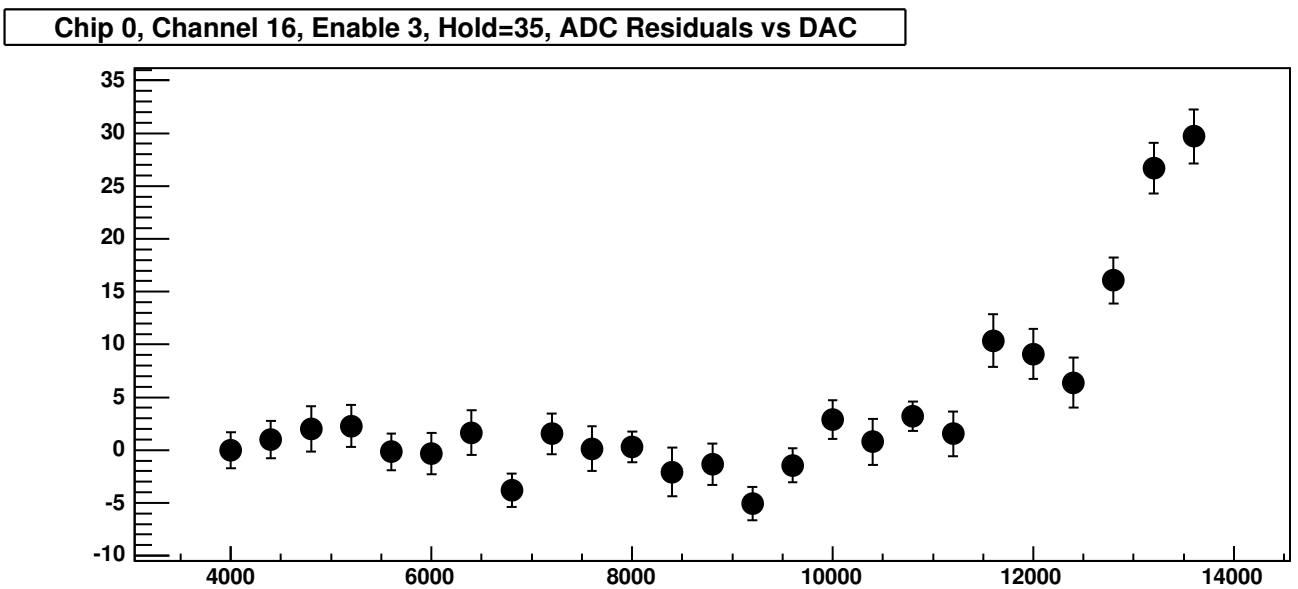

Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.25 / 16
p0 581.6 ± 1.542
p1 0.01479 ± 0.0002074
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

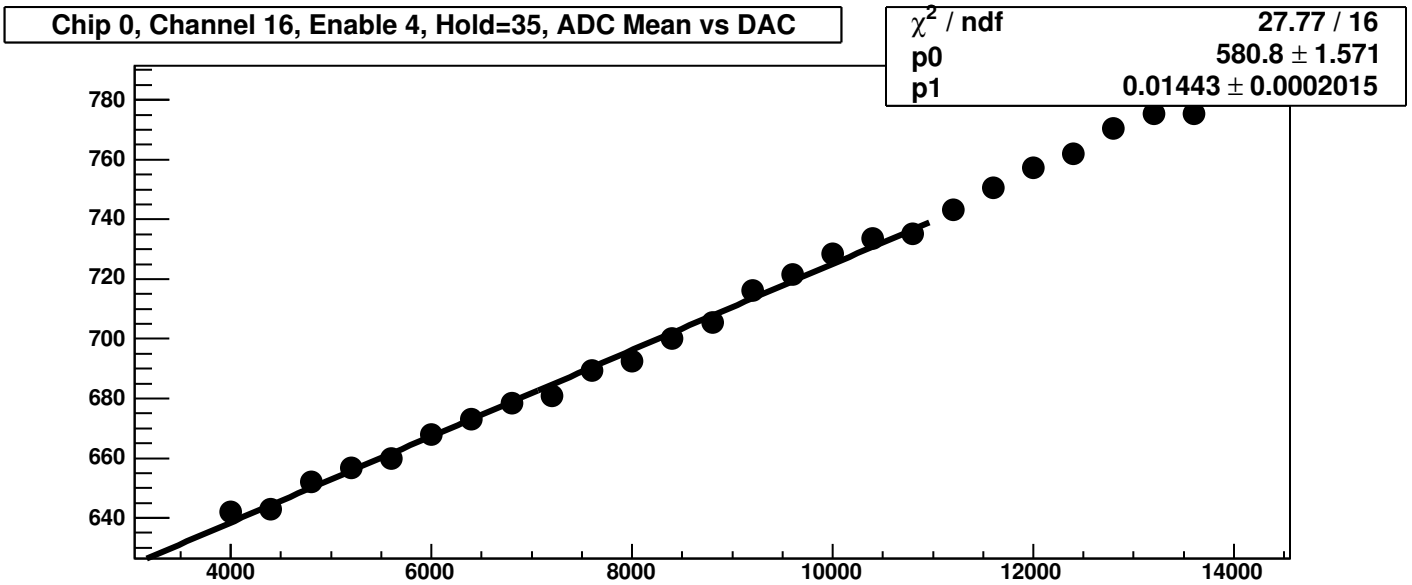

Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

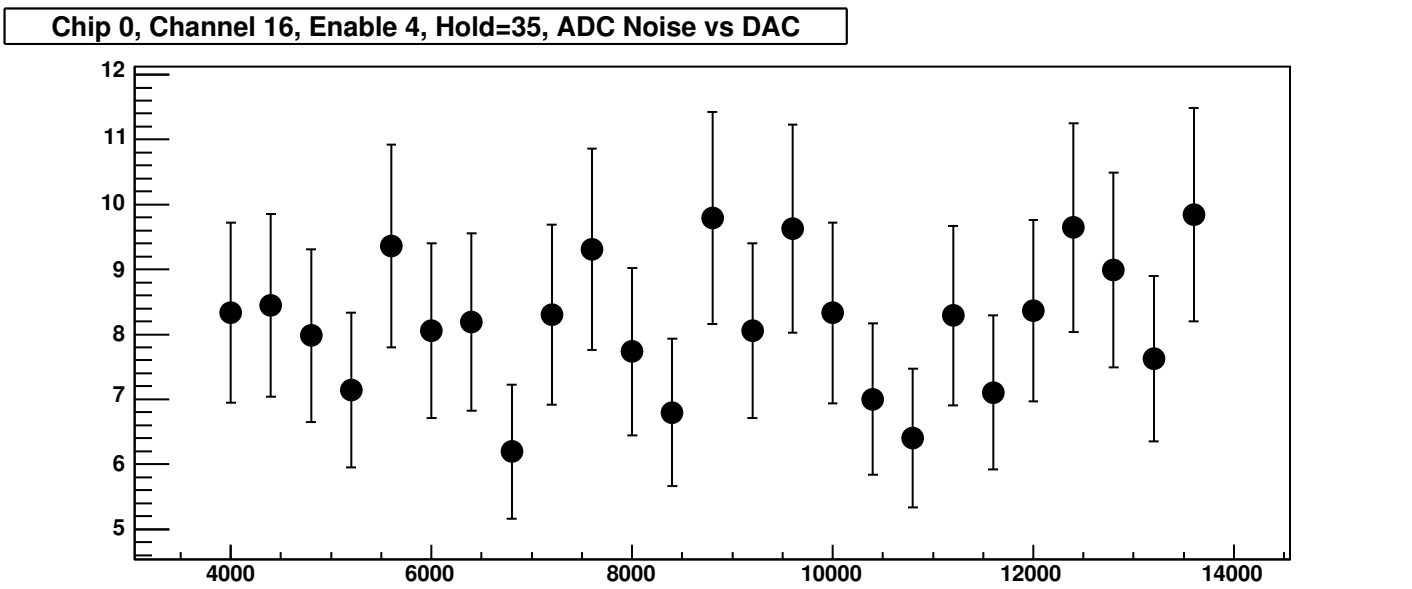

Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

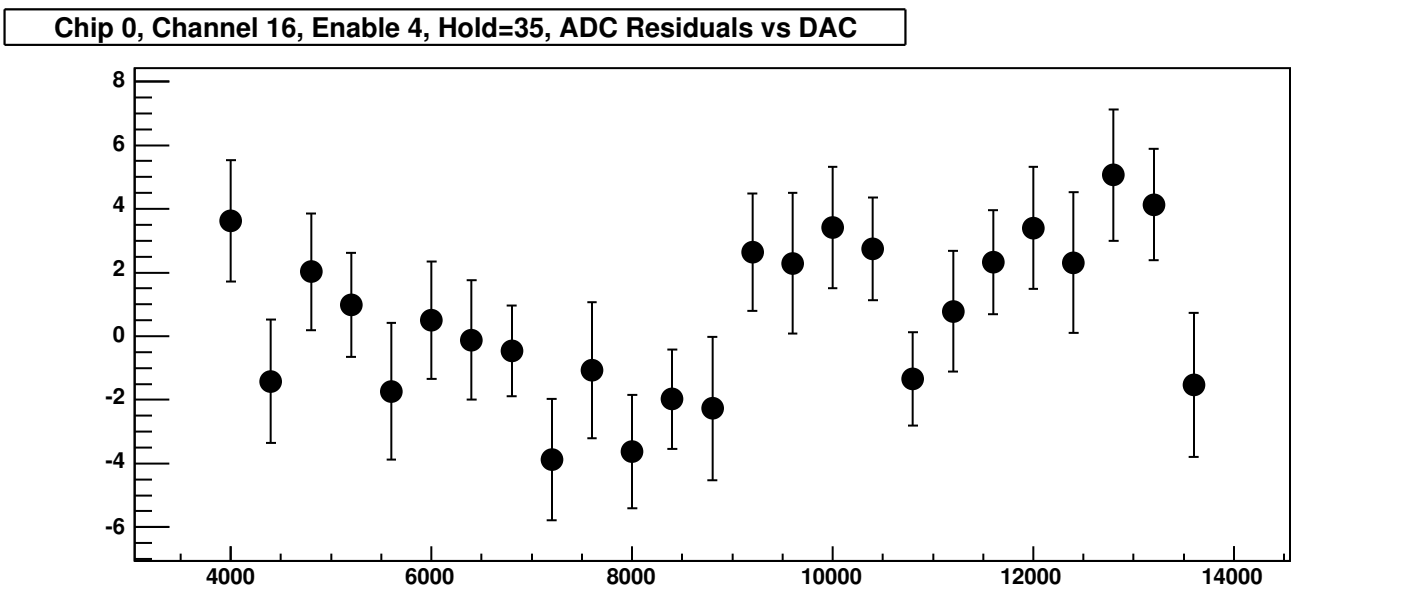

Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.04 / 16
p0 582.8 ± 1.564
p1 0.01517 ± 0.0001991
820
800
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

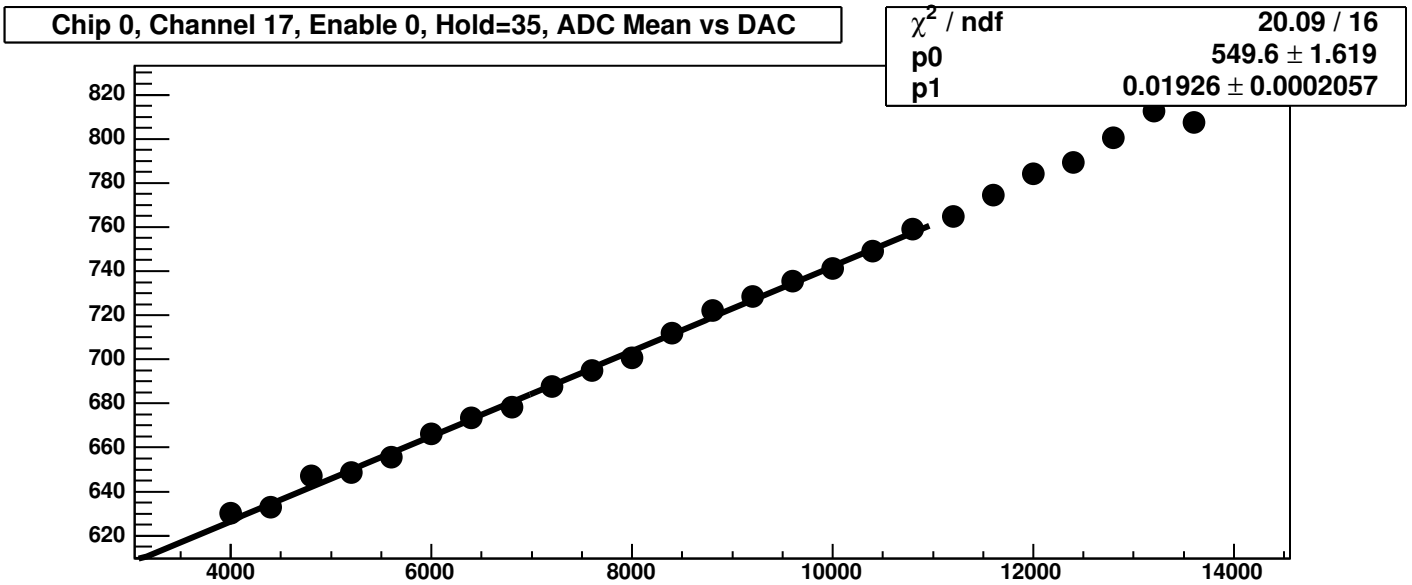

Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

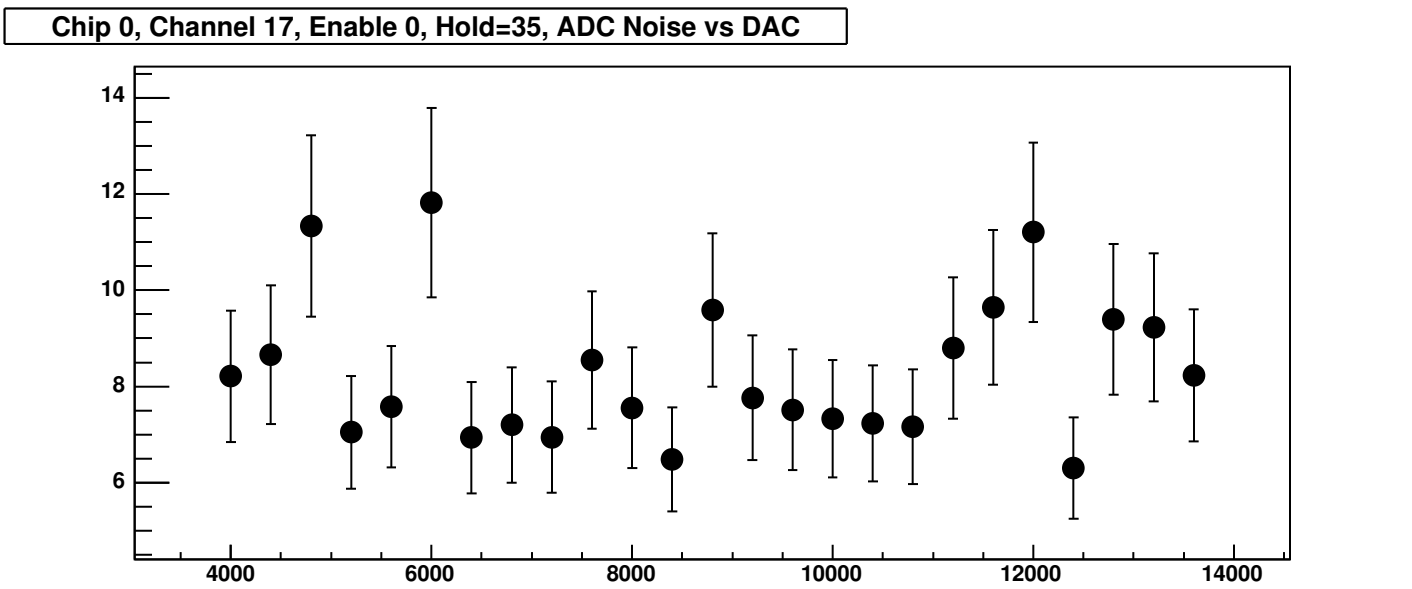

Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

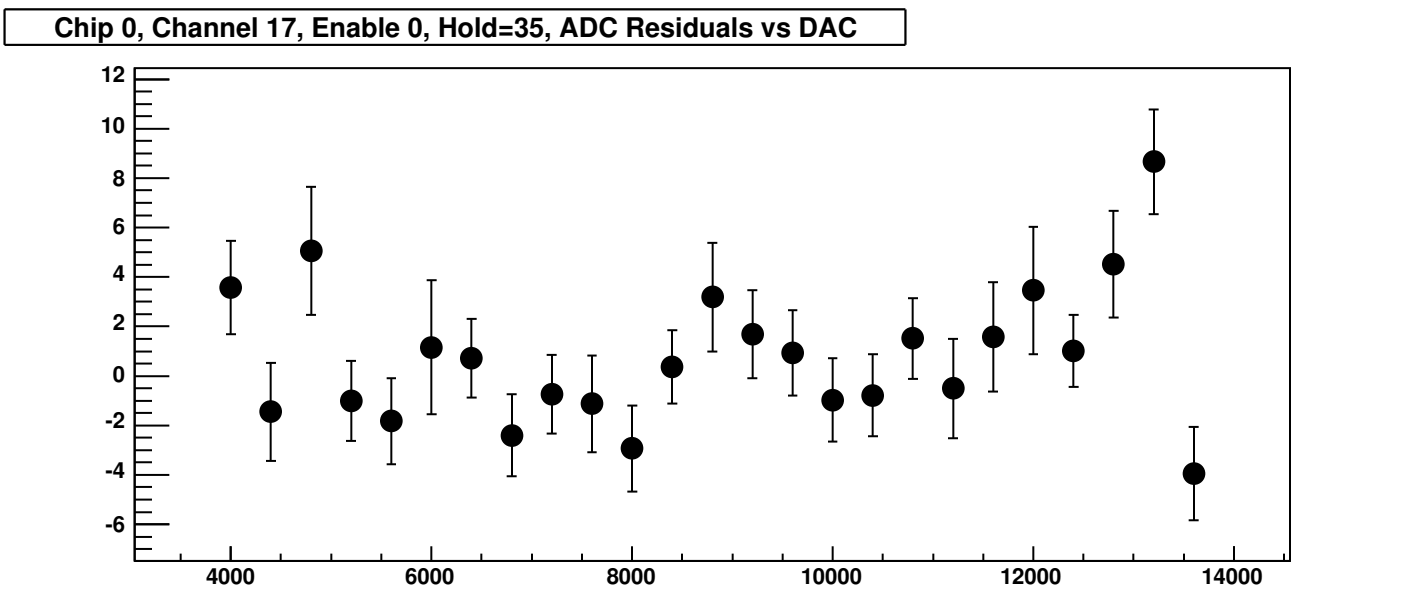

Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.77 / 16
p0 580.8 ± 1.571
p1 0.01443 ± 0.0002015
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

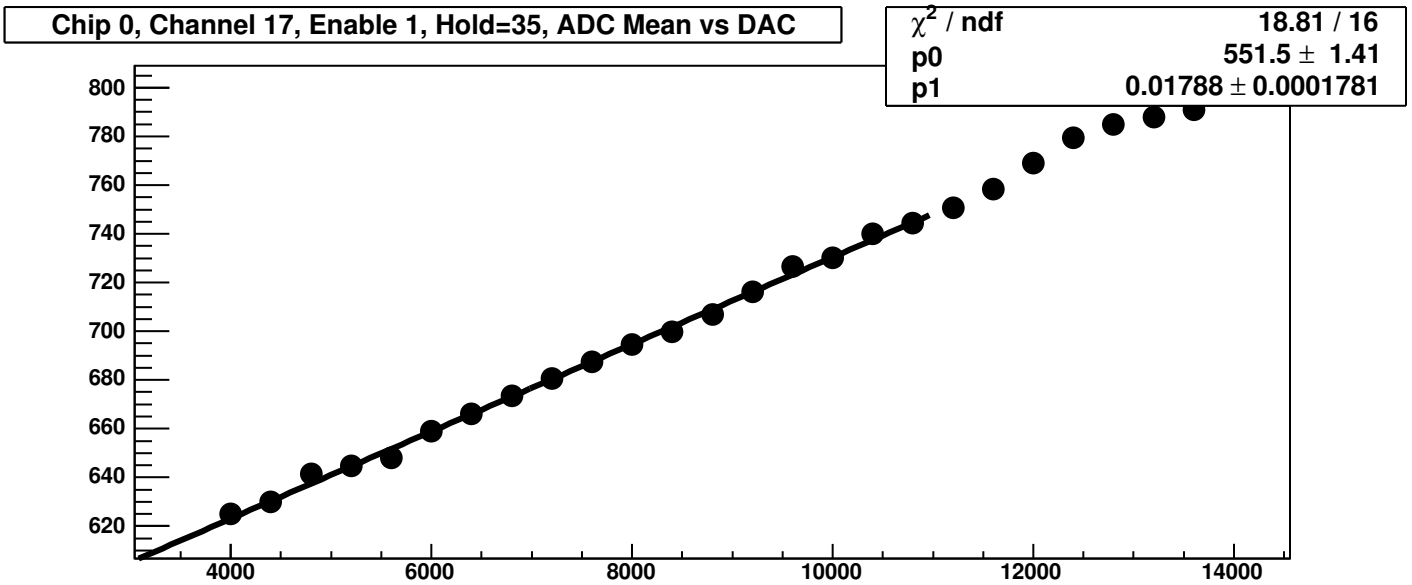

Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

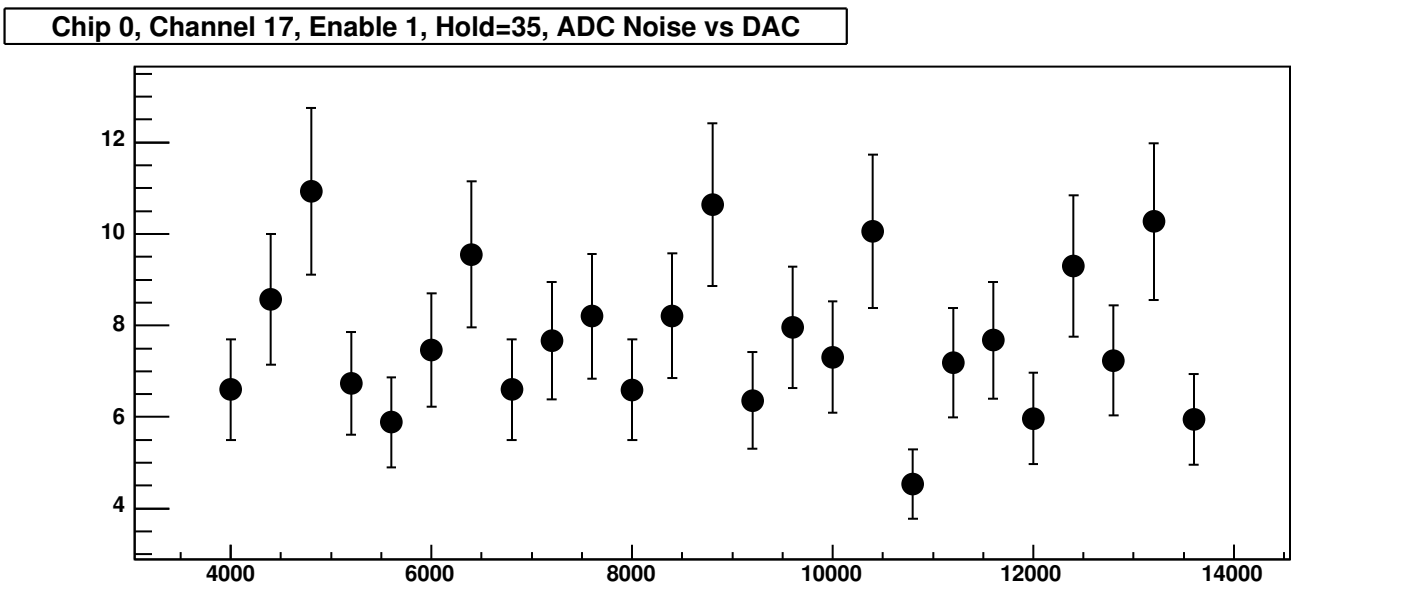

Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

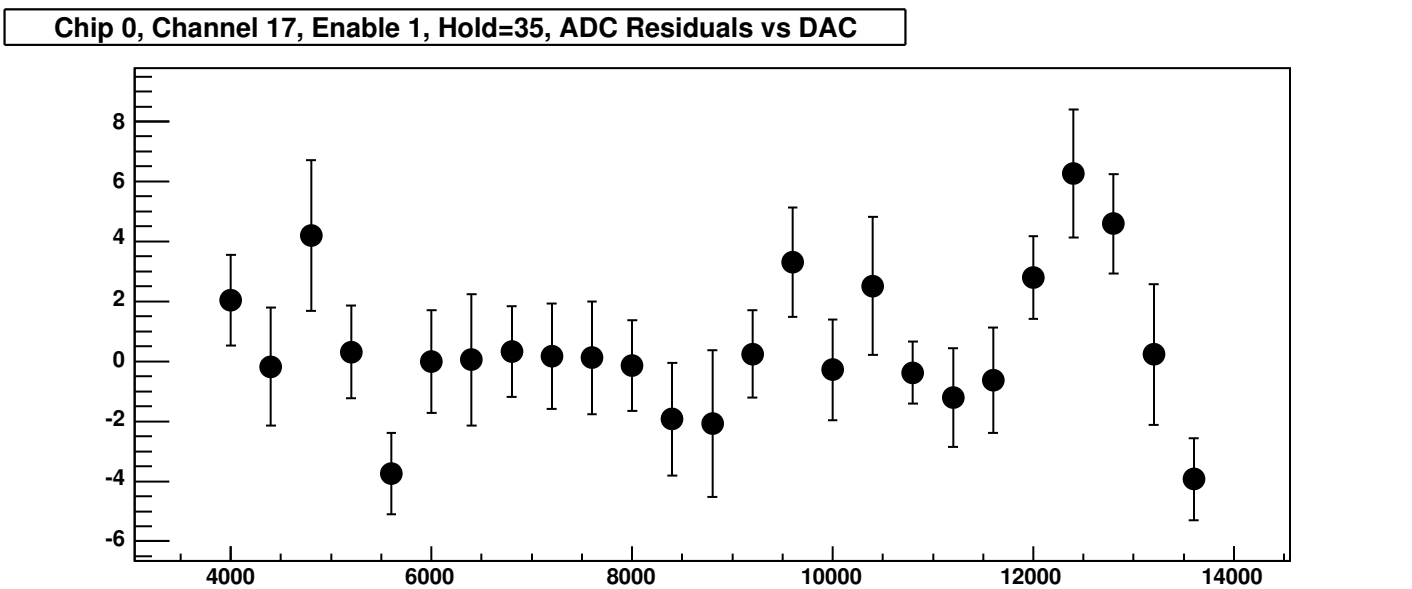

Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20.09 / 16
p0 549.6 ± 1.619
p1 0.01926 ± 0.0002057
820
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

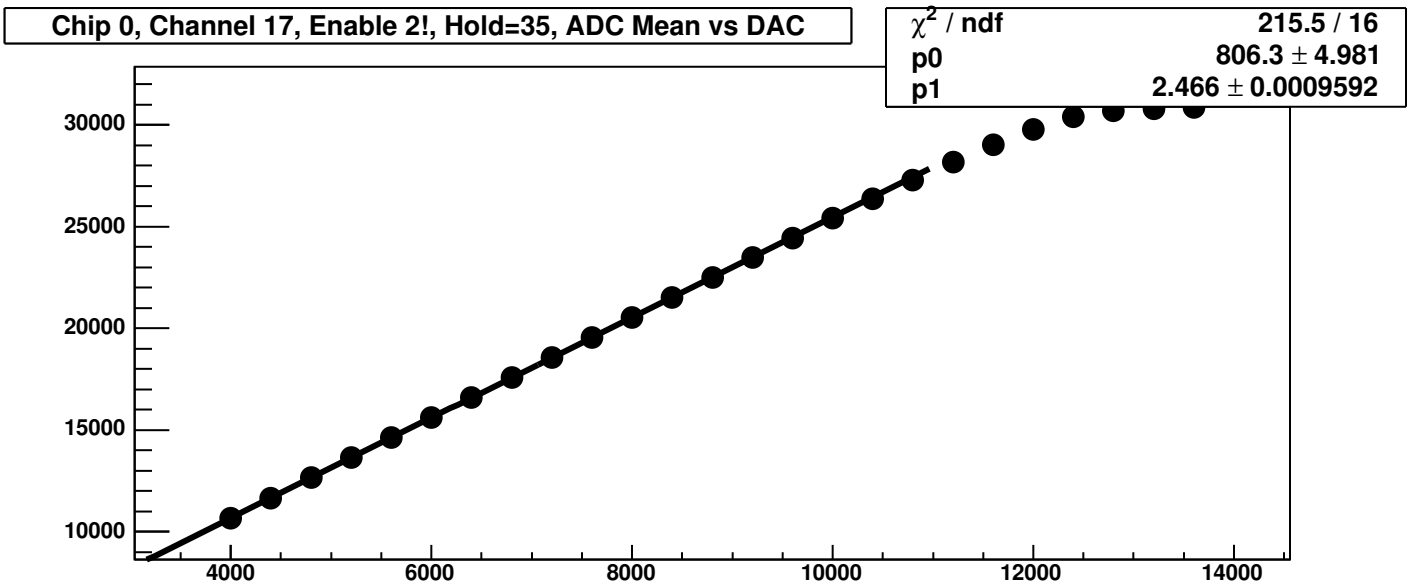

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4000
6000
8000
10000
12000
14000

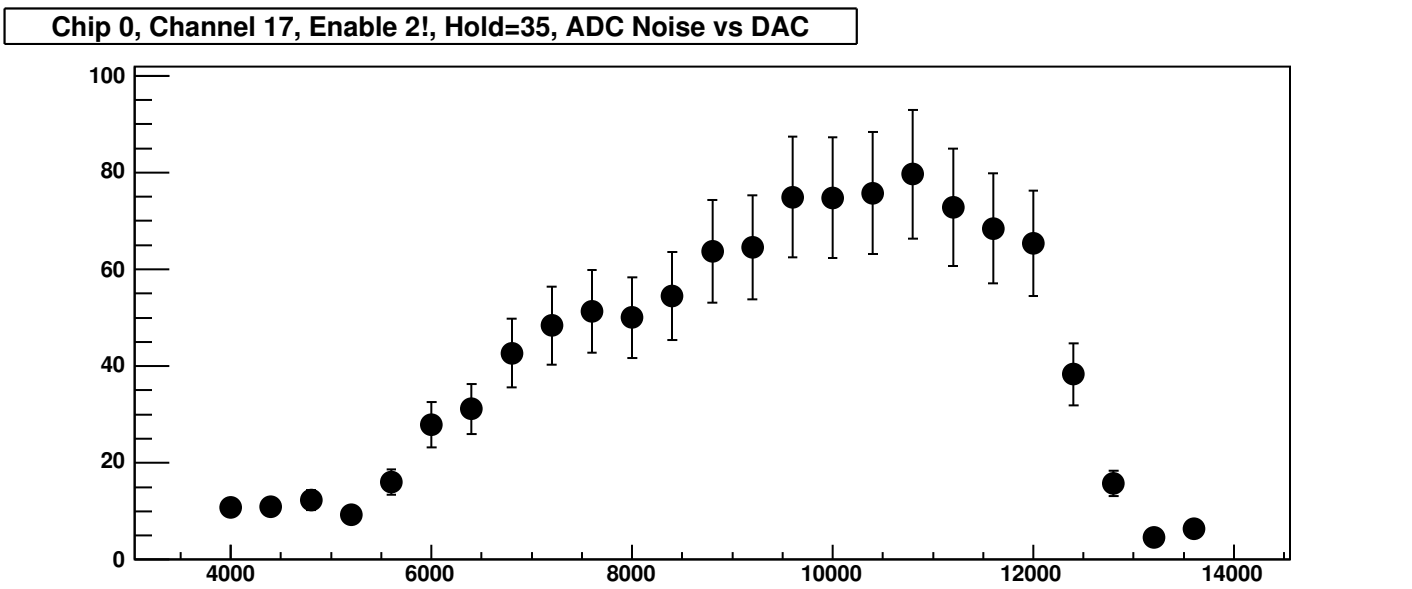

Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

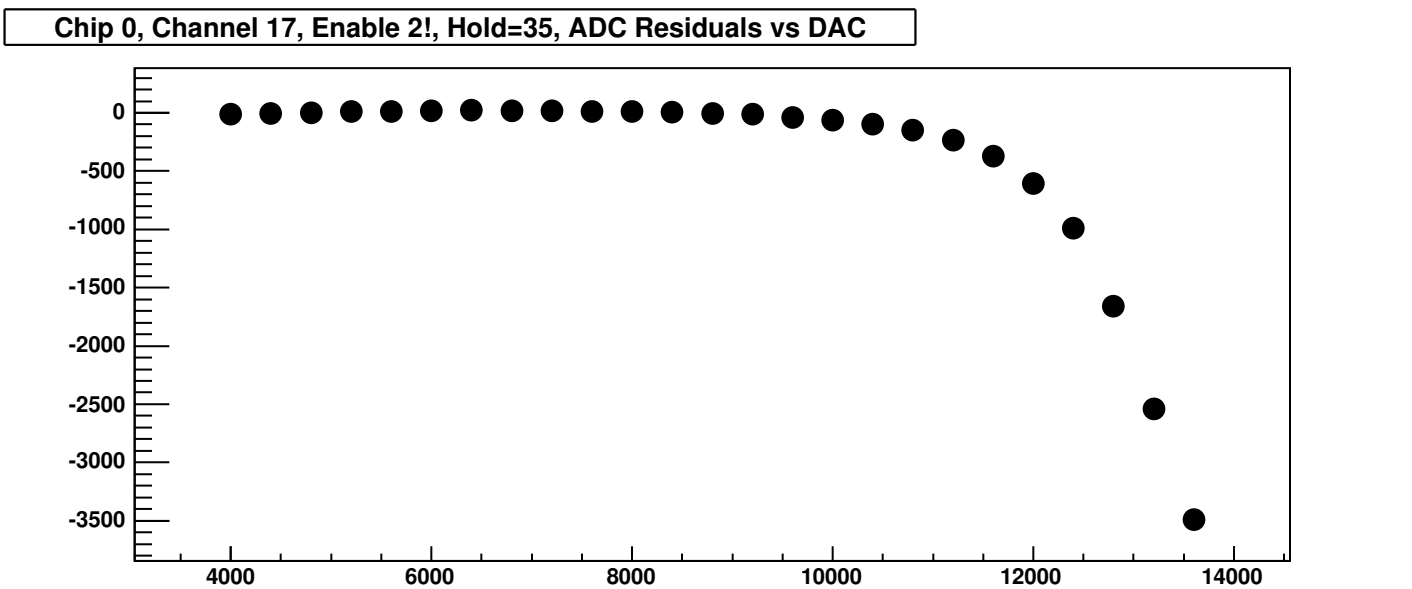

Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.81 / 16
p0 551.5 ± 1.41
p1 0.01788 ± 0.0001781
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

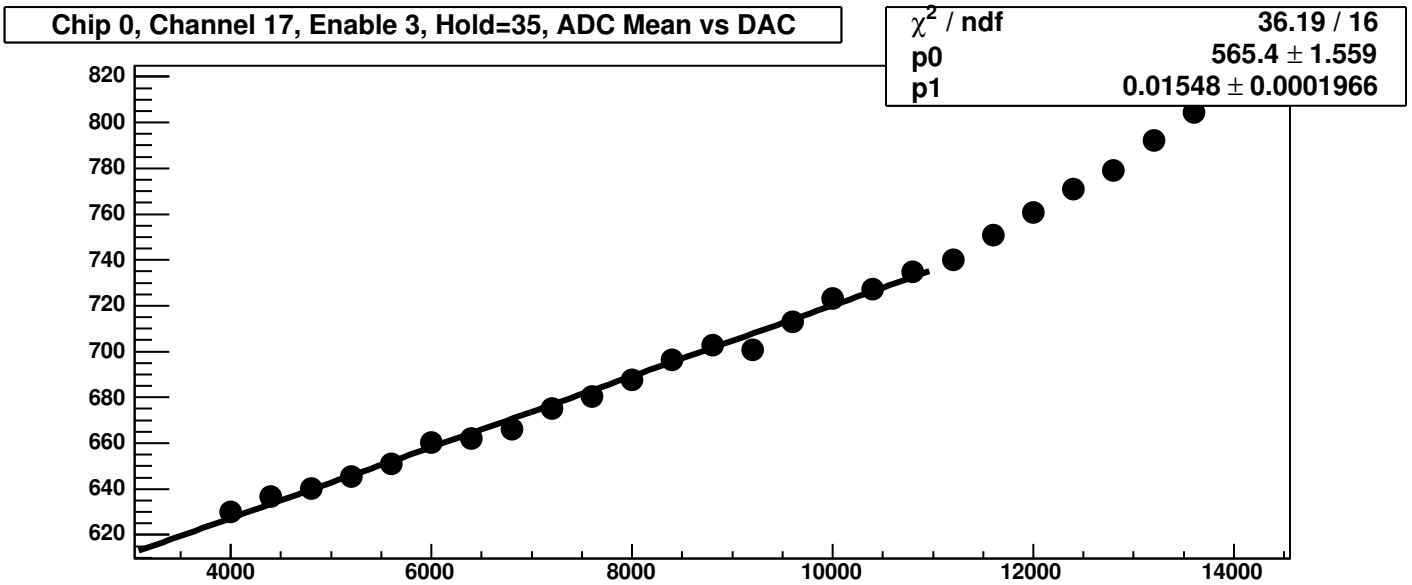

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

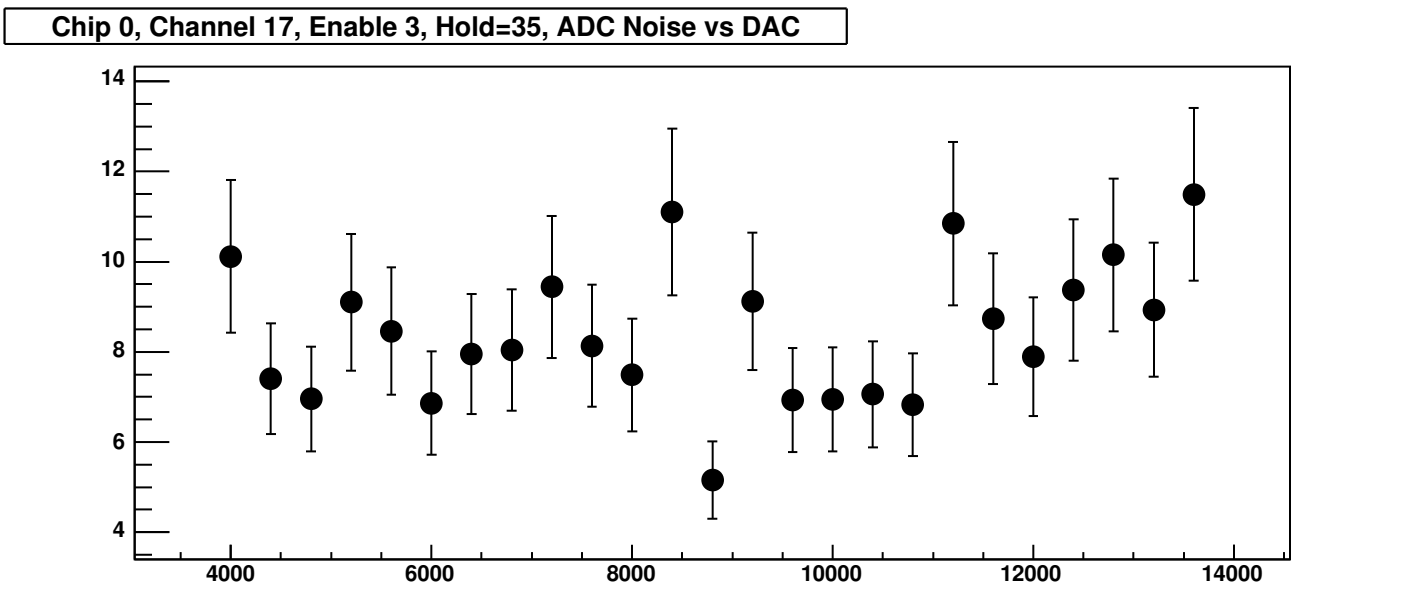

Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

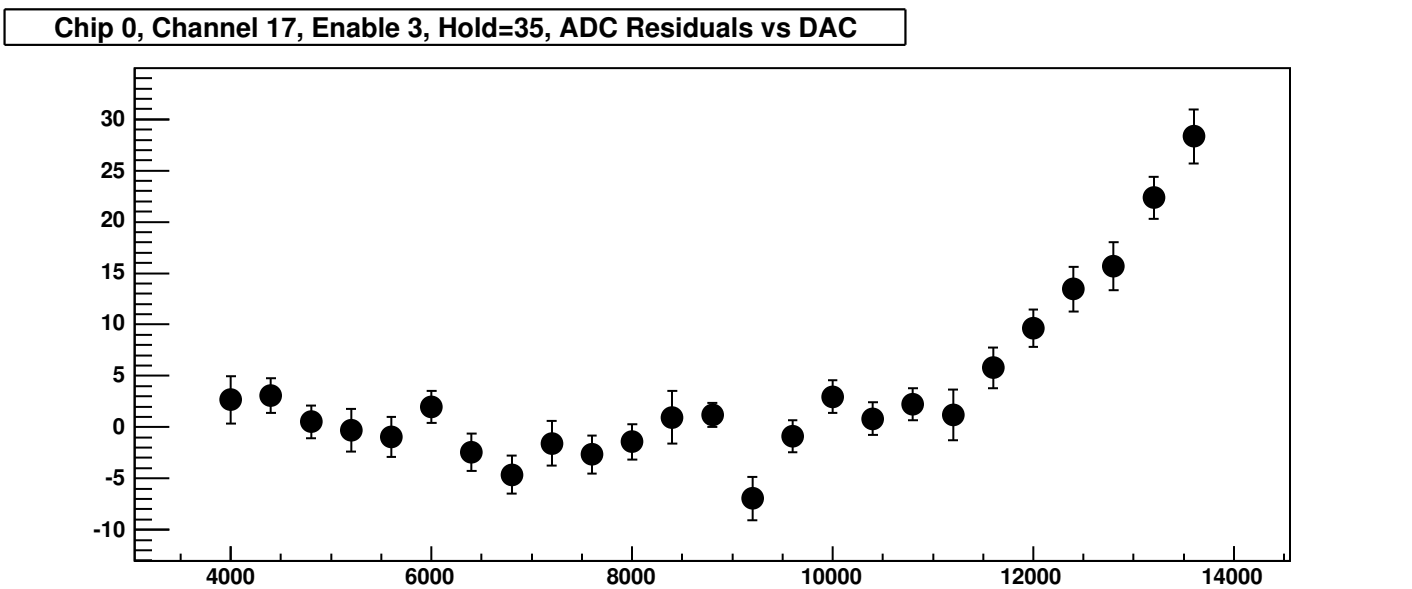

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 215.5 / 16
p0 806.3 ± 4.981
p1 2.466 ± 0.0009592
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

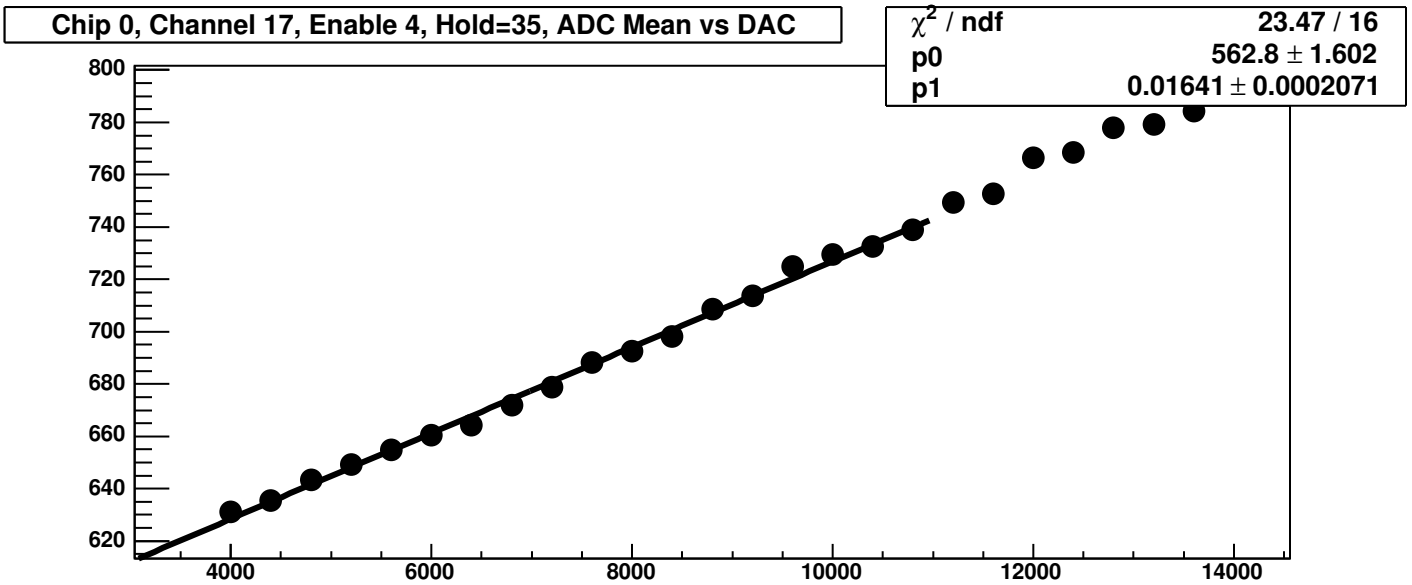

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

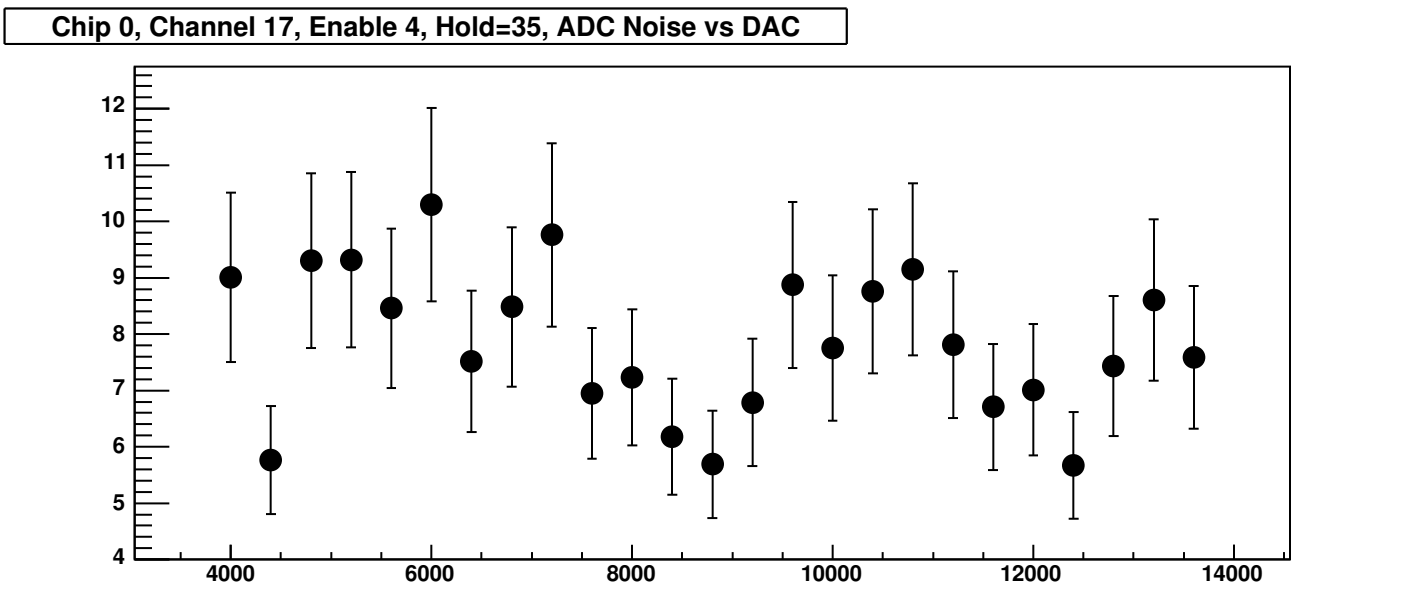

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

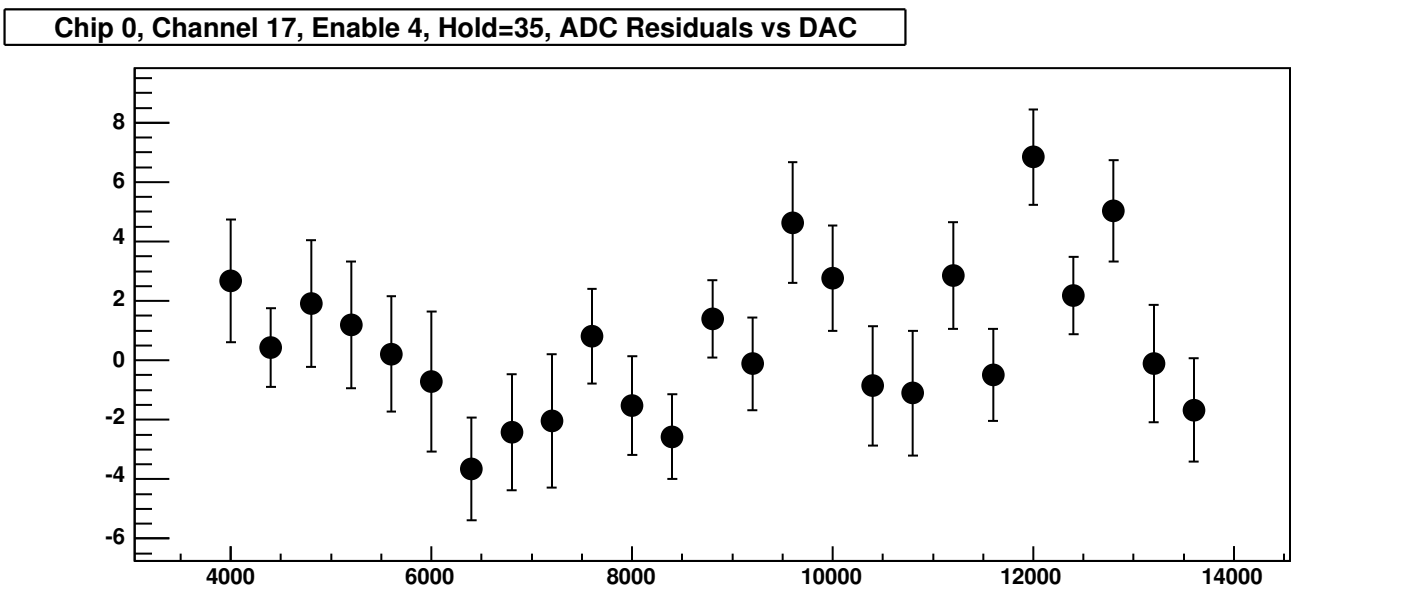

Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.19 / 16
p0 565.4 ± 1.559
p1 0.01548 ± 0.0001966
820
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

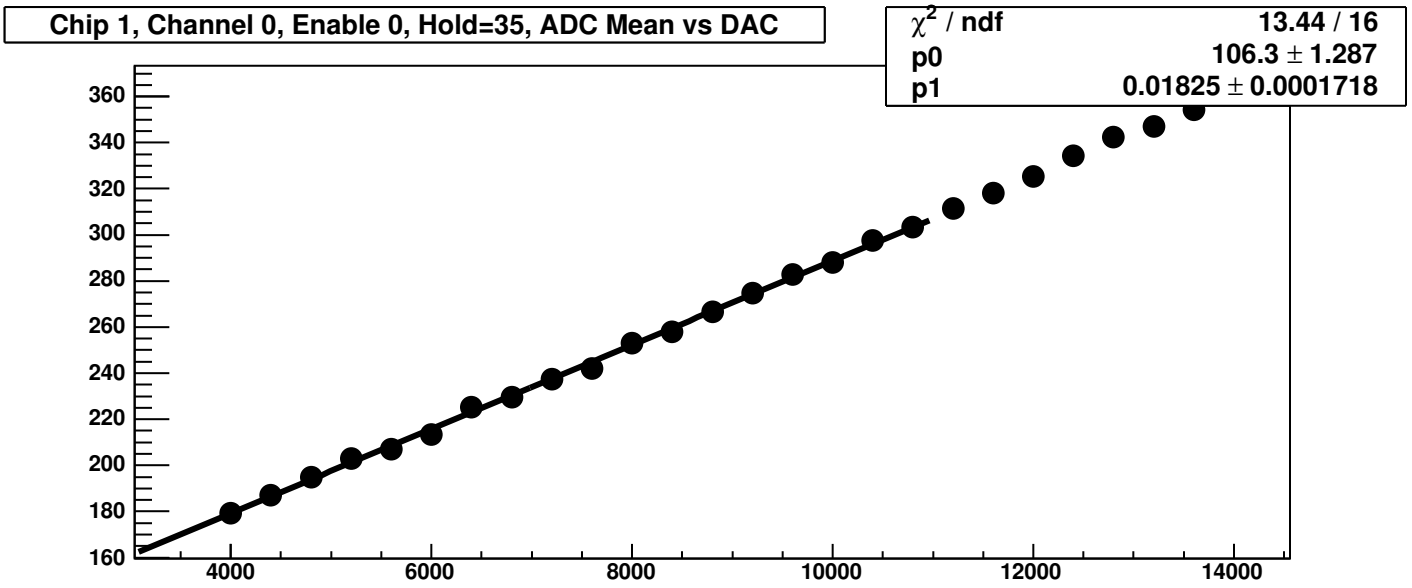

Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

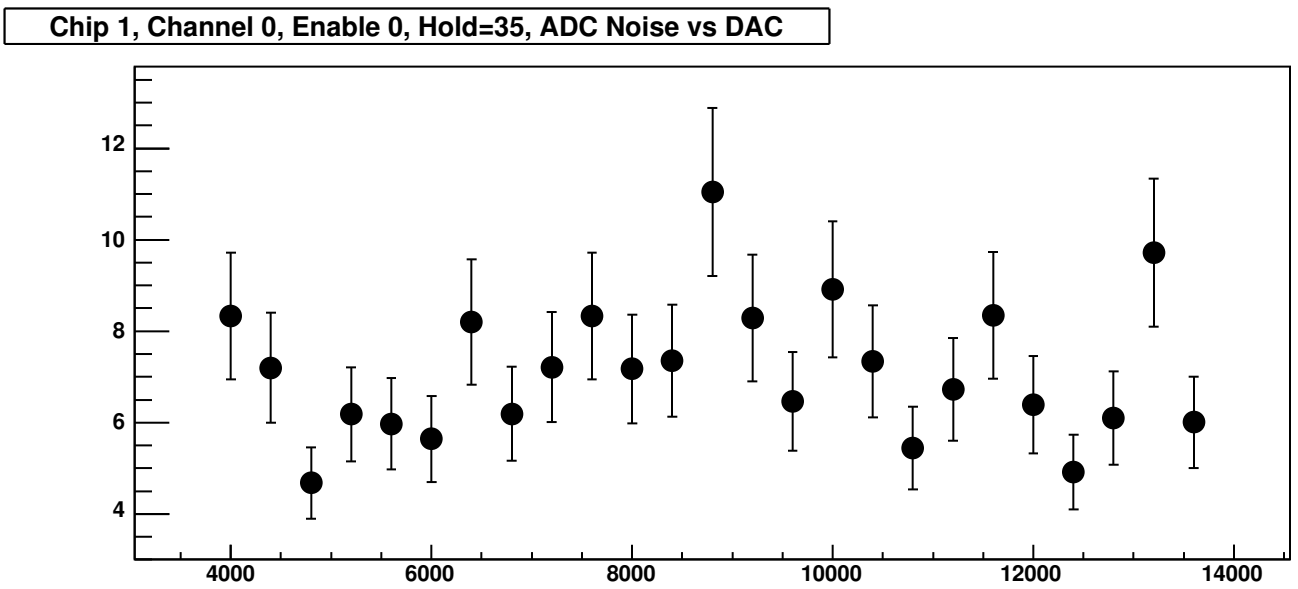

Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

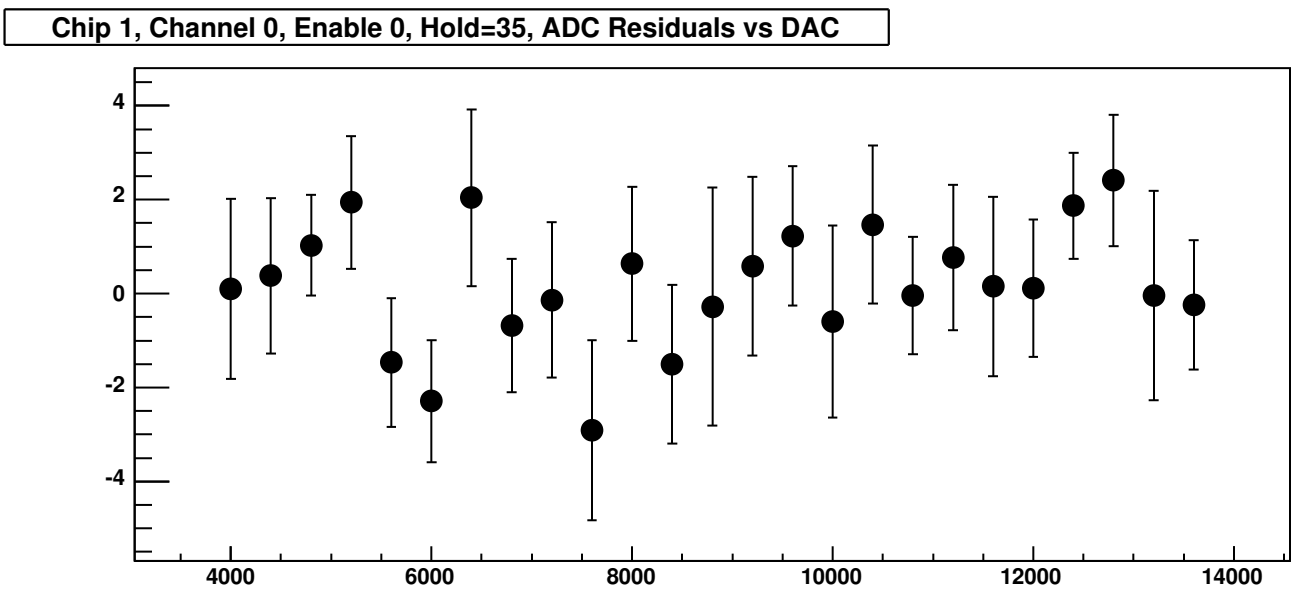

Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.47 / 16
p0 562.8 ± 1.602
p1 0.01641 ± 0.0002071
800
780
760
740
720
700
680
660
640
620
4000
6000
8000
10000
12000
14000

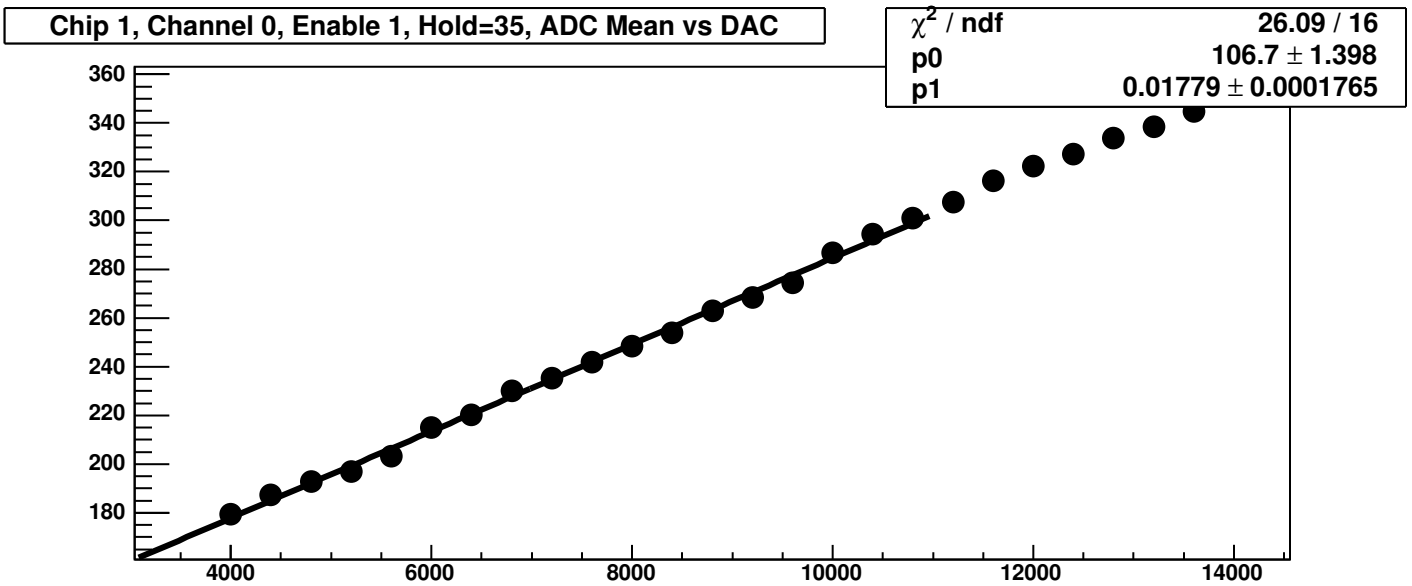

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

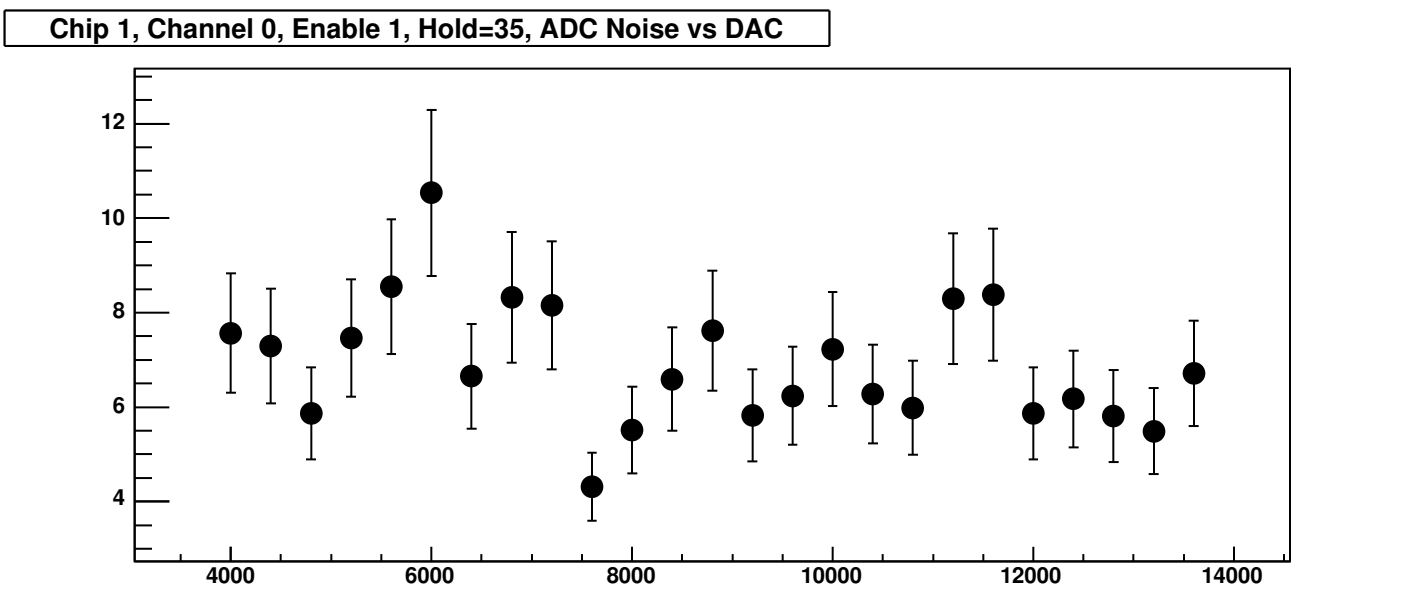

Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

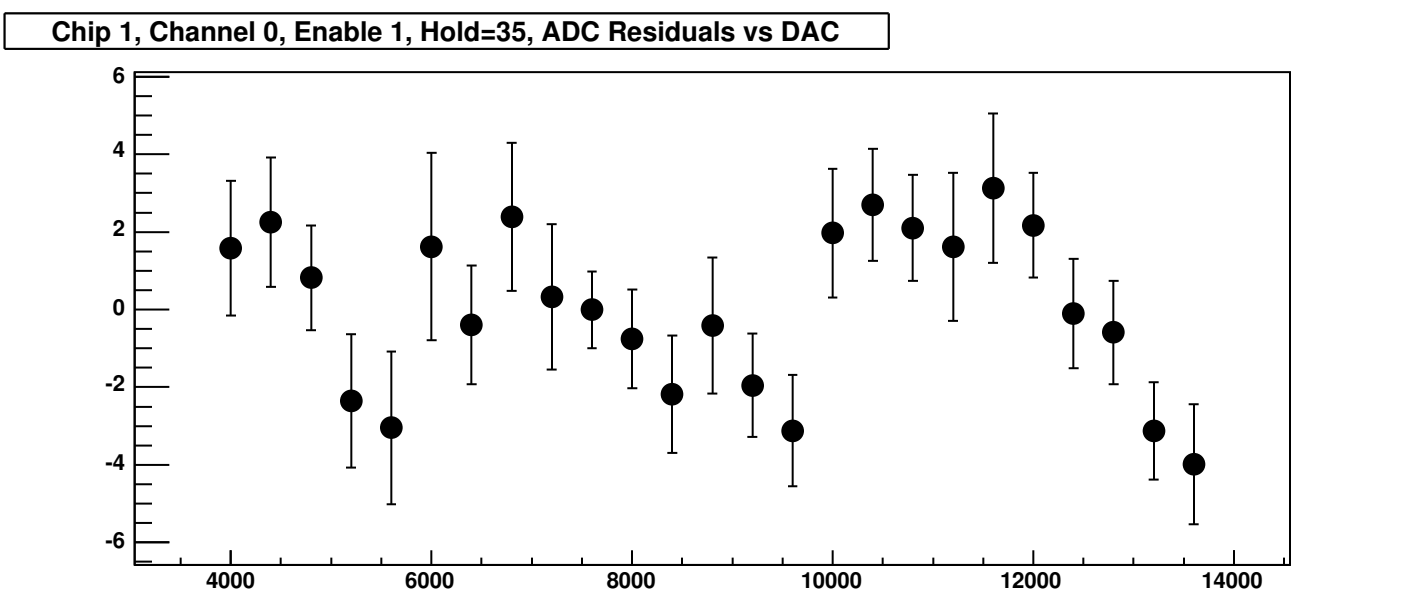

Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 13.44 / 16
p0 106.3 ± 1.287
p1 0.01825 ± 0.0001718
360
340
320
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

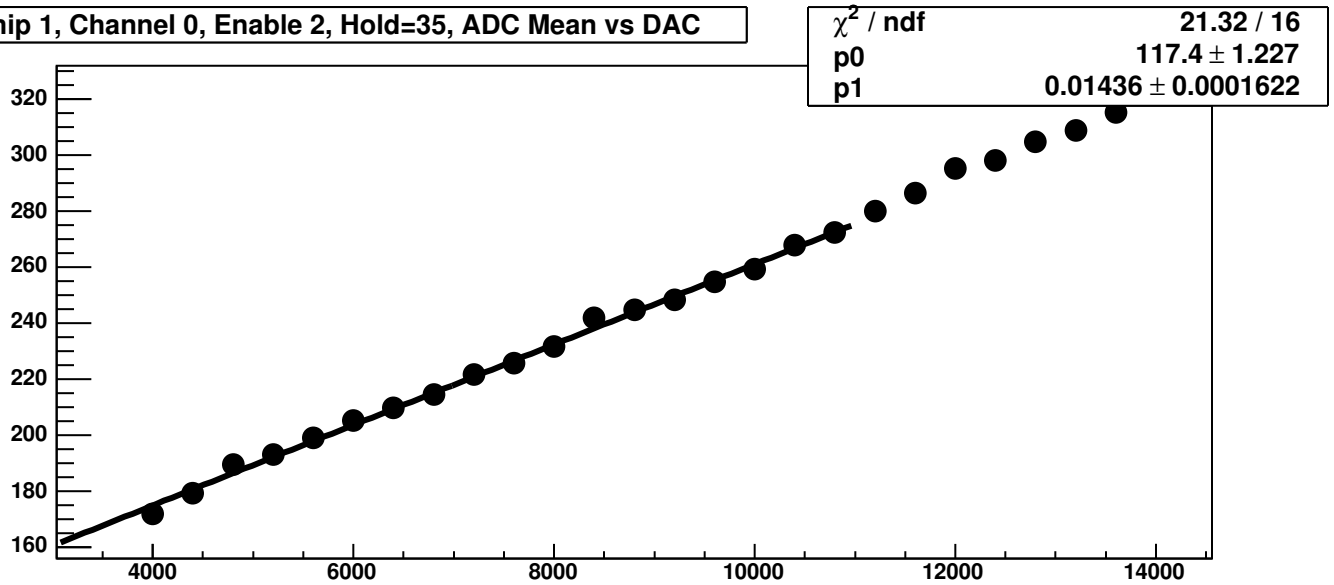

Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.09 / 16
p0 106.7 ± 1.398
p1 0.01779 ± 0.0001765
360
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

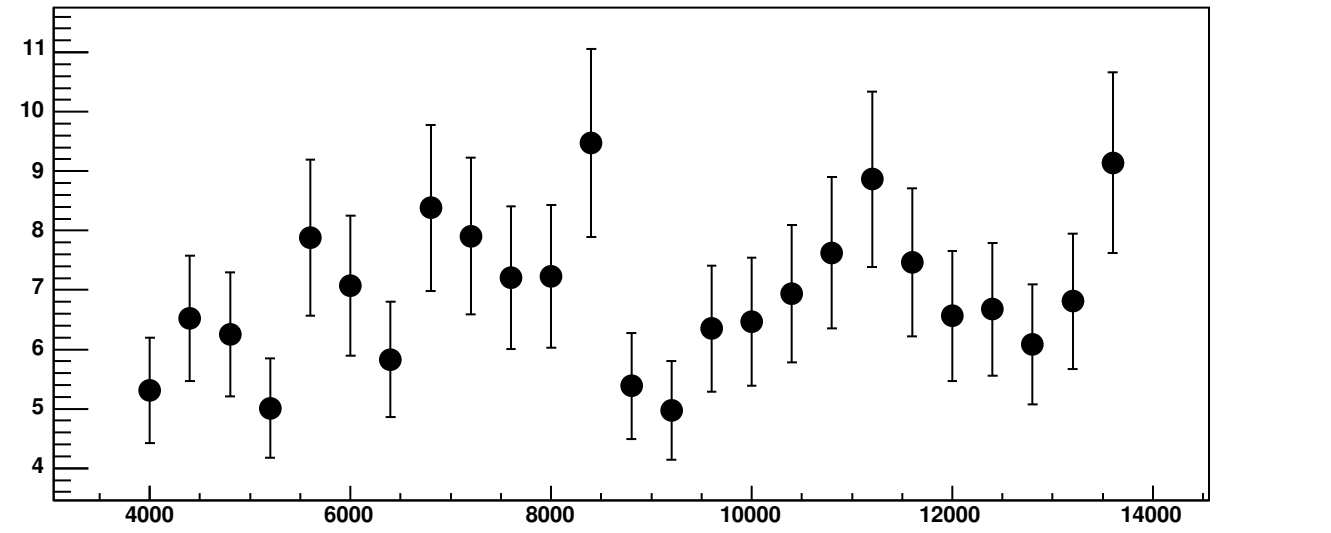

Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

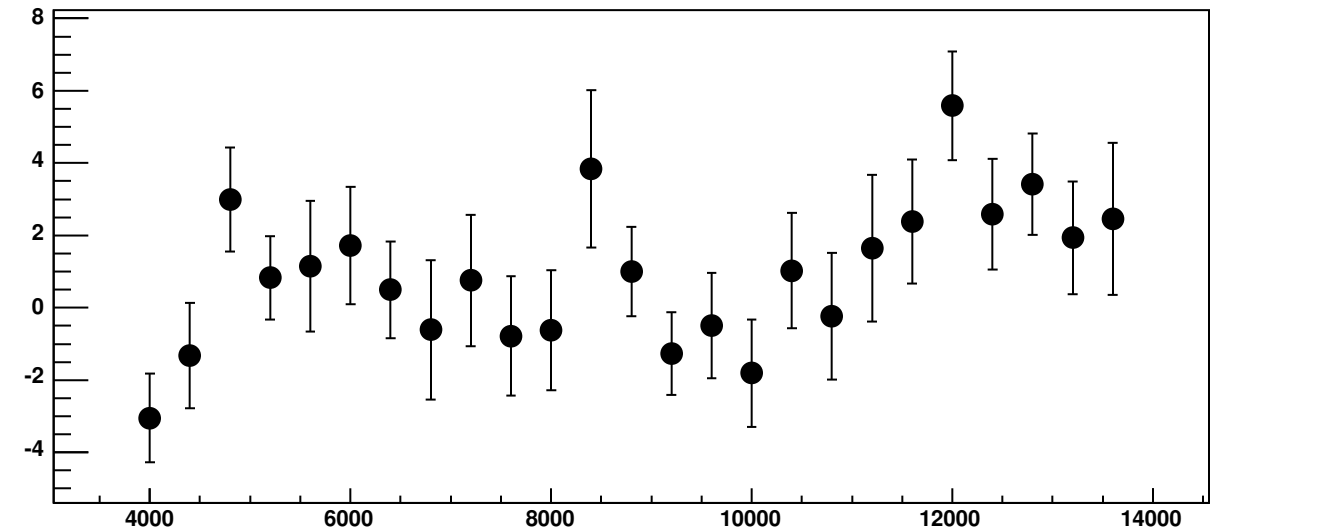
Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

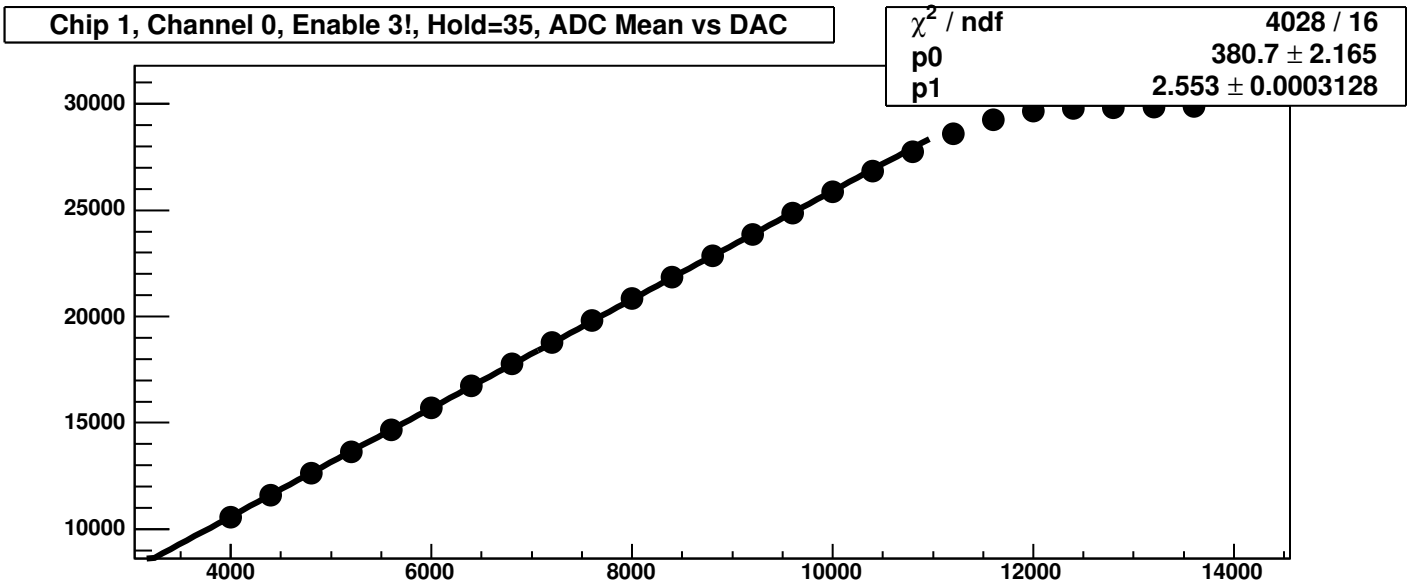


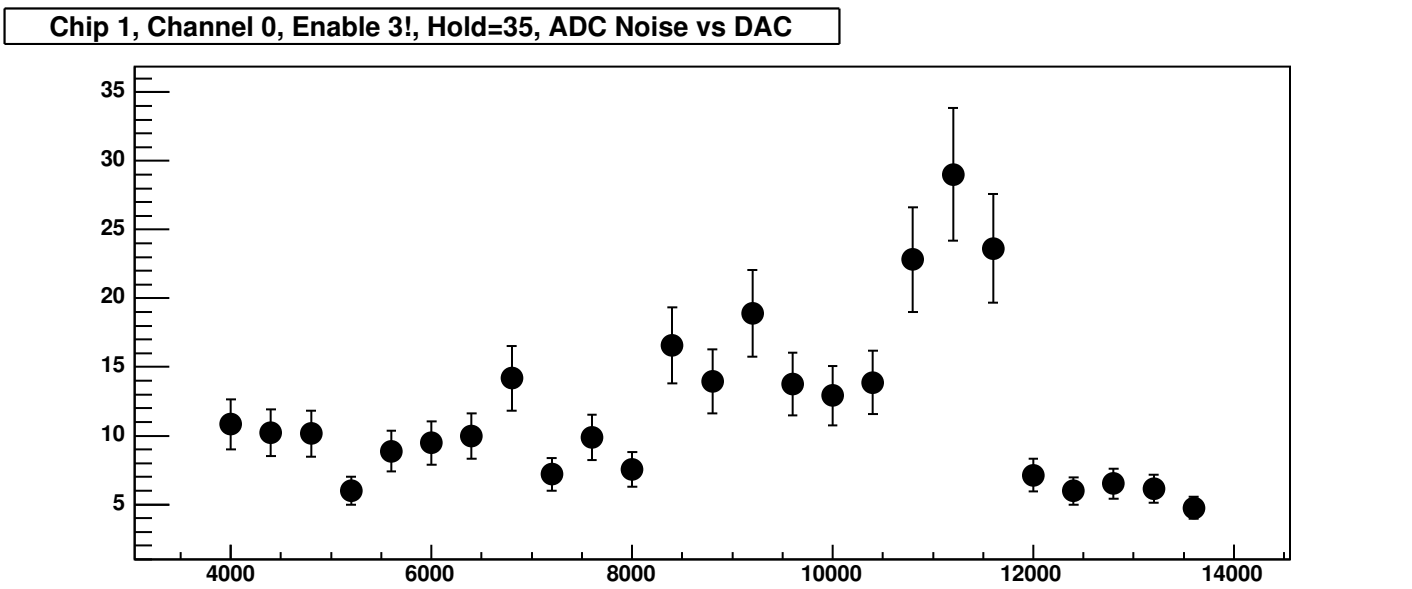
Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

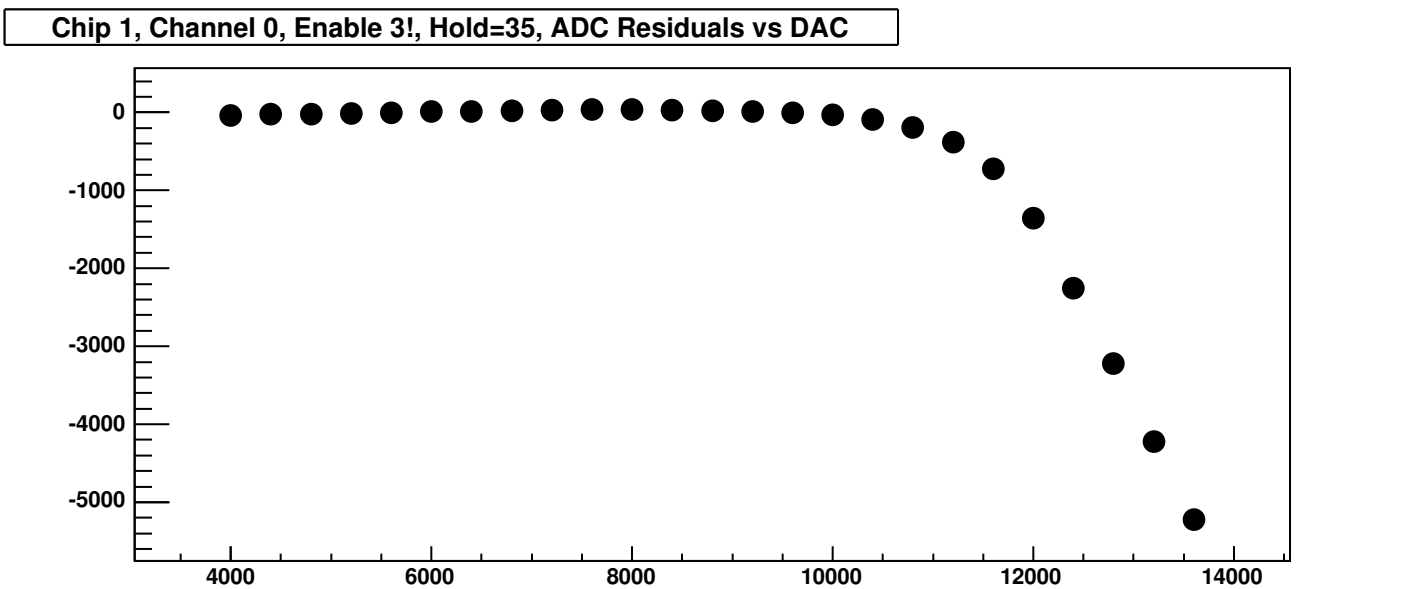


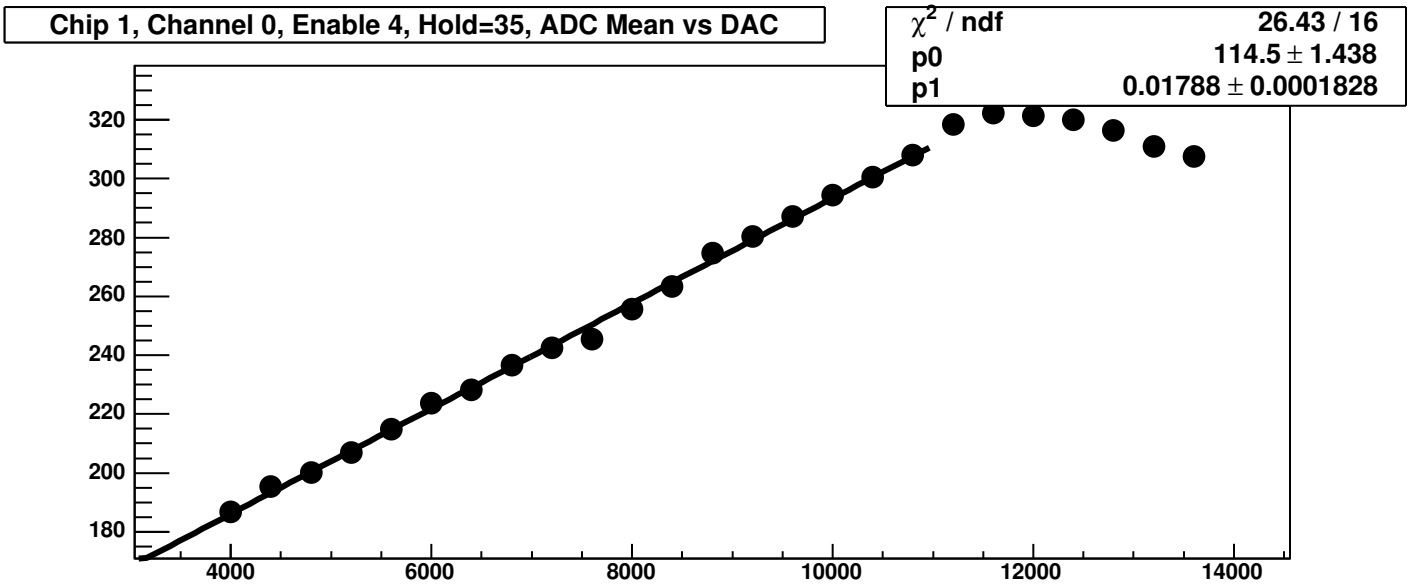
Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

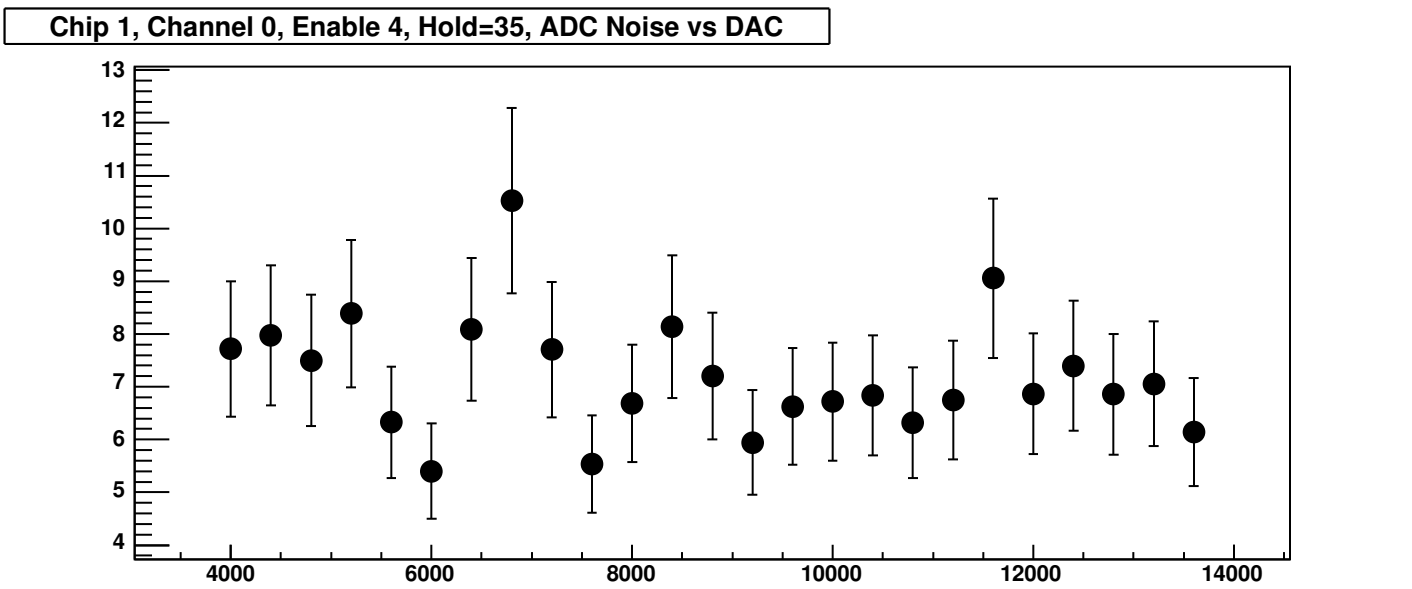


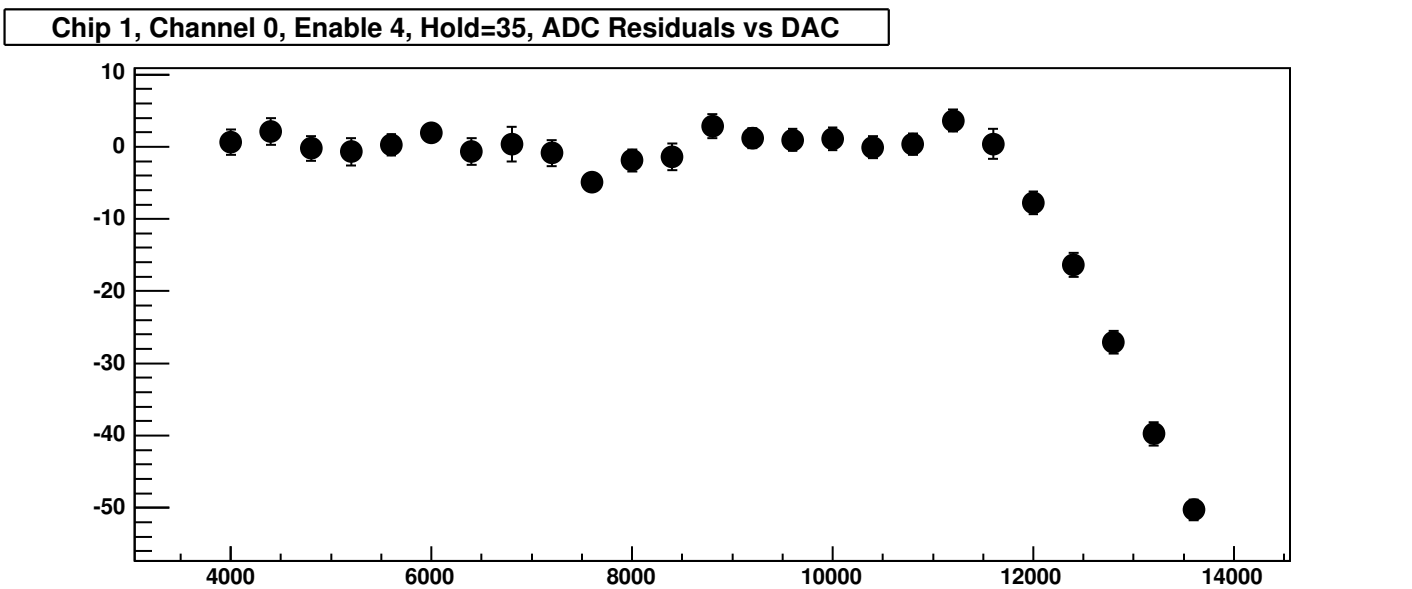

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4028 / 16
p0 380.7 ± 2.165
p1 2.553 ± 0.0003128
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

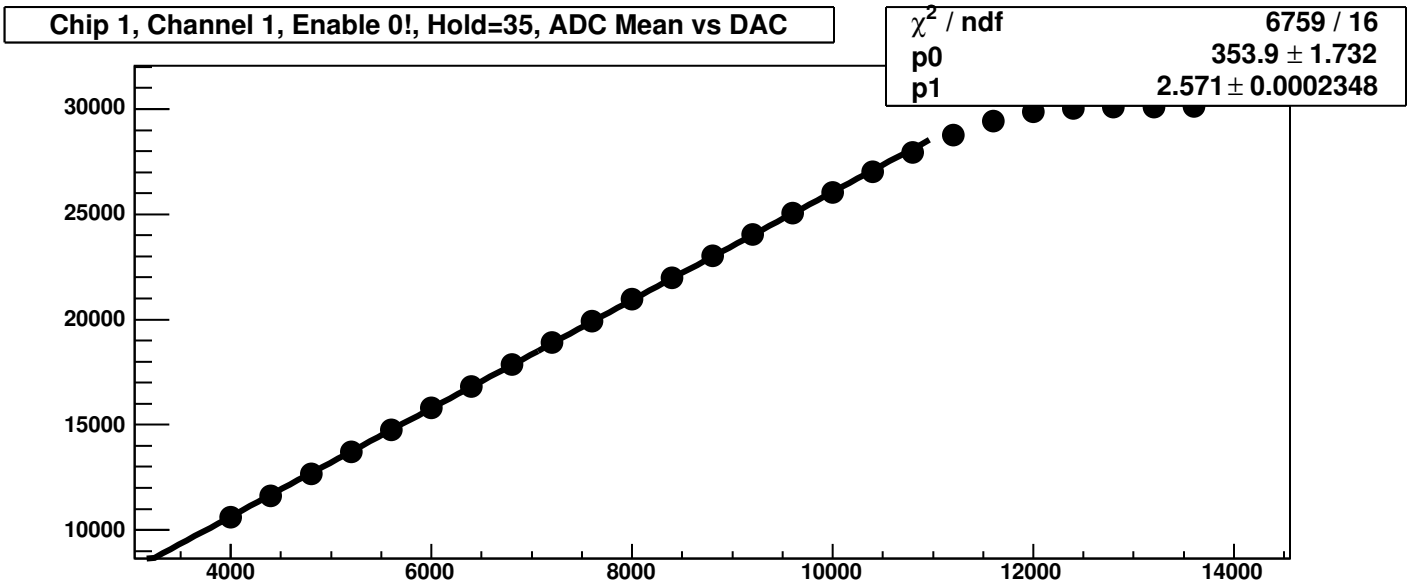

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

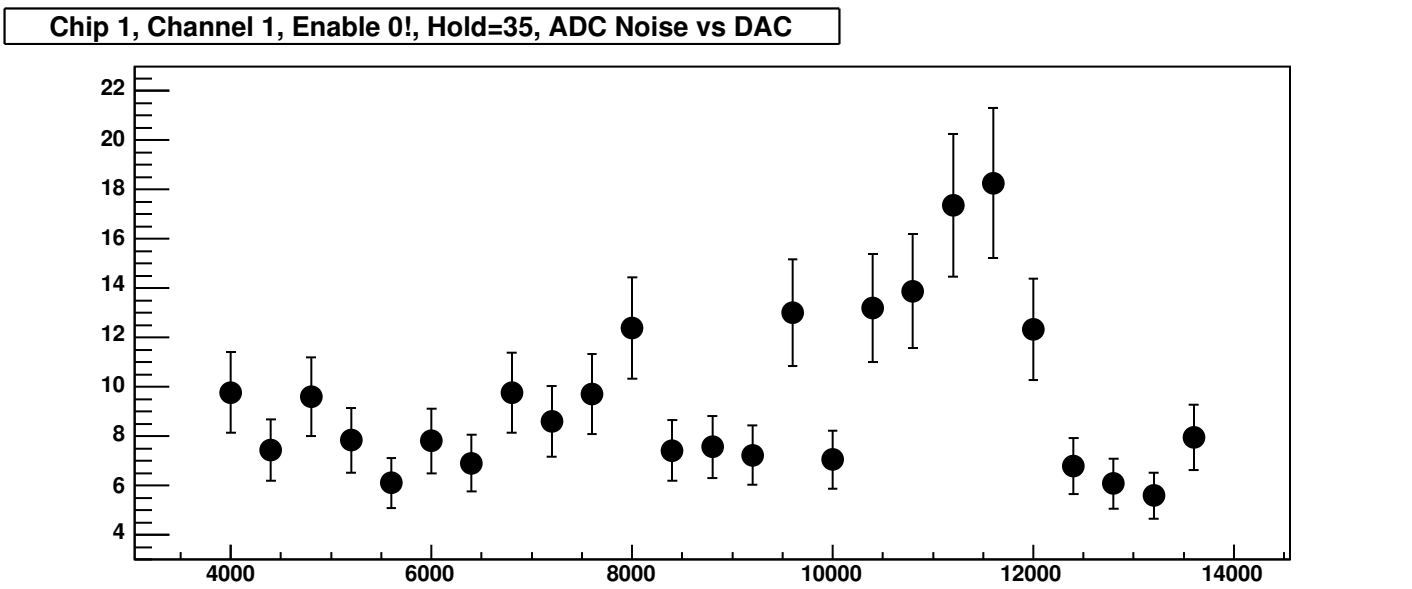

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

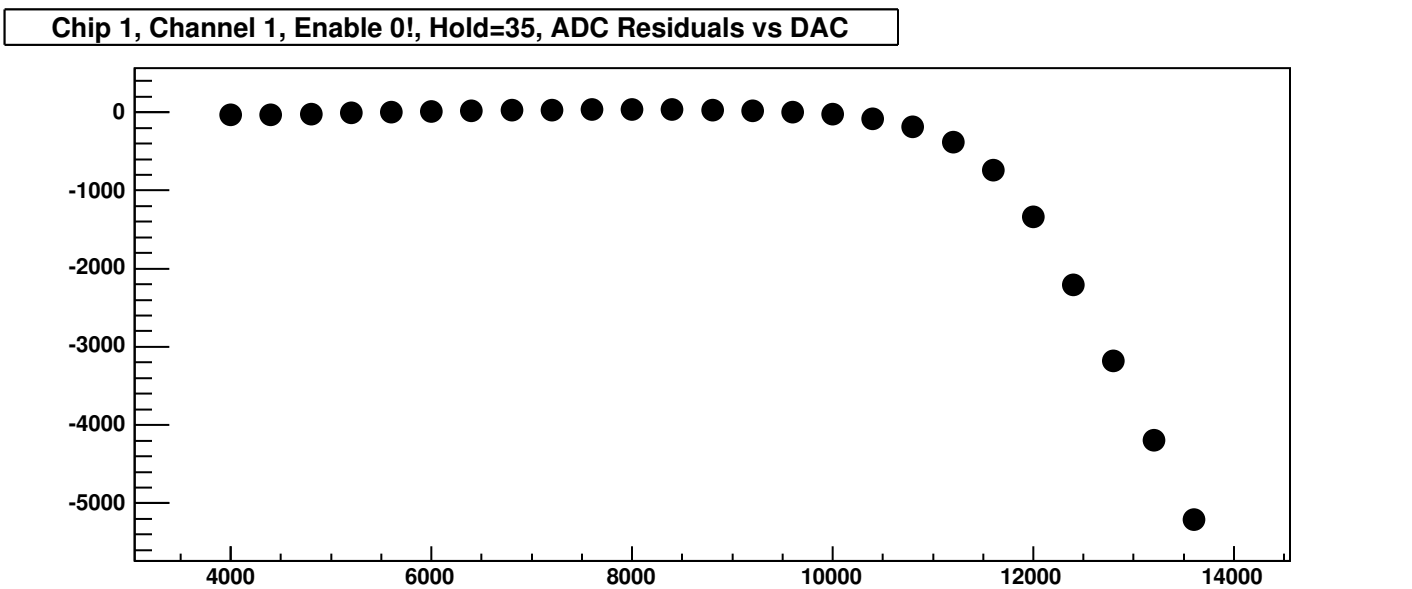

Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.43 / 16
p0 114.5 ± 1.438
p1 0.01788 ± 0.0001828
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

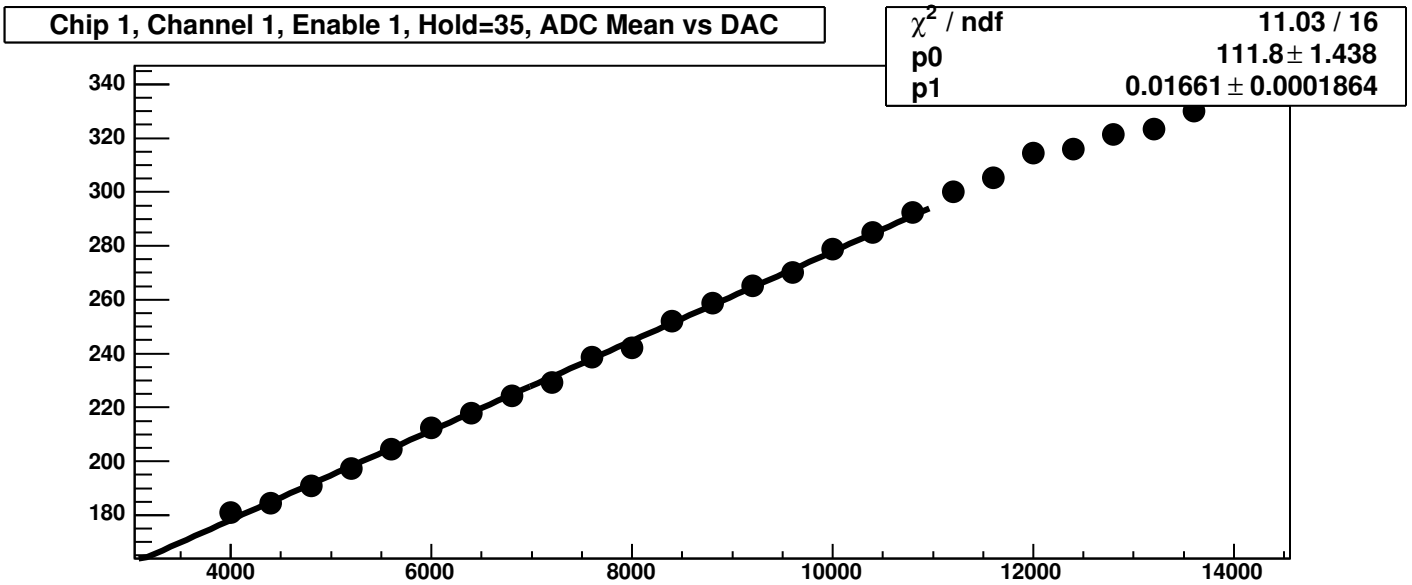

Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

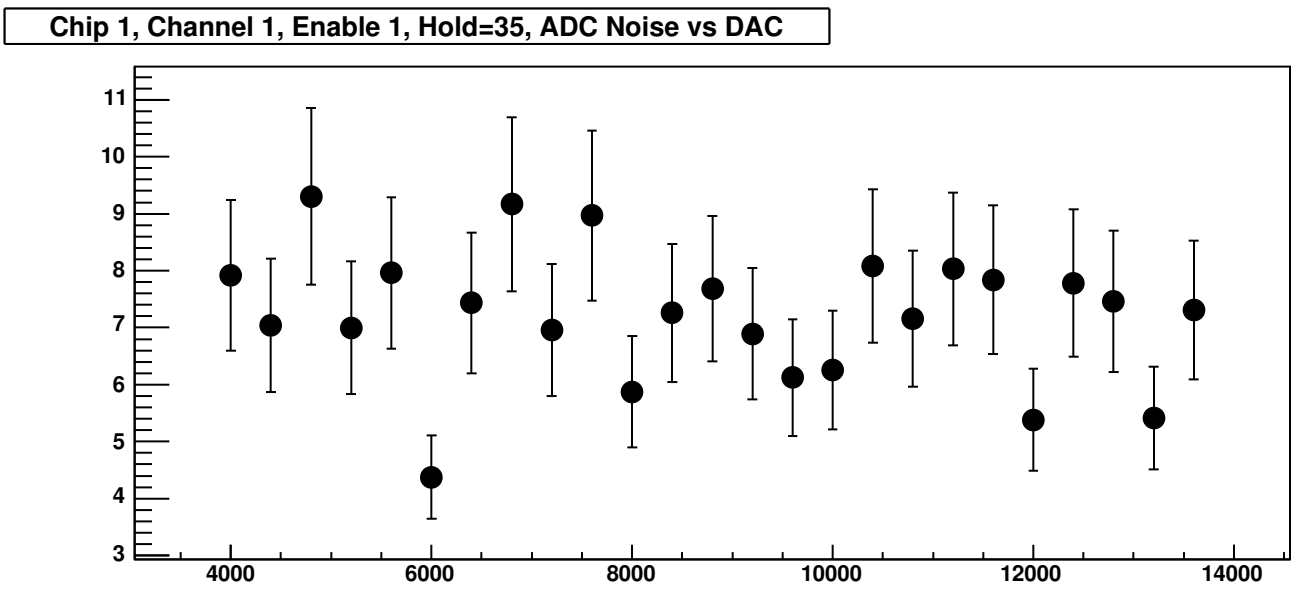

Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

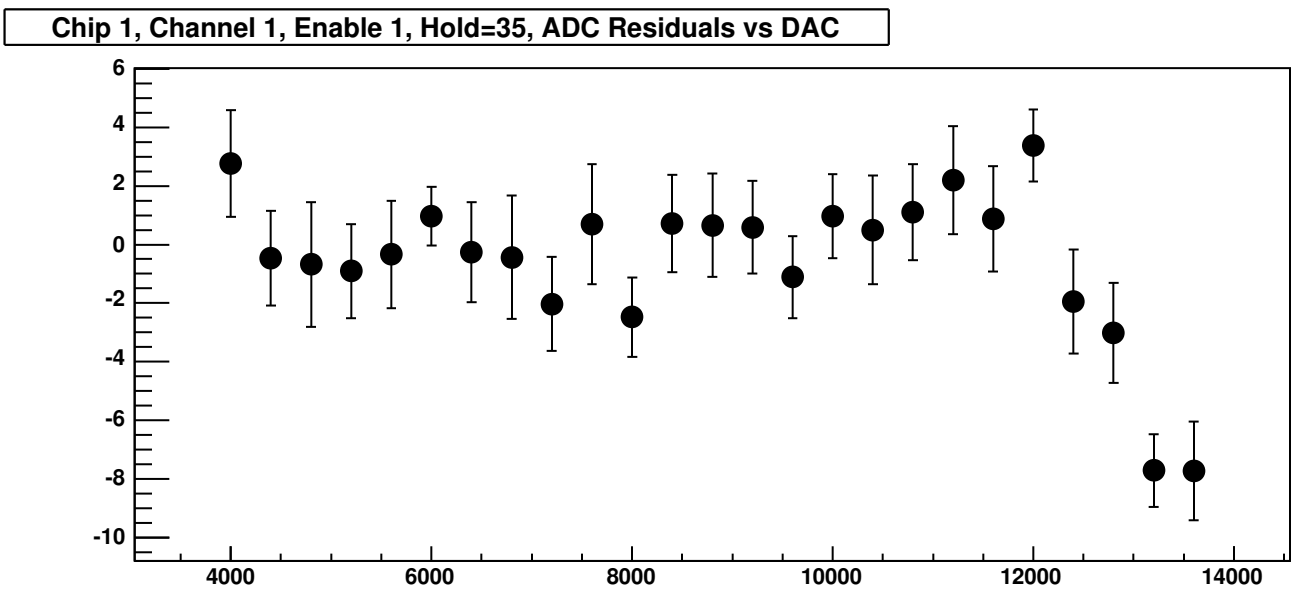

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6759 / 16
p0 353.9 ± 1.732
p1 2.571 ± 0.0002348
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

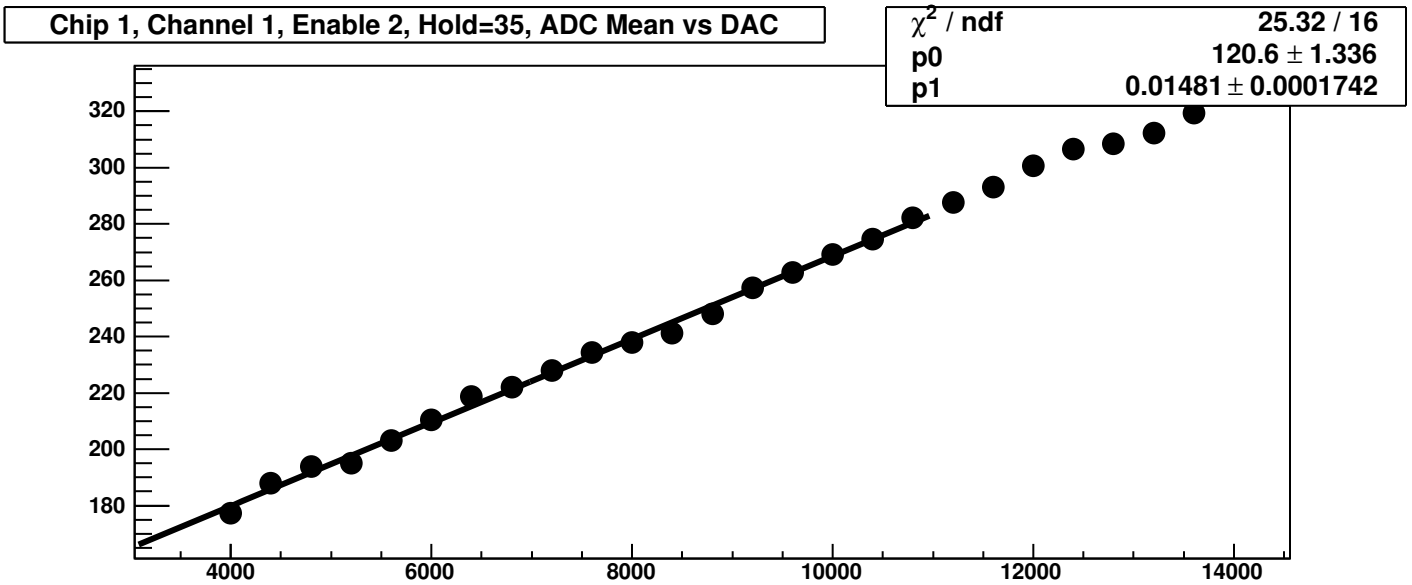

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

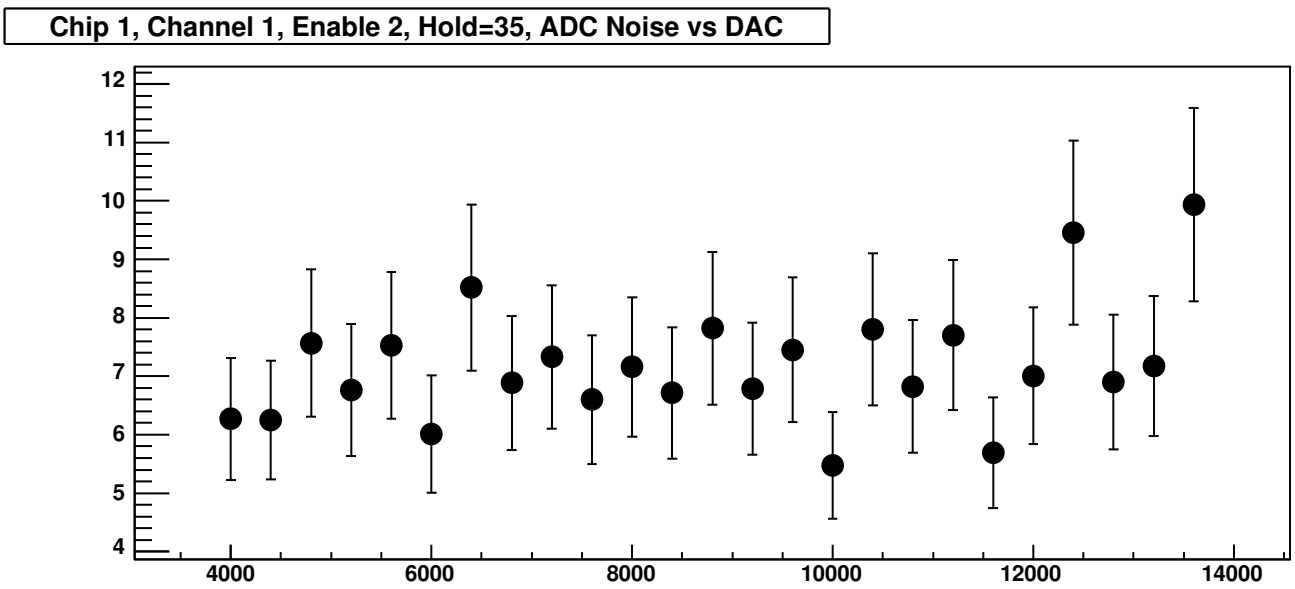

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

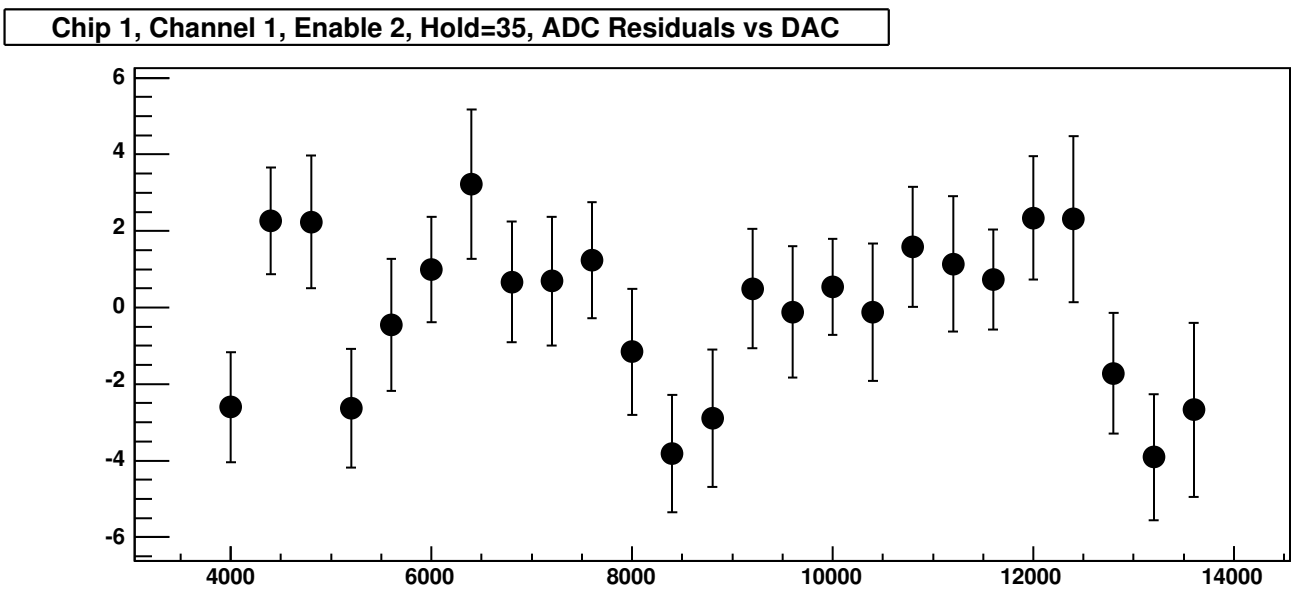

Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 11.03 / 16
p0 111.8 ± 1.438
p1 0.01661 ± 0.0001864
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

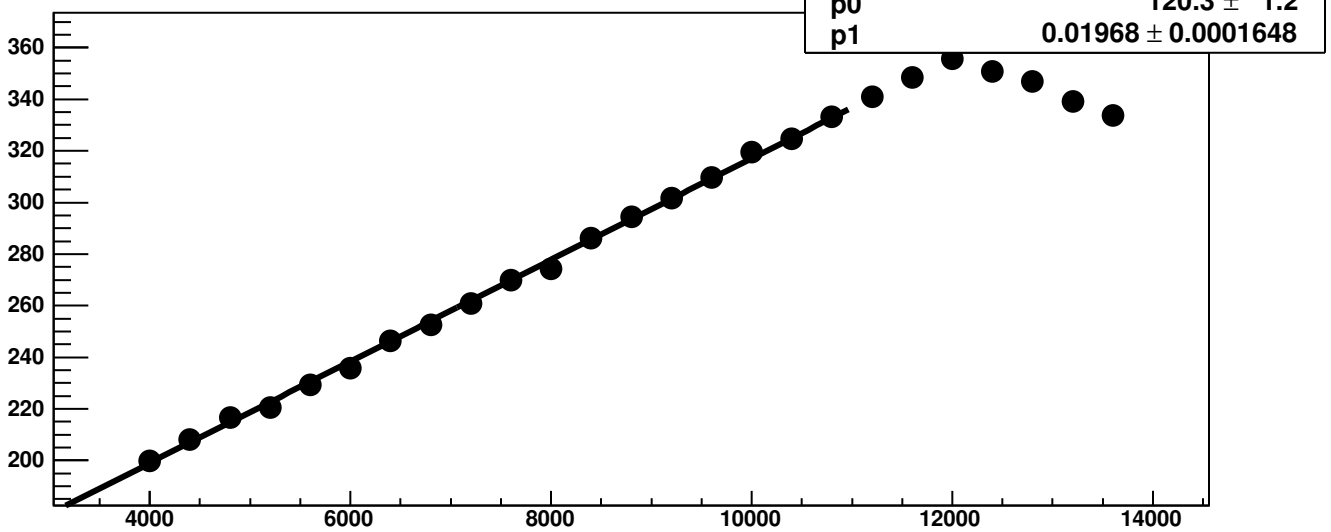

Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.32 / 16
p0 120.6 ± 1.336
p1 0.01481 ± 0.0001742
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

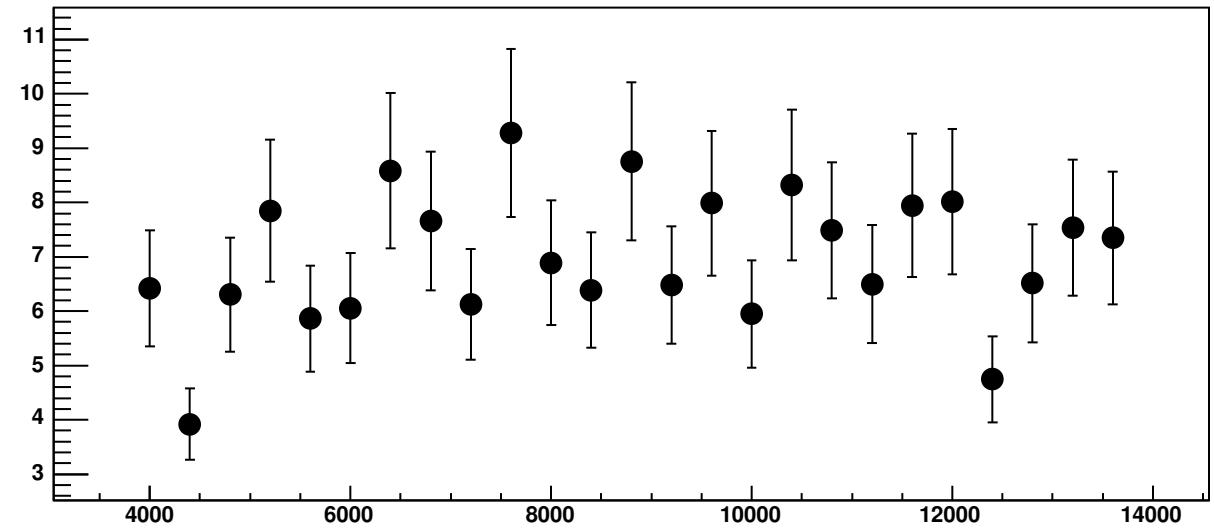

Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

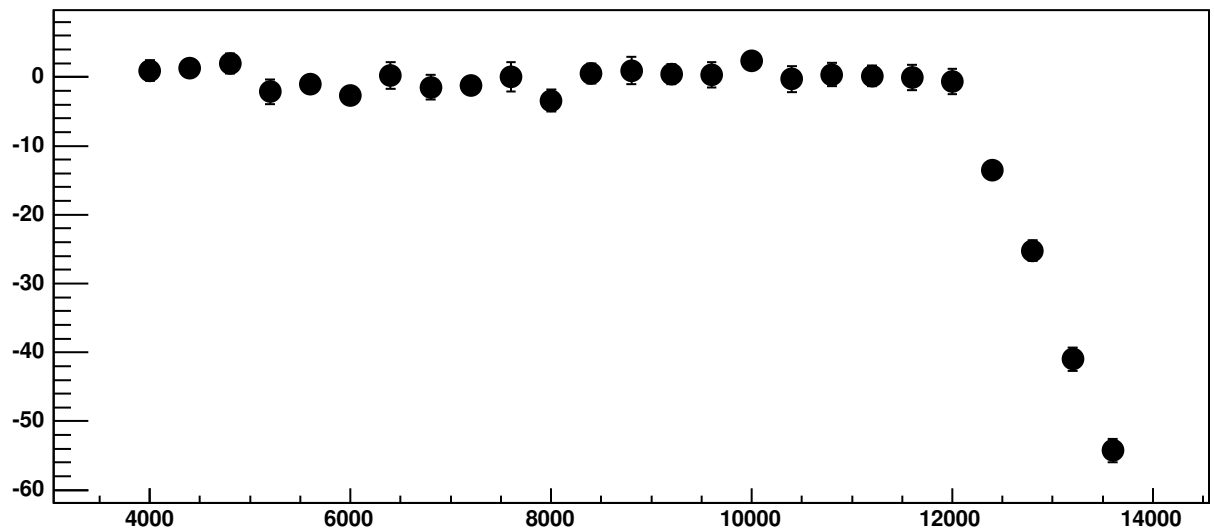
Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

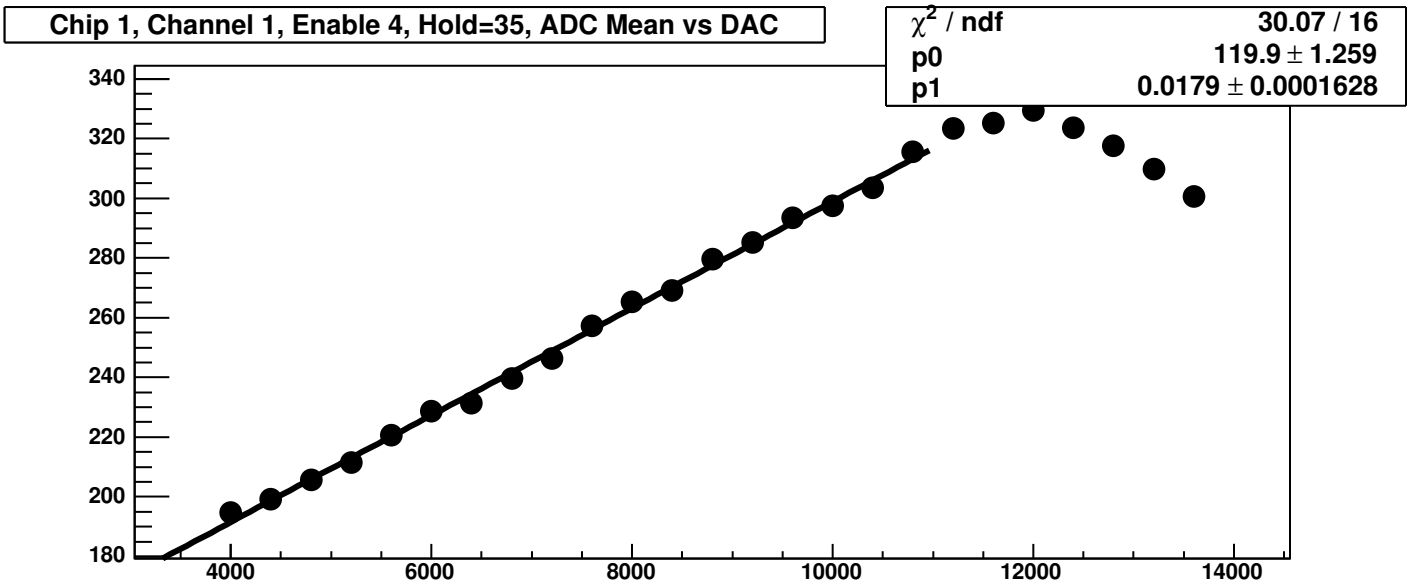


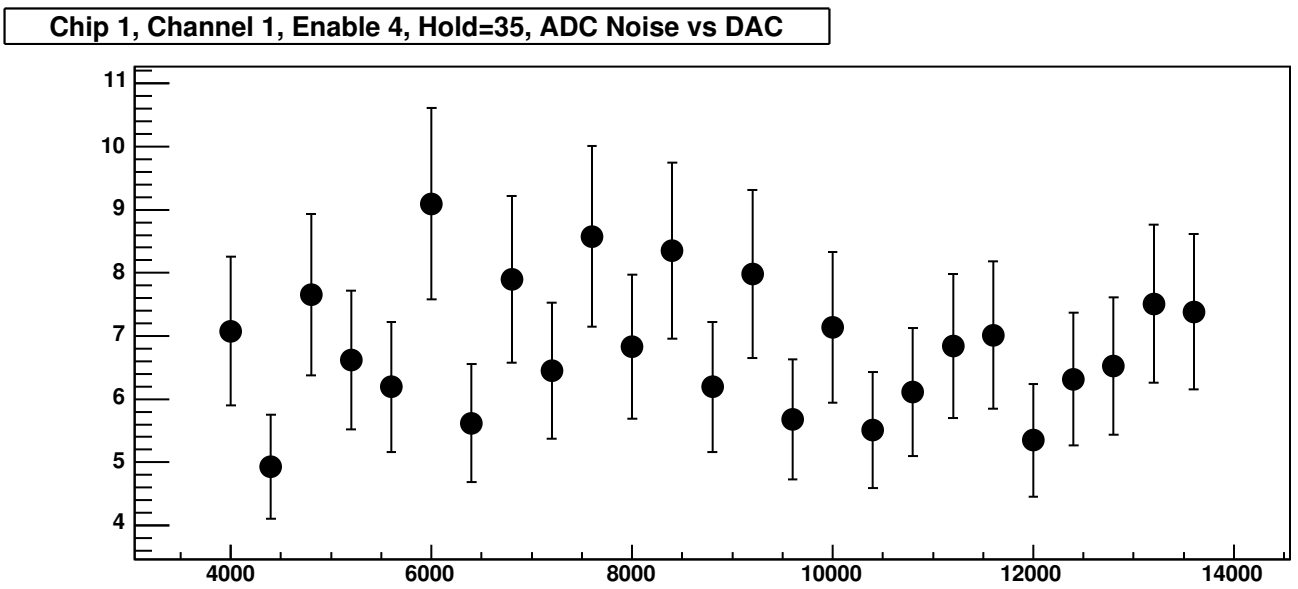
Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC

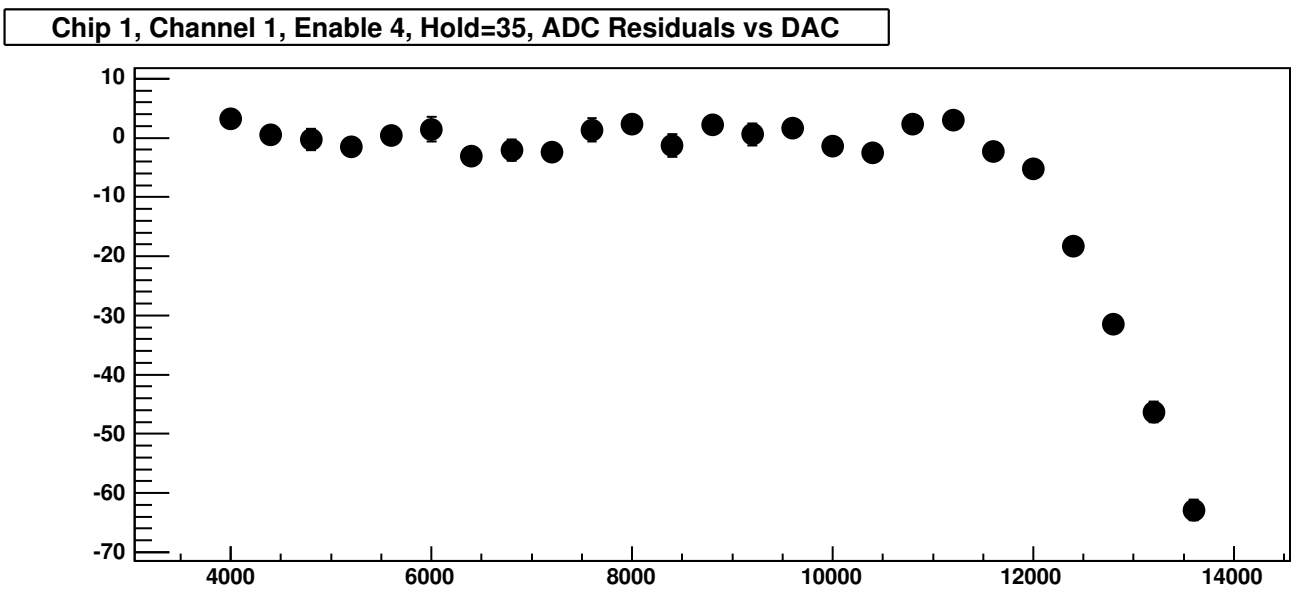


Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

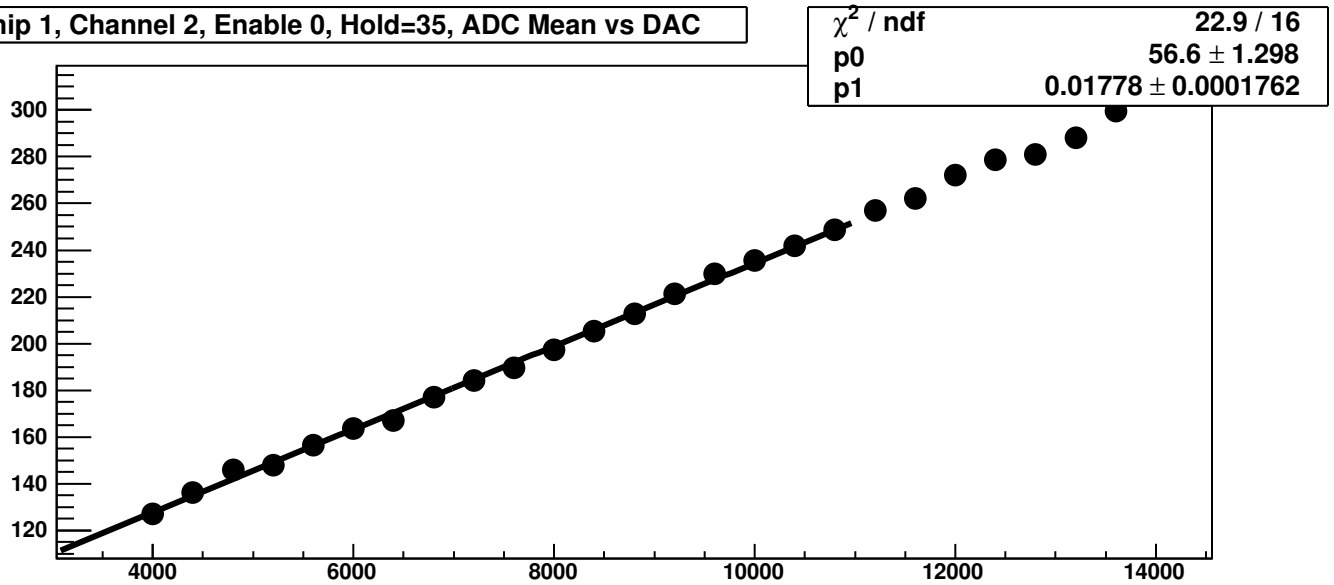



Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.07 / 16
p0 119.9 ± 1.259
p1 0.0179 ± 0.0001628
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

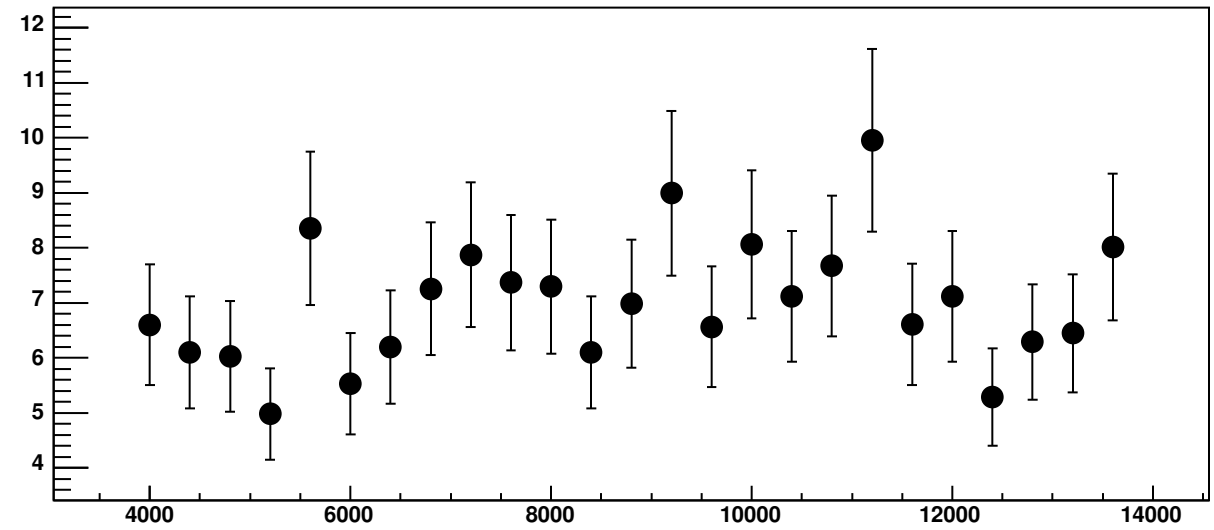

Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
4000
6000
8000
10000
12000
14000

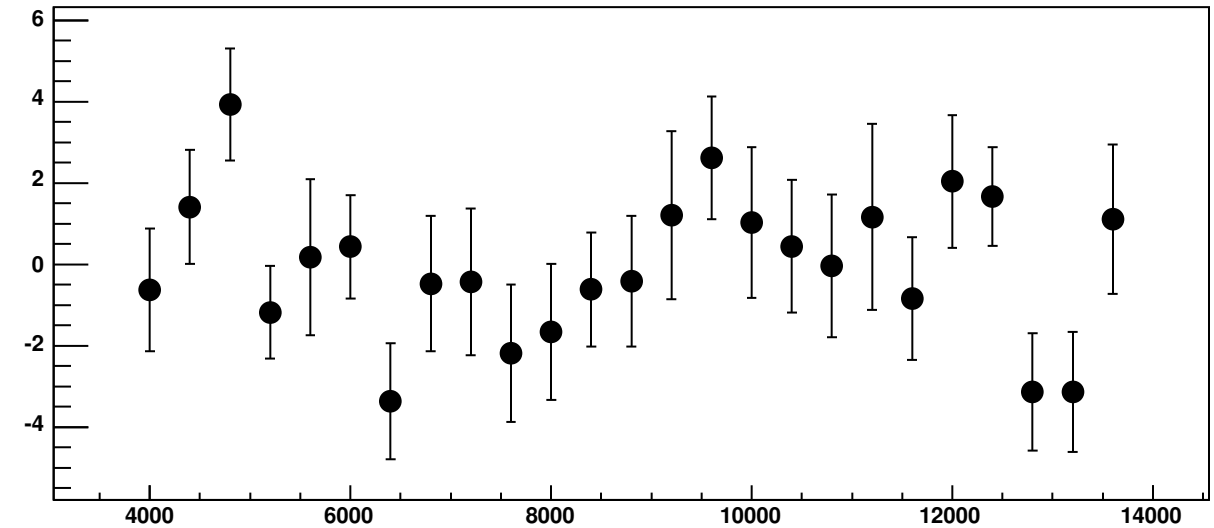
Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



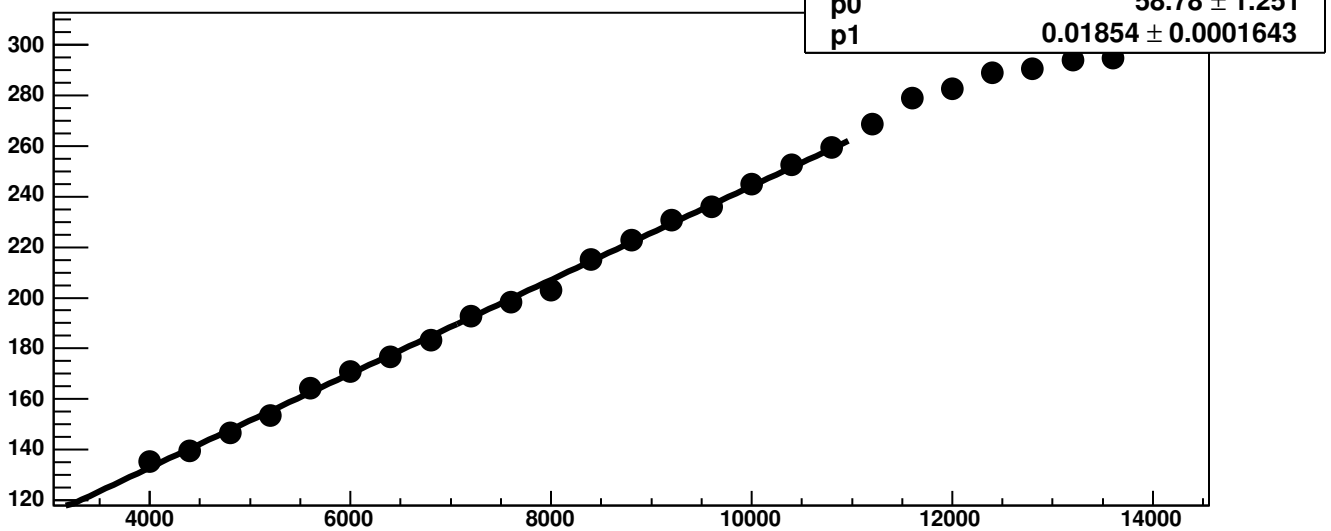
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



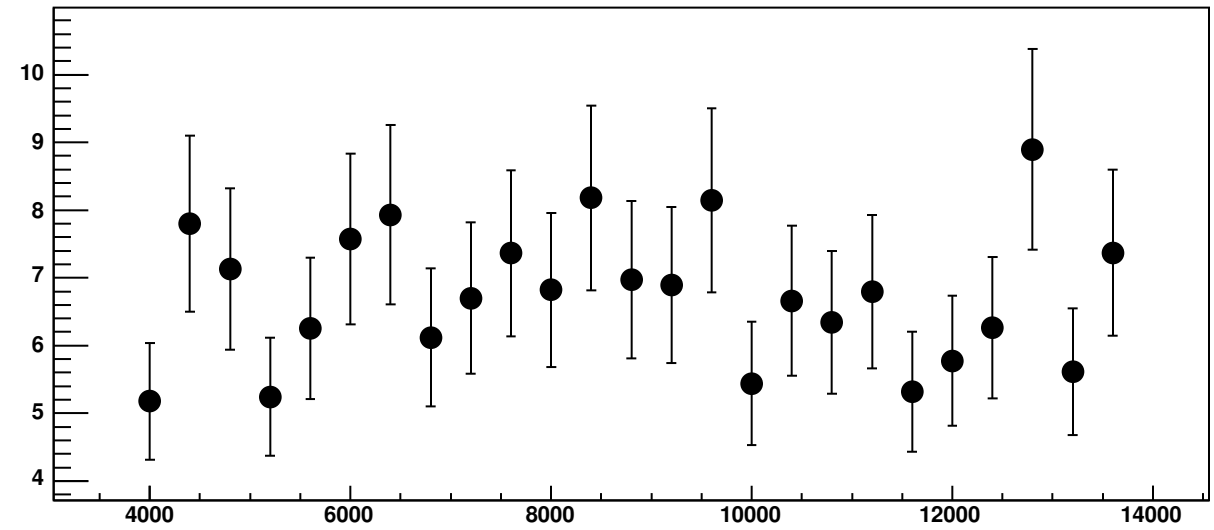
Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



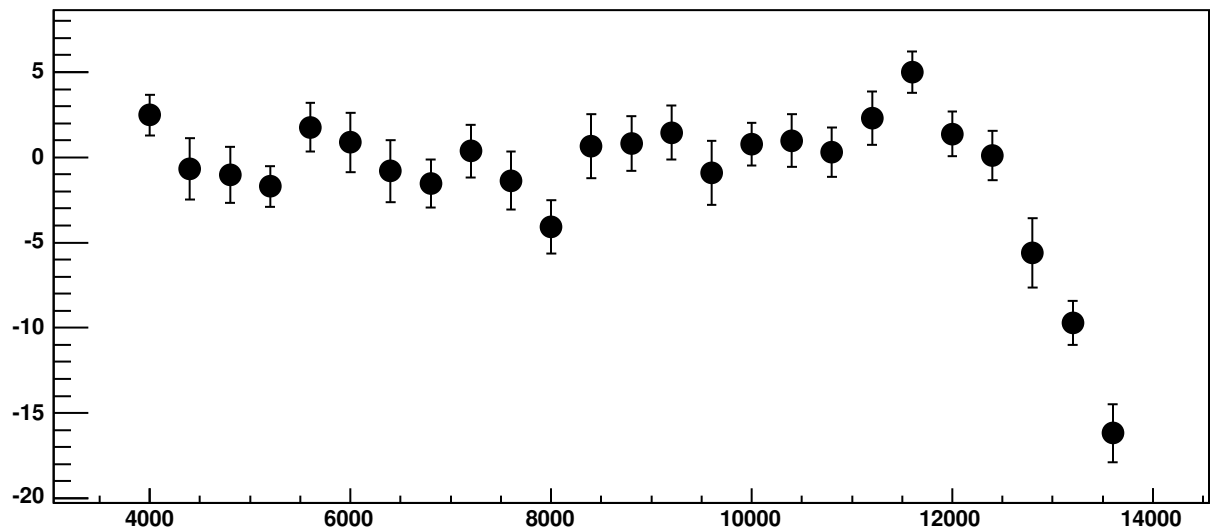
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



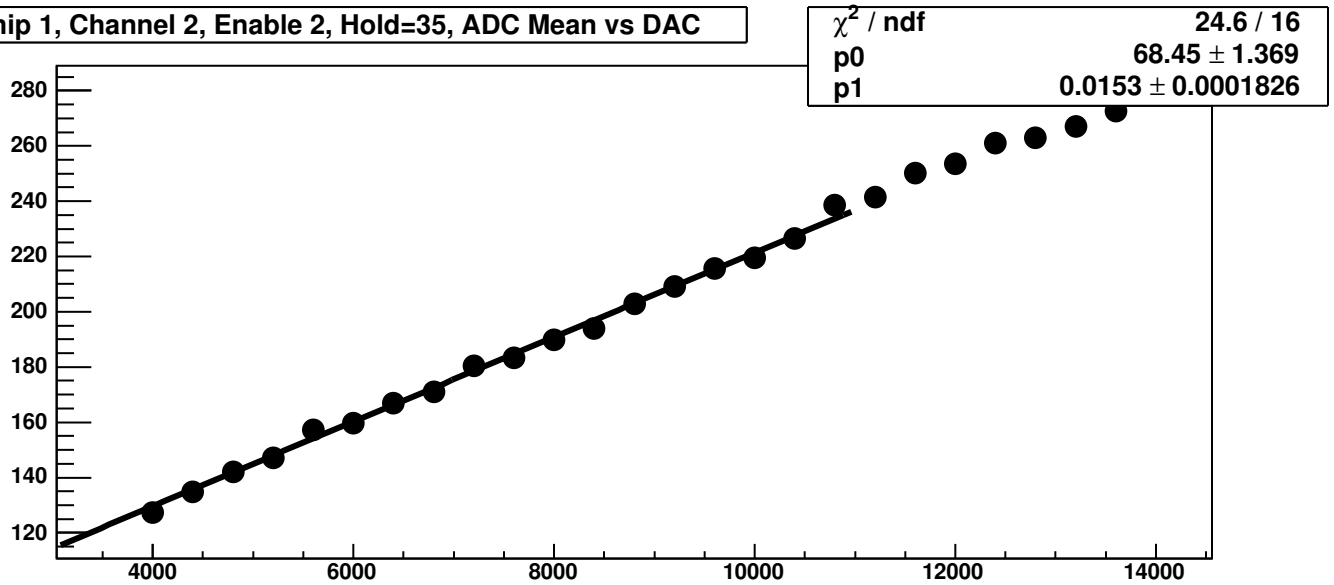
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



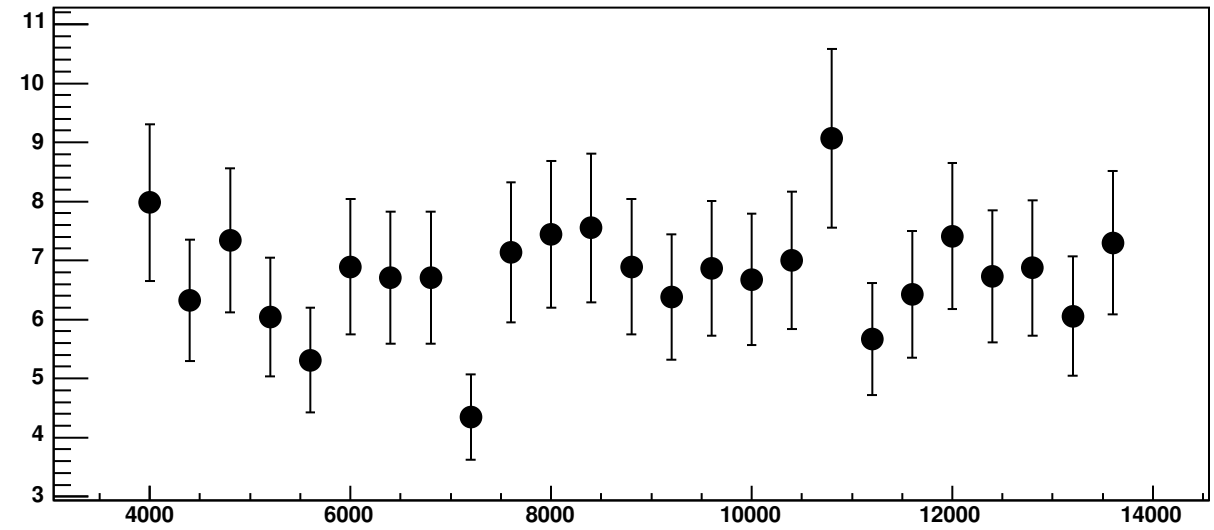
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



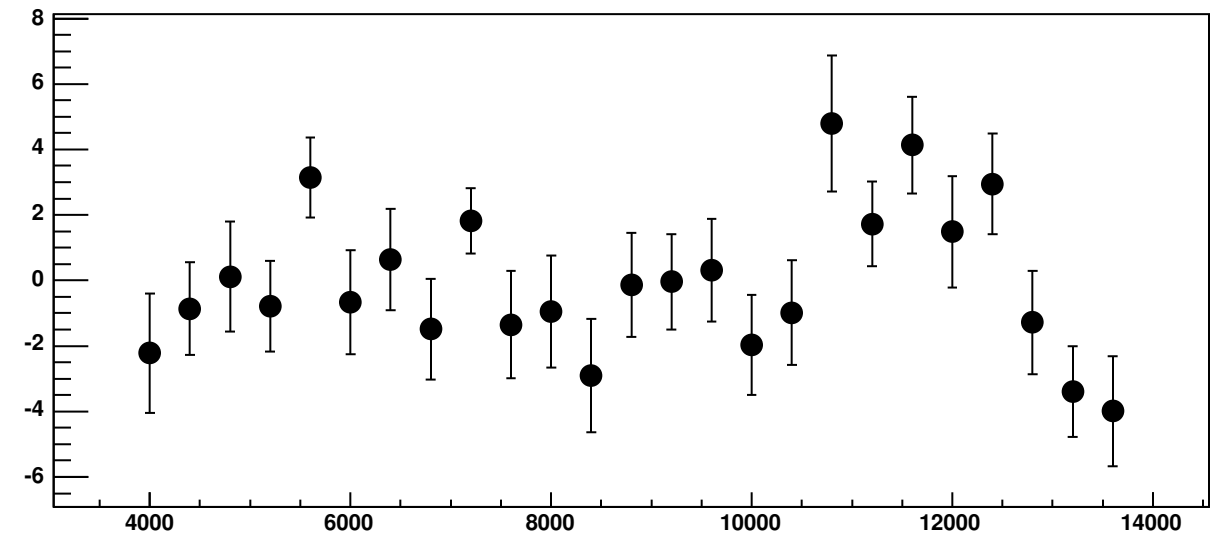
Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

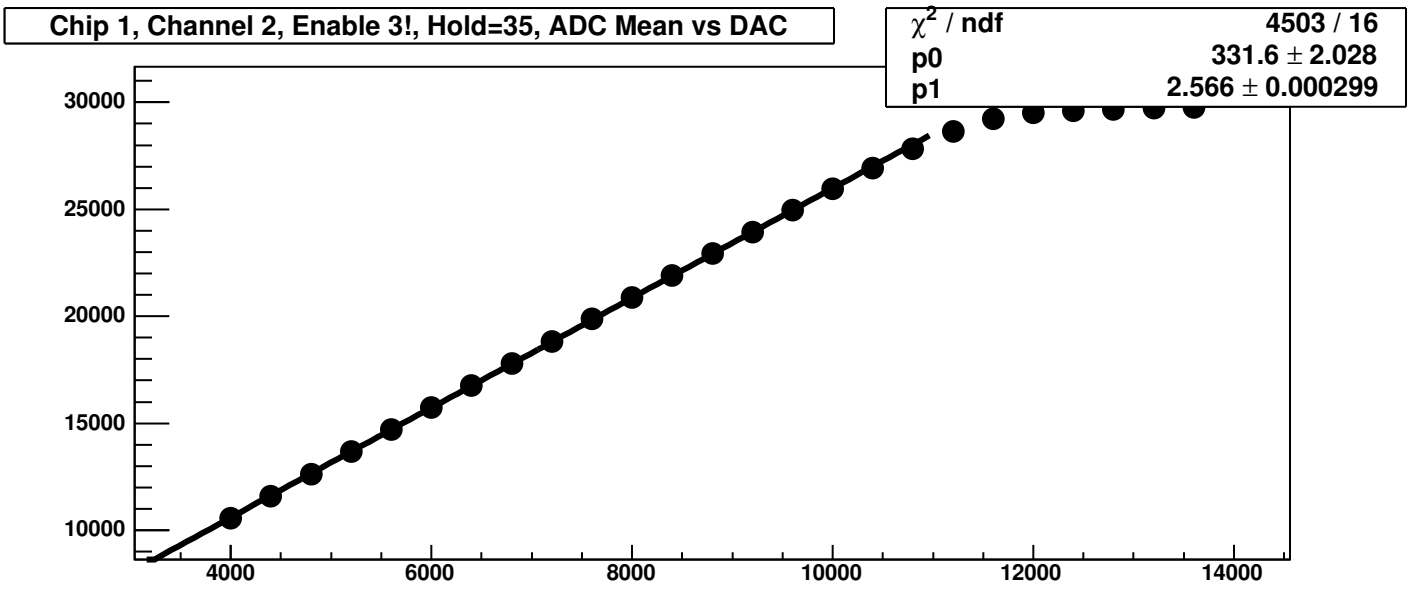


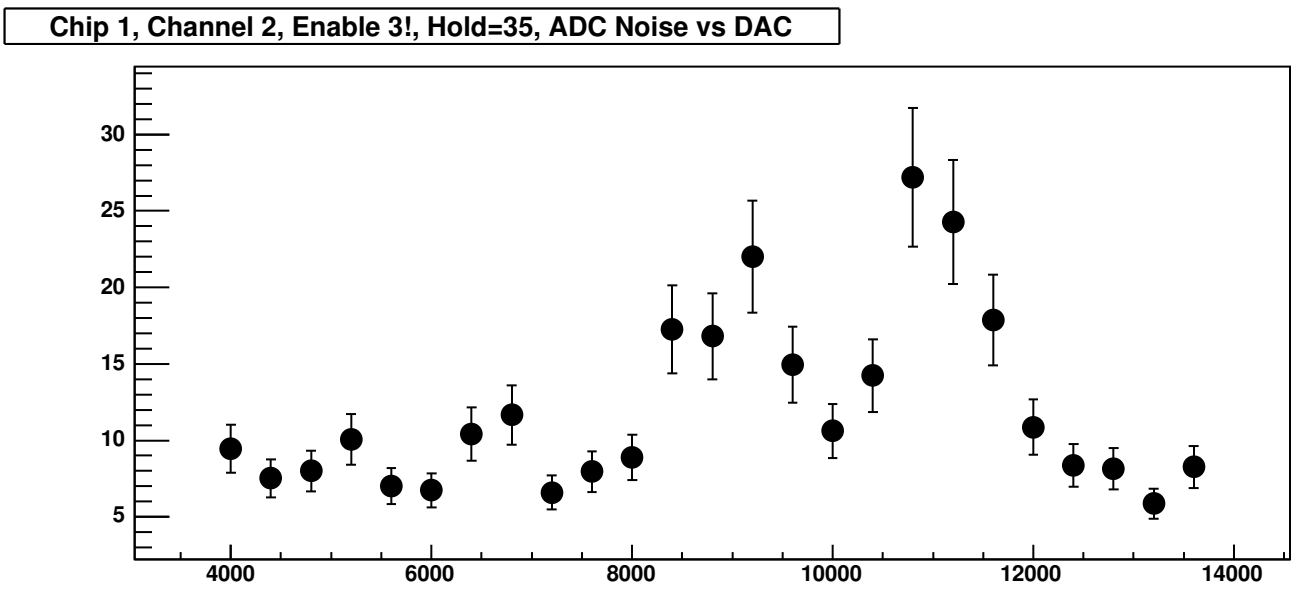
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

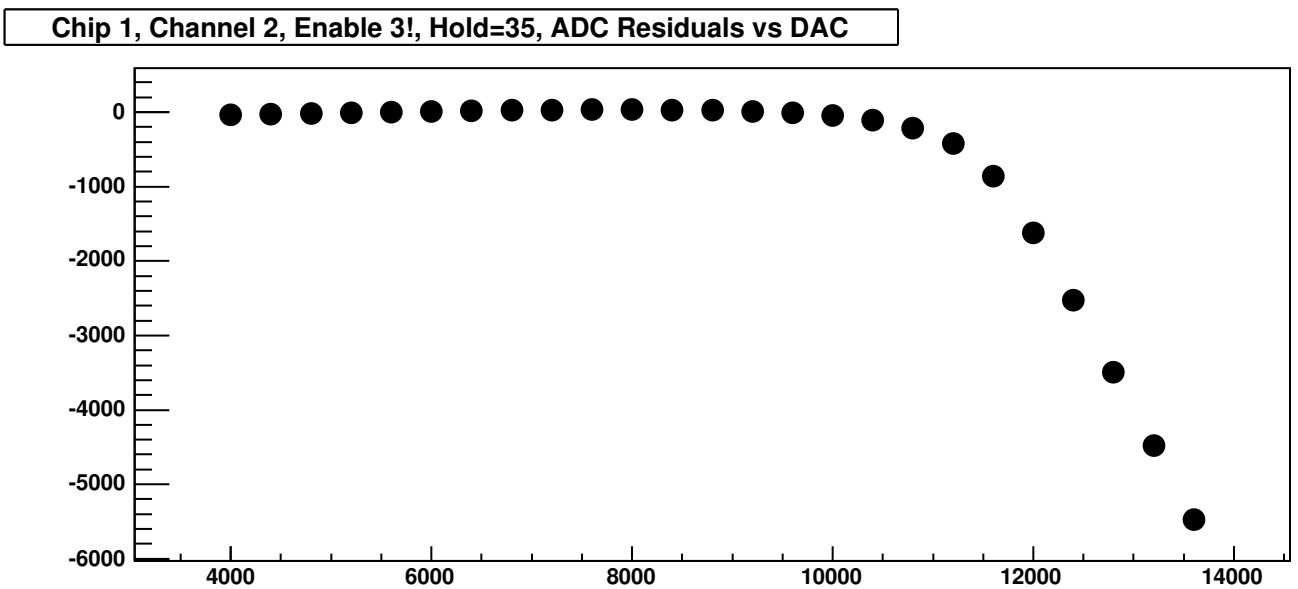


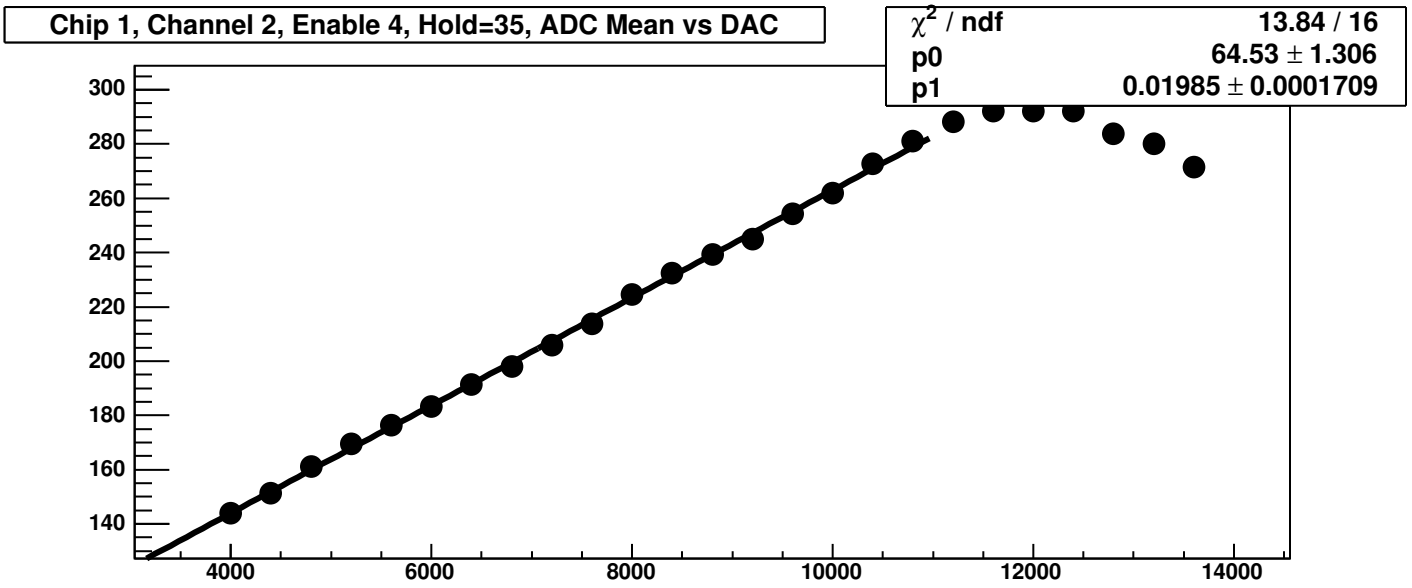
Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

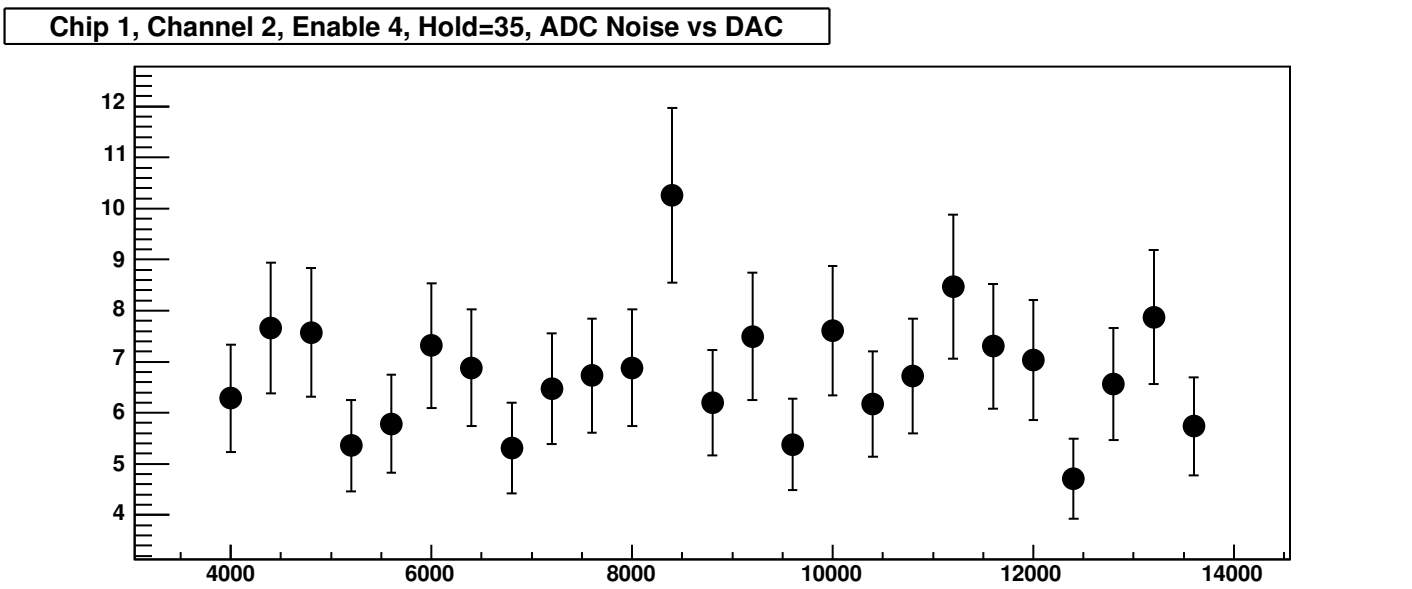


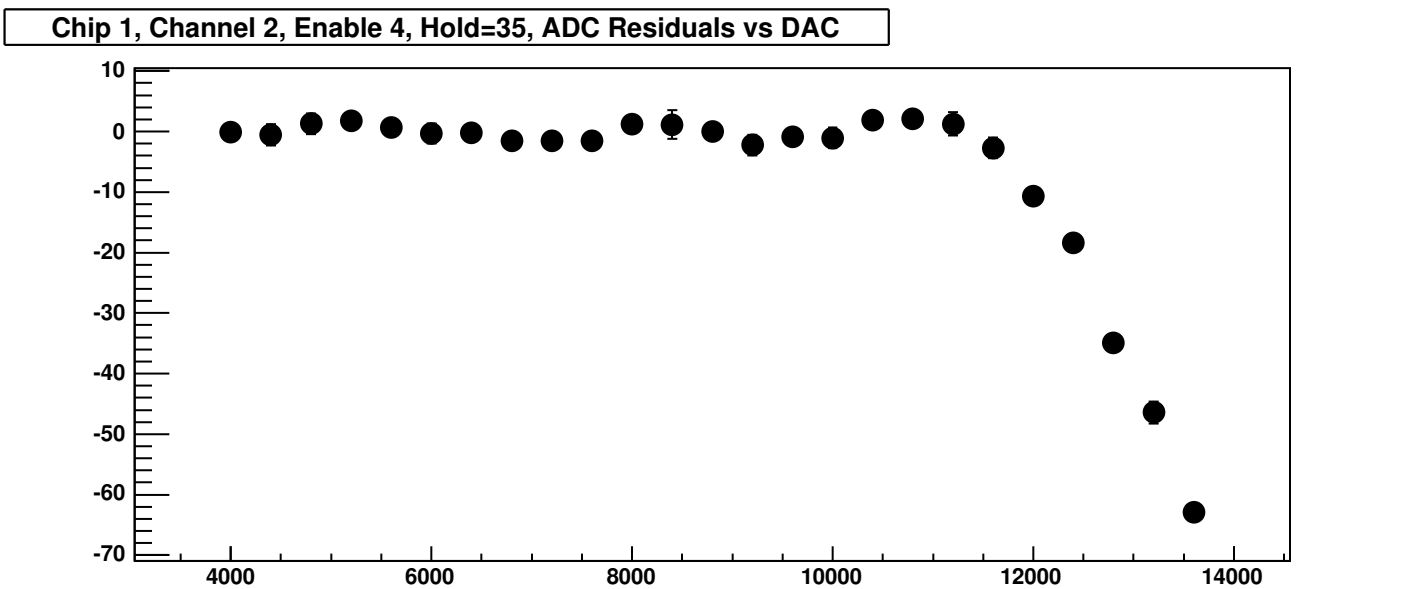

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4503 / 16
p0 331.6 ± 2.028
p1 2.566 ± 0.000299
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

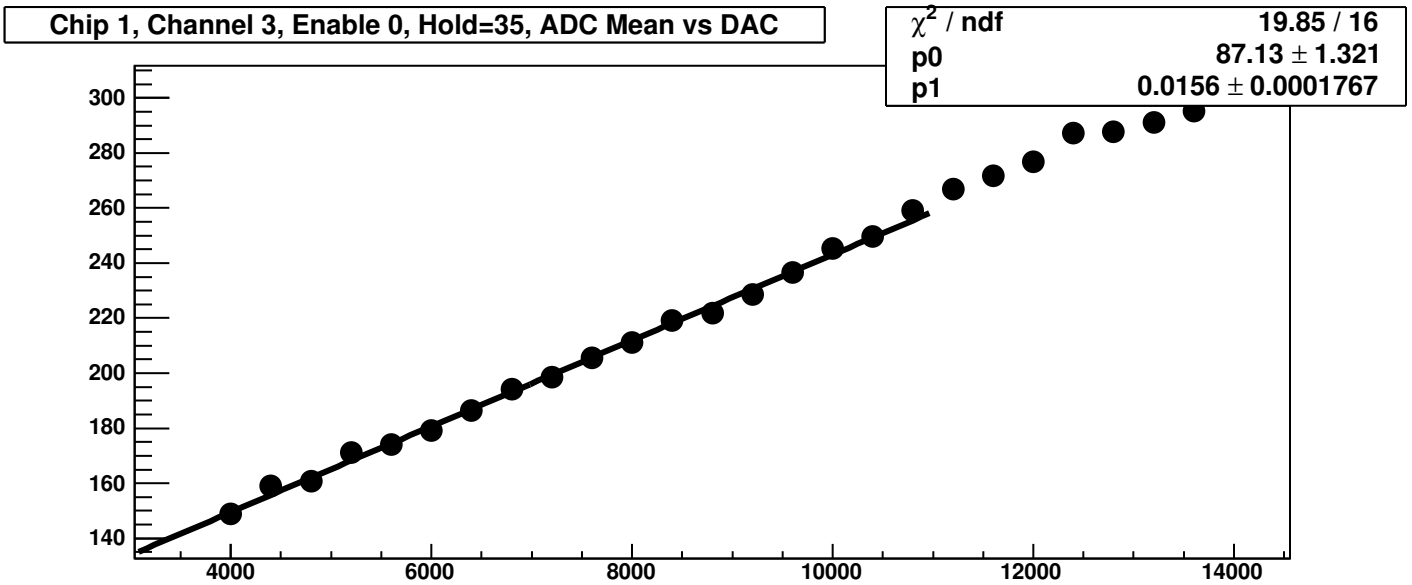

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

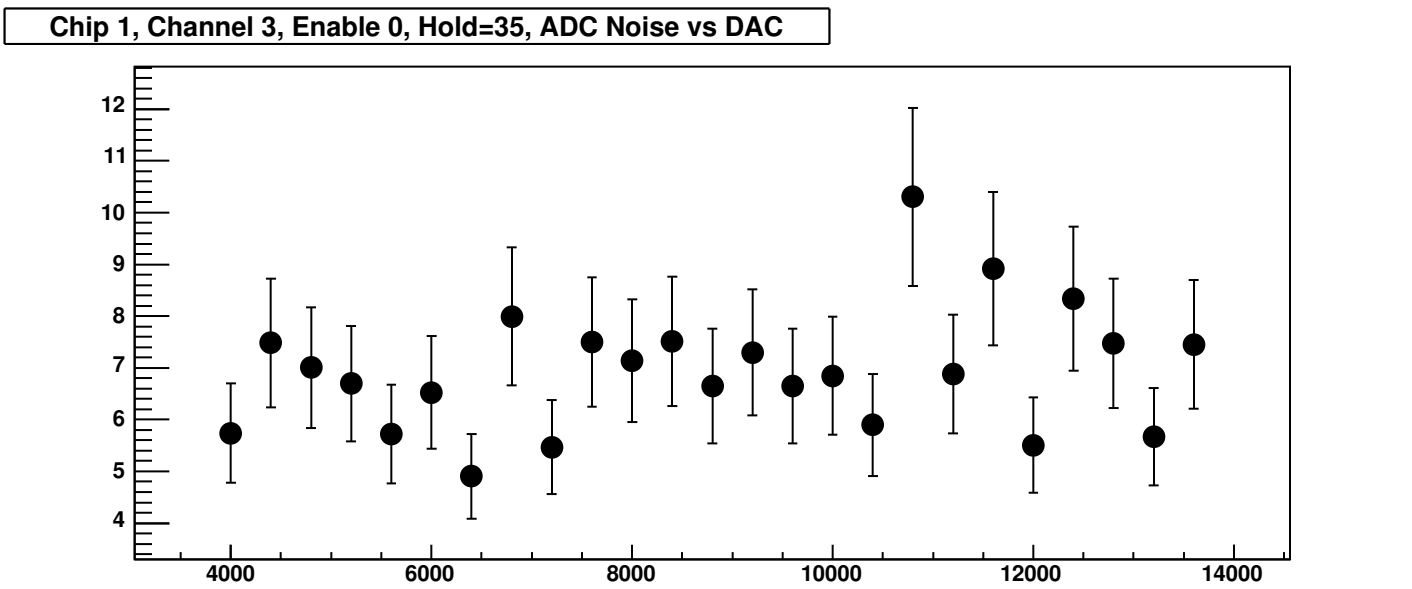

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000

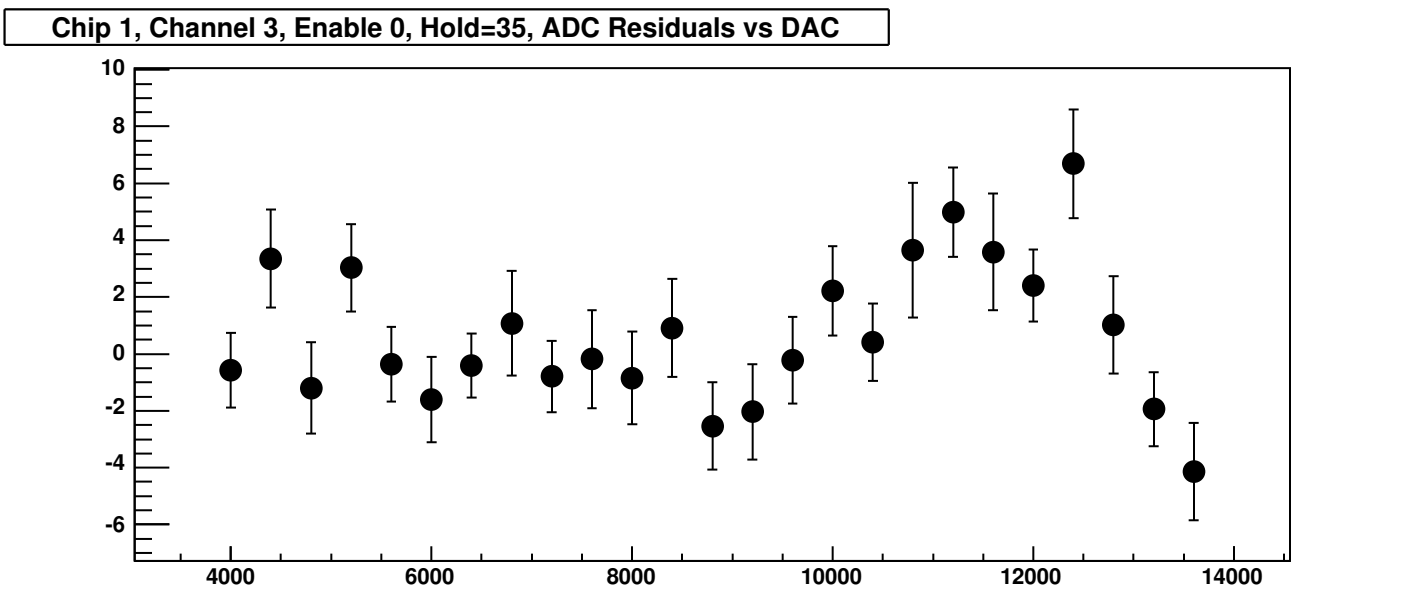

Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.84 / 16
p0 64.53 ± 1.306
p1 0.01985 ± 0.0001709
300
280
260
240
220
200
180
160
140
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

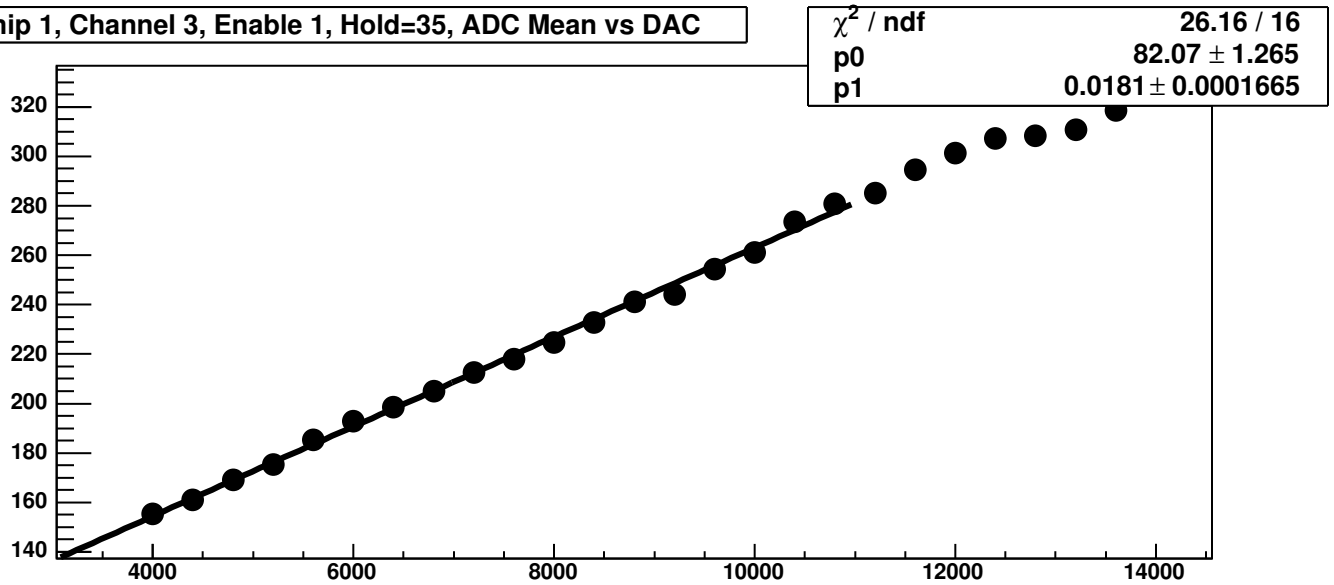

Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 19.85 / 16
p0 87.13 ± 1.321
p1 0.0156 ± 0.0001767
300
280
260
240
220
200
180
160
140
4000
6000
8000
10000
12000
14000

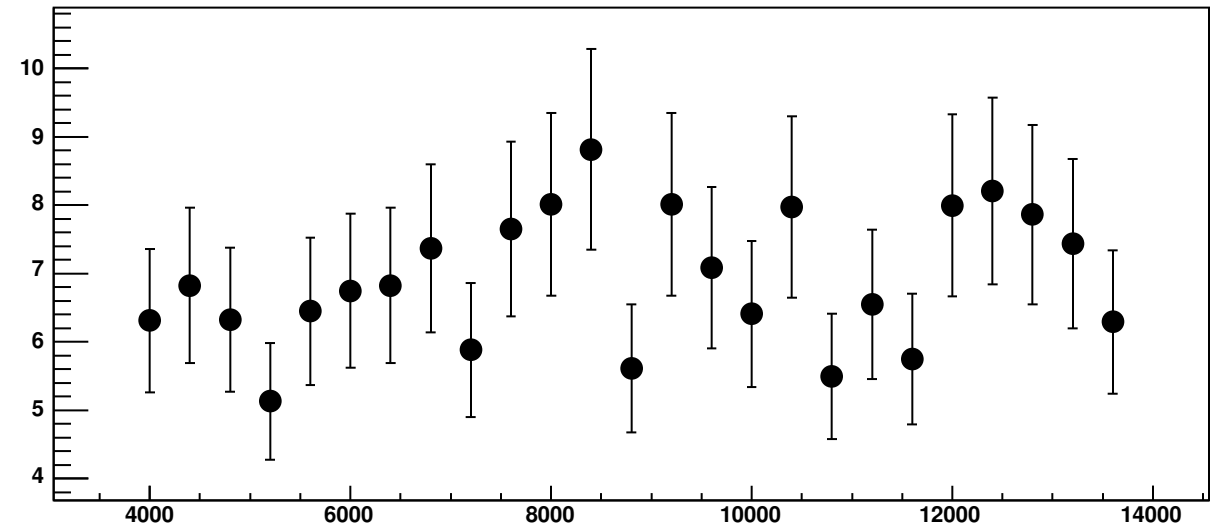

Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

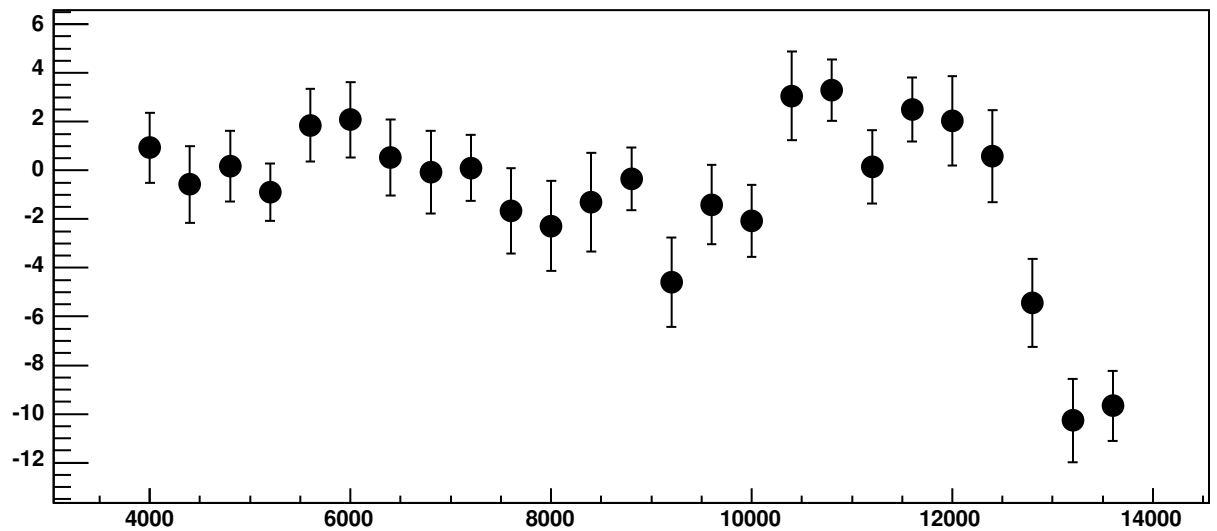
Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

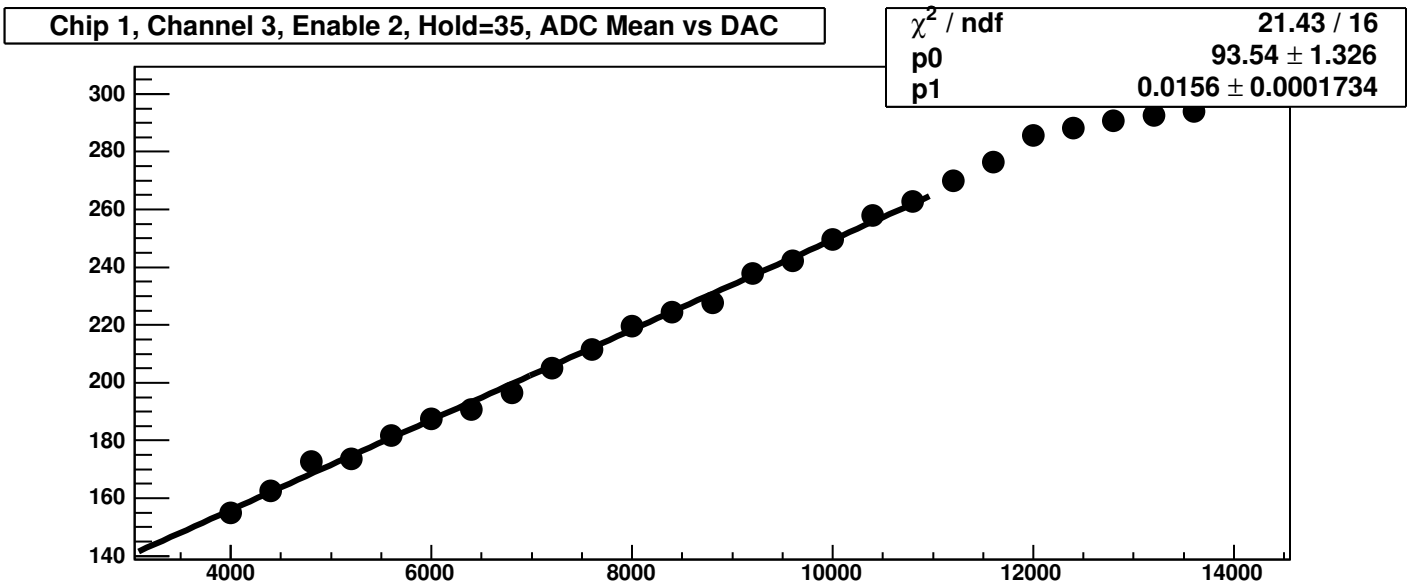


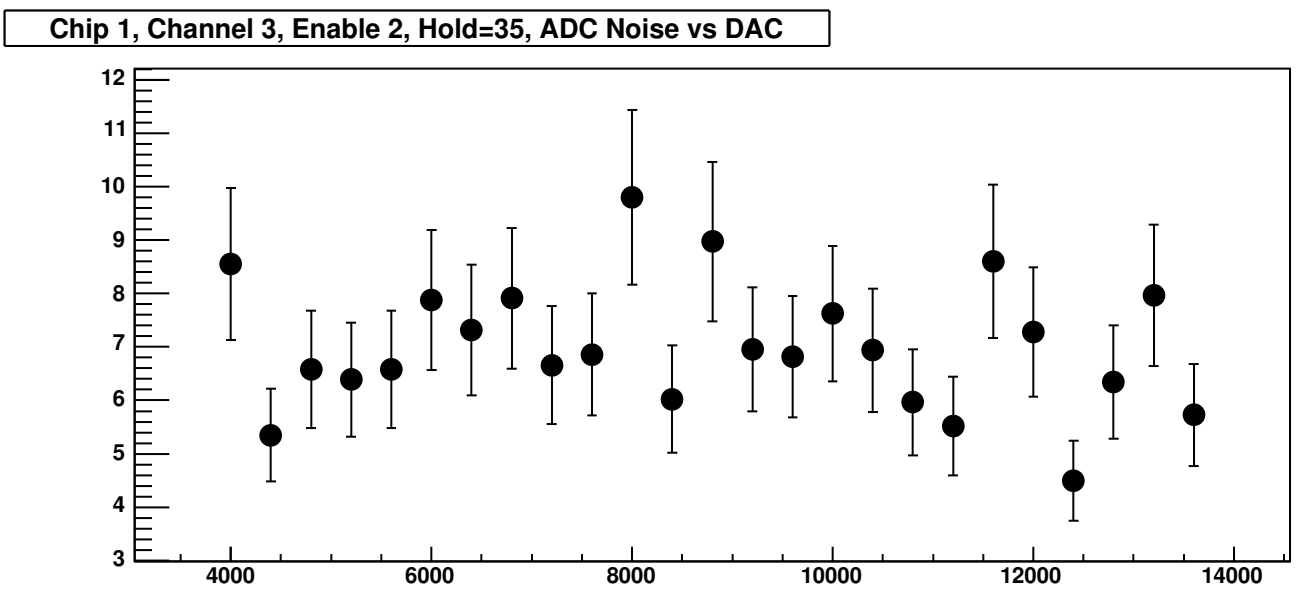
Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC

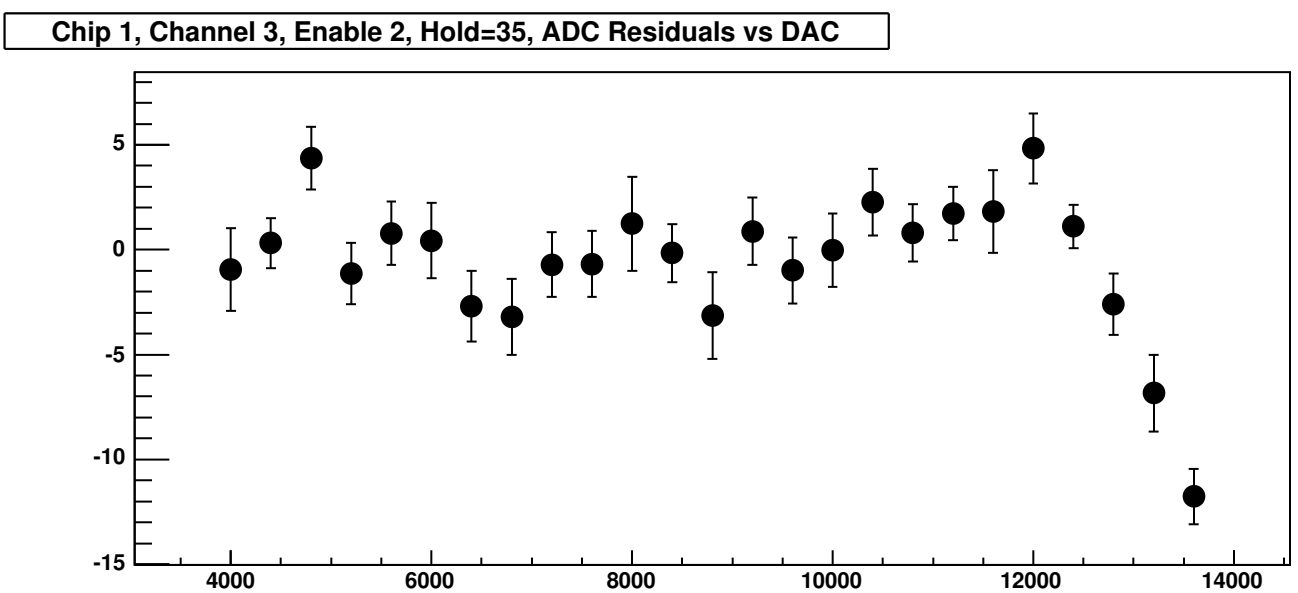


Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

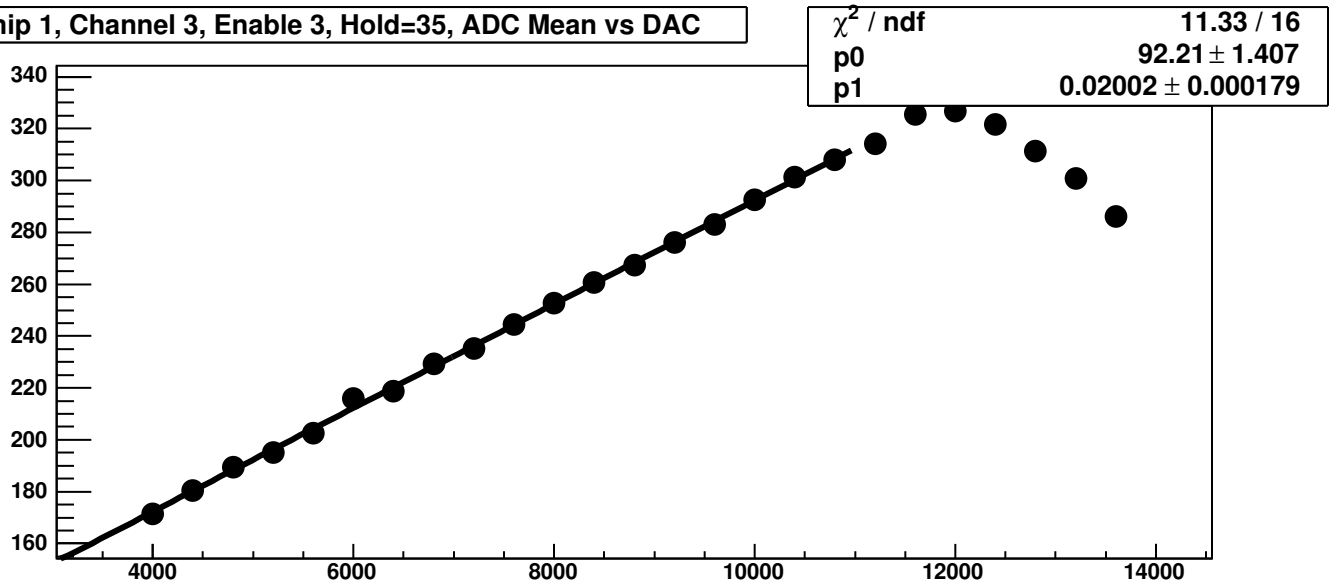



Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.43 / 16
p0 93.54 ± 1.326
p1 0.0156 ± 0.0001734
300
280
260
240
220
200
180
160
140
4000
6000
8000
10000
12000
14000

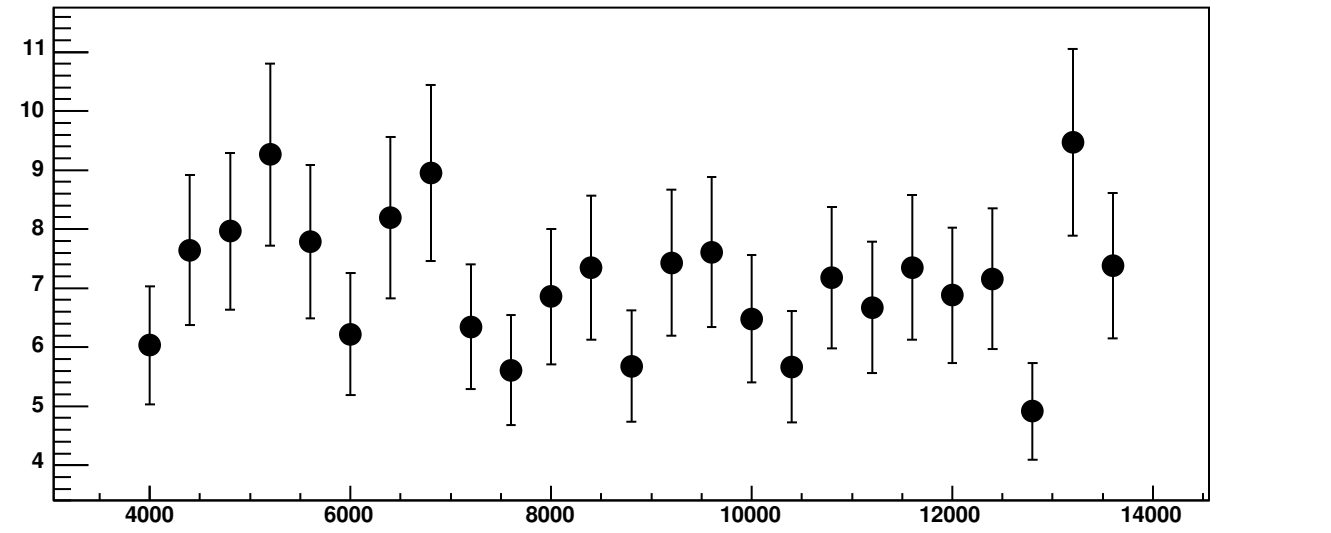

Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

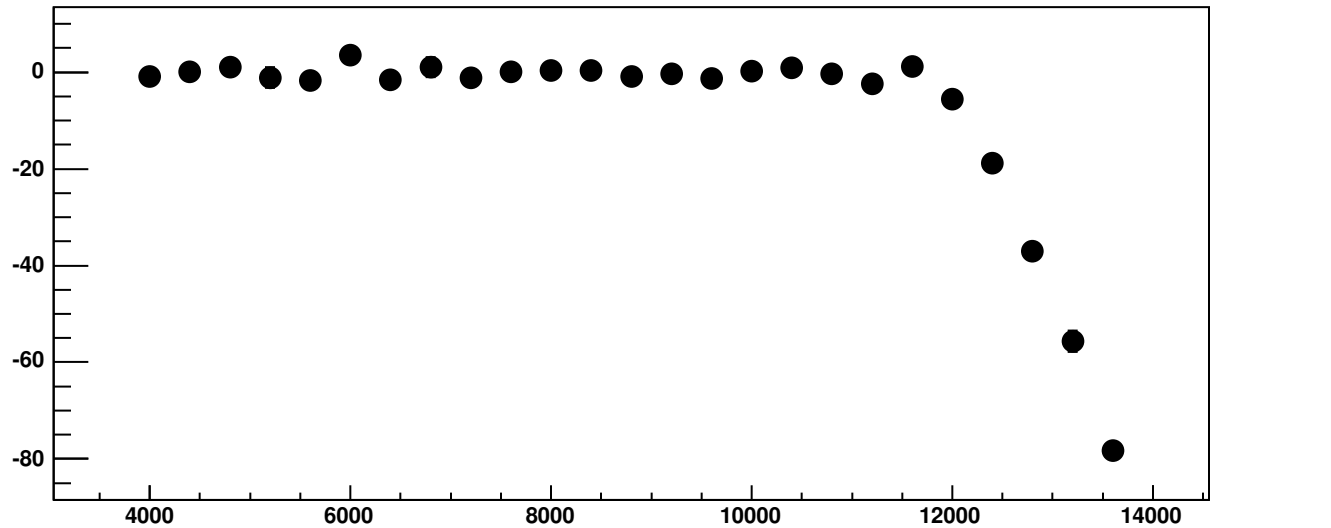
Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

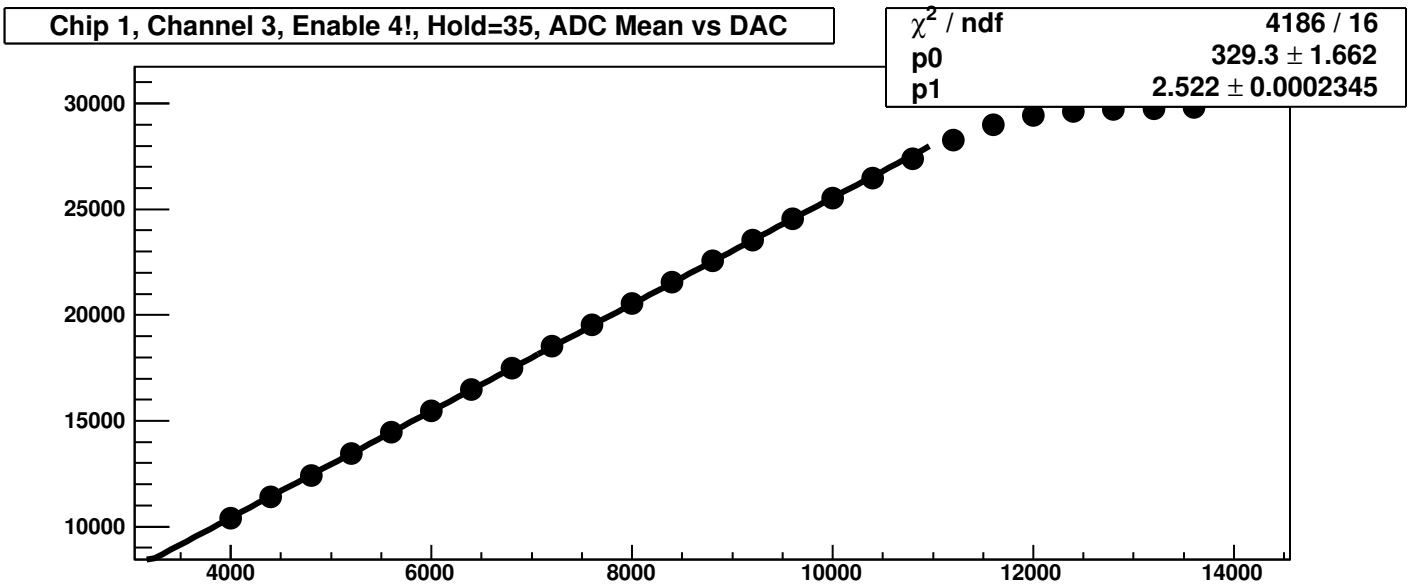


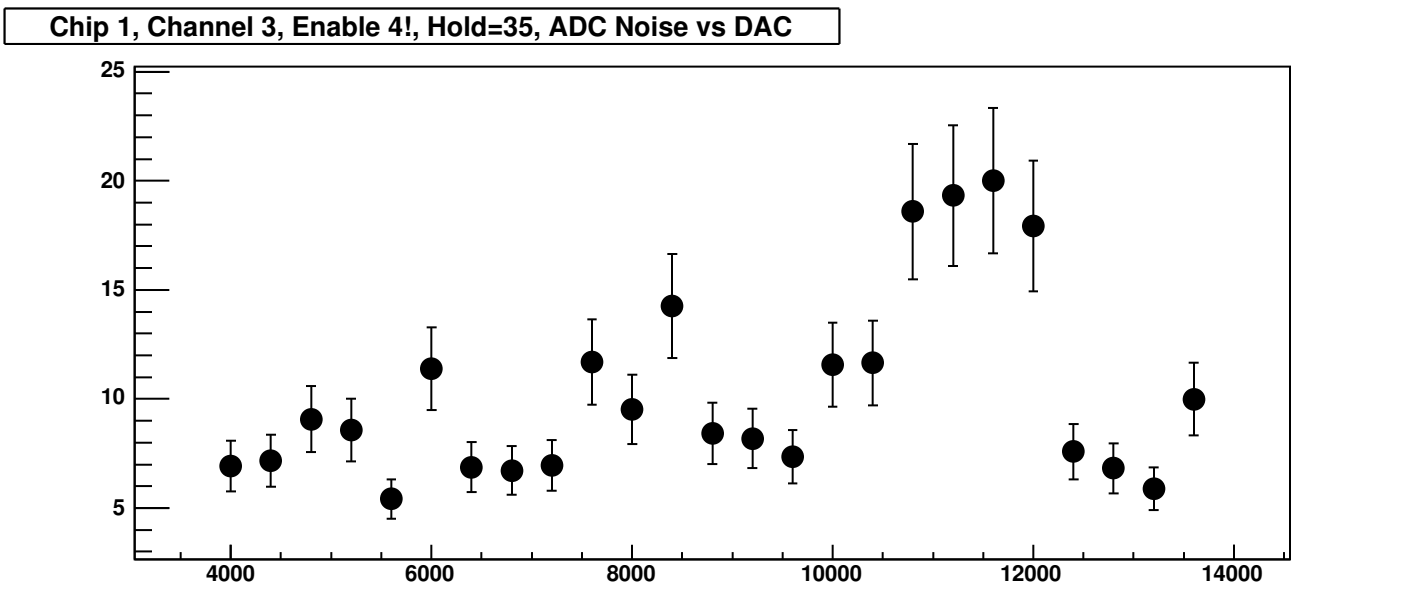
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

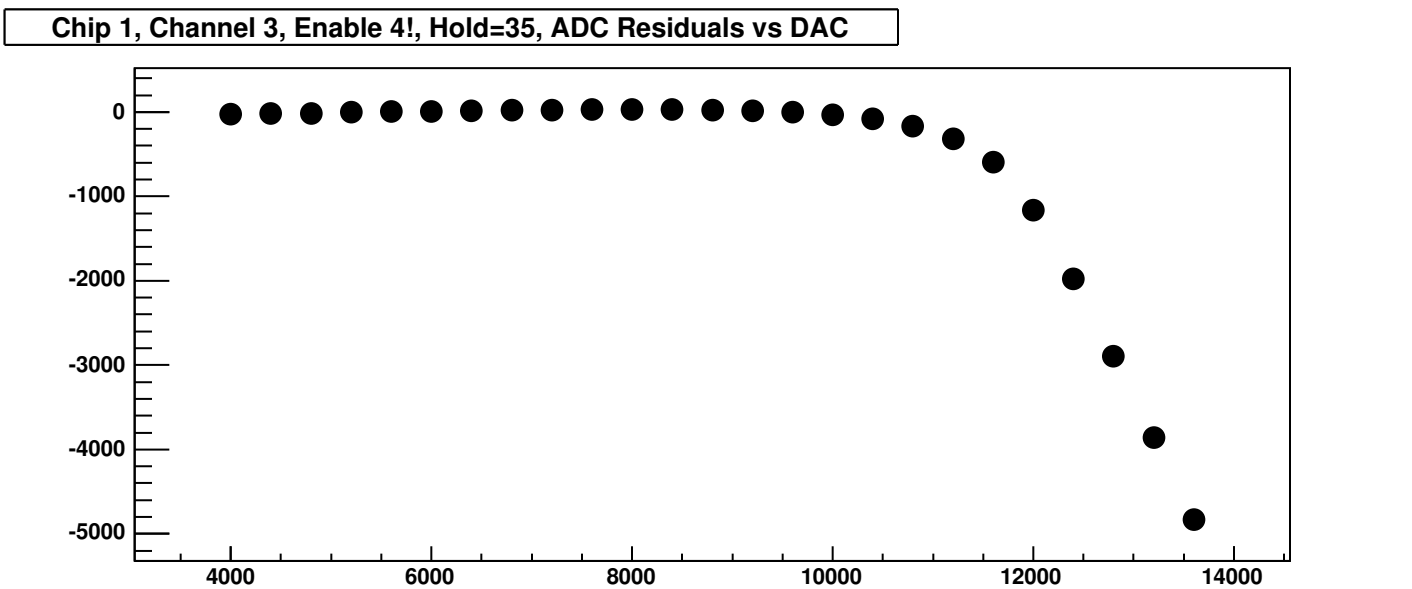


Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

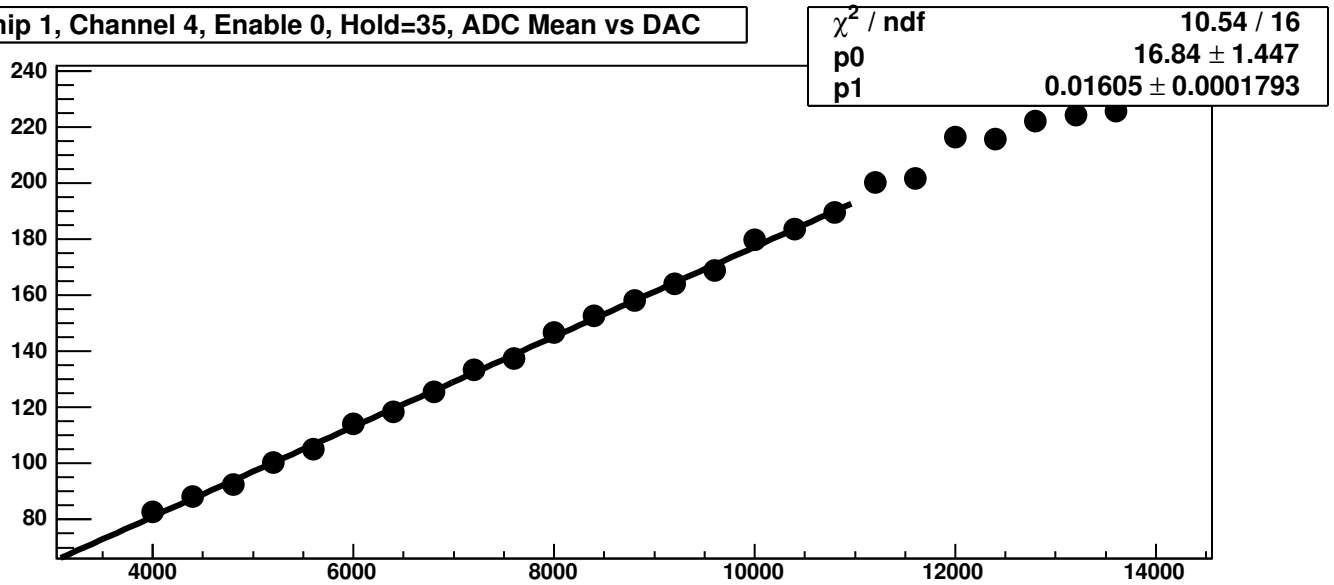



Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 4186 / 16
p0 329.3 ± 1.662
p1 2.522 ± 0.0002345
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

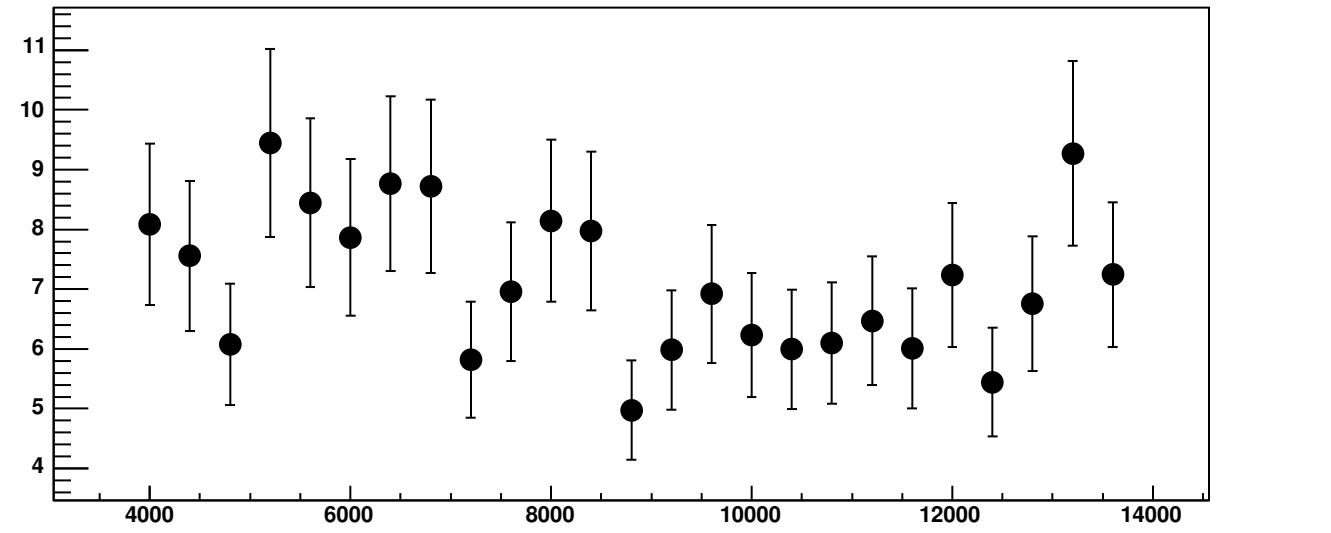

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

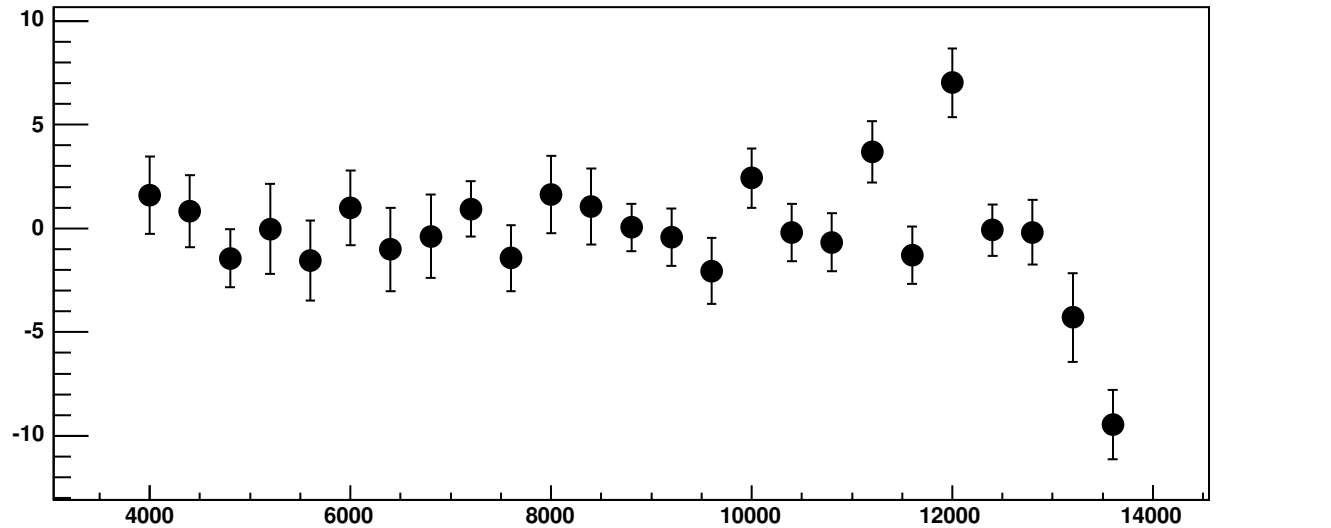
Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

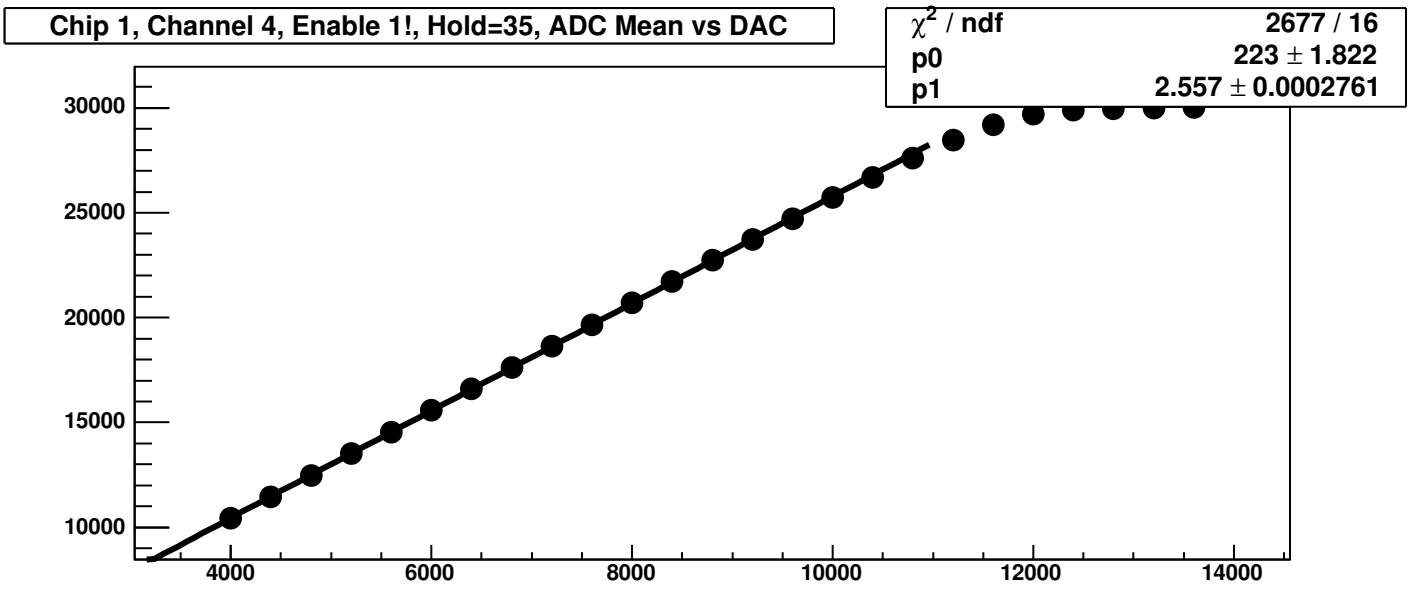


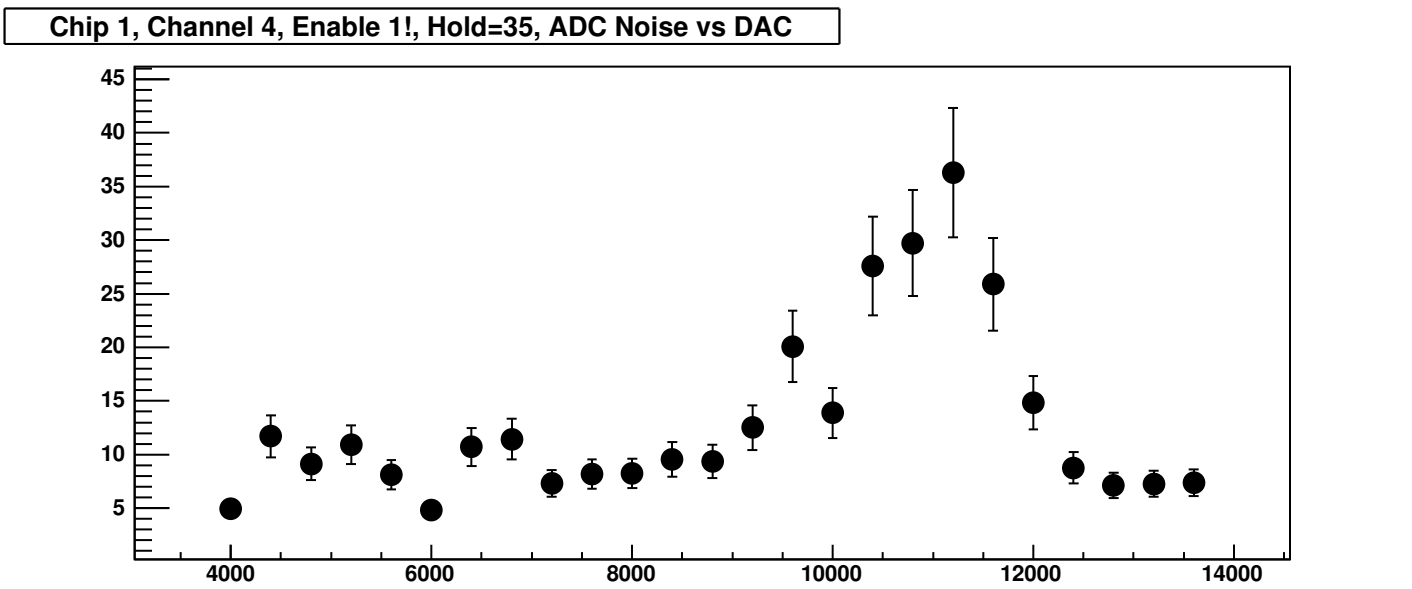
Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

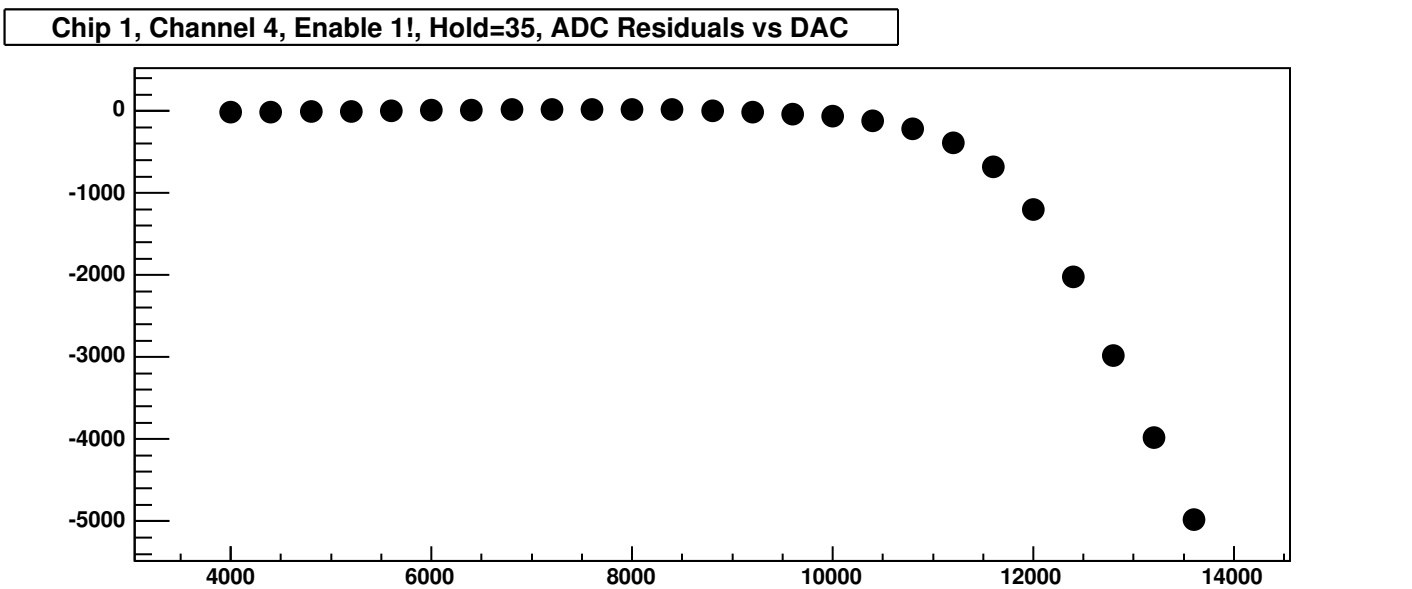


Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

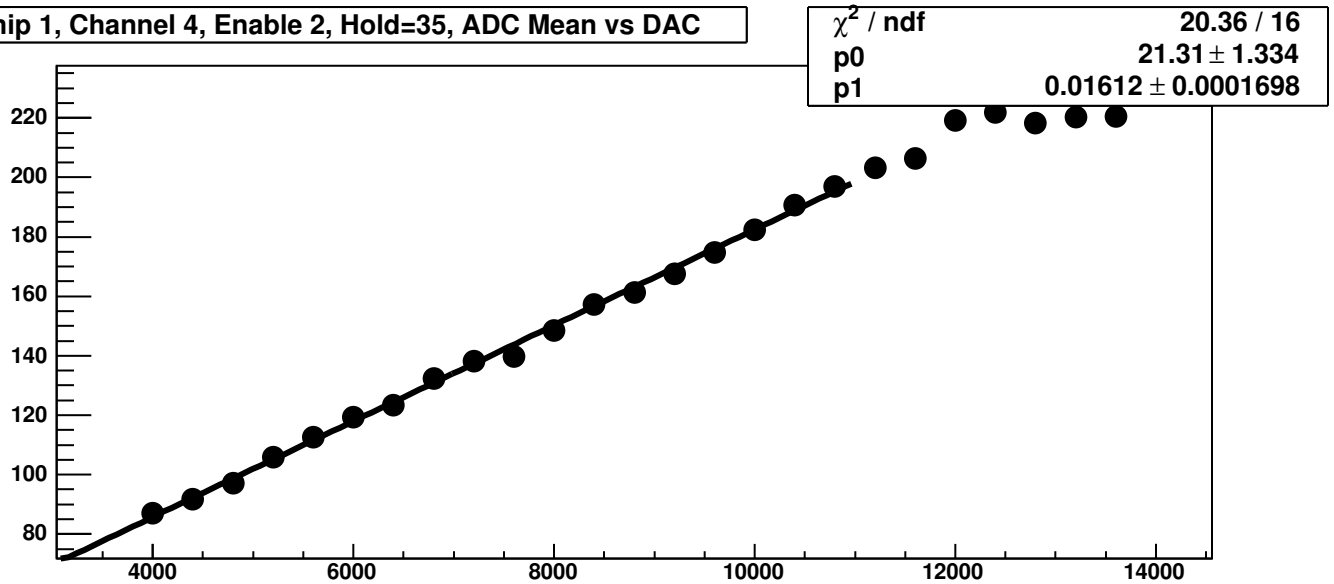



Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 2677 / 16
p0 223 ± 1.822
p1 2.557 ± 0.0002761
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

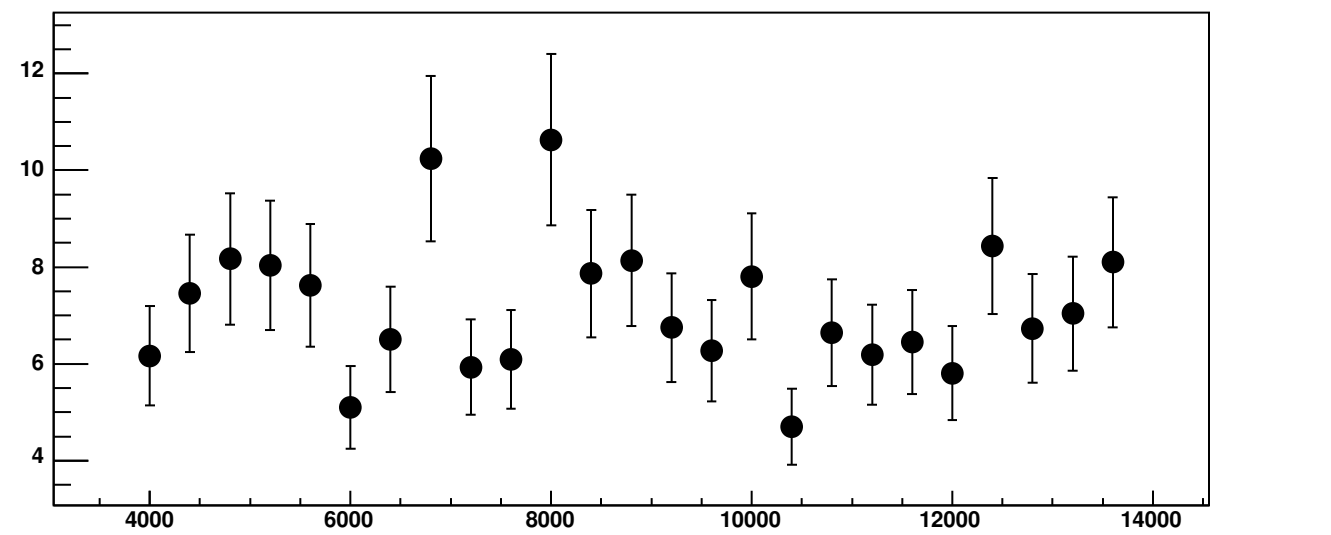

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

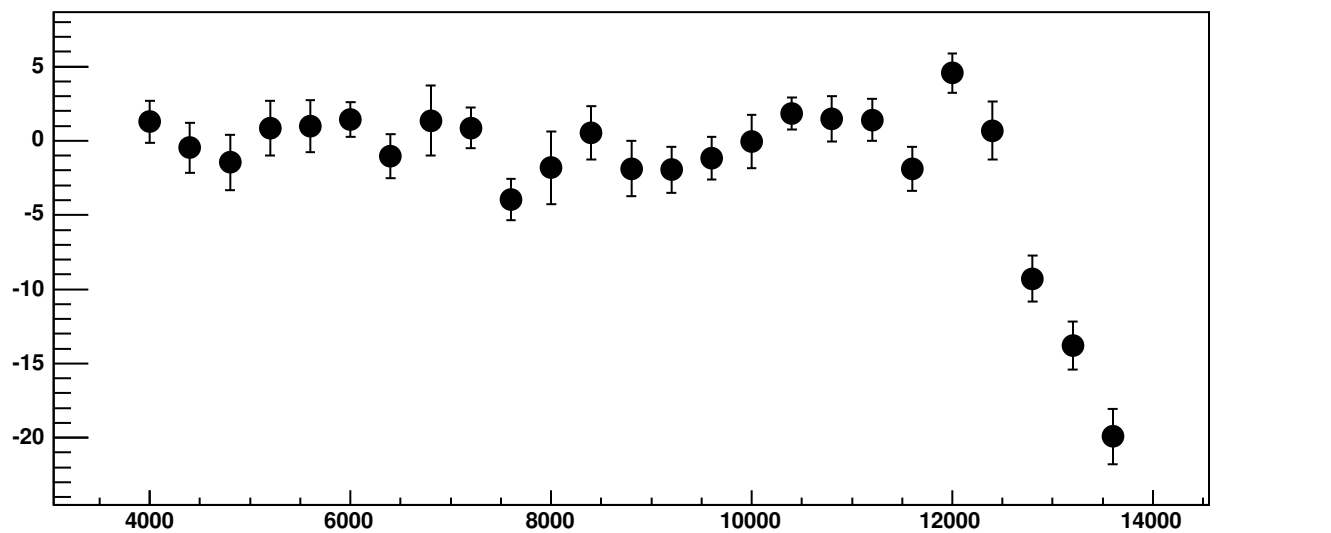
Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

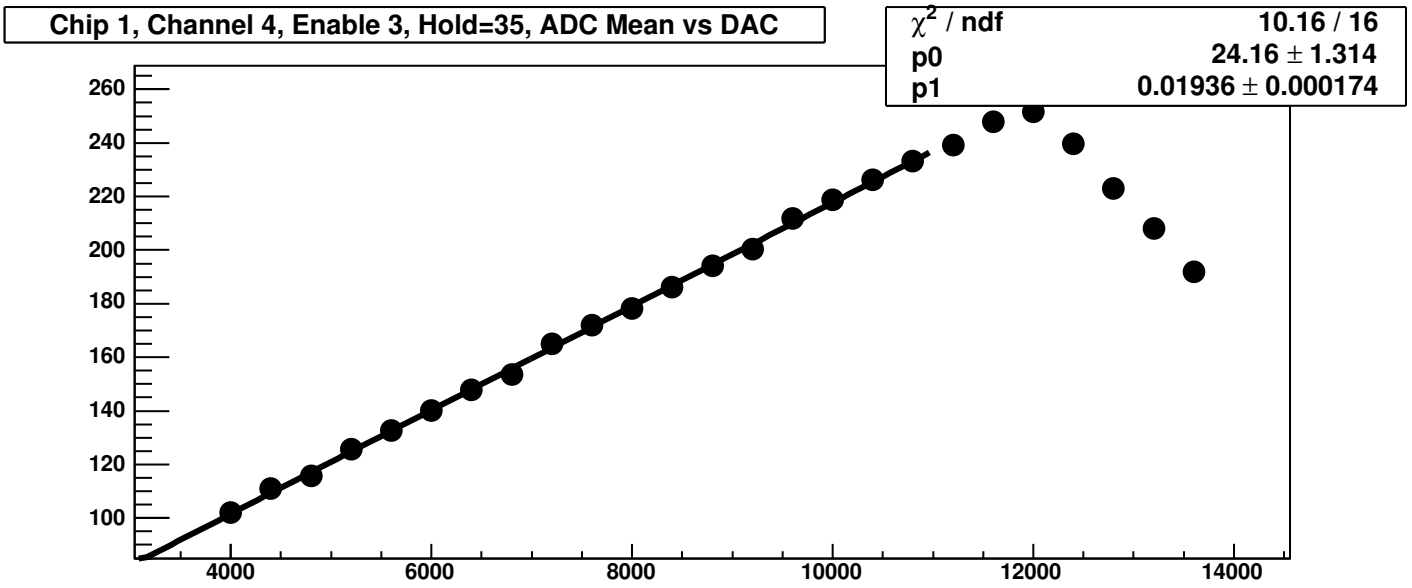


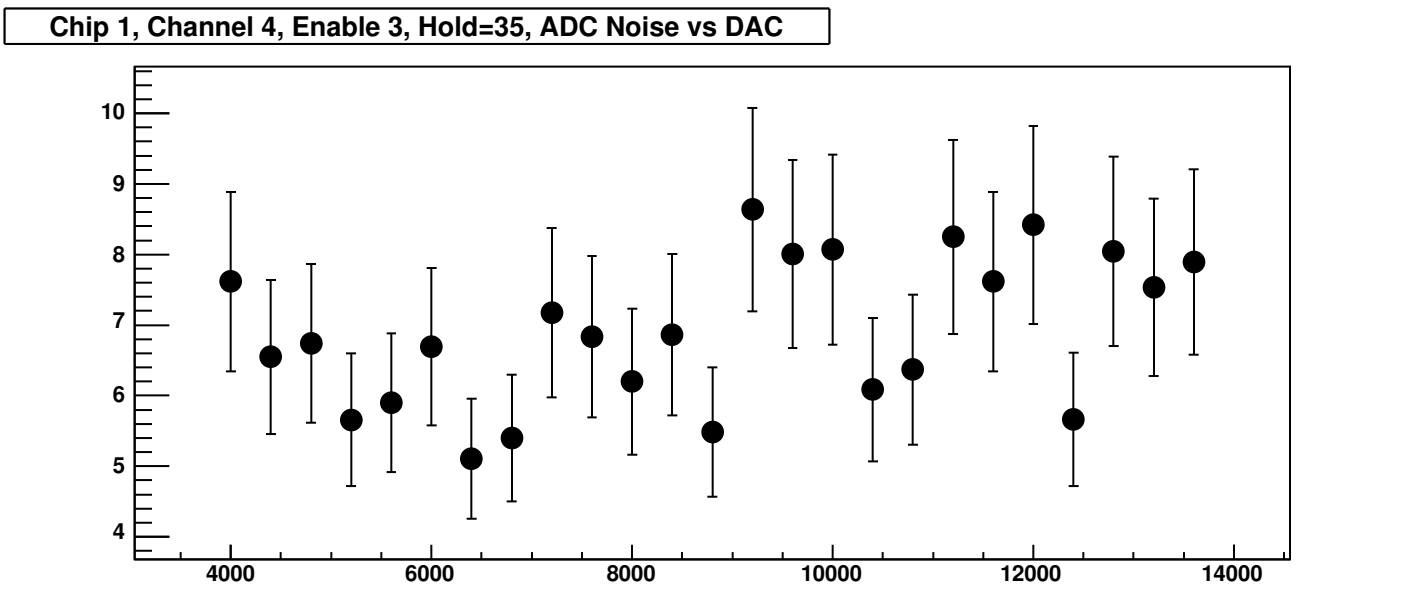
Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

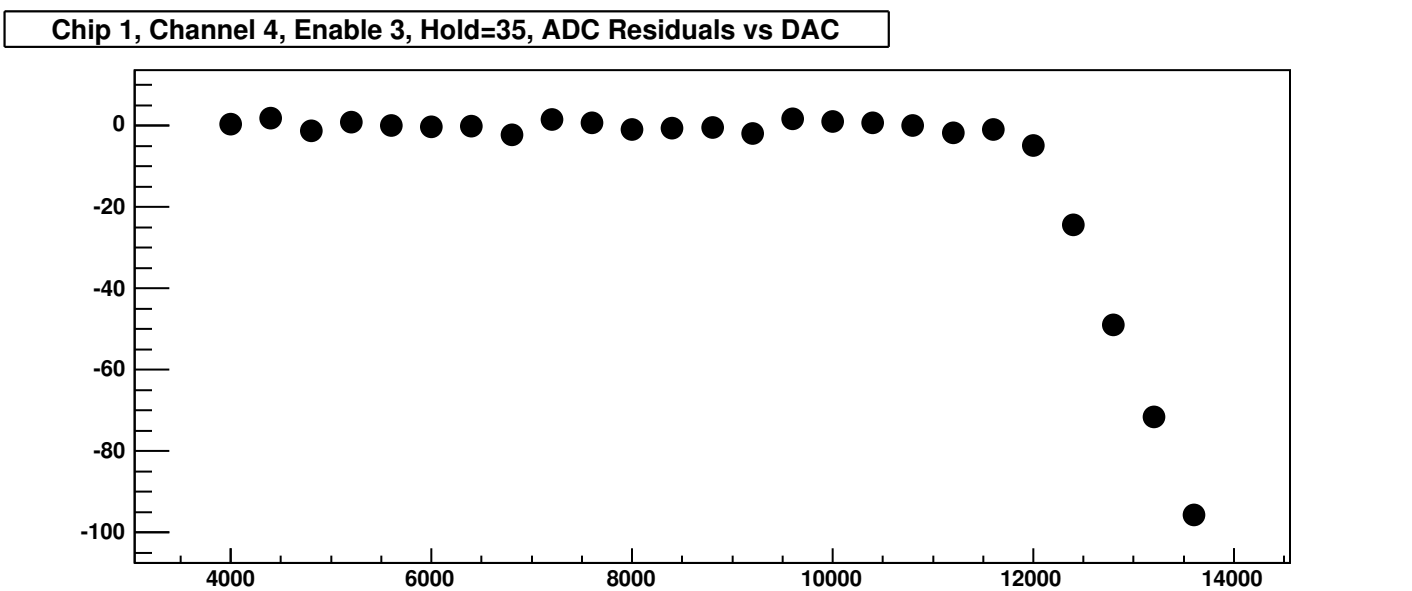


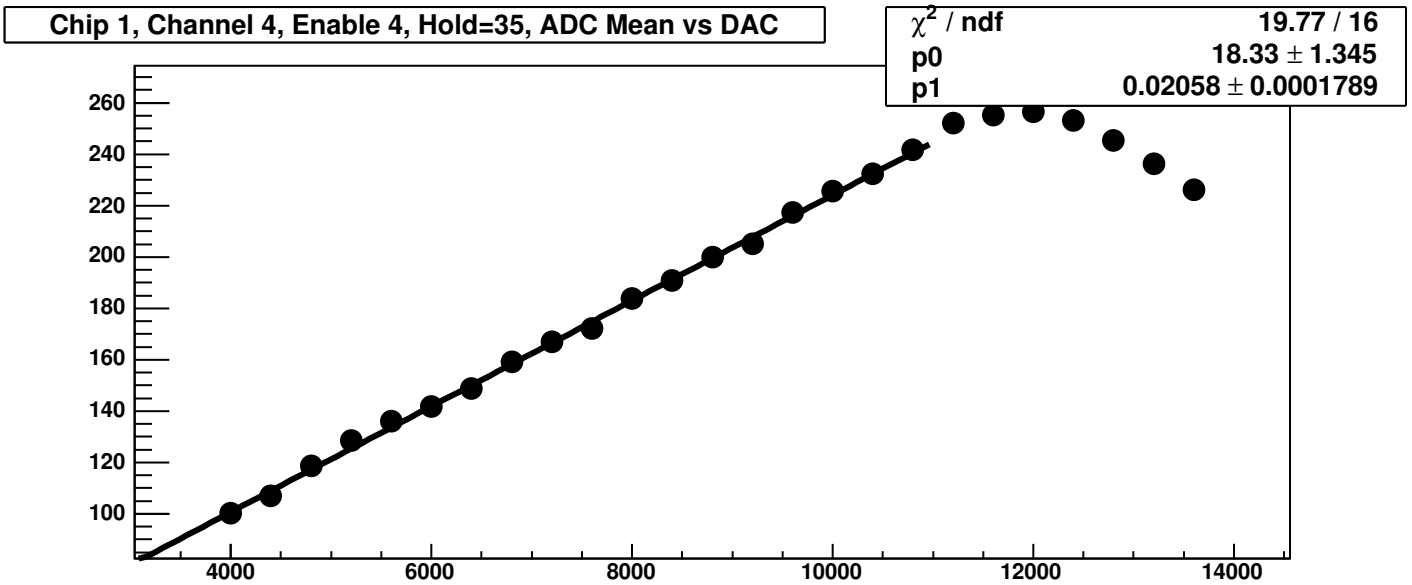
Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

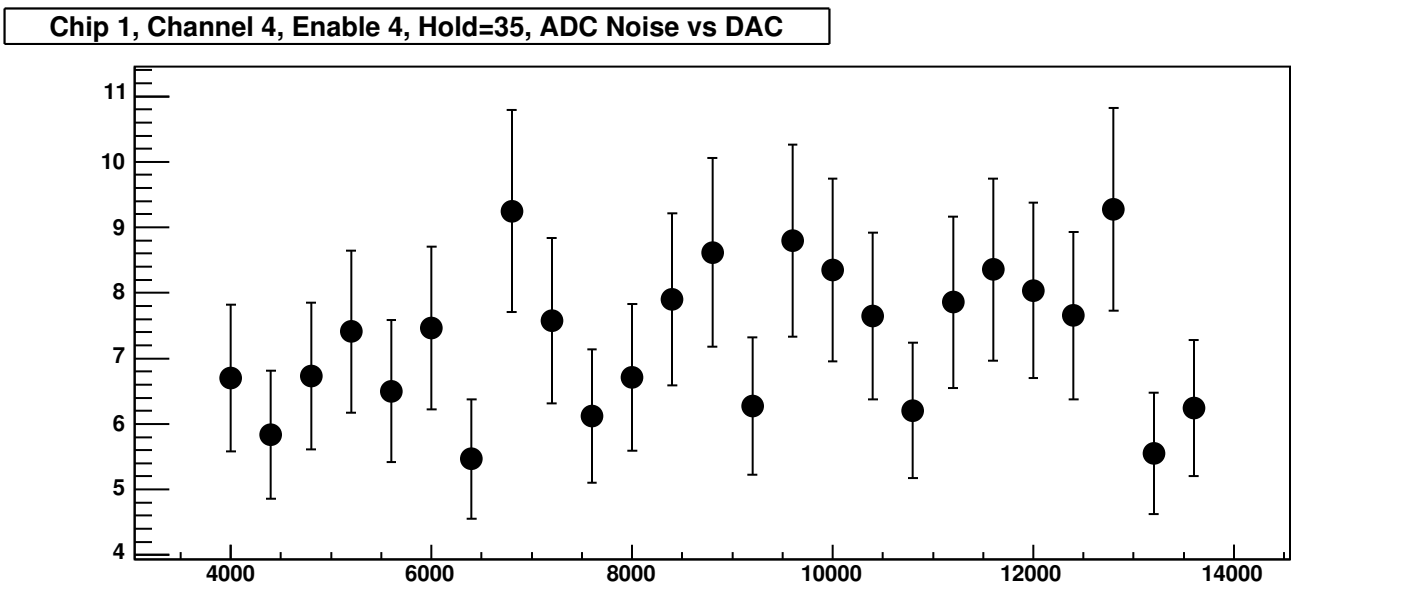


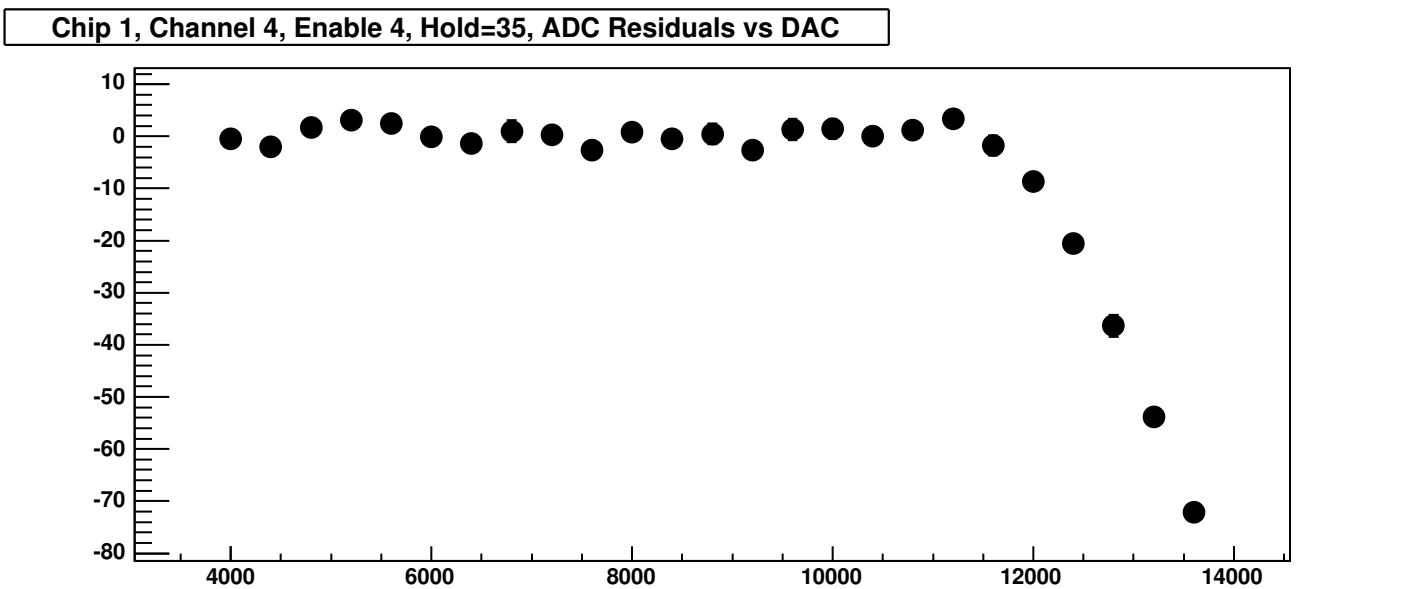

Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 10.16 / 16
p0 24.16 ± 1.314
p1 0.01936 ± 0.000174
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

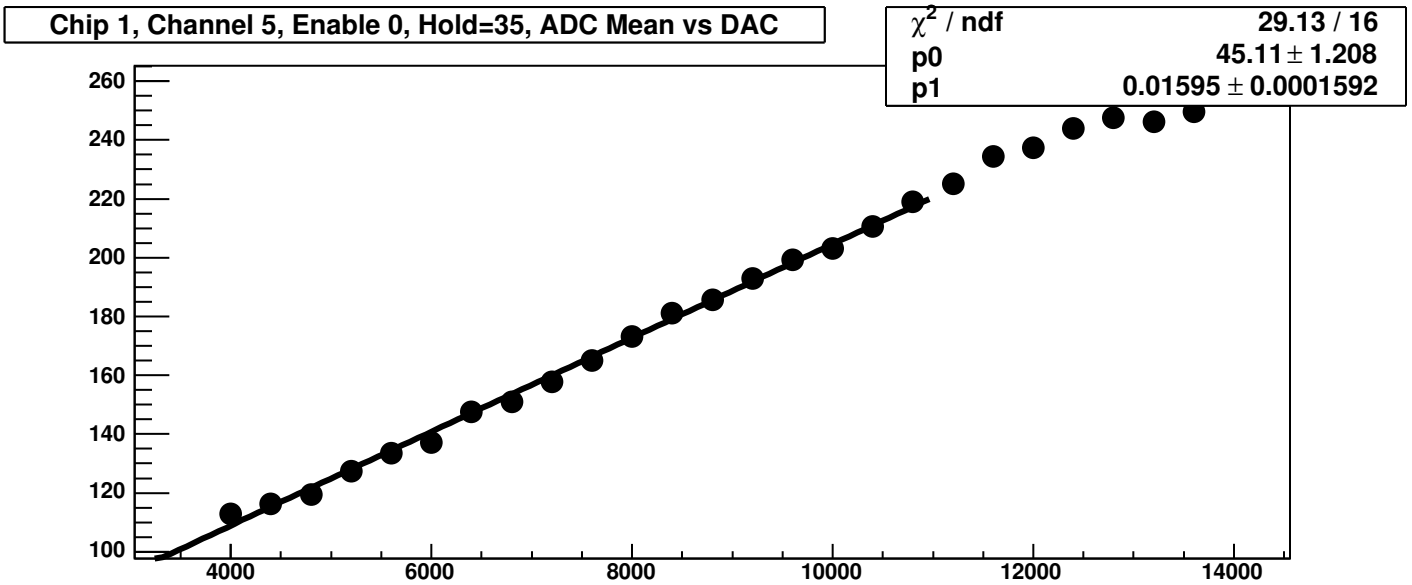

Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

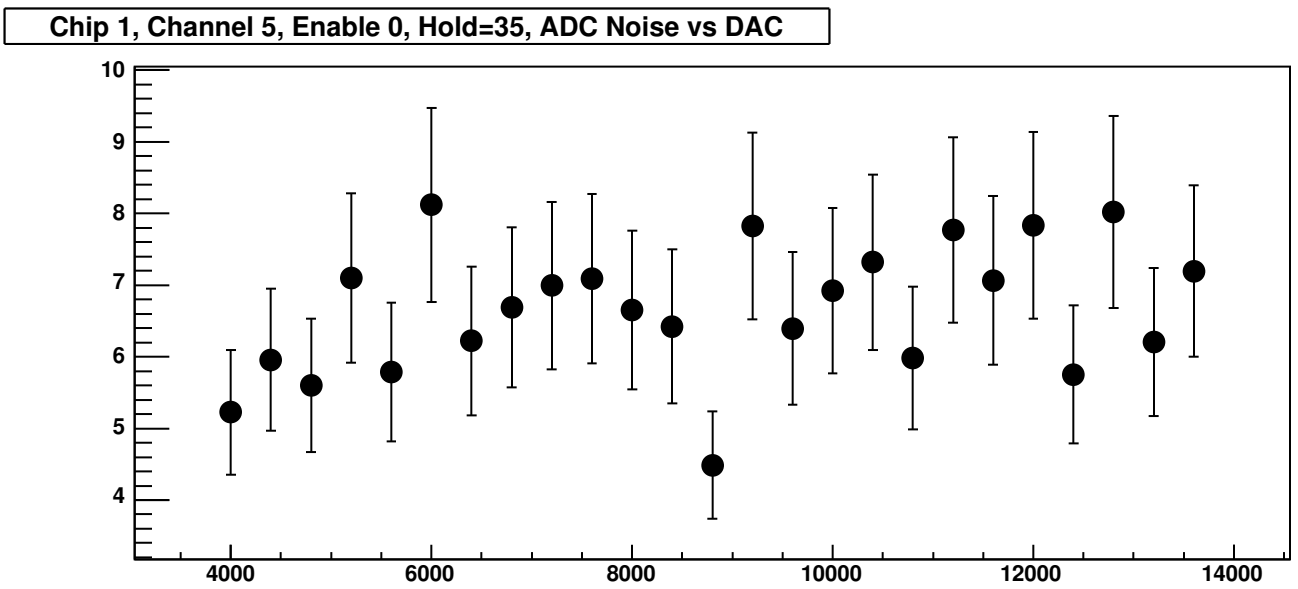

Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

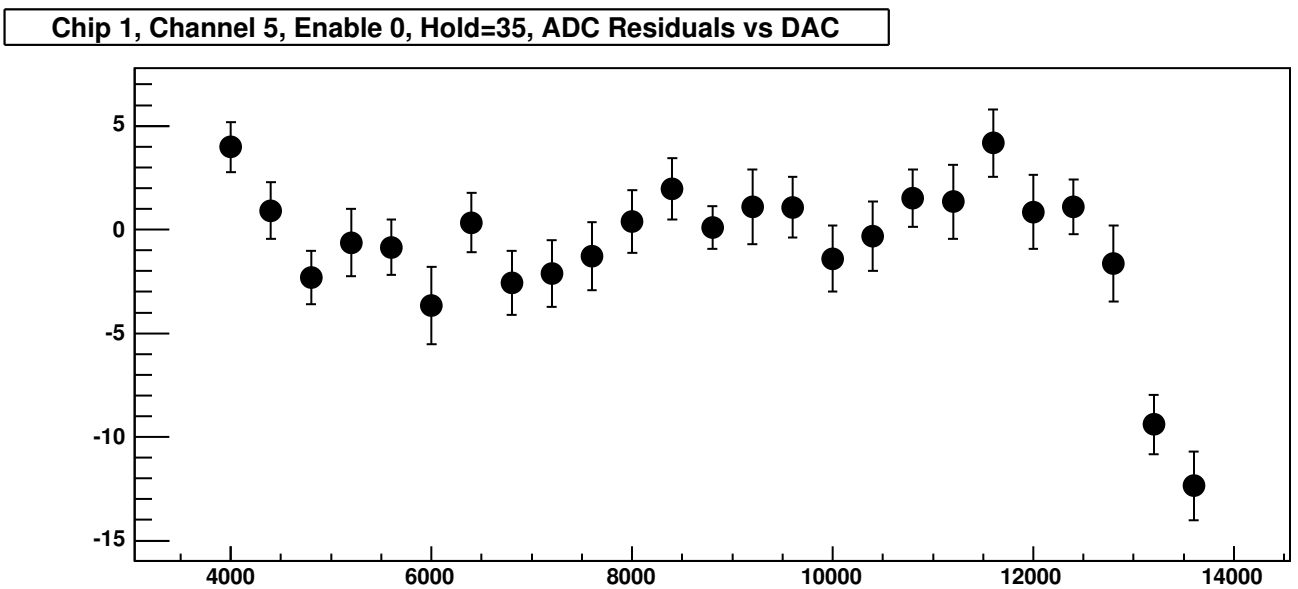

Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.77 / 16
p0 18.33 ± 1.345
p1 0.02058 ± 0.0001789
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

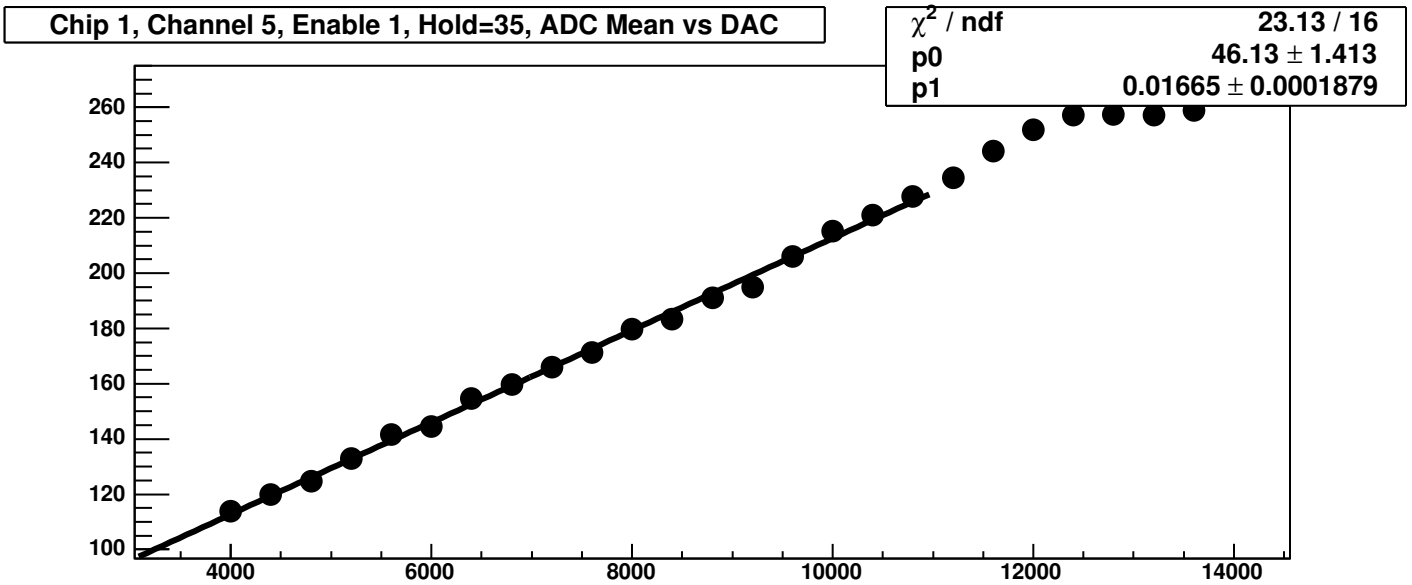

Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

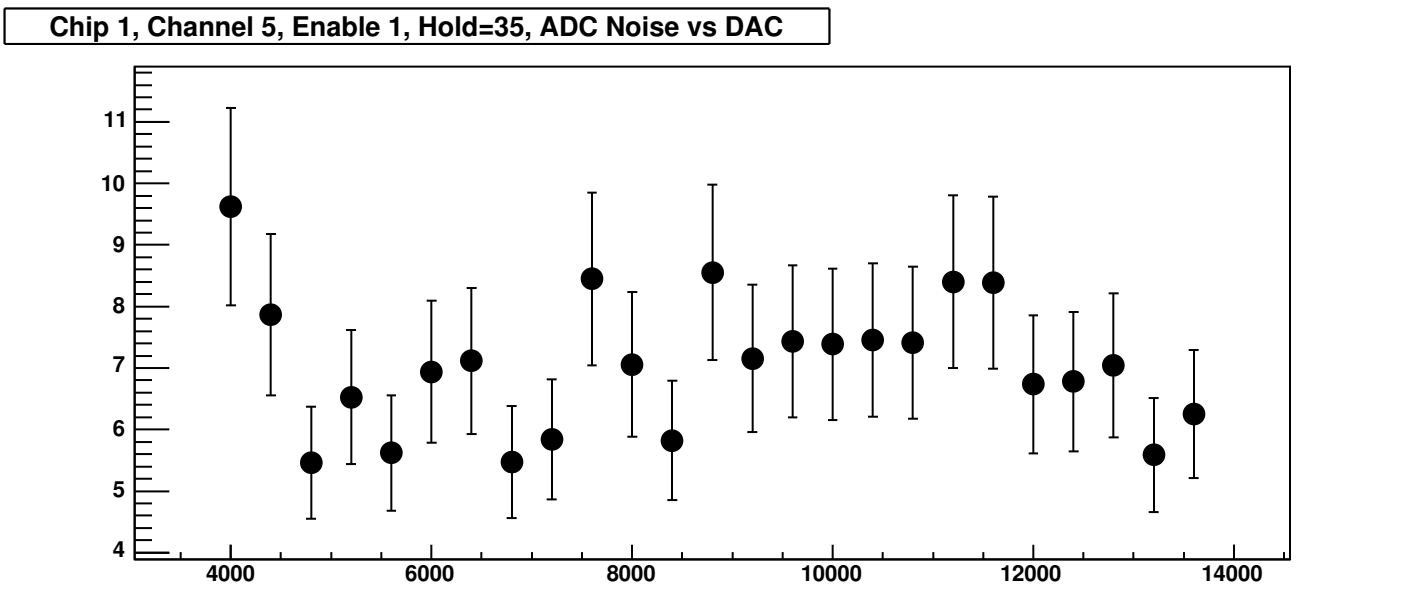

Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
-80
4000
6000
8000
10000
12000
14000

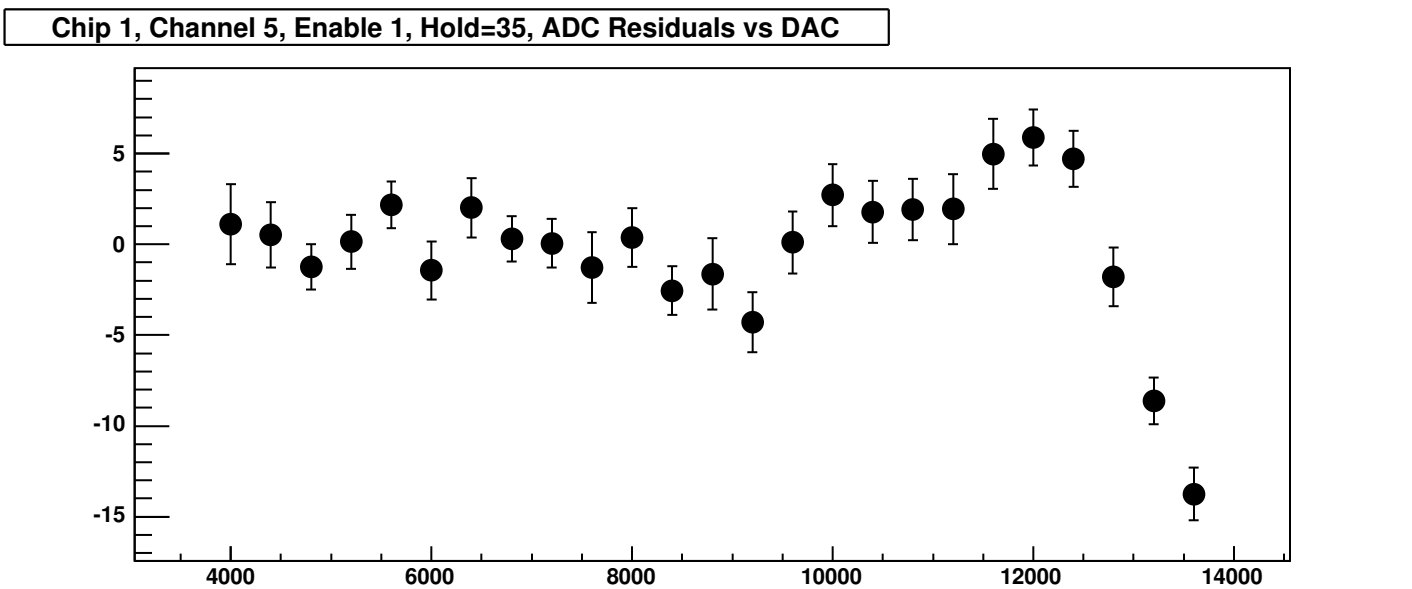

Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 29.13 / 16
p0 45.11 ± 1.208
p1 0.01595 ± 0.0001592
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

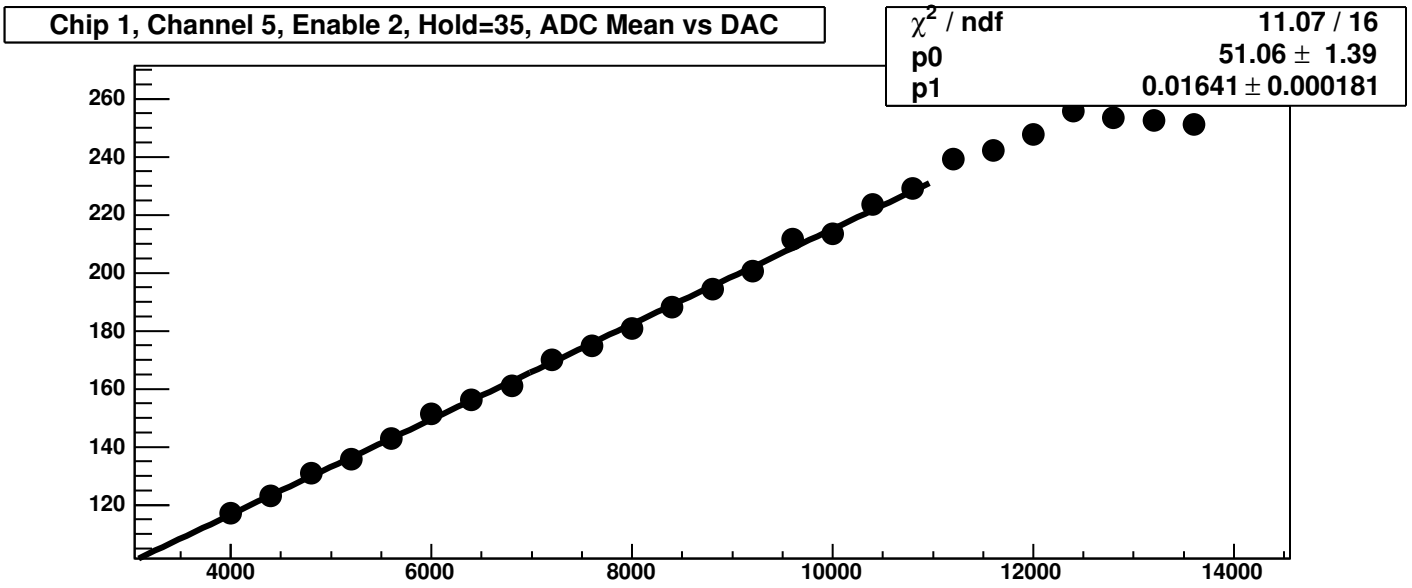

Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

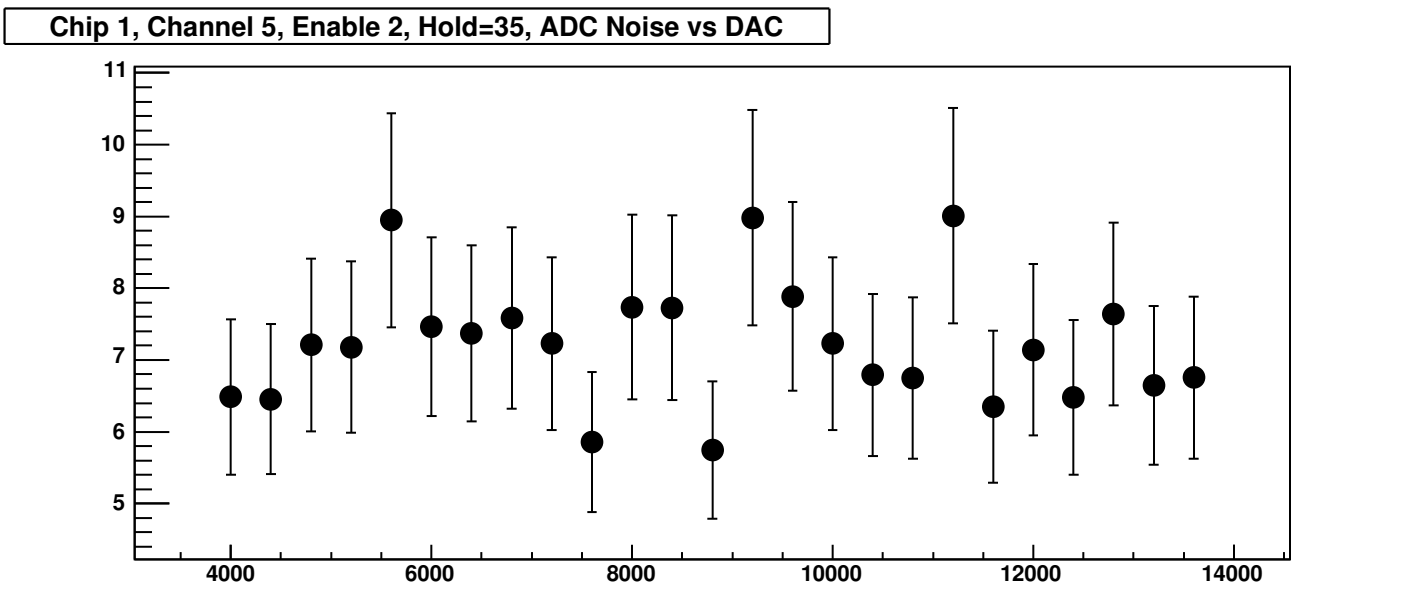

Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

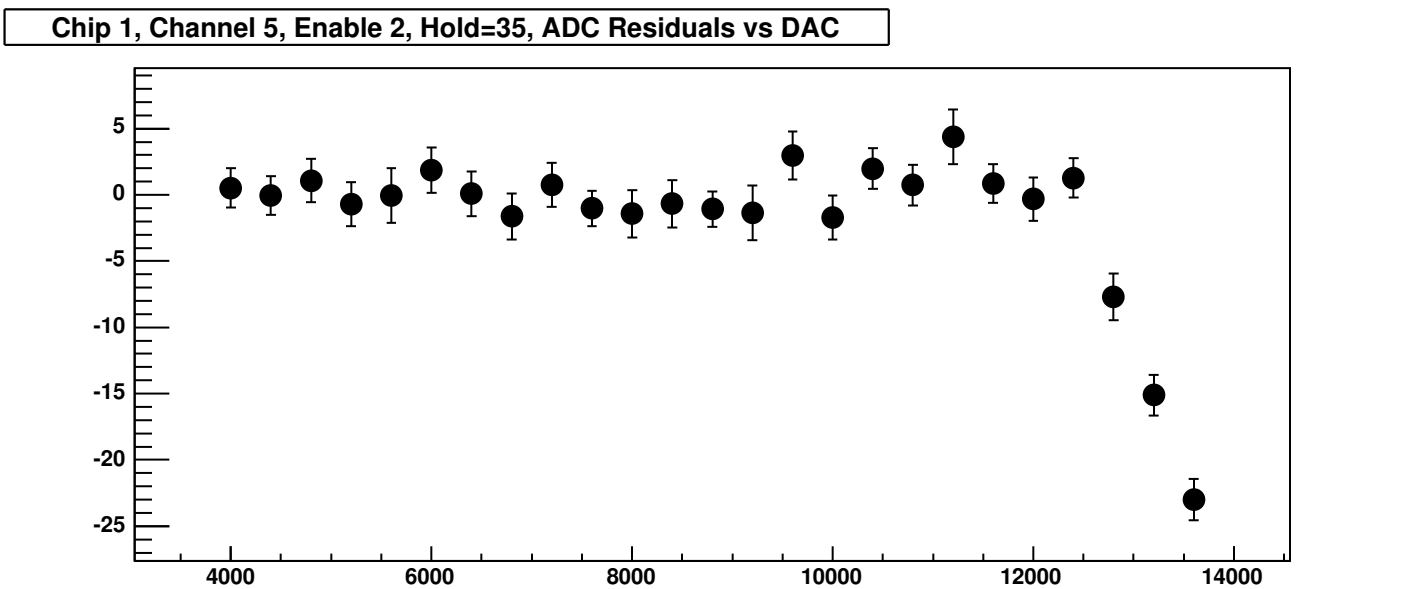

Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.13 / 16
p0 46.13 ± 1.413
p1 0.01665 ± 0.0001879
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

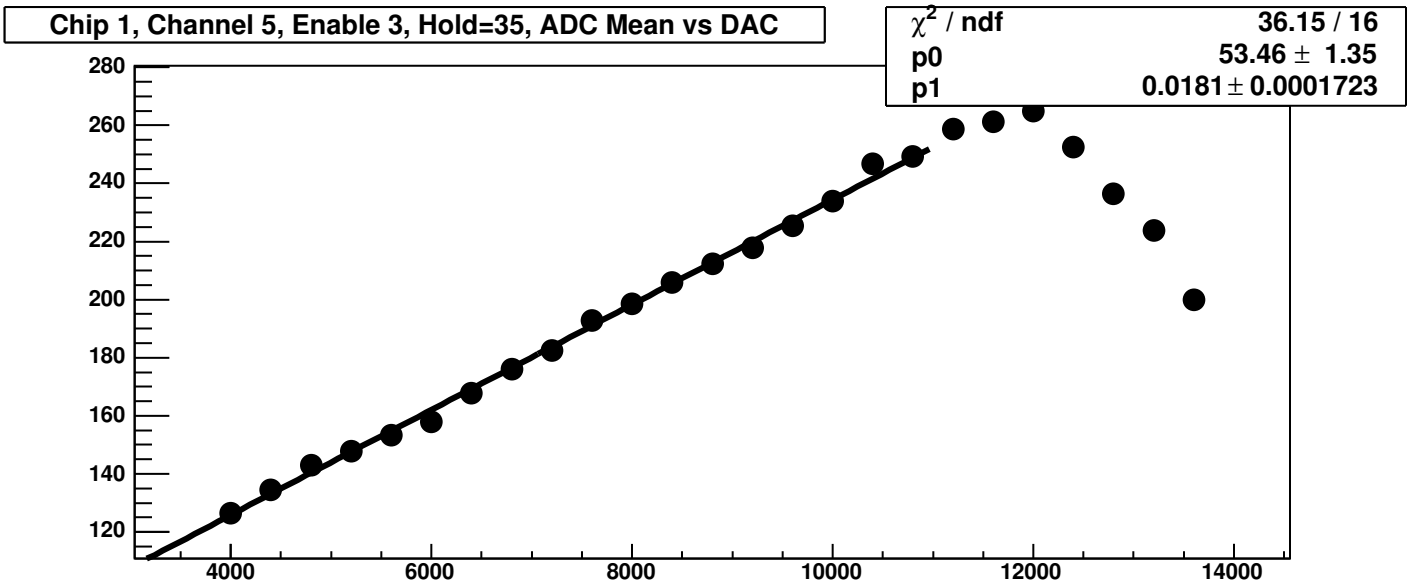

Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

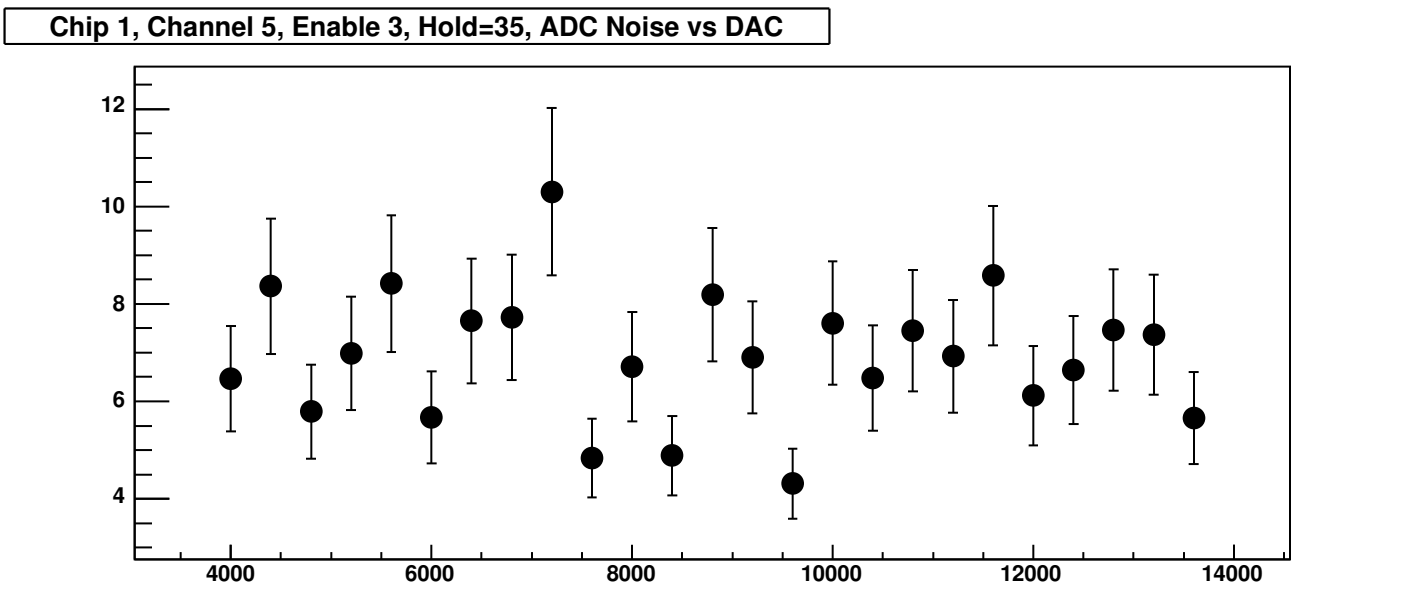

Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

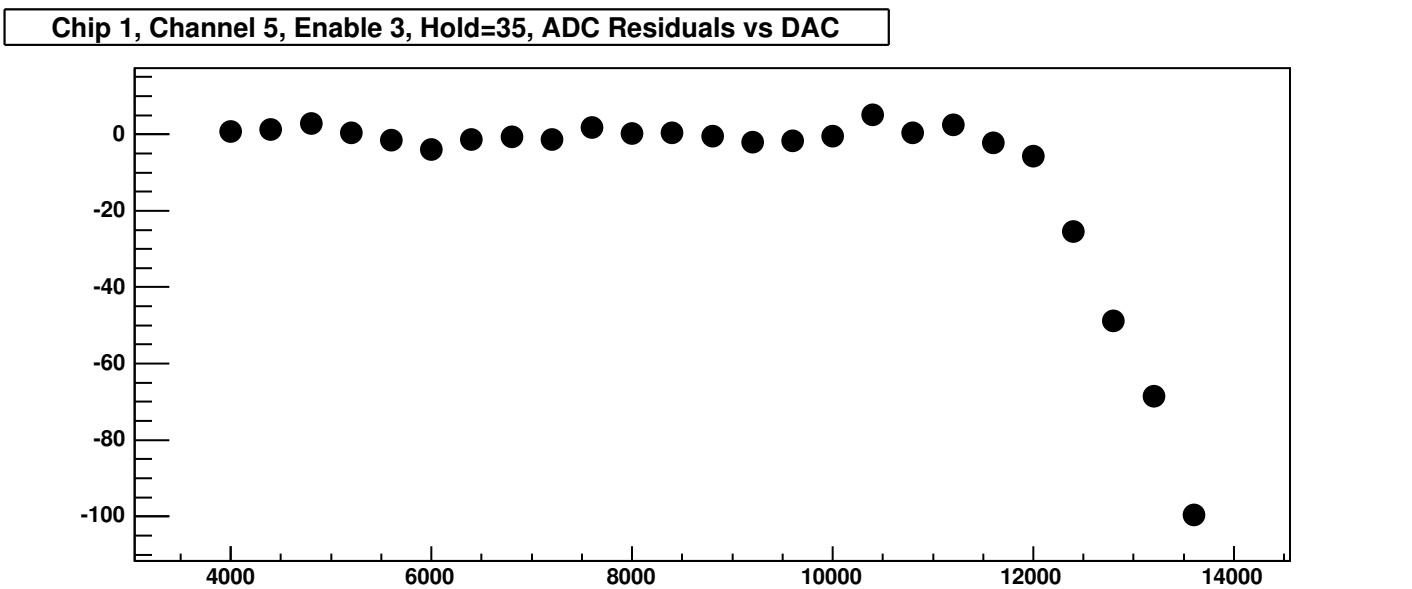

Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 11.07 / 16
p0 51.06 ± 1.39
p1 0.01641 ± 0.000181
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000

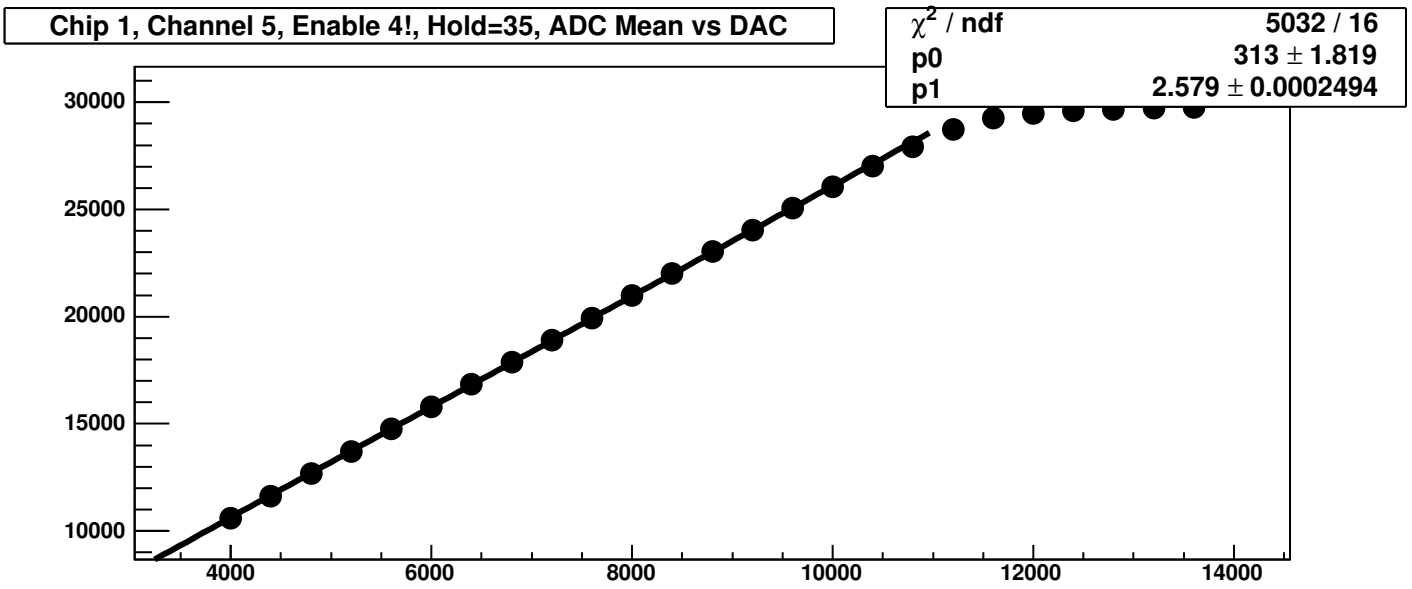

Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

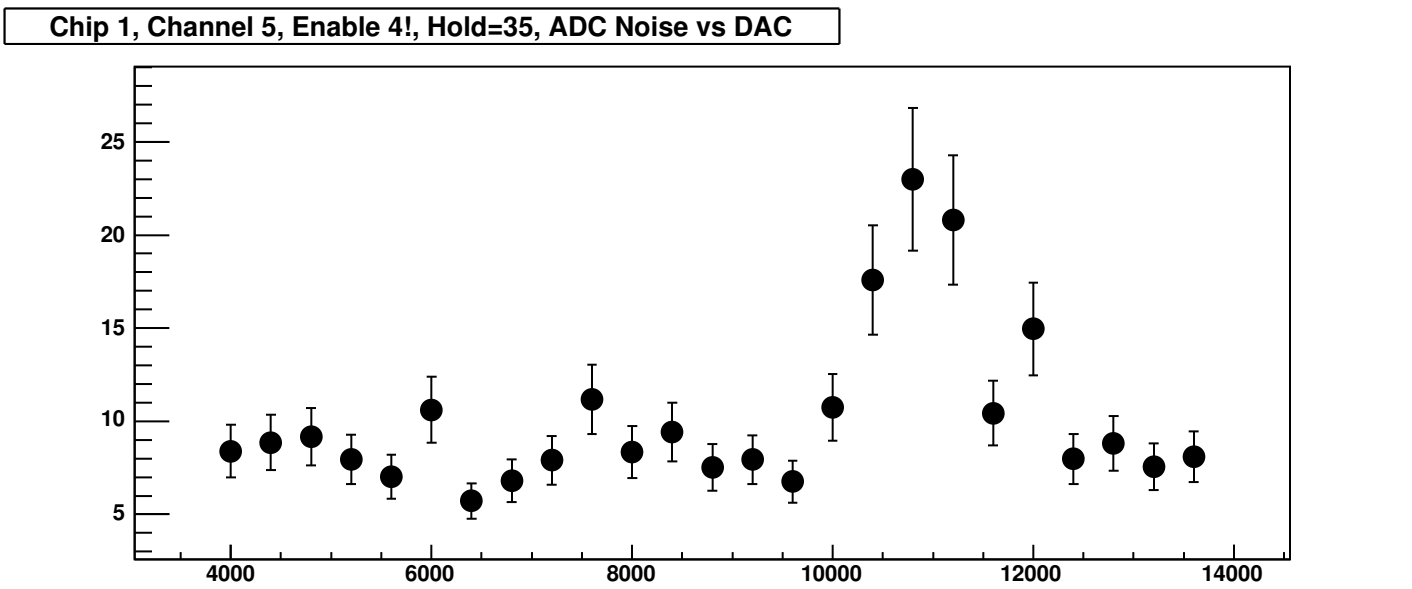

Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

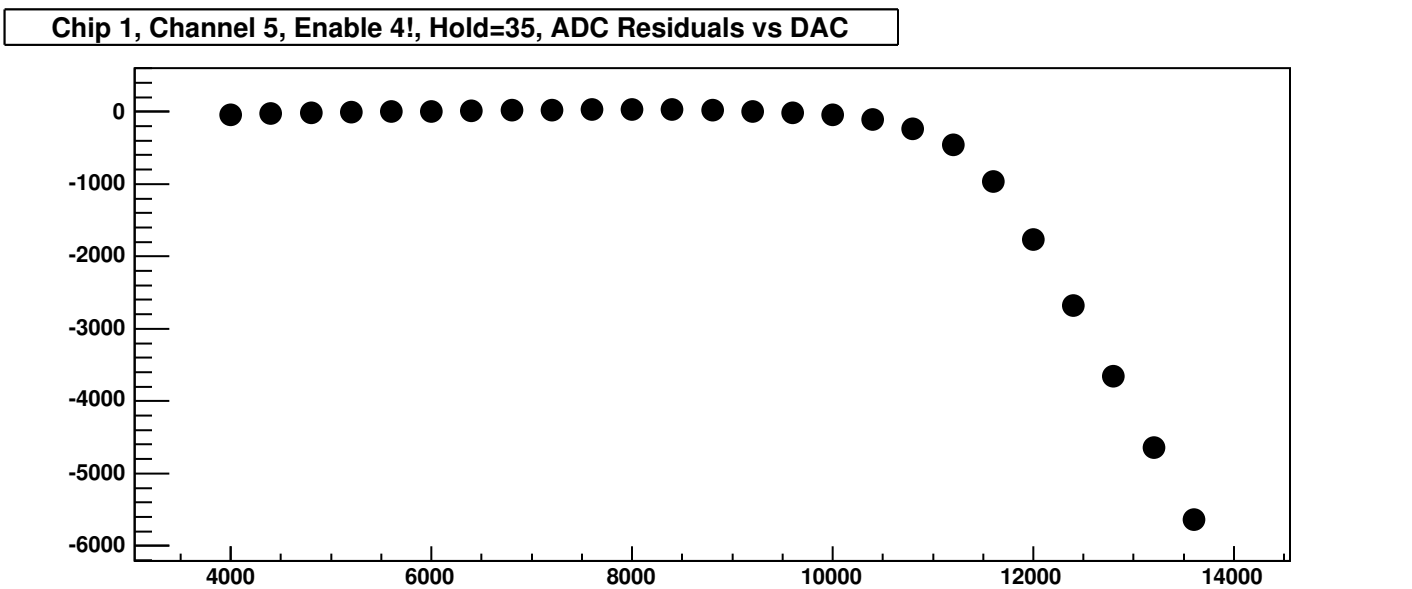

Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.15 / 16
p0 53.46 ± 1.35
p1 0.0181 ± 0.0001723
280
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000

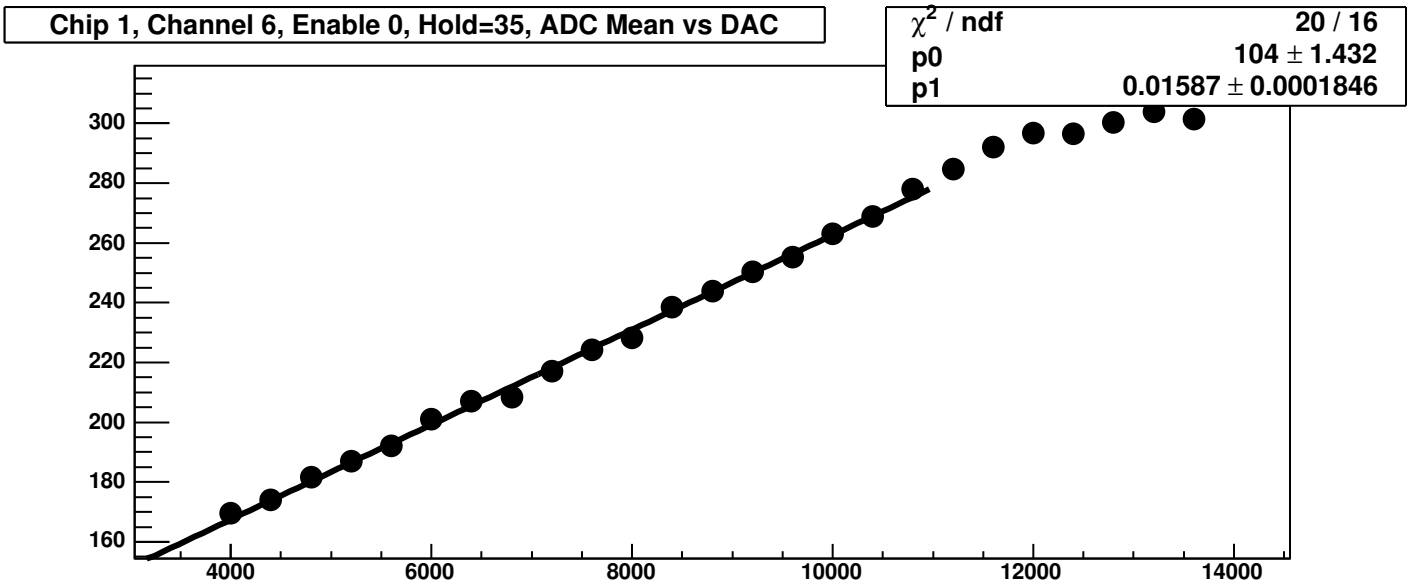

Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

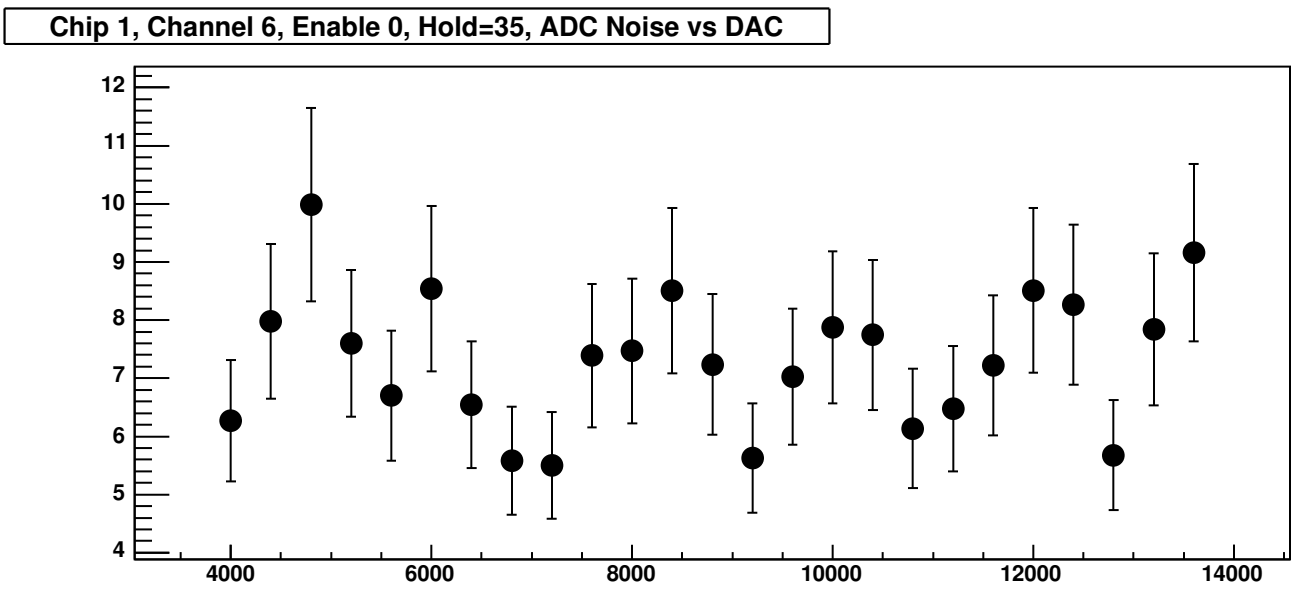

Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

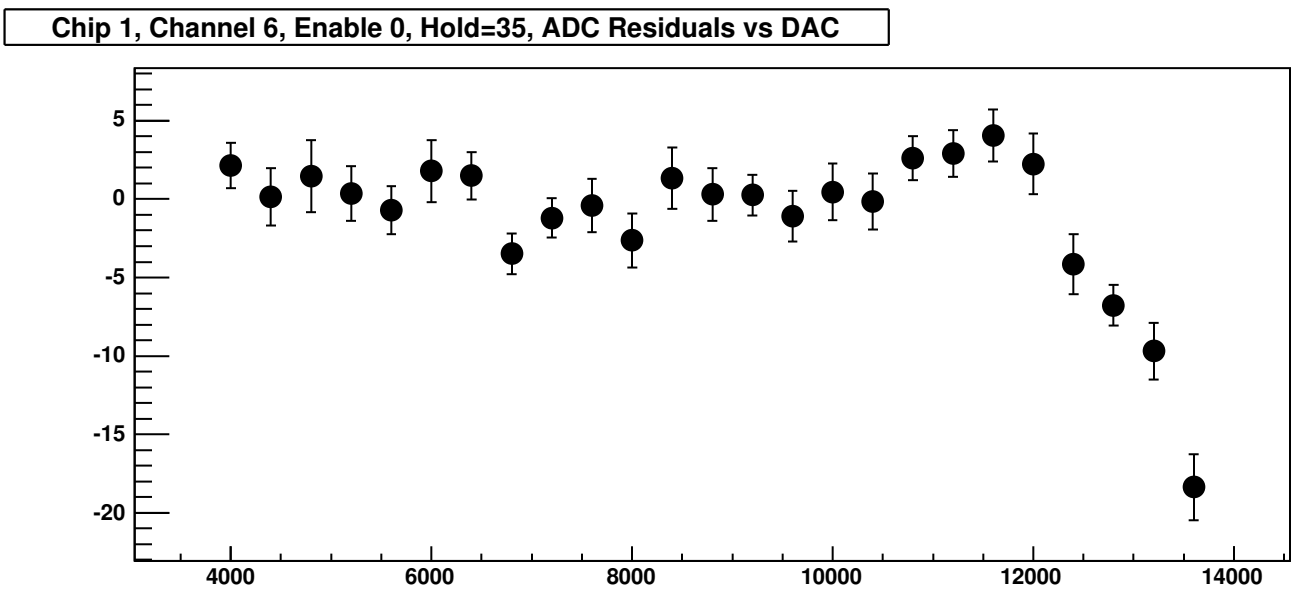

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 5032 / 16
p0 313 ± 1.819
p1 2.579 ± 0.0002494
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
4000
6000
8000
10000
12000
14000

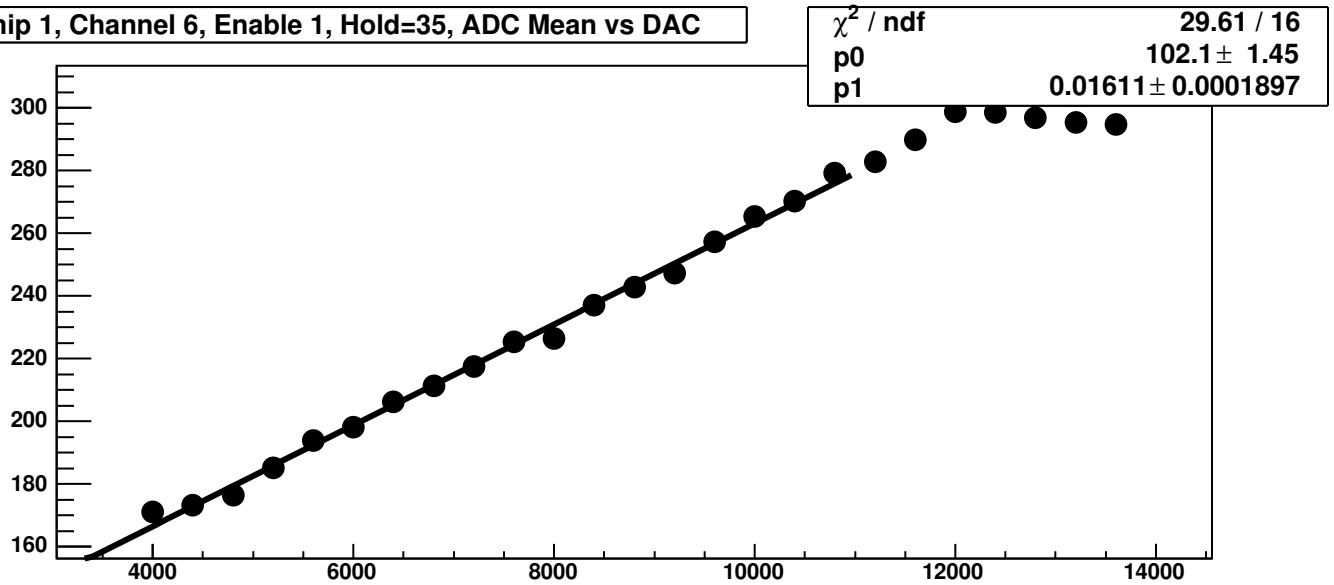

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20 / 16
p0 104 ± 1.432
p1 0.01587 ± 0.0001846
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

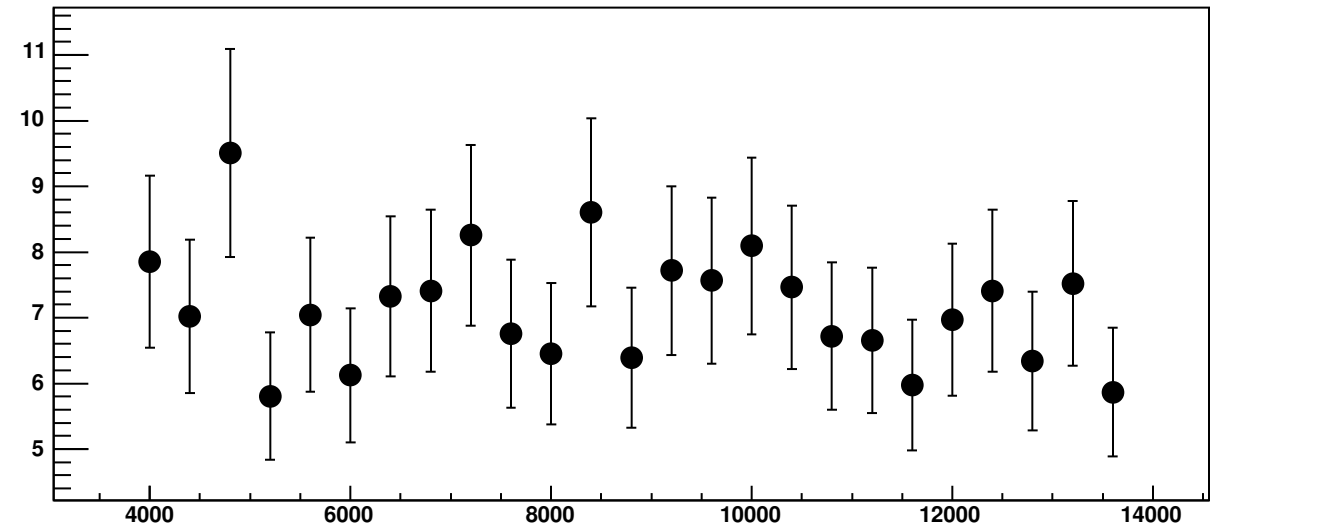

Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

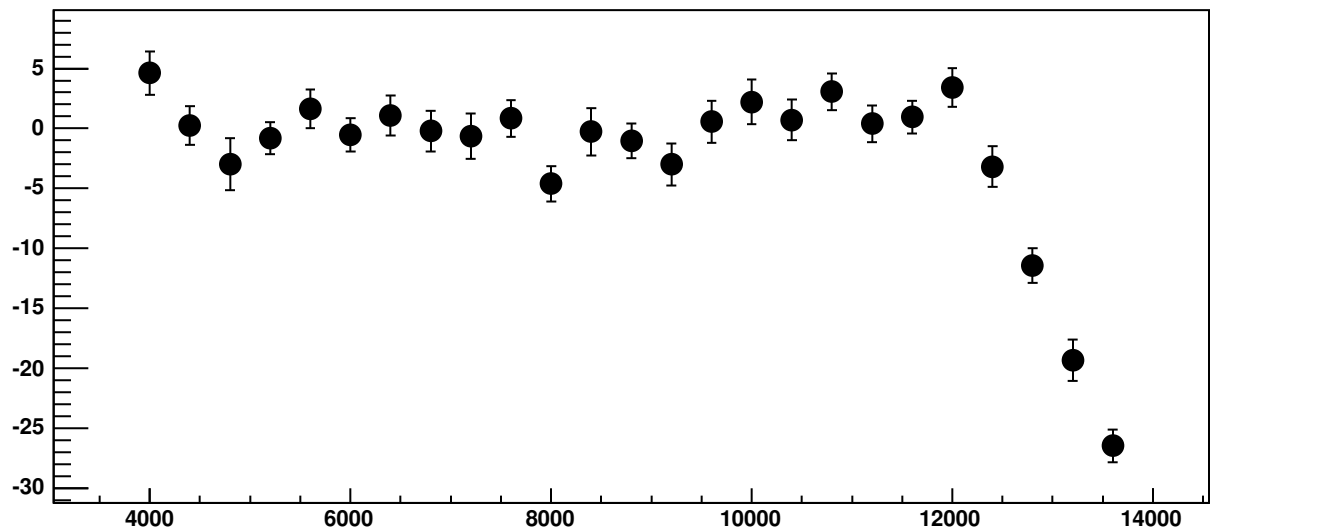
Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



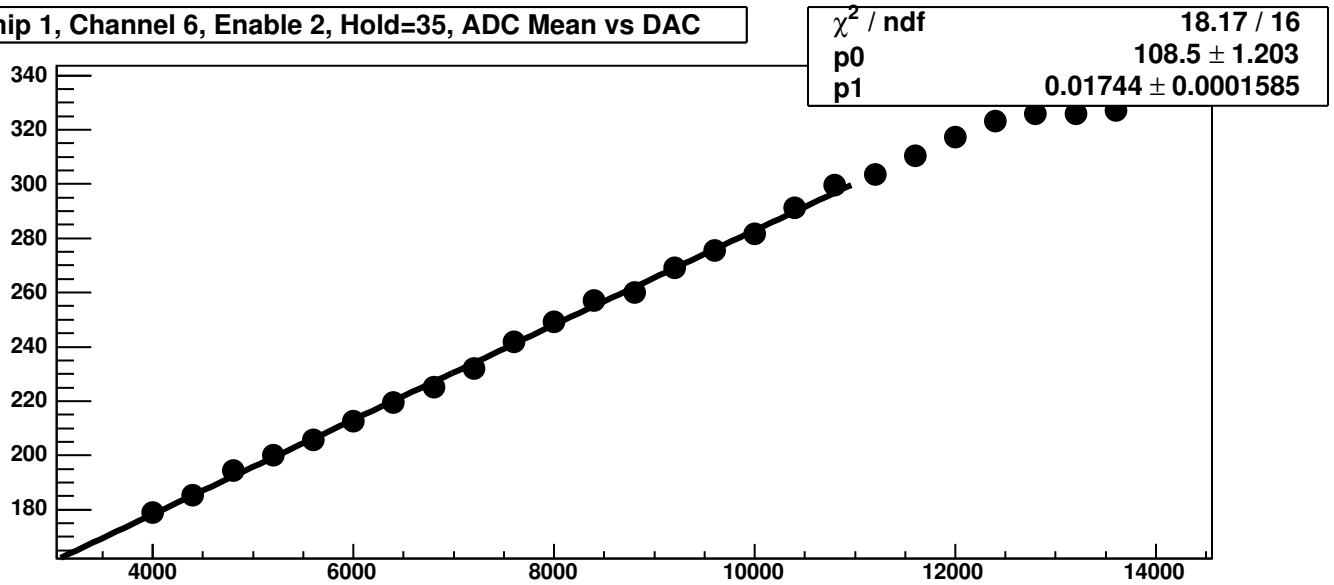
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



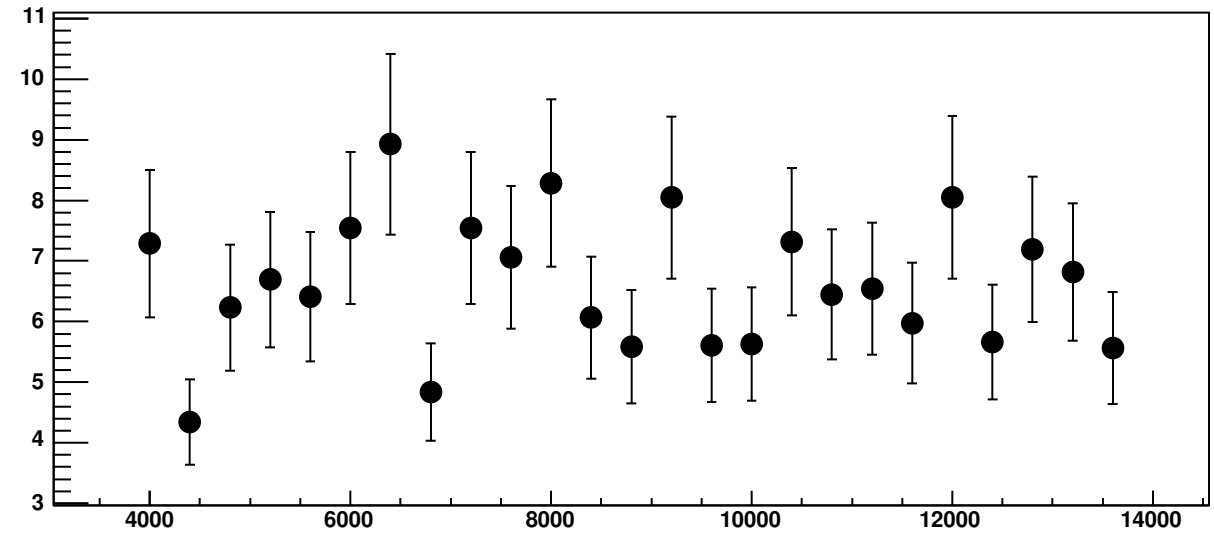
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



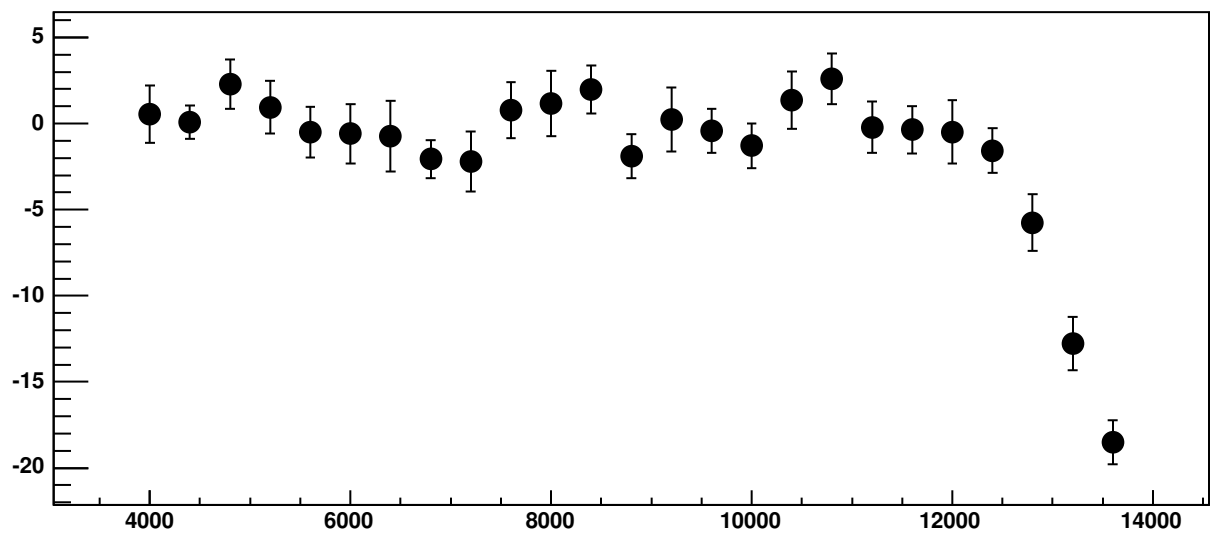
Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

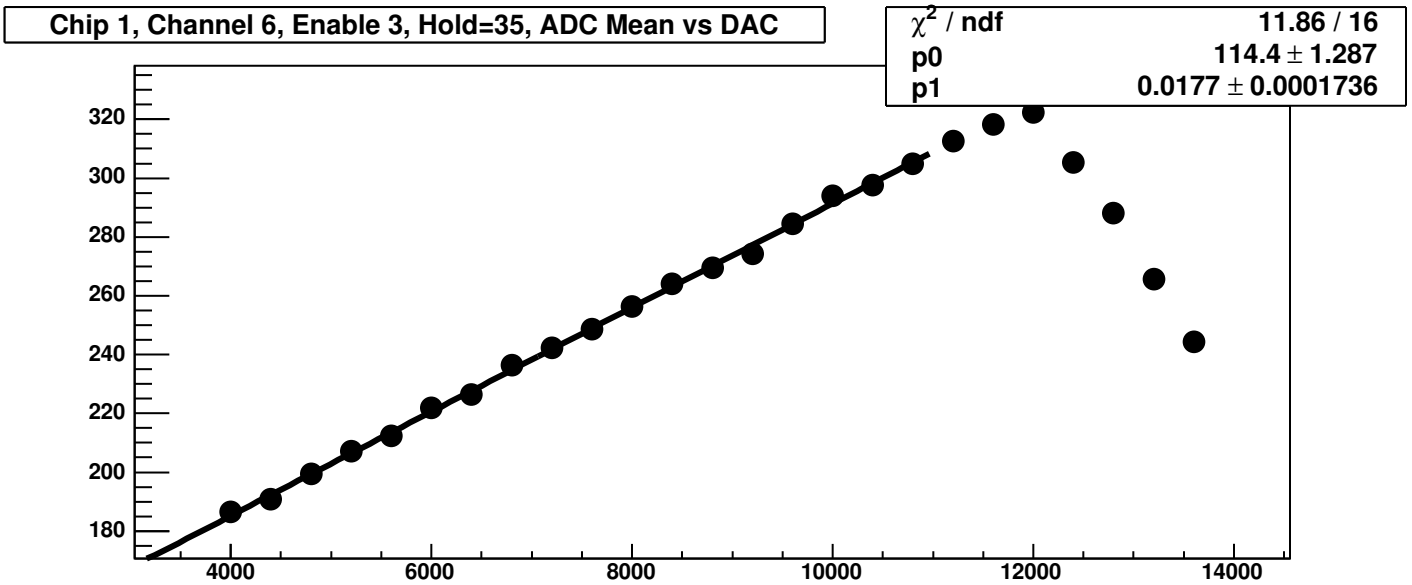


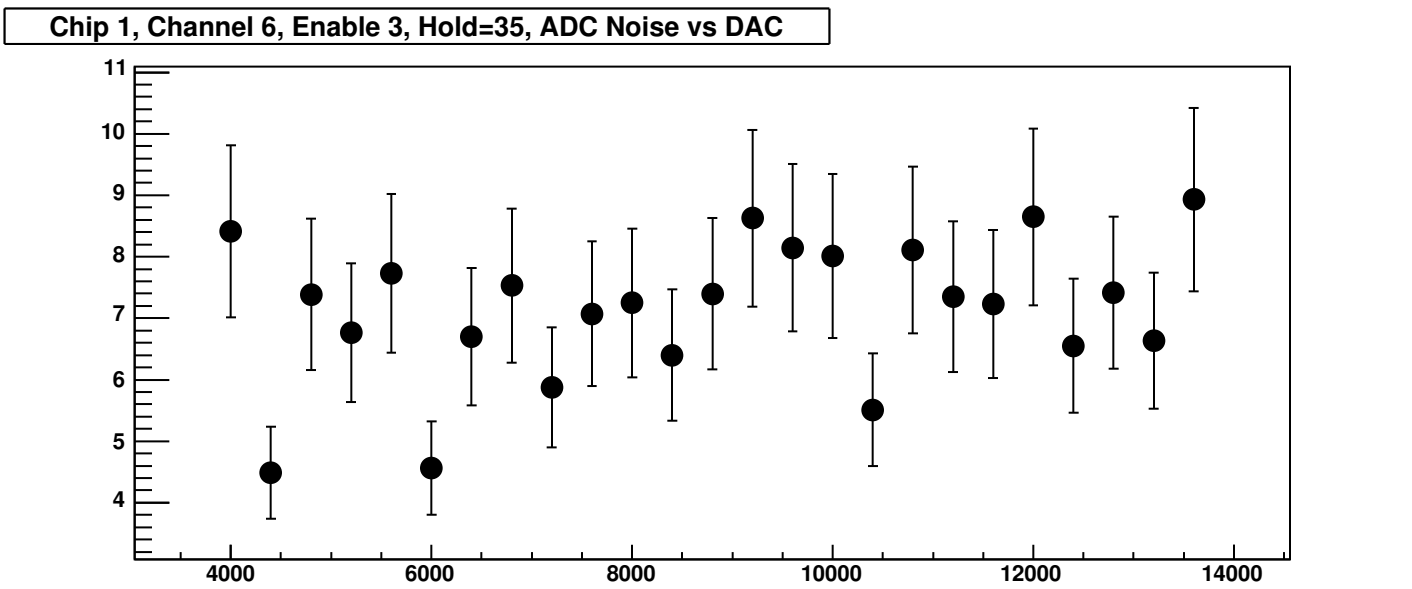
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC

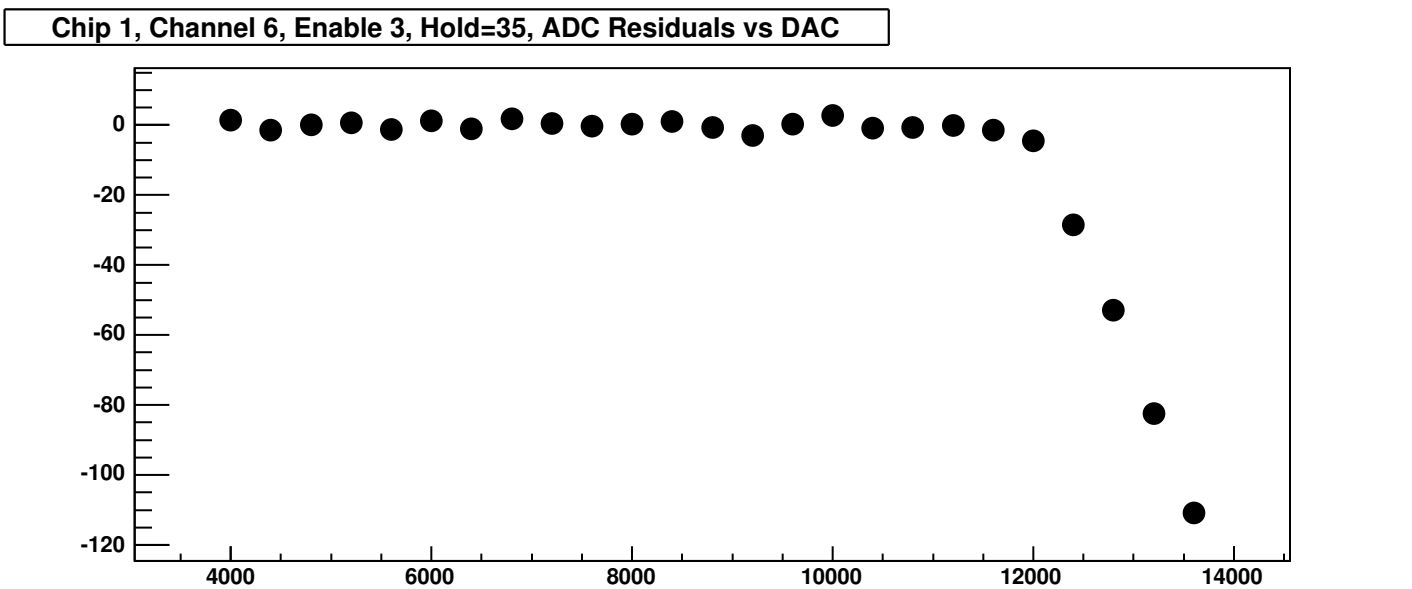


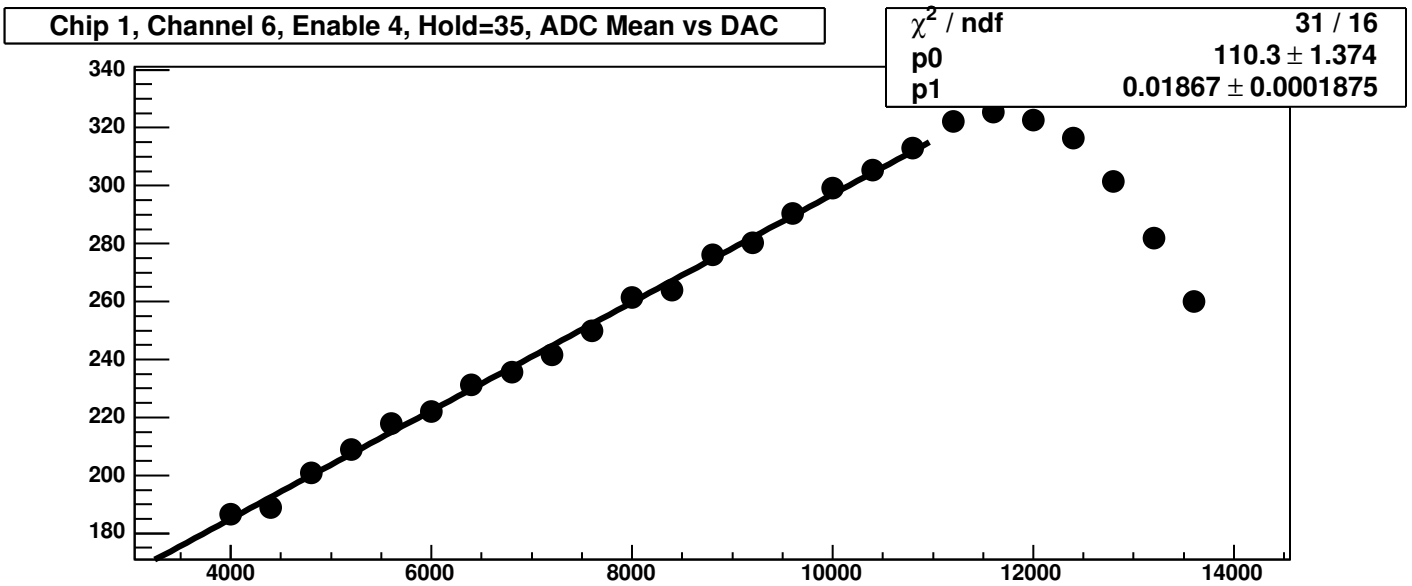
Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

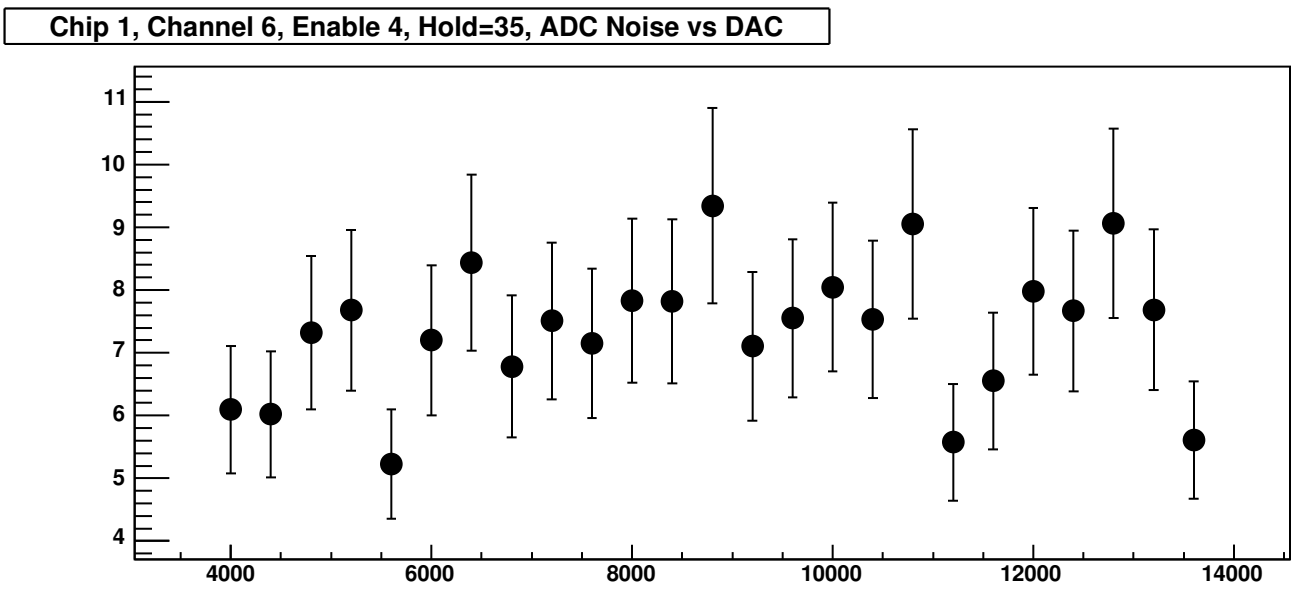


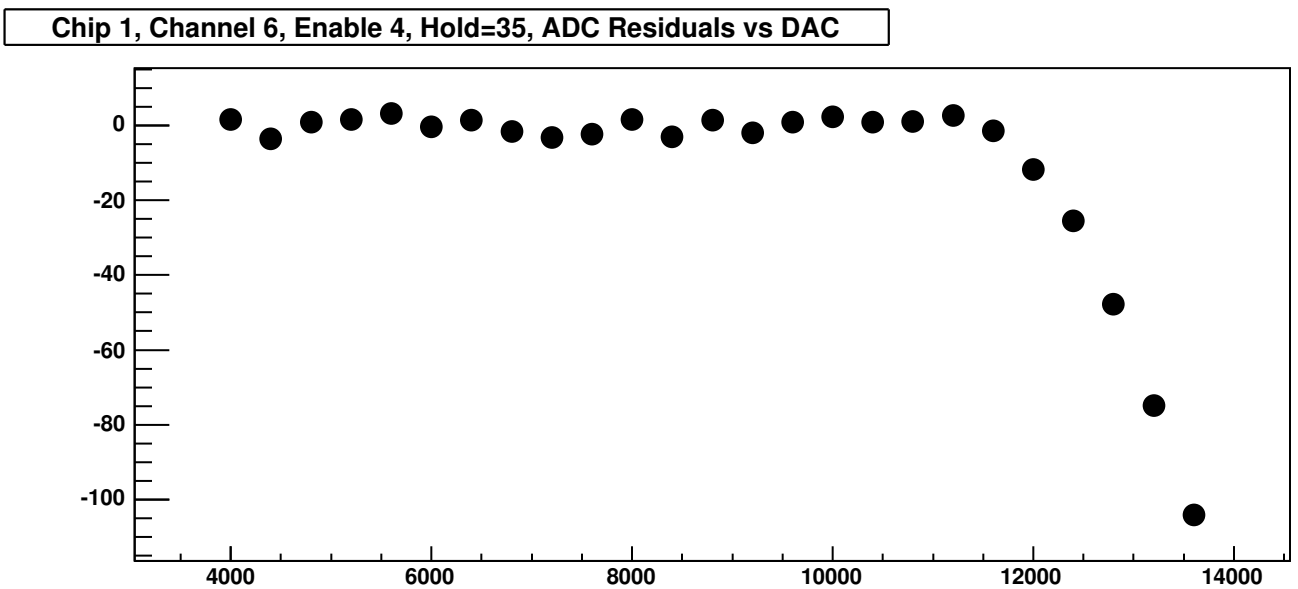

Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 11.86 / 16
p0 114.4 ± 1.287
p1 0.0177 ± 0.0001736
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

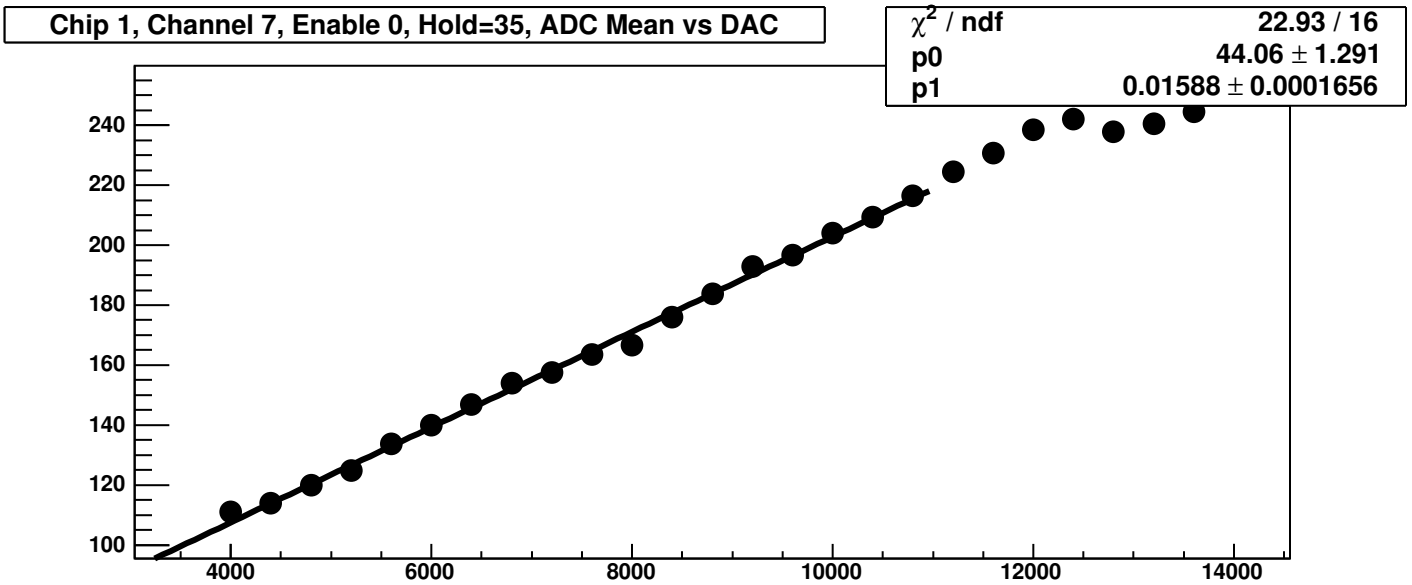

Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

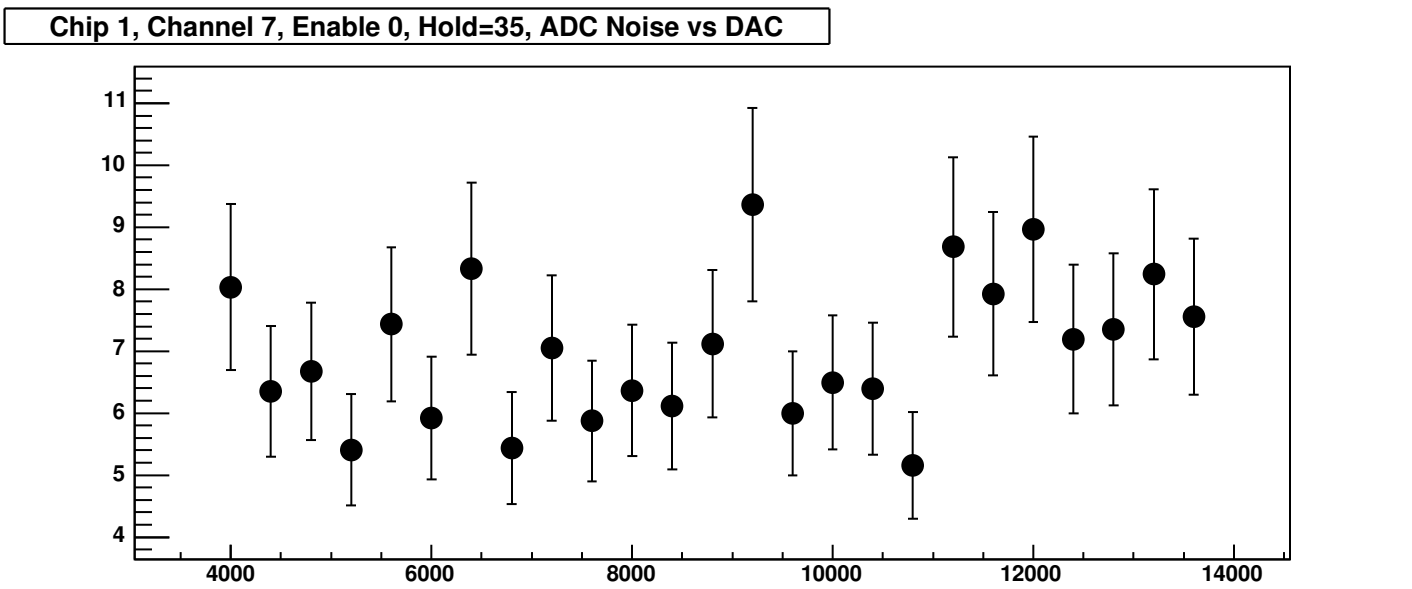

Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000

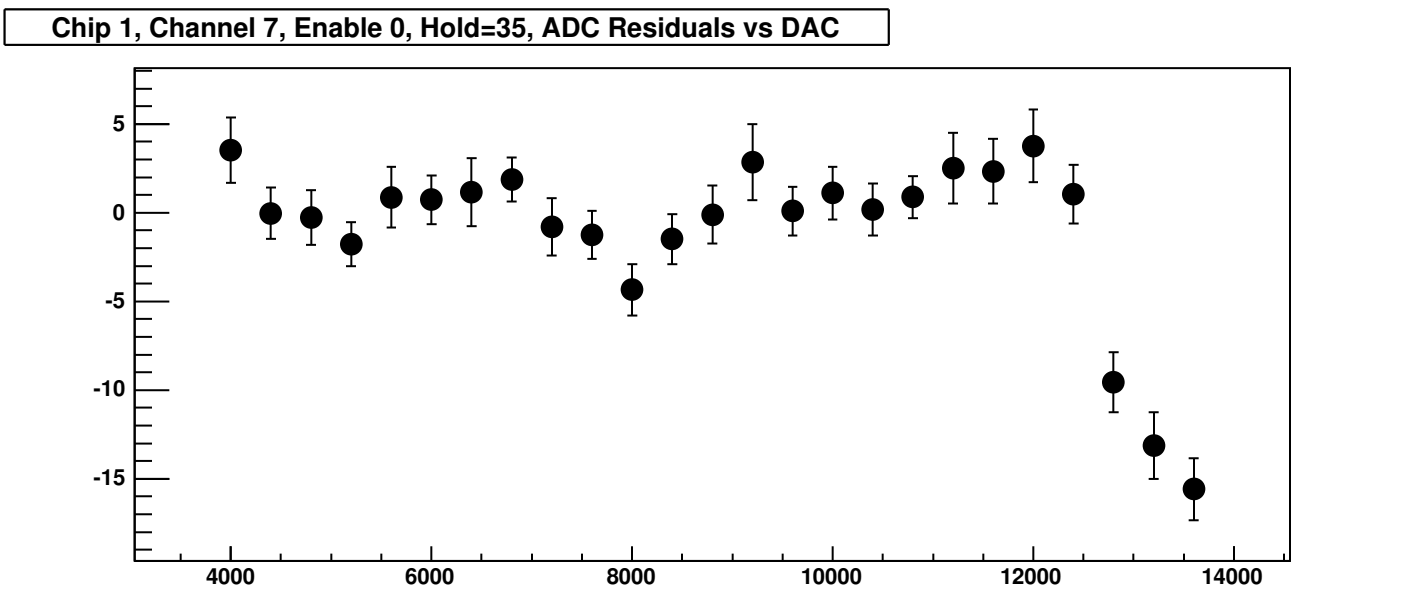

Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31 / 16
p0 110.3 ± 1.374
p1 0.01867 ± 0.0001875
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

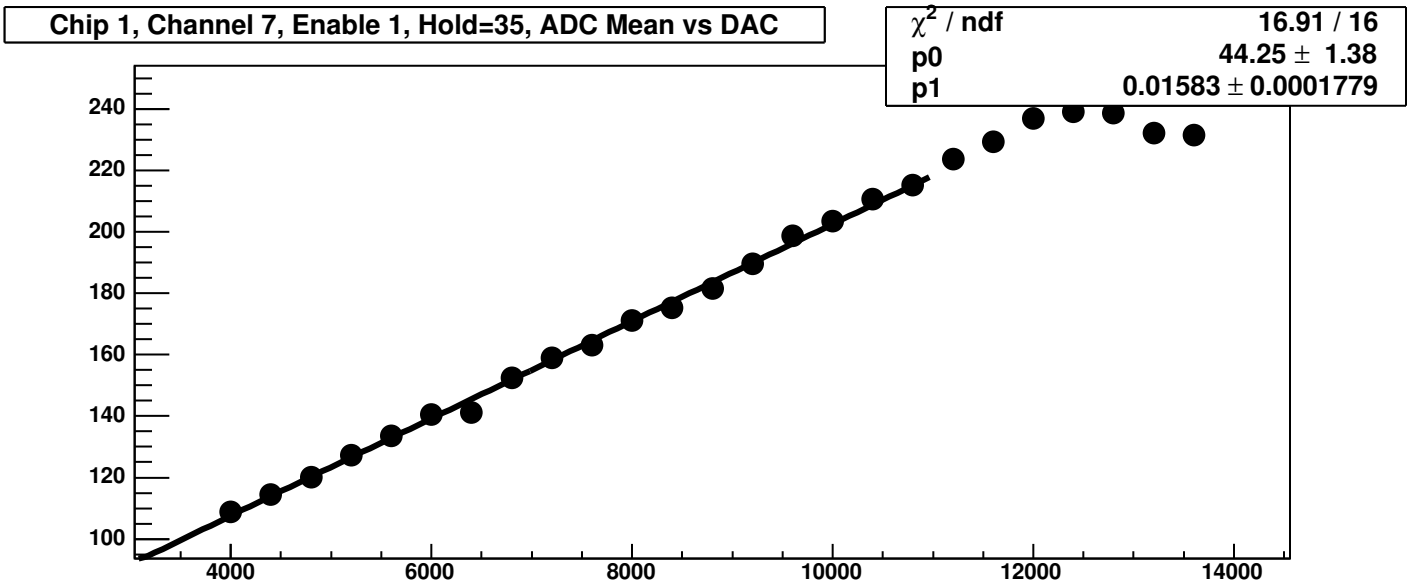

Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

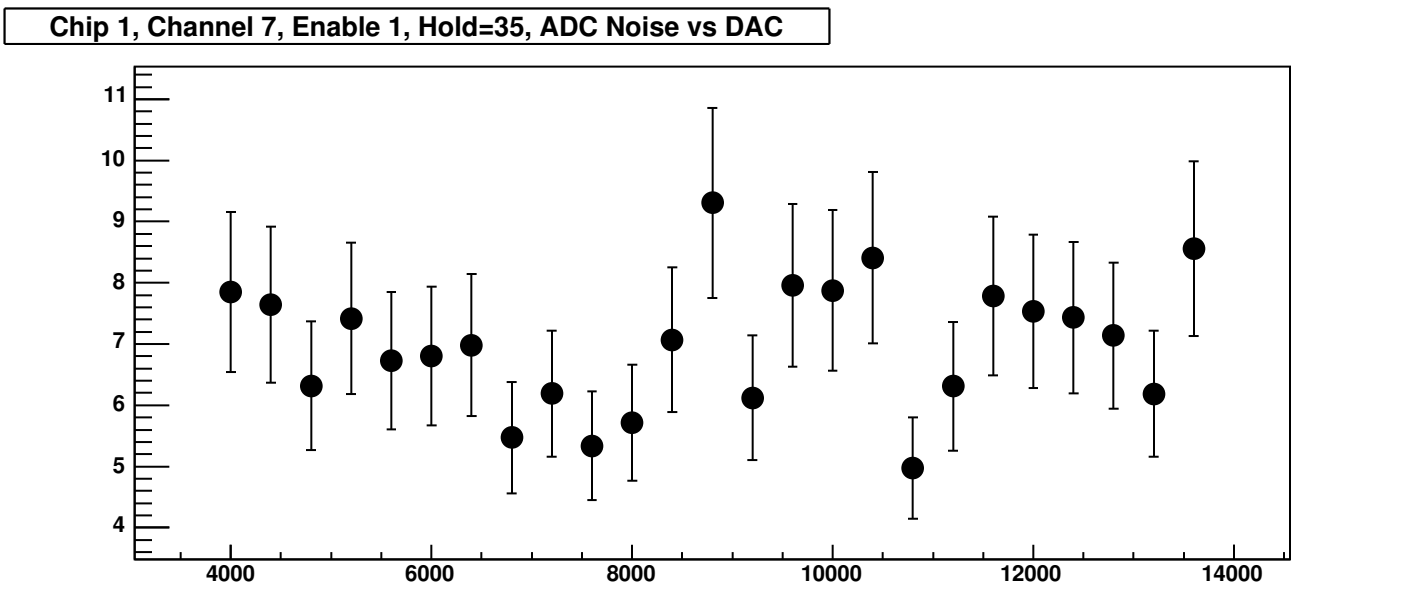

Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

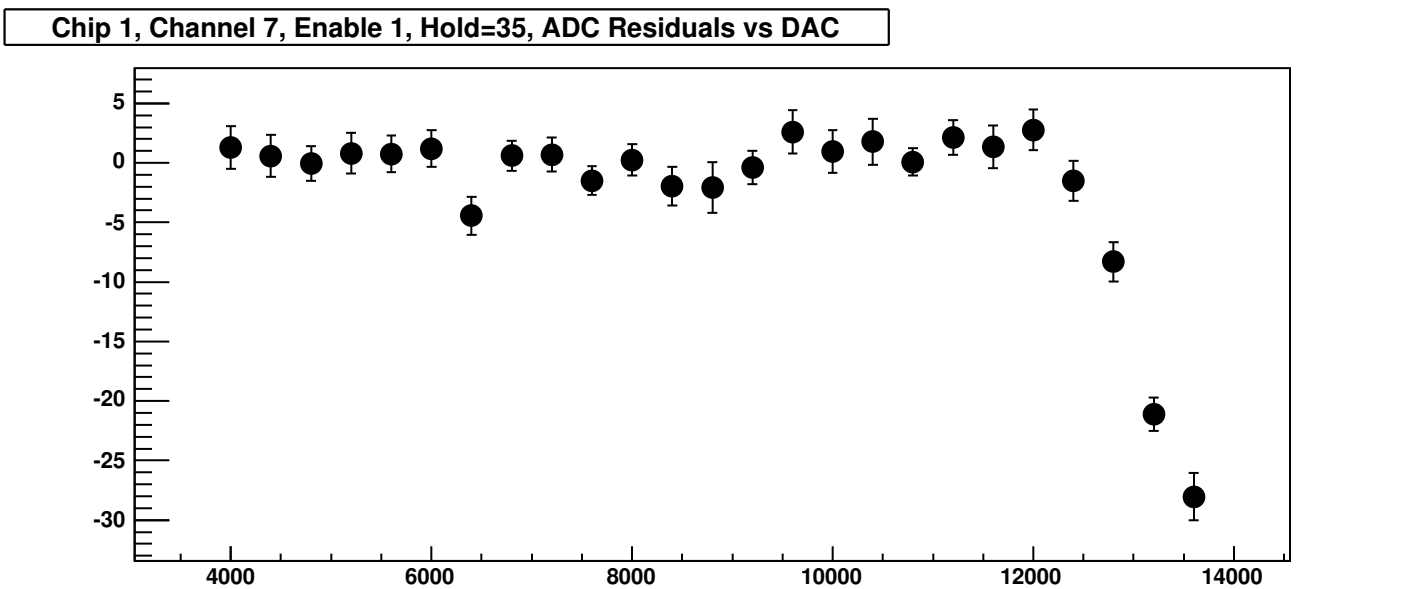

Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 22.93 / 16
p0 44.06 ± 1.291
p1 0.01588 ± 0.0001656
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

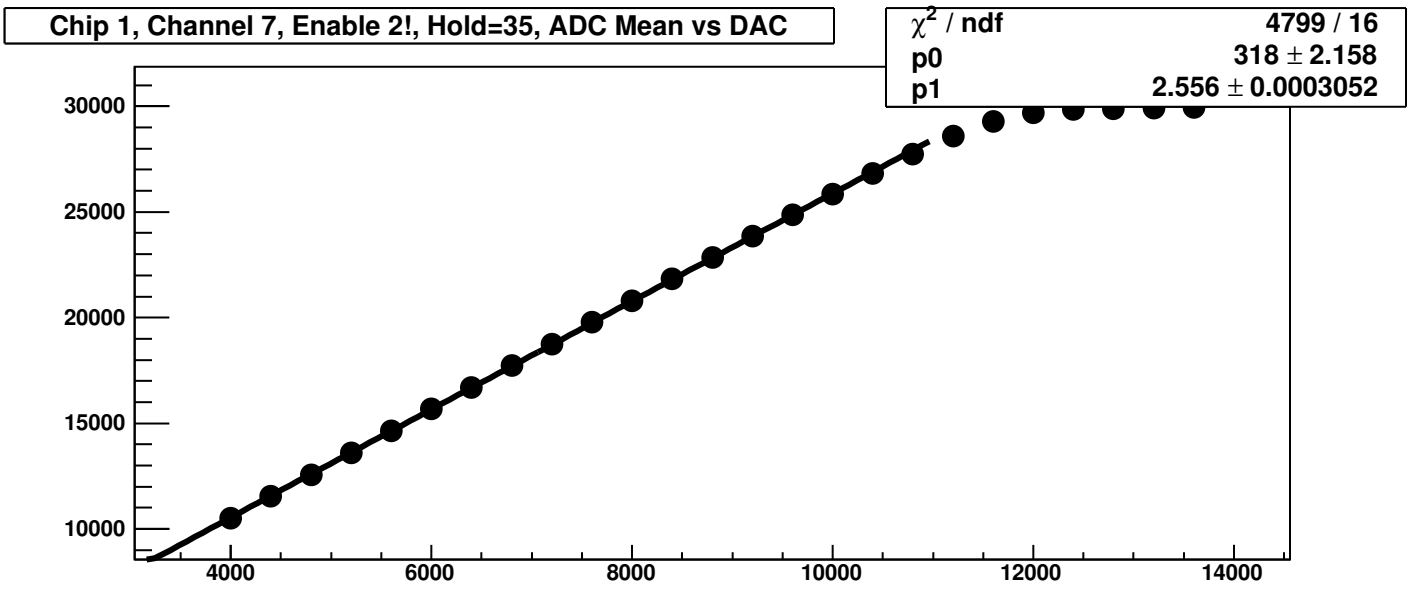

Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

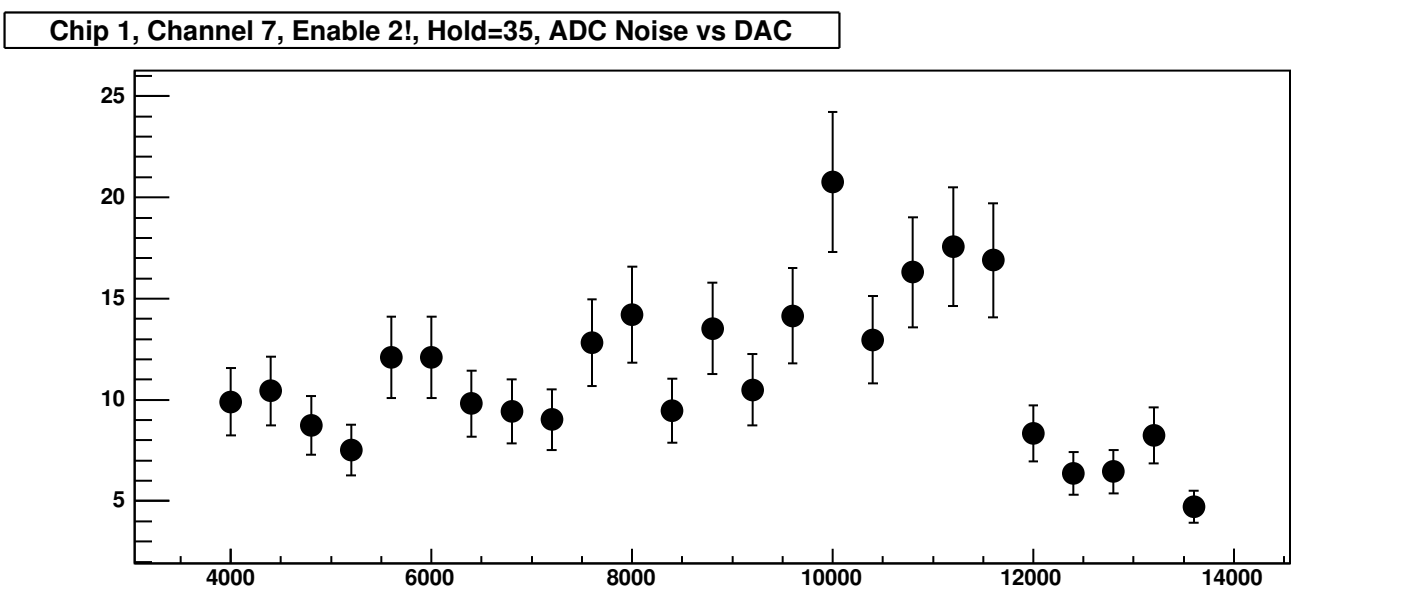

Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

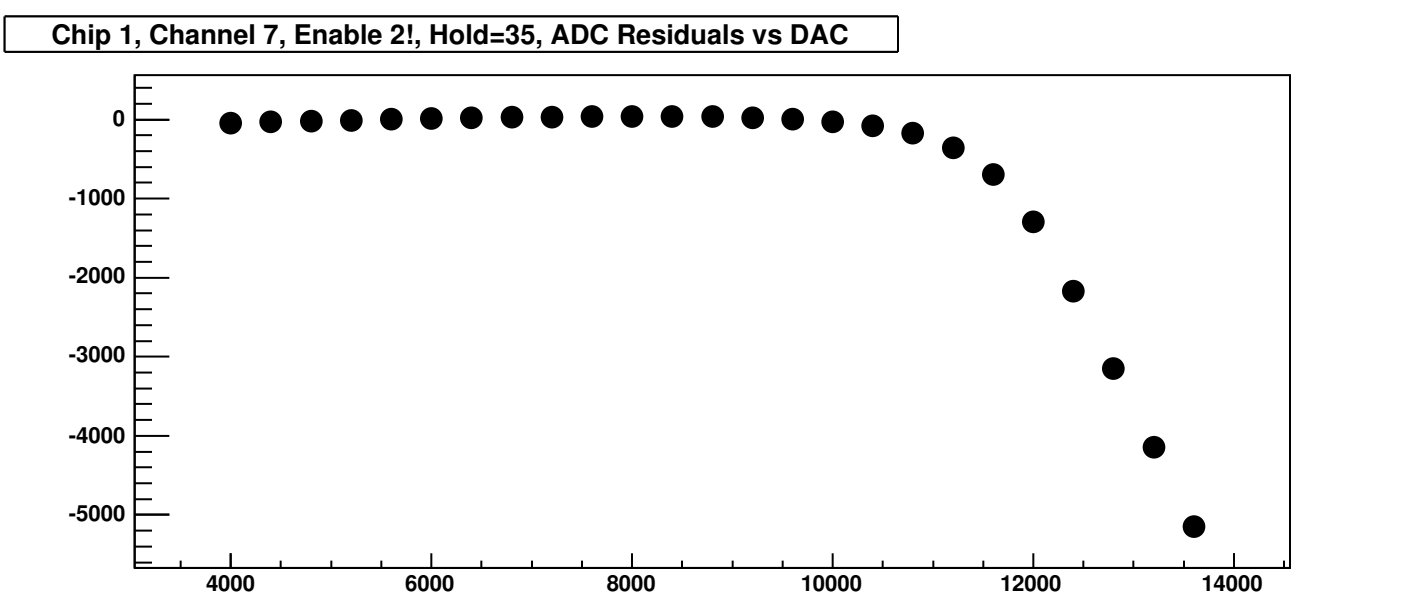

Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.91 / 16
p0 44.25 ± 1.38
p1 0.01583 ± 0.0001779
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

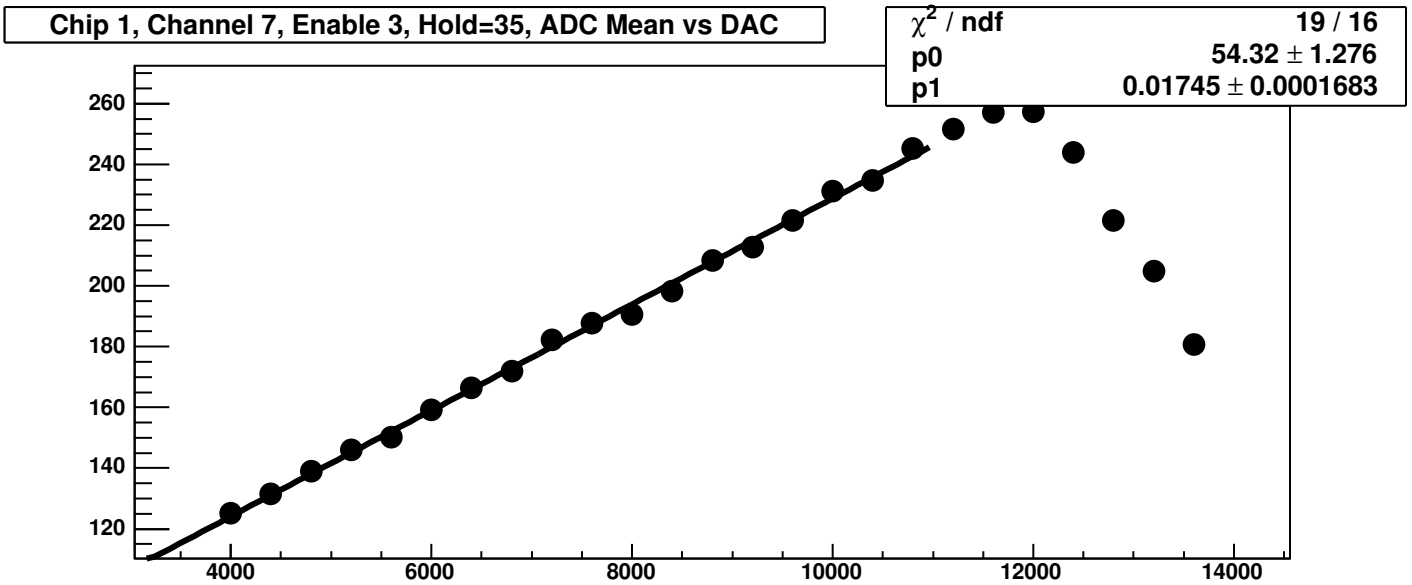

Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

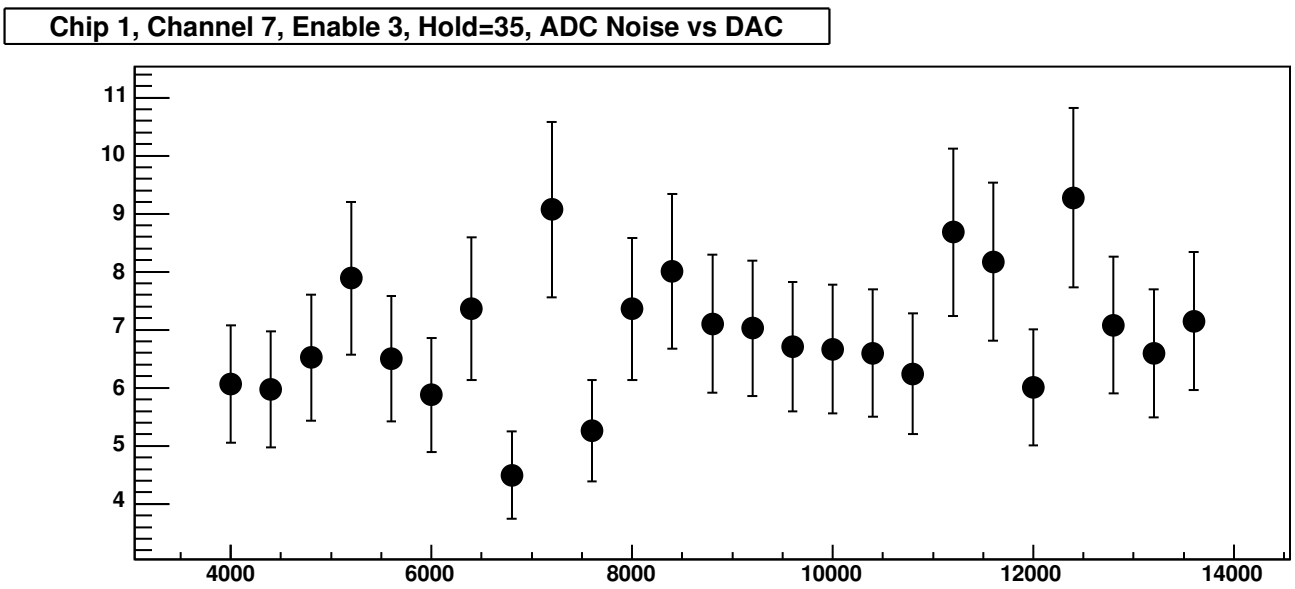

Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

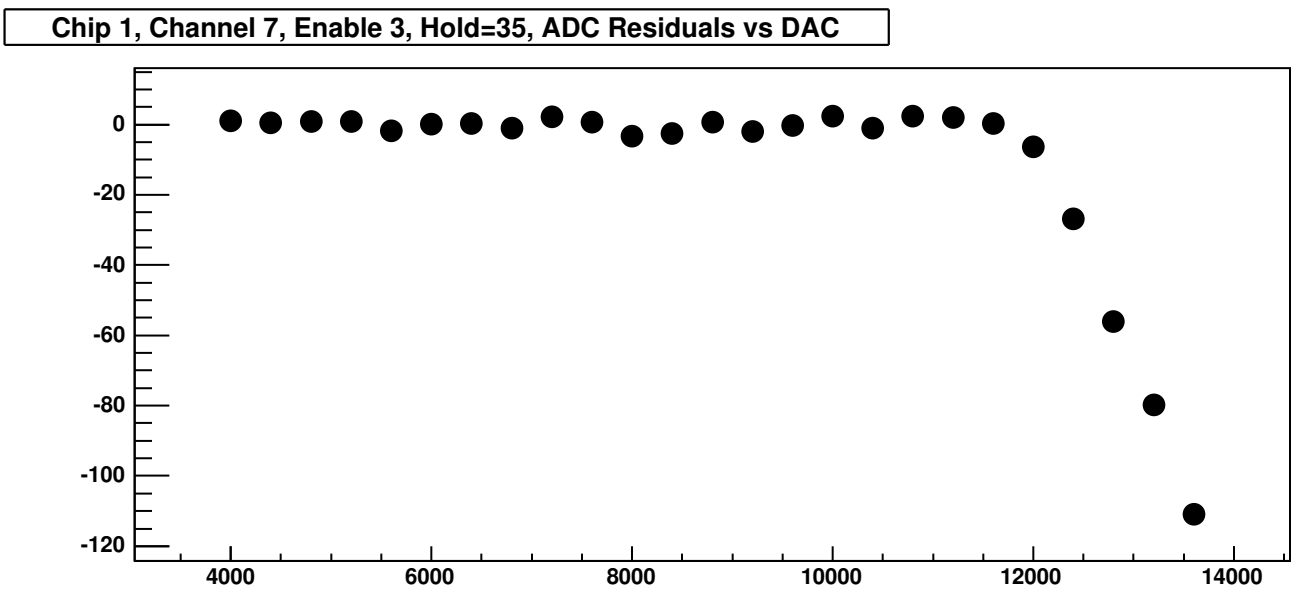

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 4799 / 16
p0 318 ± 2.158
p1 2.556 ± 0.0003052
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

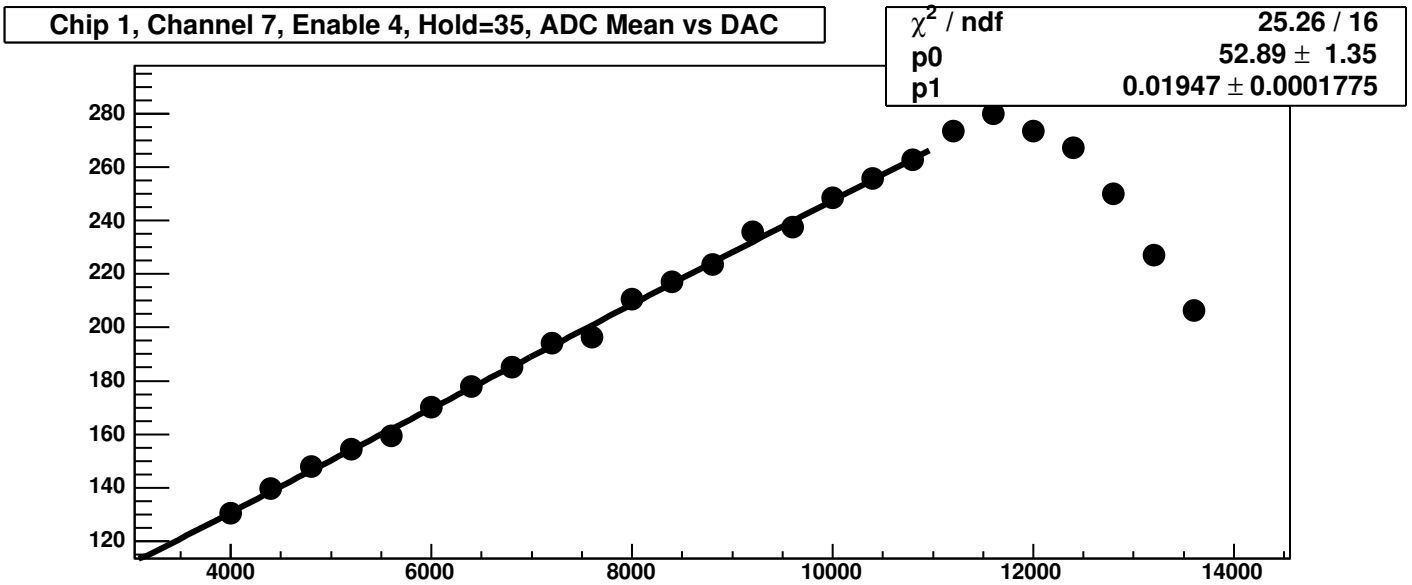

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
4000
6000
8000
10000
12000
14000

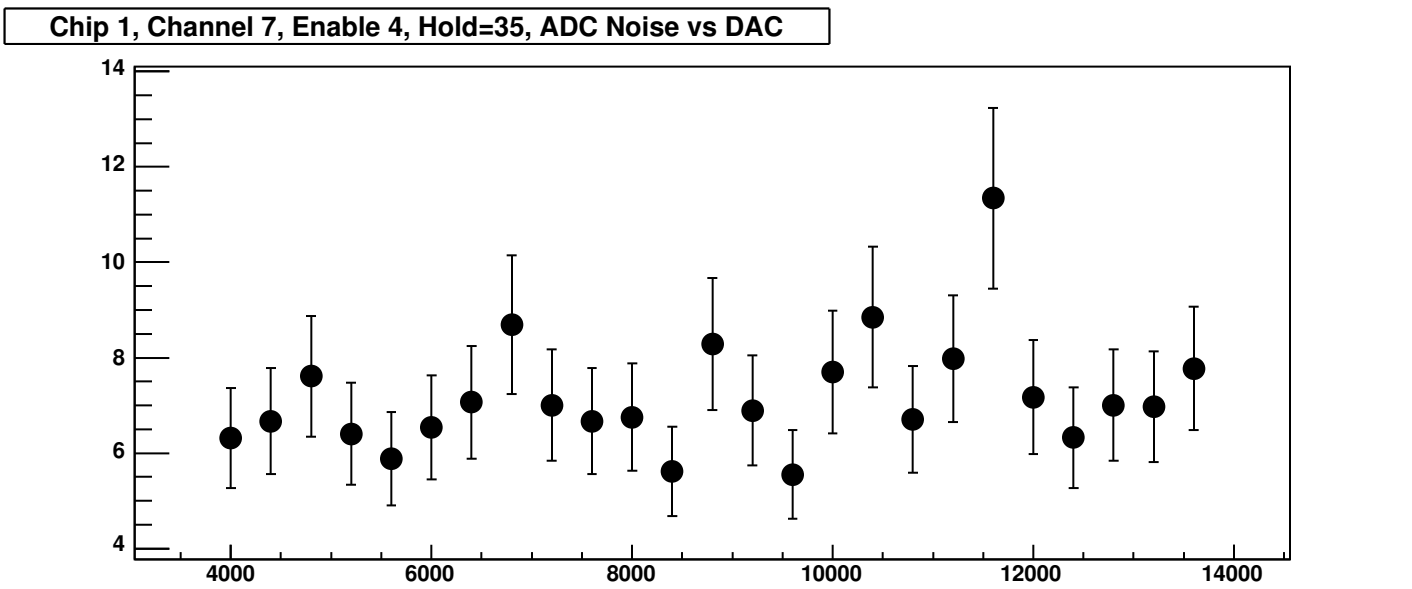

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

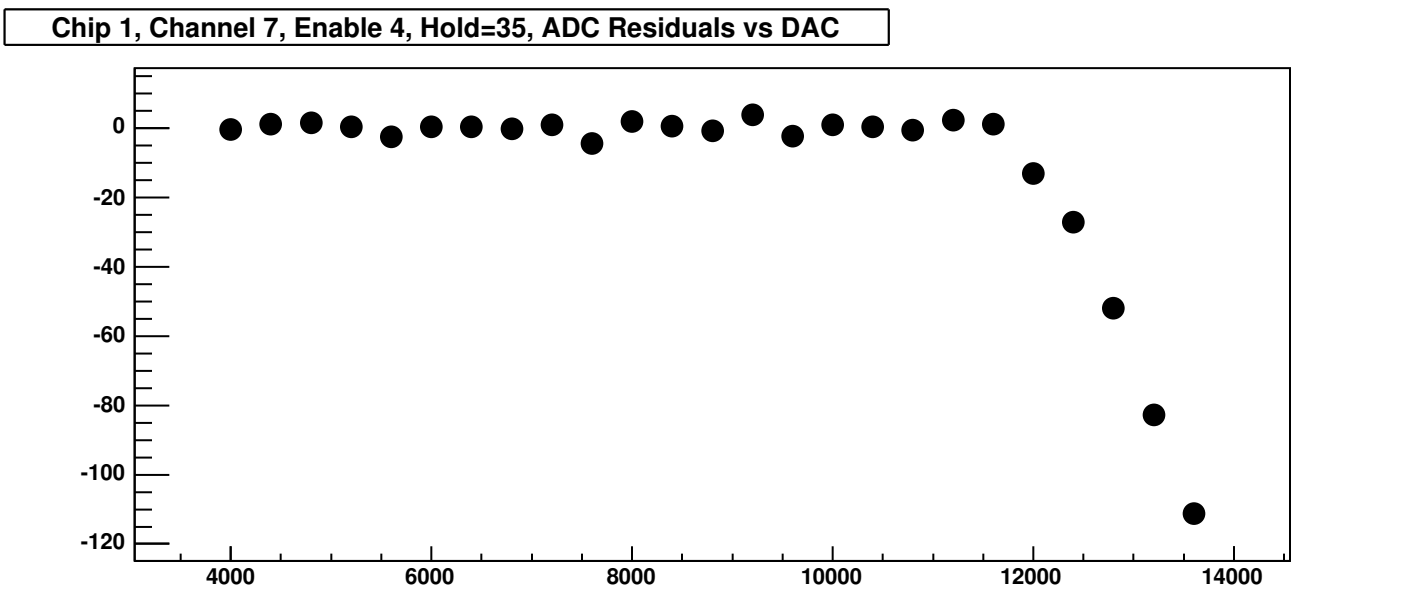

Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19 / 16
p0 54.32 ± 1.276
p1 0.01745 ± 0.0001683
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000

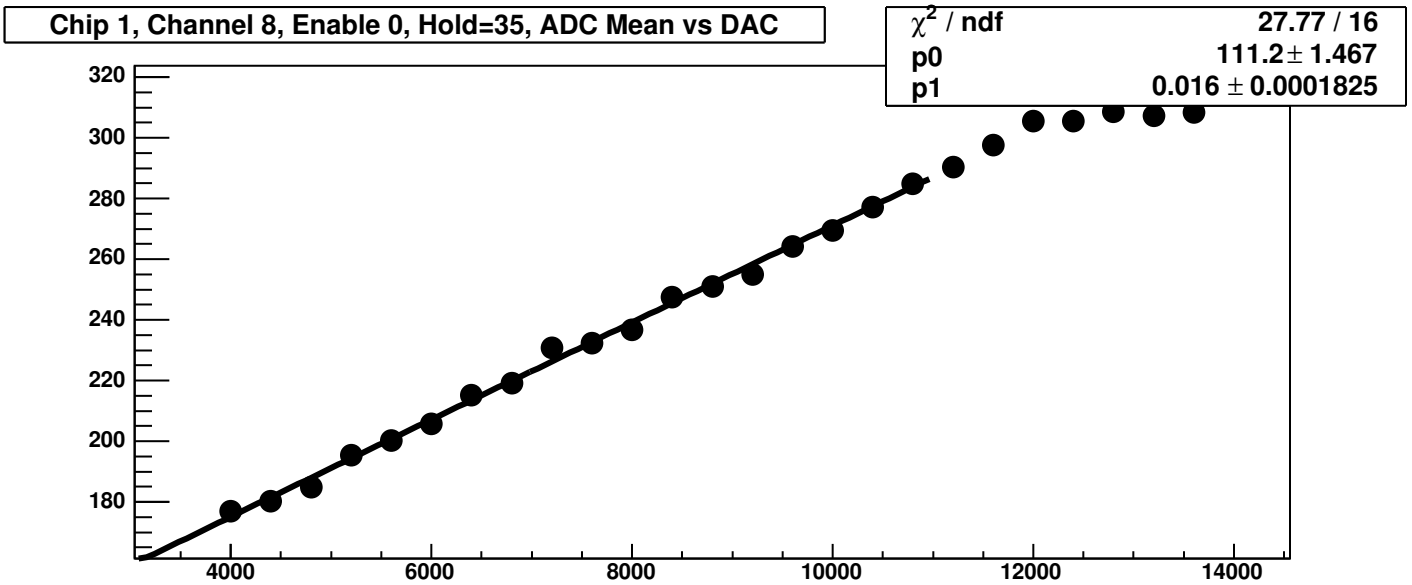

Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

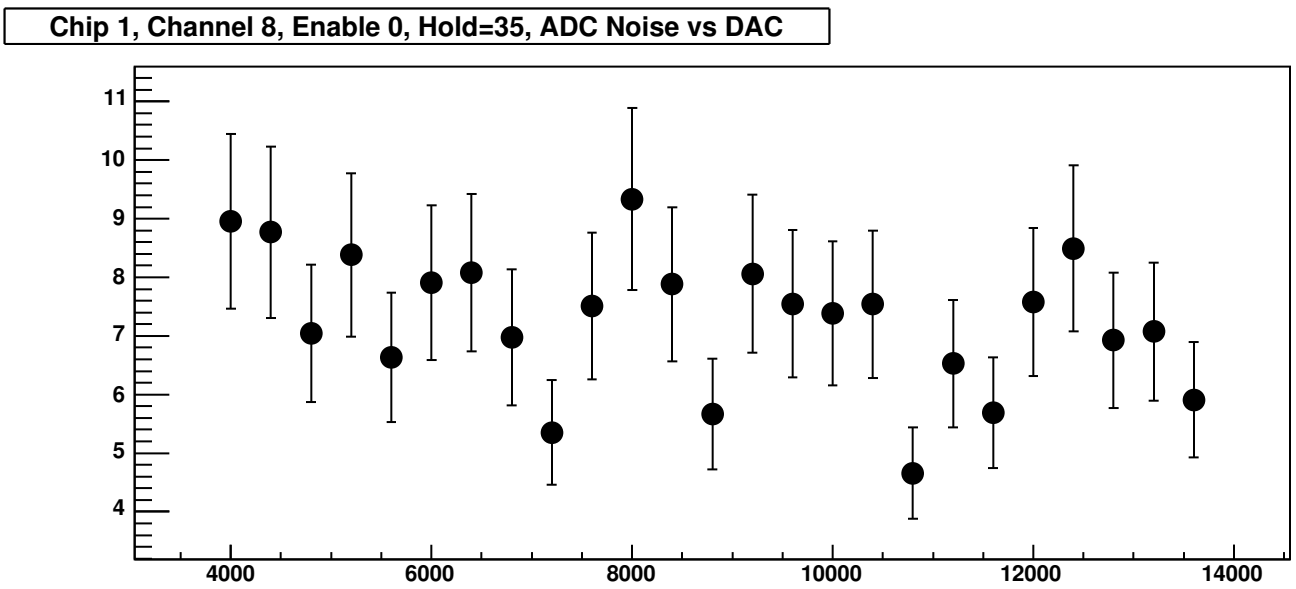

Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000

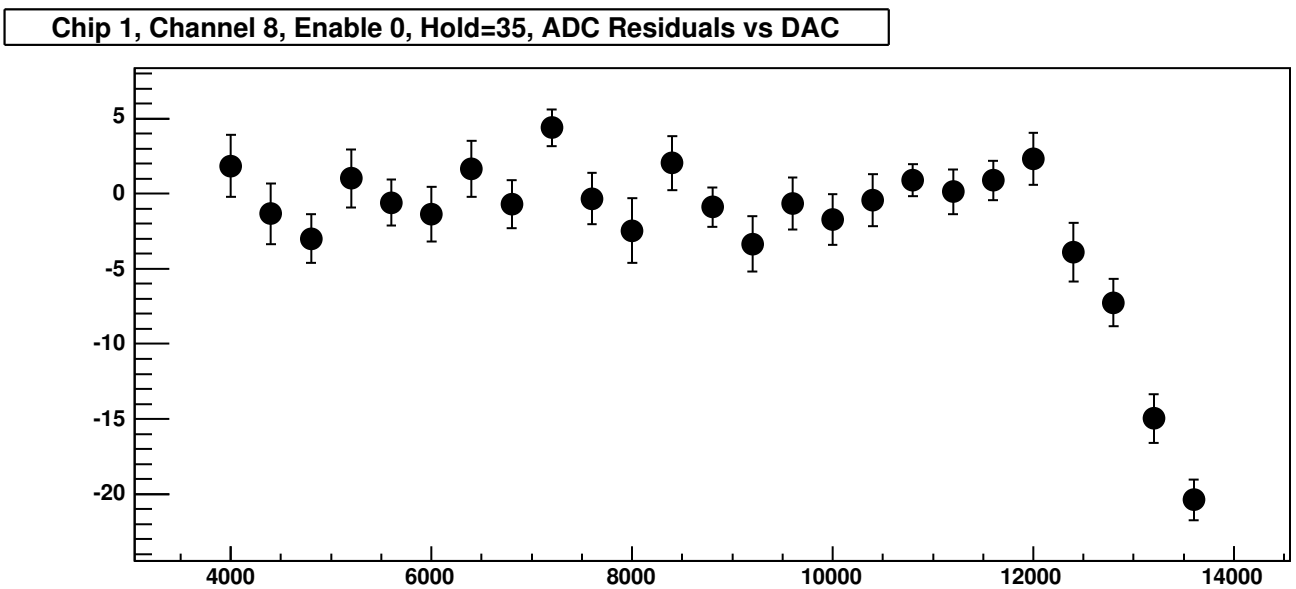

Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.26 / 16
p0 52.89 ± 1.35
p1 0.01947 ± 0.0001775
280
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

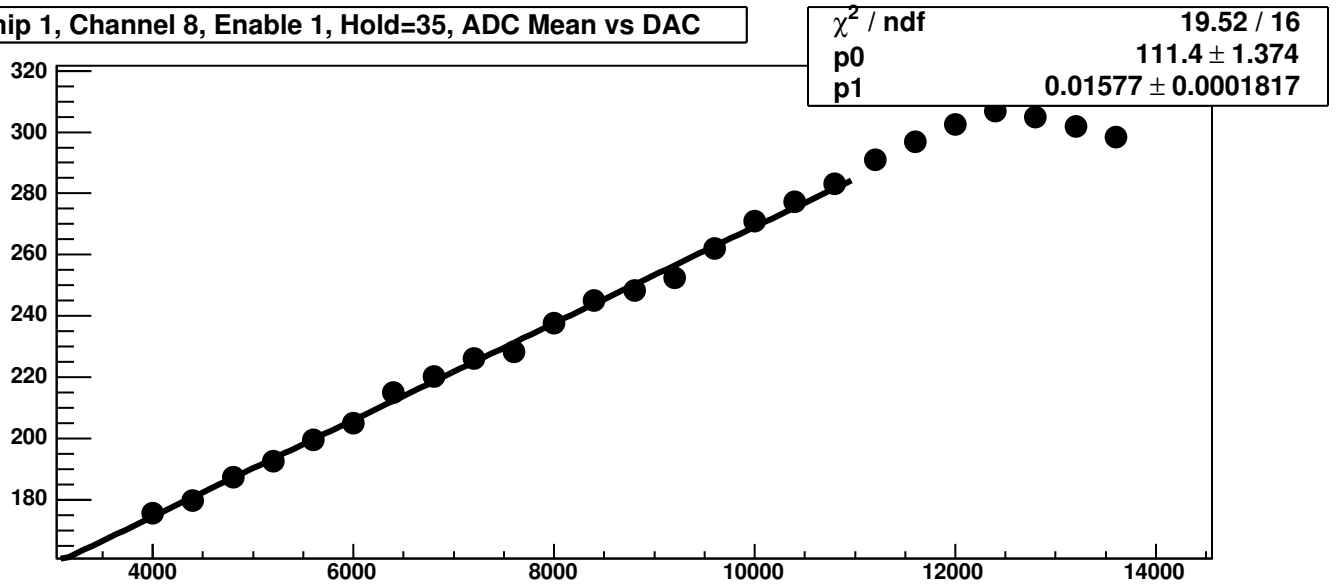

Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.77 / 16
p0 111.2 ± 1.467
p1 0.016 ± 0.0001825
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

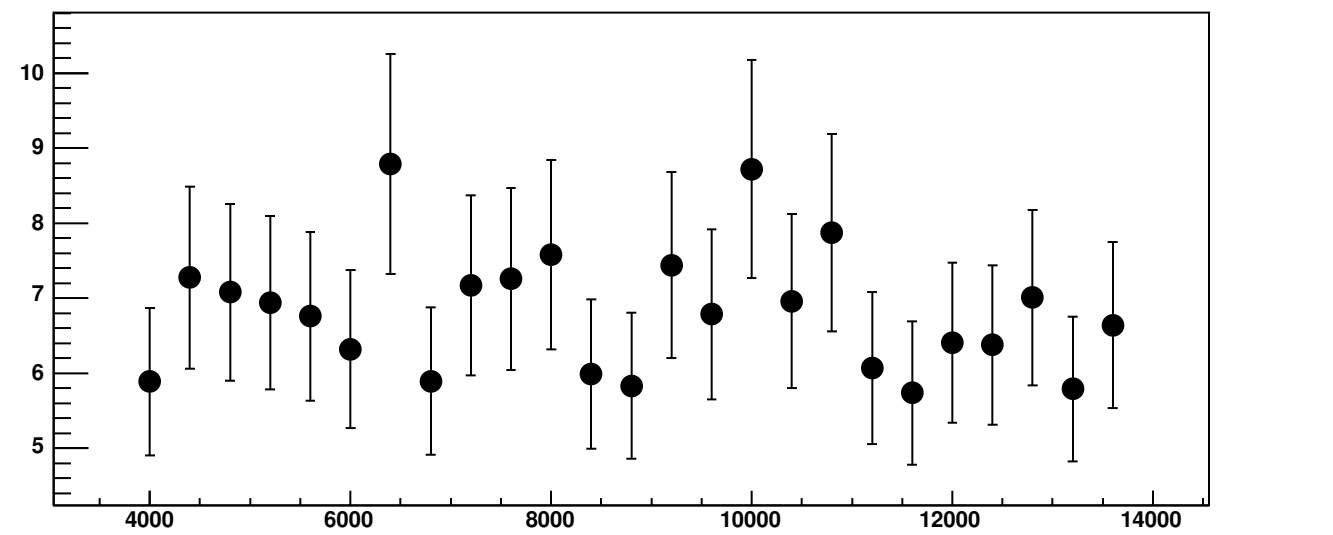

Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

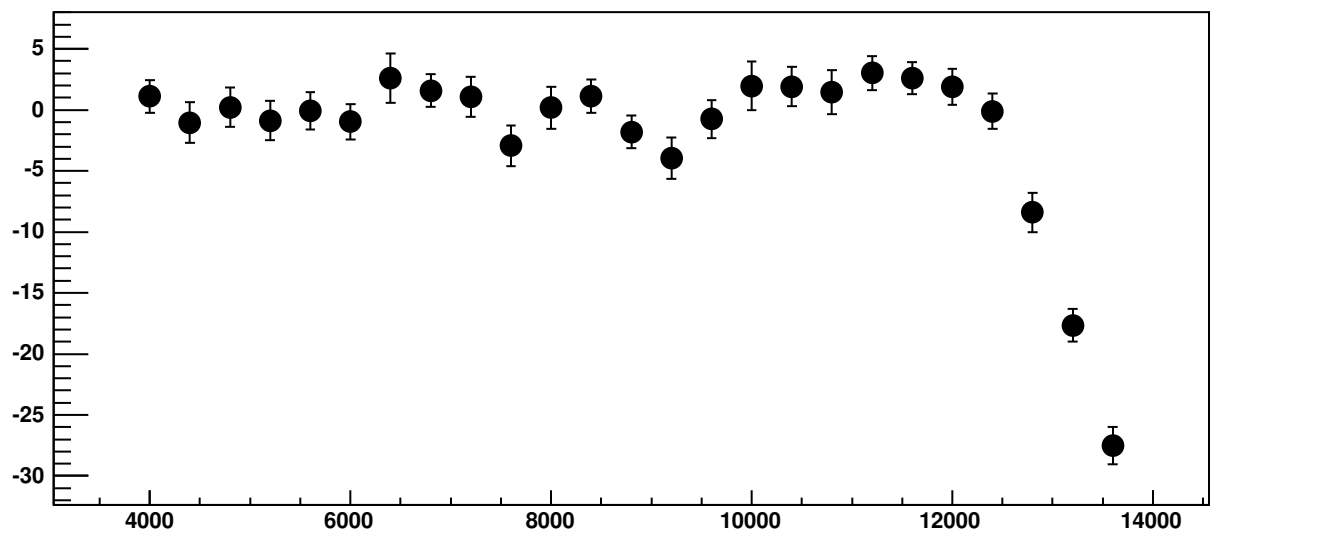
Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

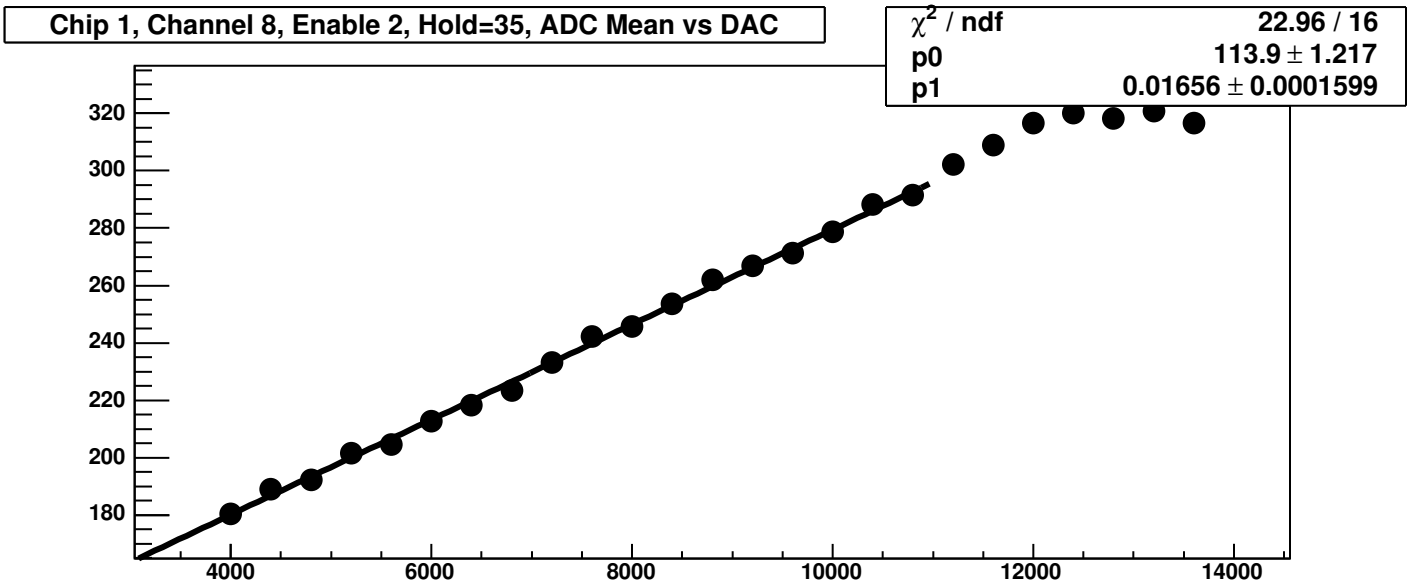


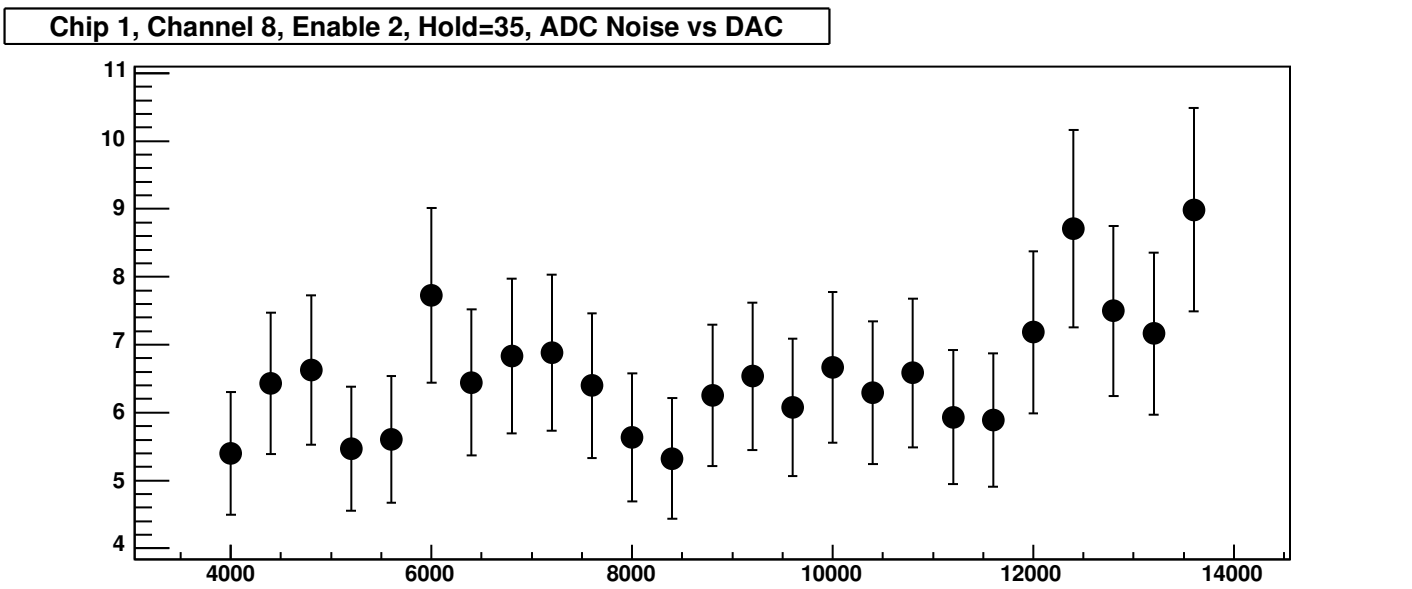
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

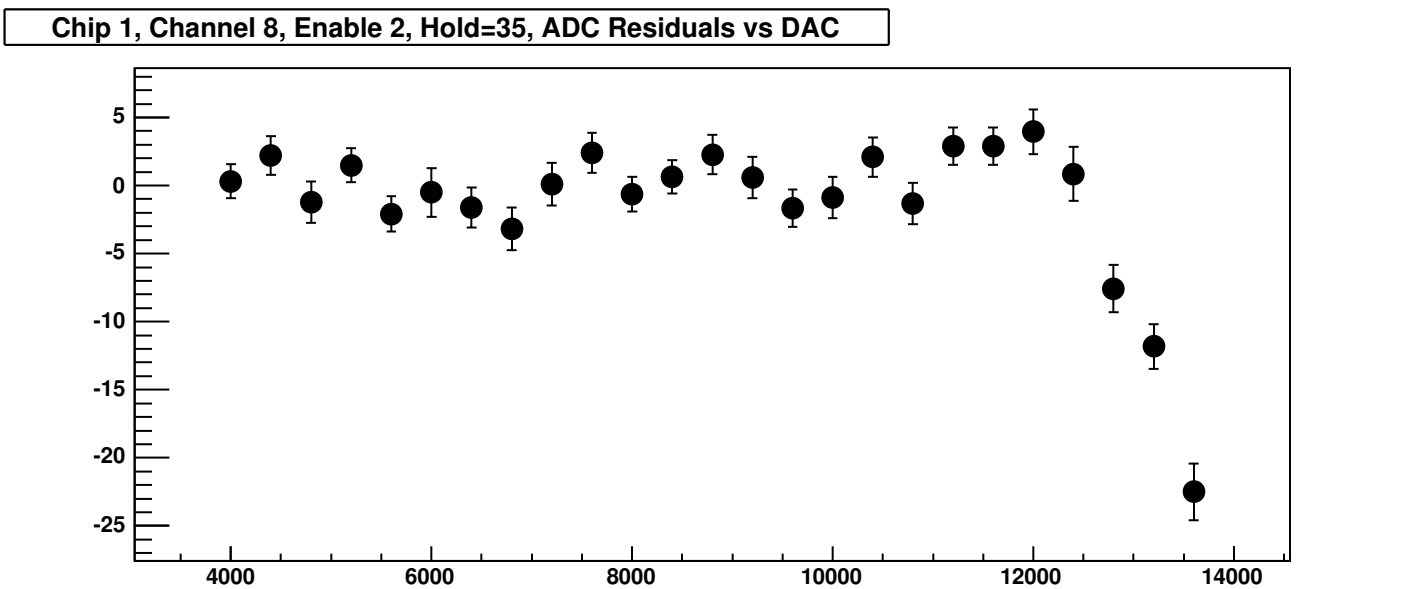


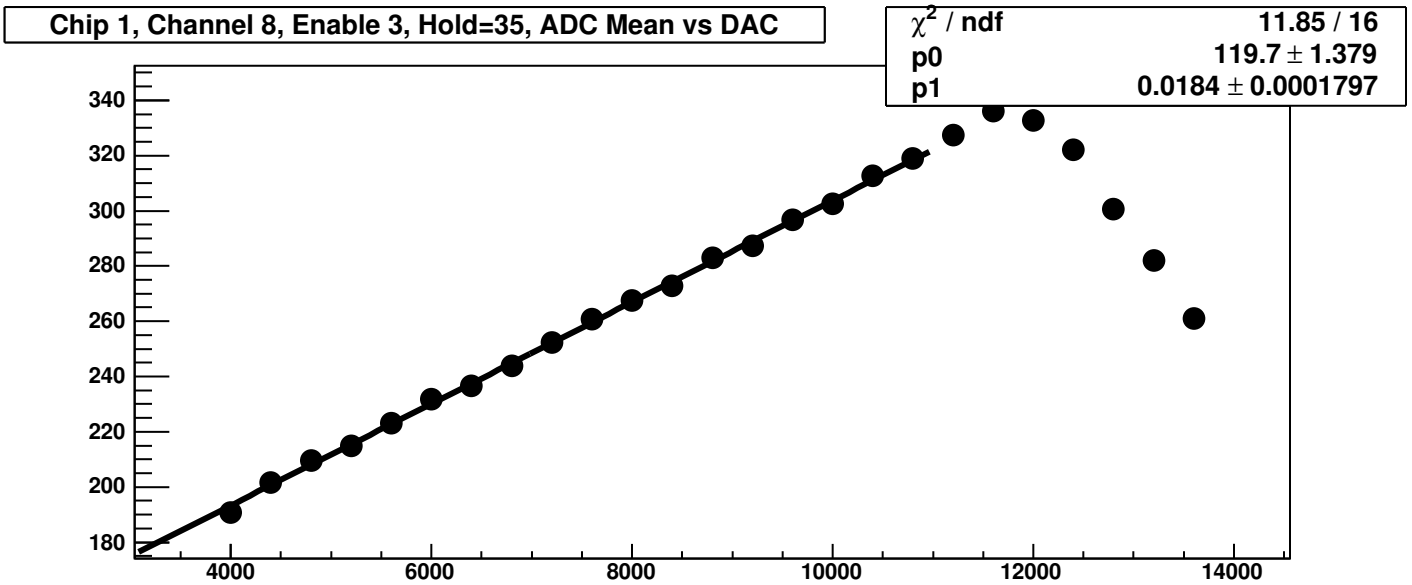
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

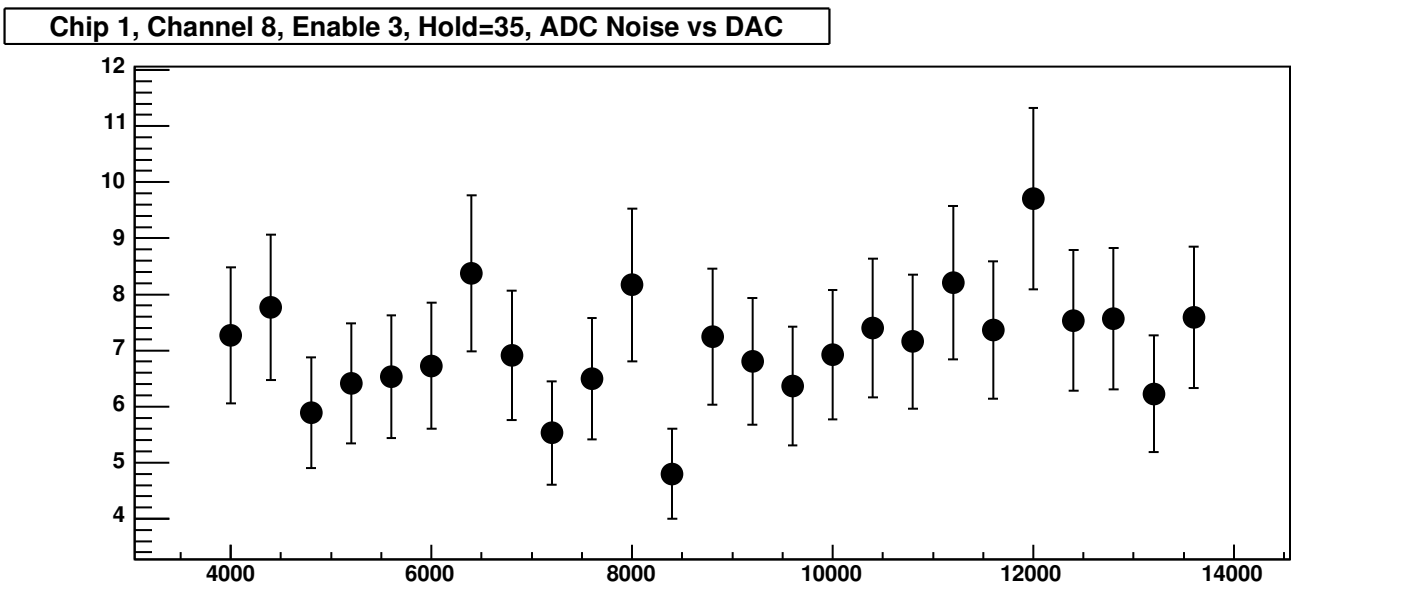


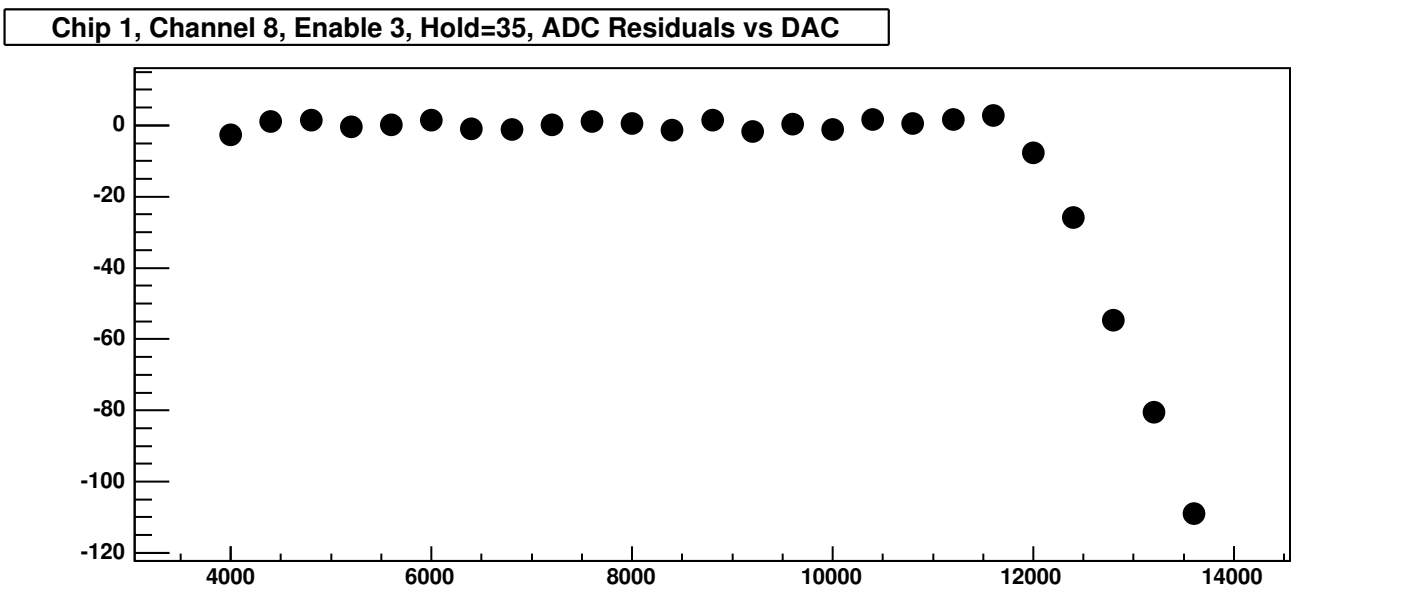

Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.96 / 16
p0 113.9 ± 1.217
p1 0.01656 ± 0.0001599
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

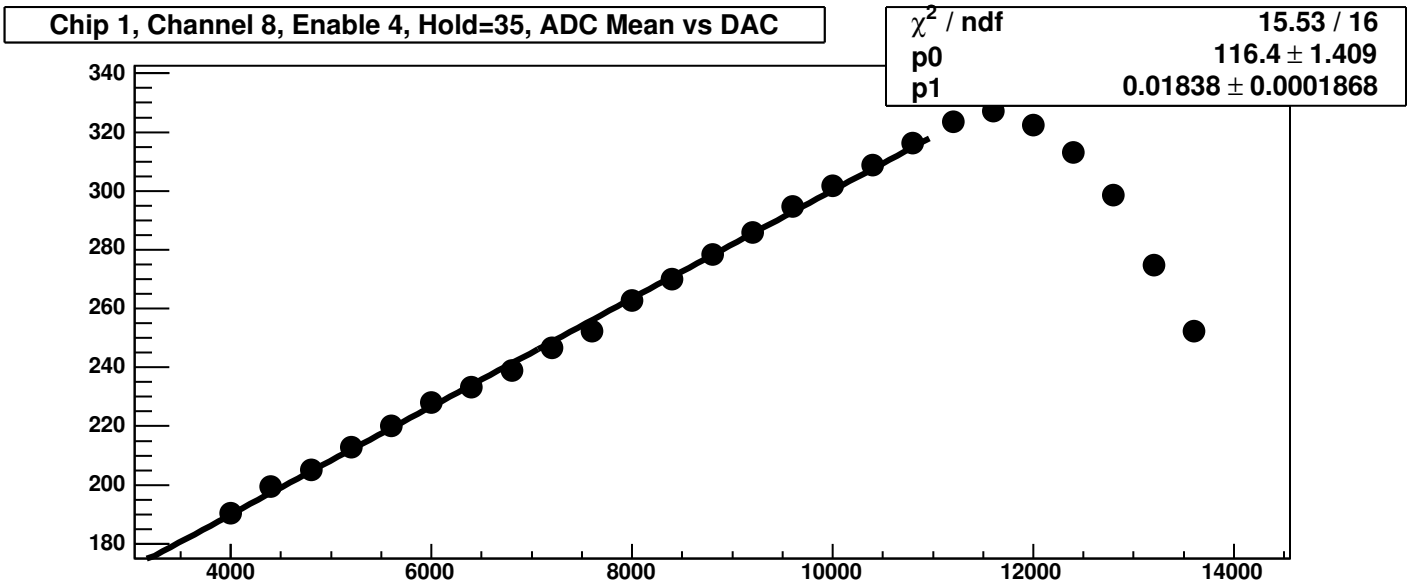

Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

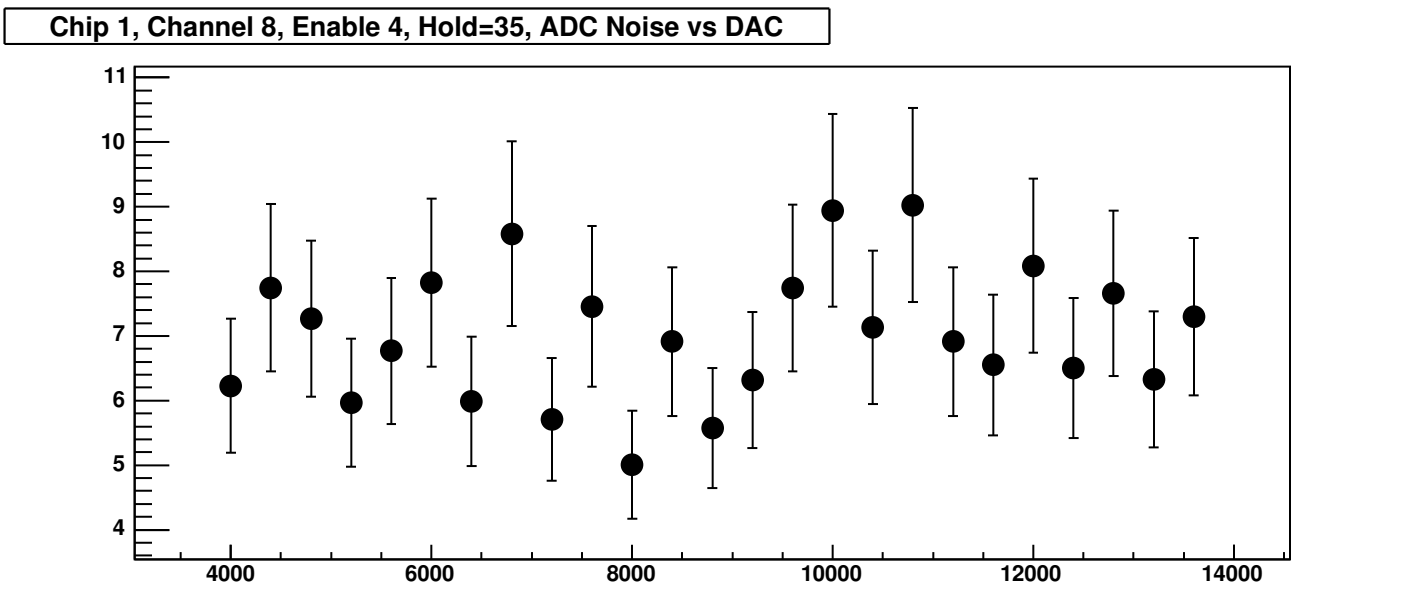

Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

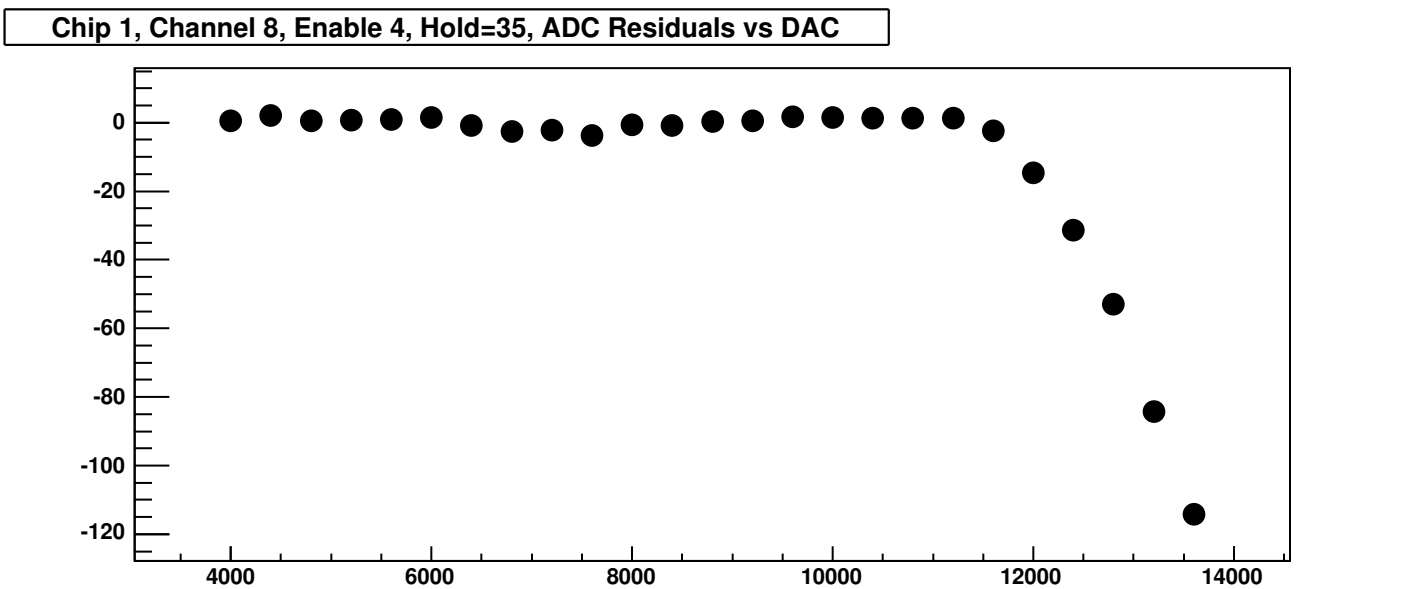

Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 11.85 / 16
p0 119.7 ± 1.379
p1 0.0184 ± 0.0001797
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

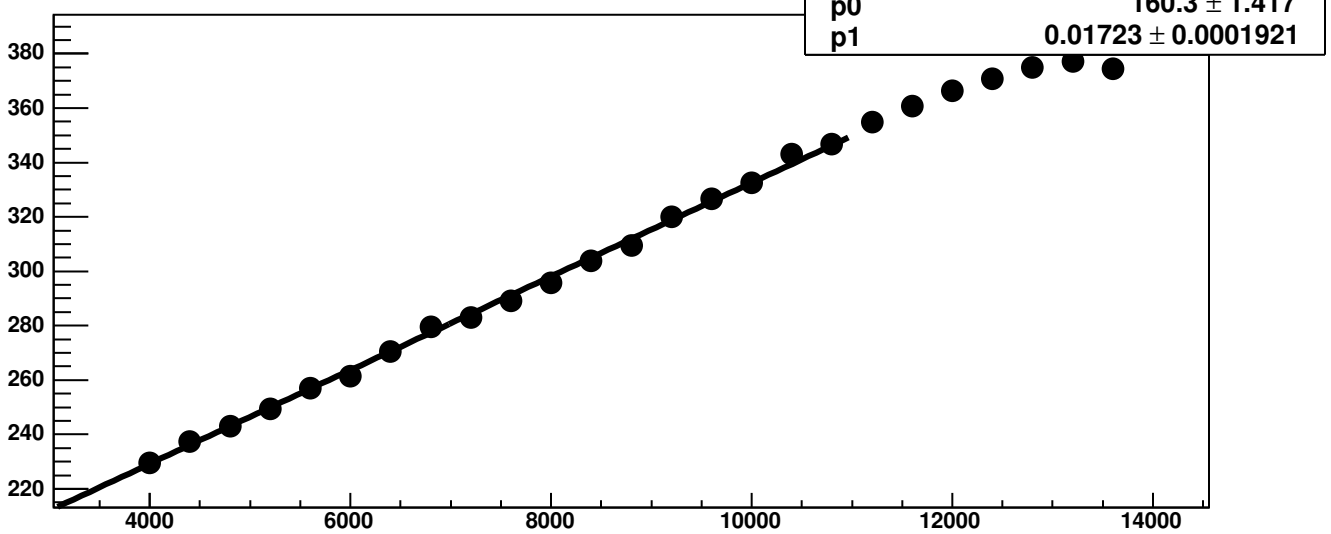

Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.53 / 16
p0 116.4 ± 1.409
p1 0.01838 ± 0.0001868
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

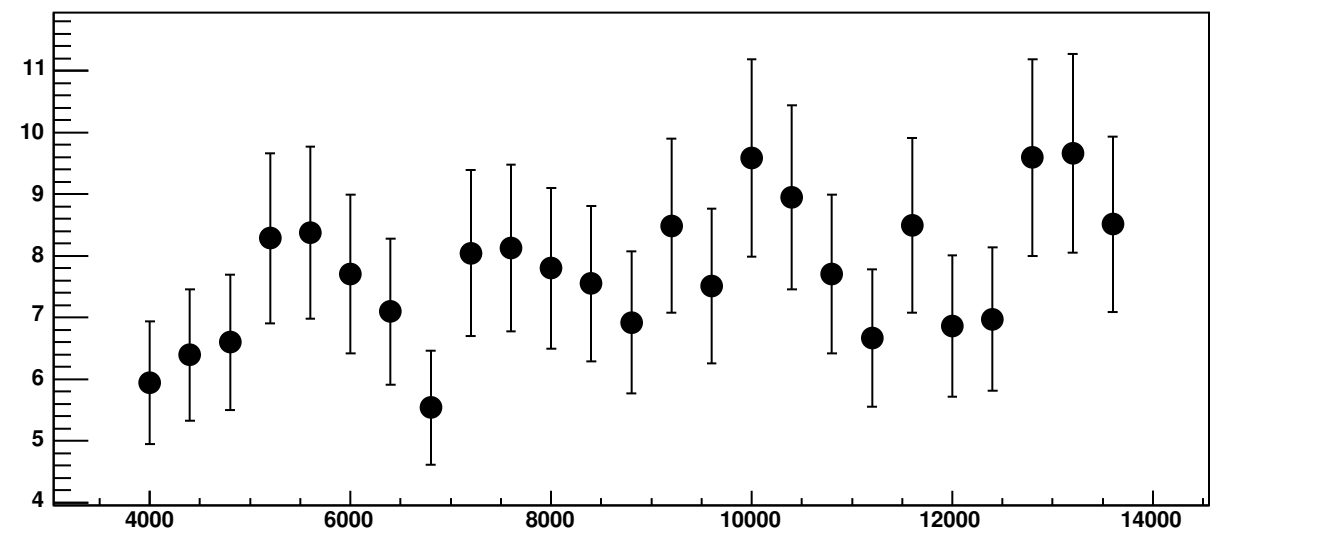

Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000

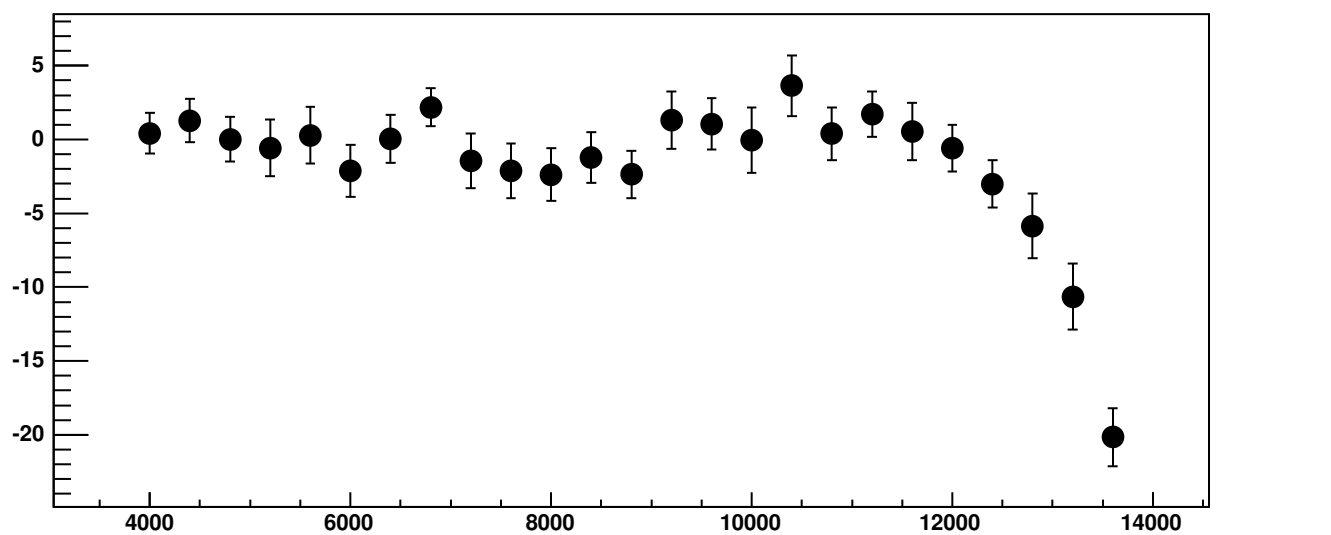
Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



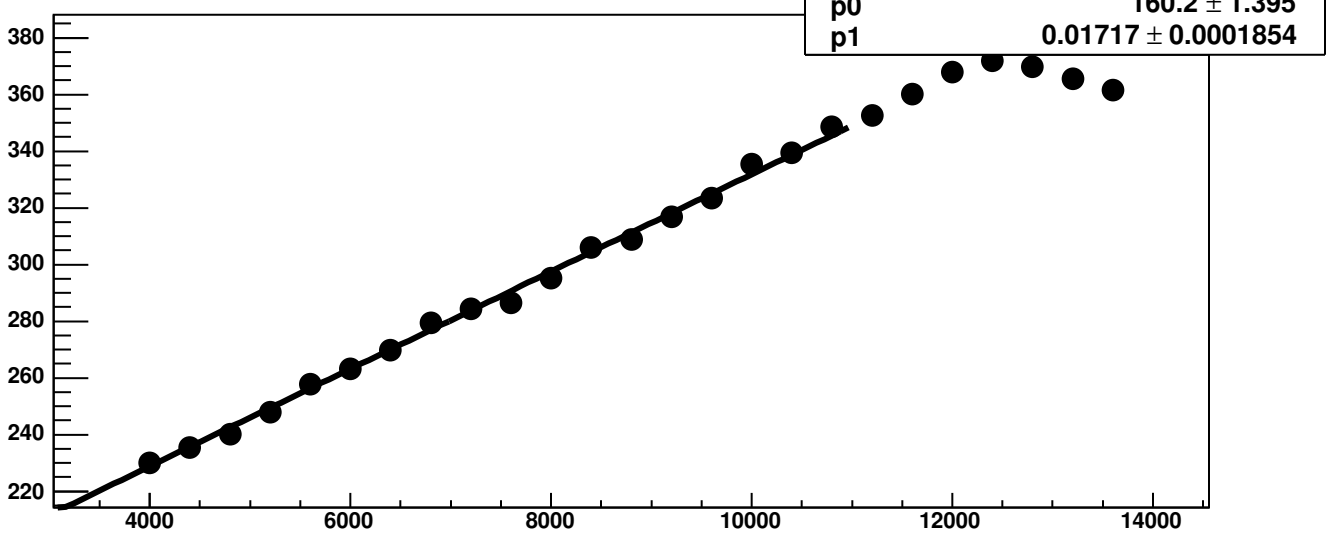
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



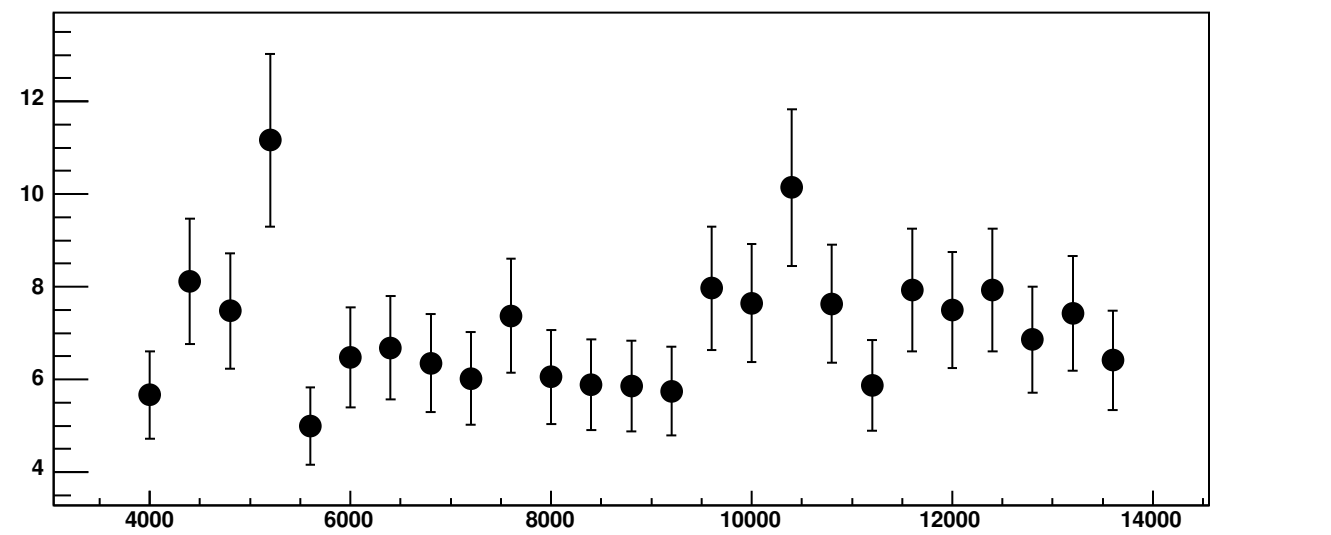
Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



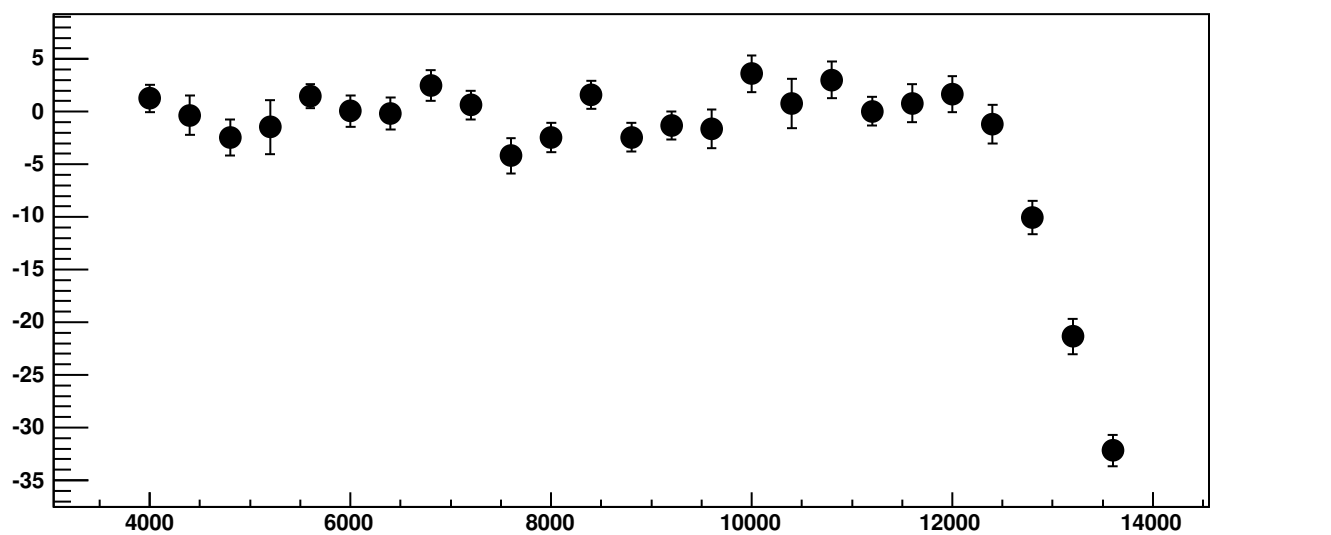
Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

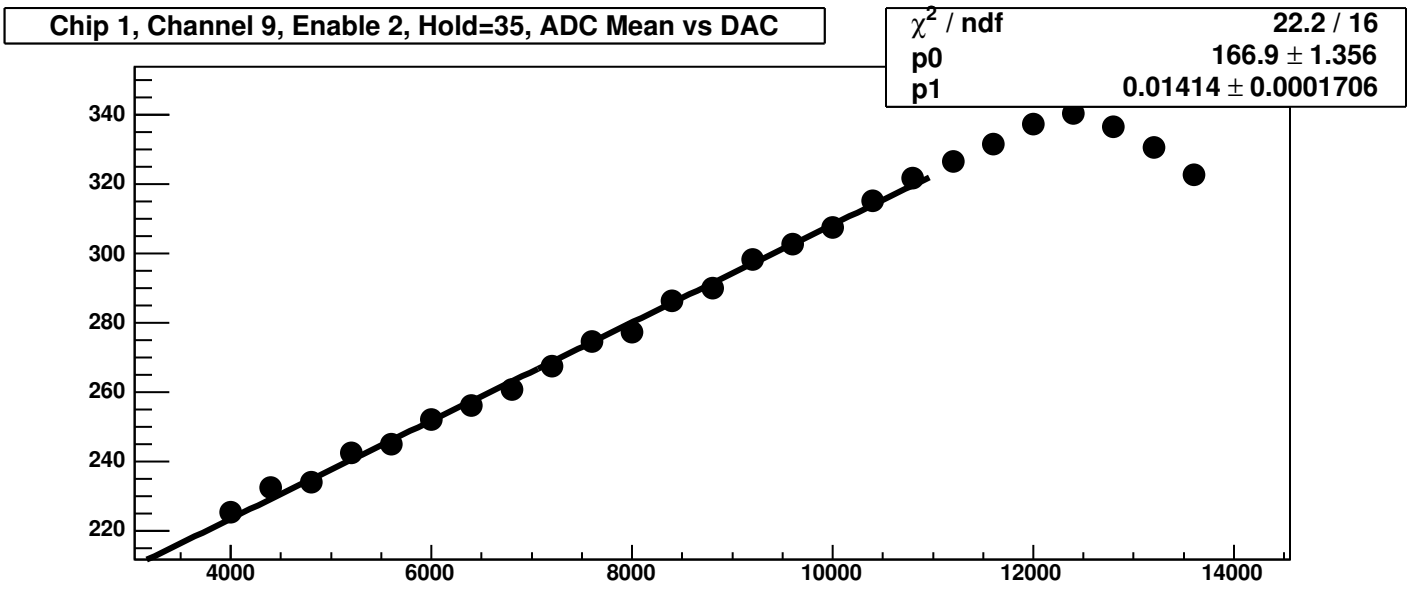


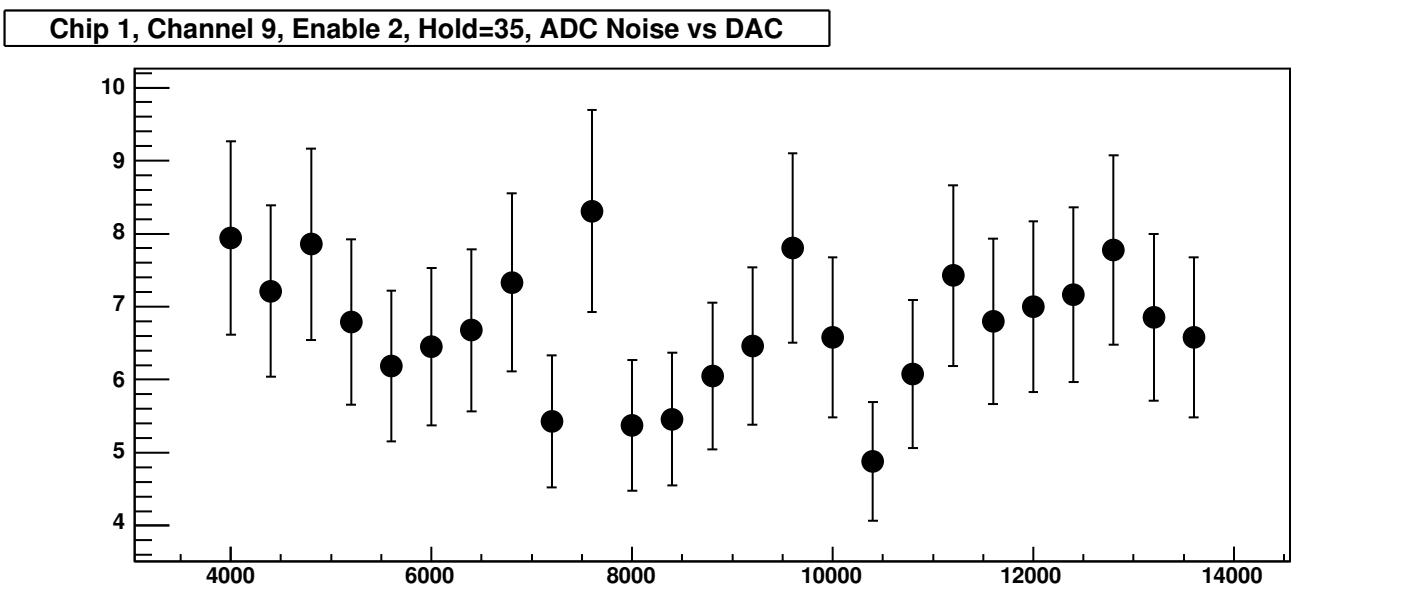
Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

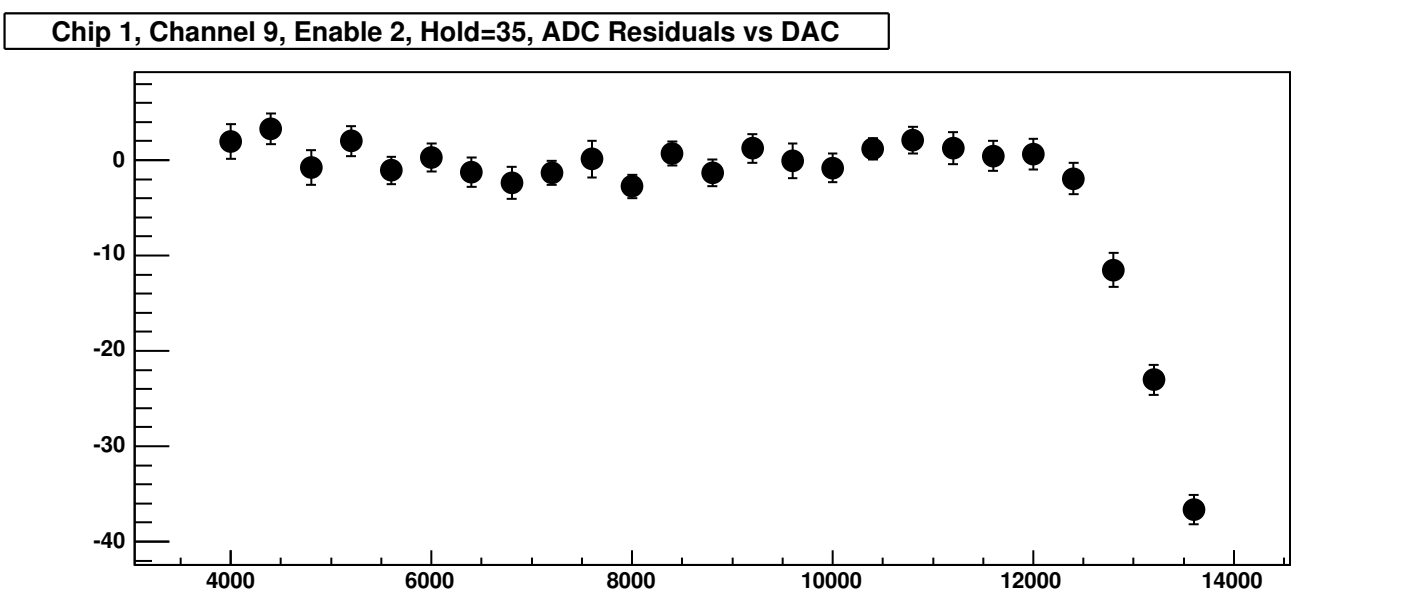


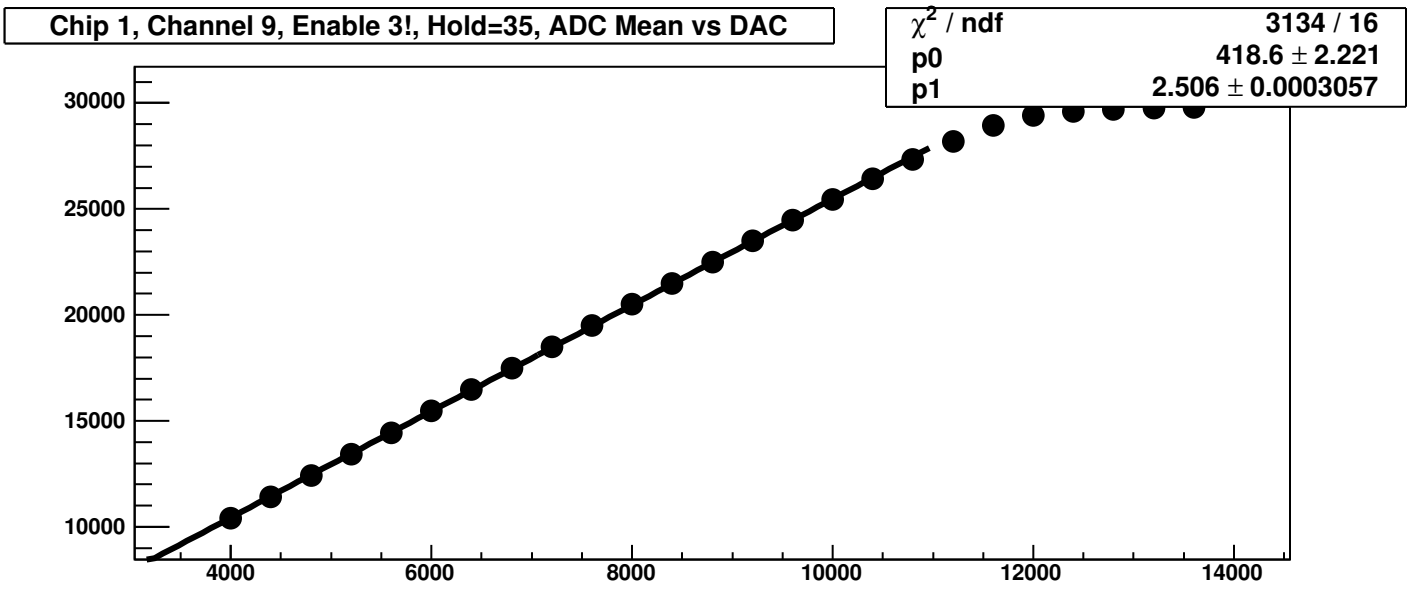
Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

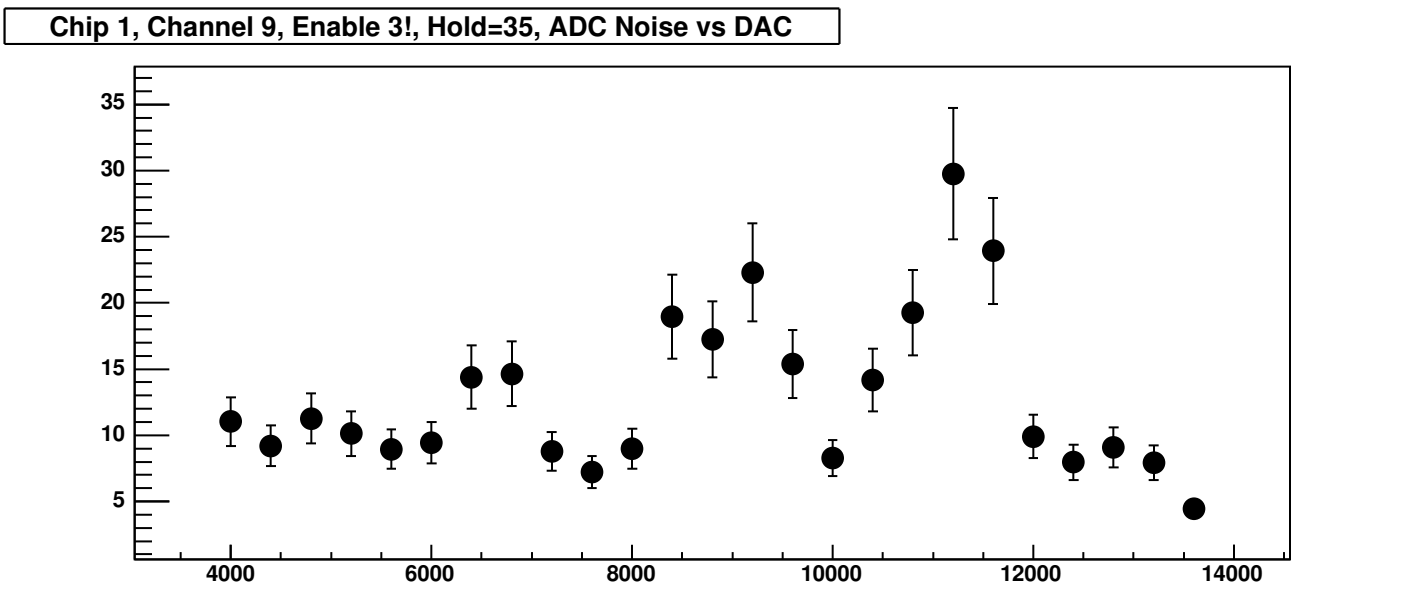


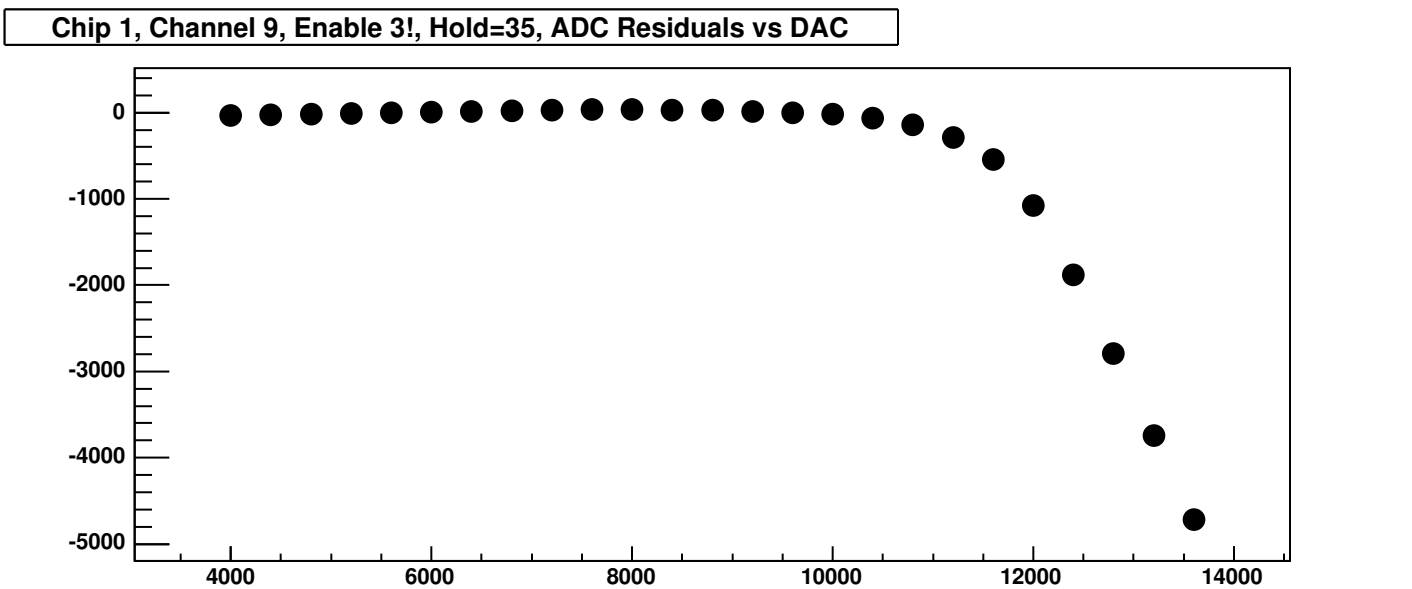

Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.2 / 16
p0 166.9 ± 1.356
p1 0.01414 ± 0.0001706
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000

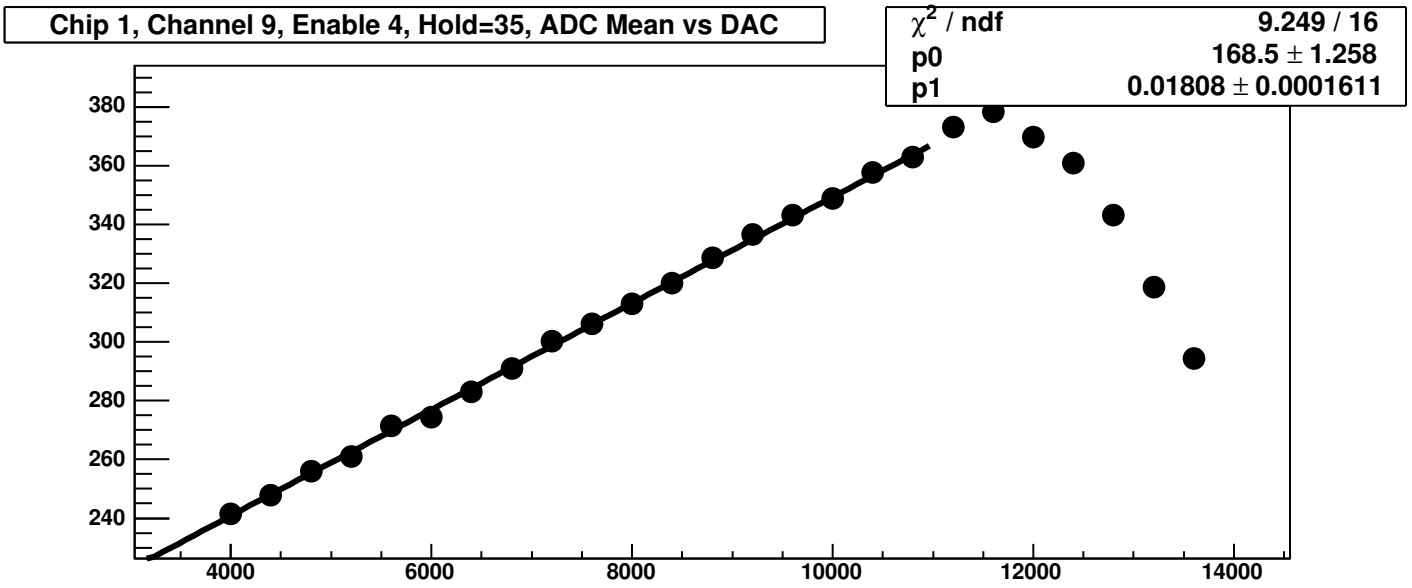

Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

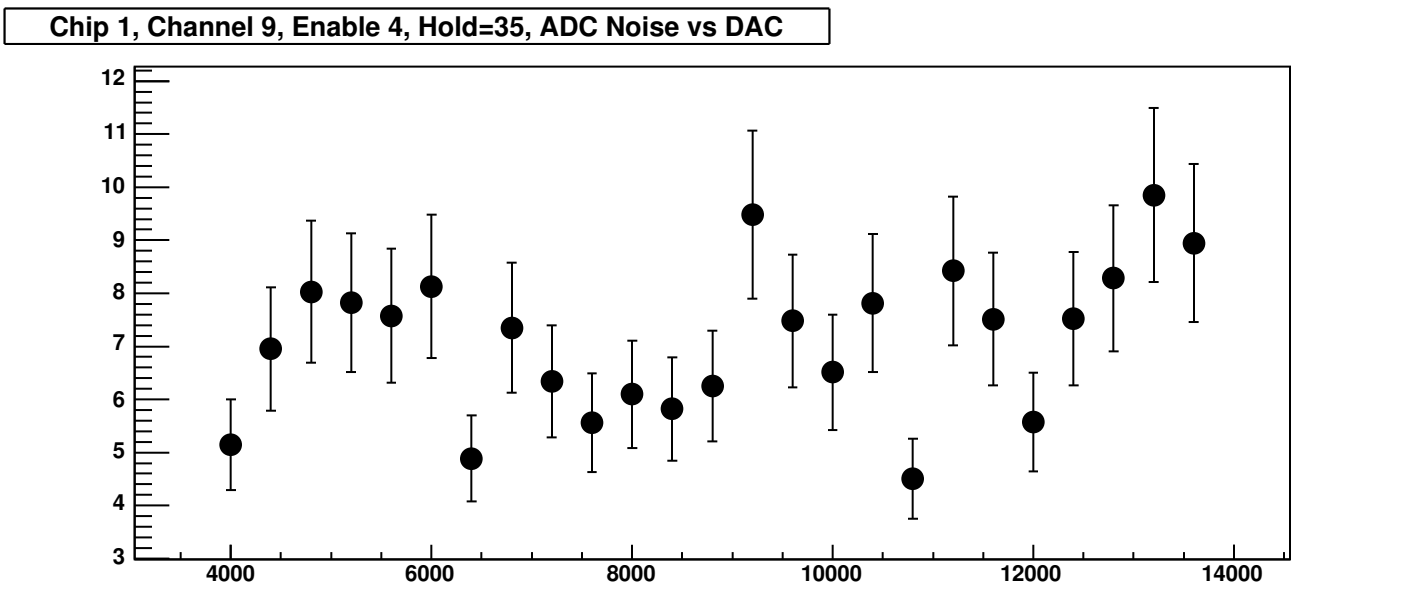

Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

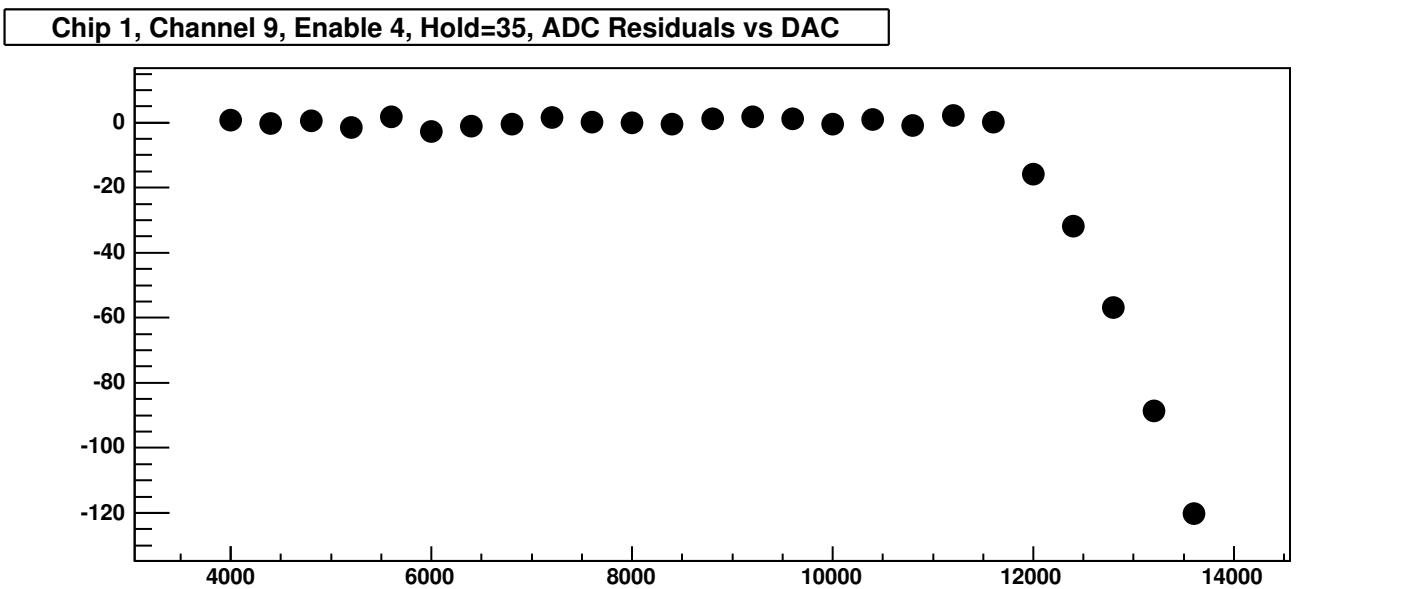

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 3134 / 16
p0 418.6 ± 2.221
p1 2.506 ± 0.0003057
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

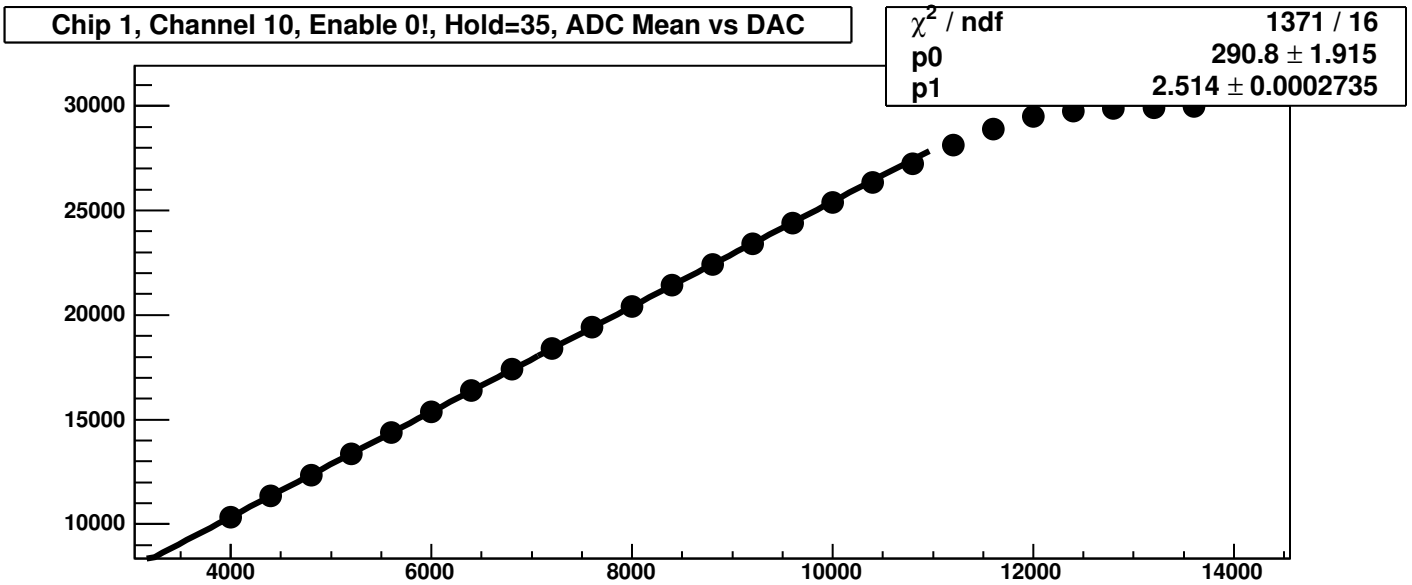

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

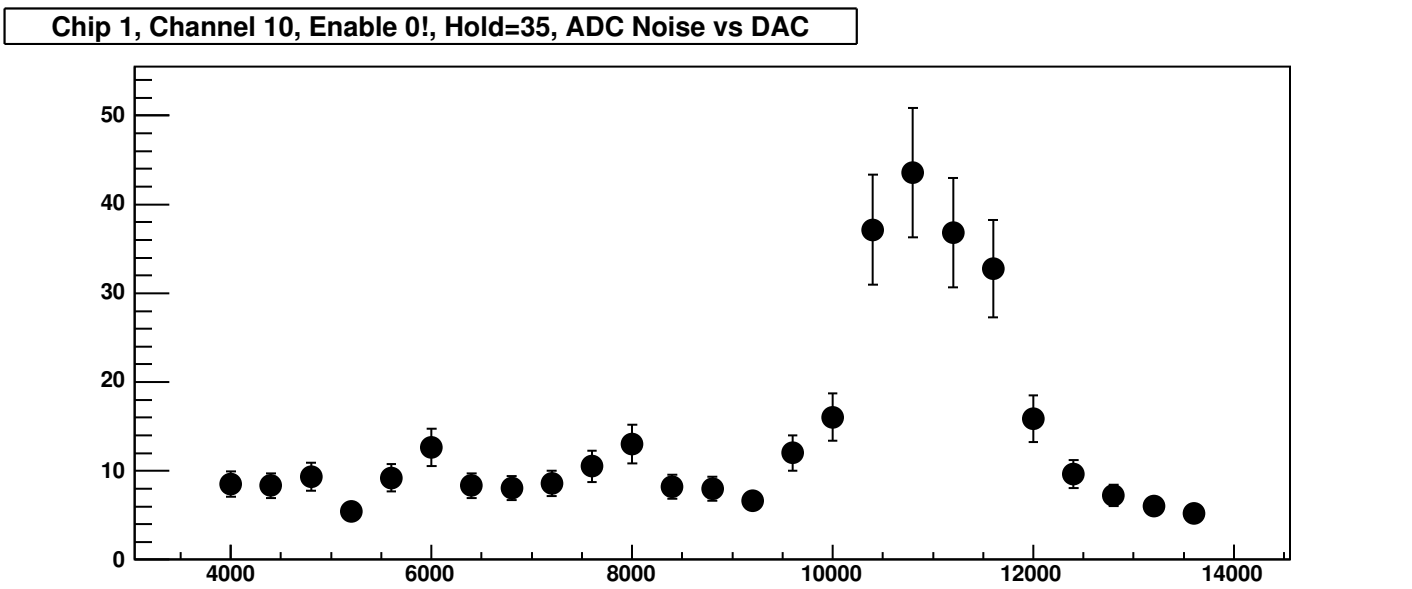

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

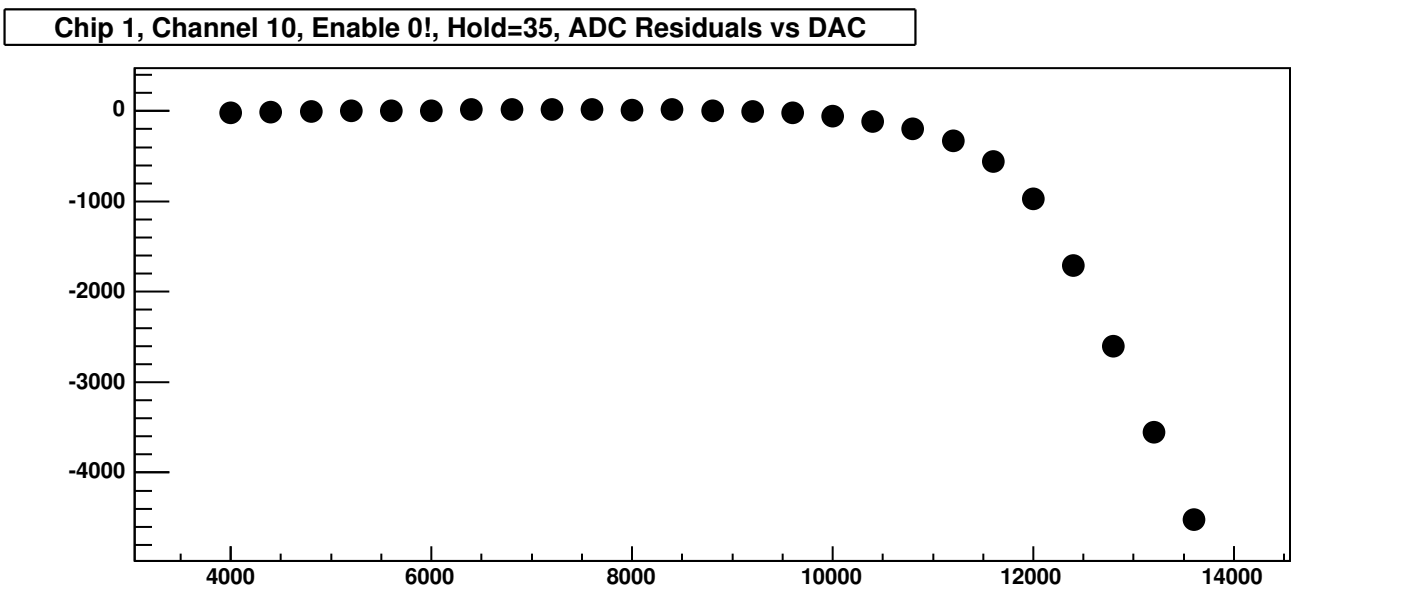

Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 9.249 / 16
p0 168.5 ± 1.258
p1 0.01808 ± 0.0001611
380
360
340
320
300
280
260
240
4000
6000
8000
10000
12000
14000

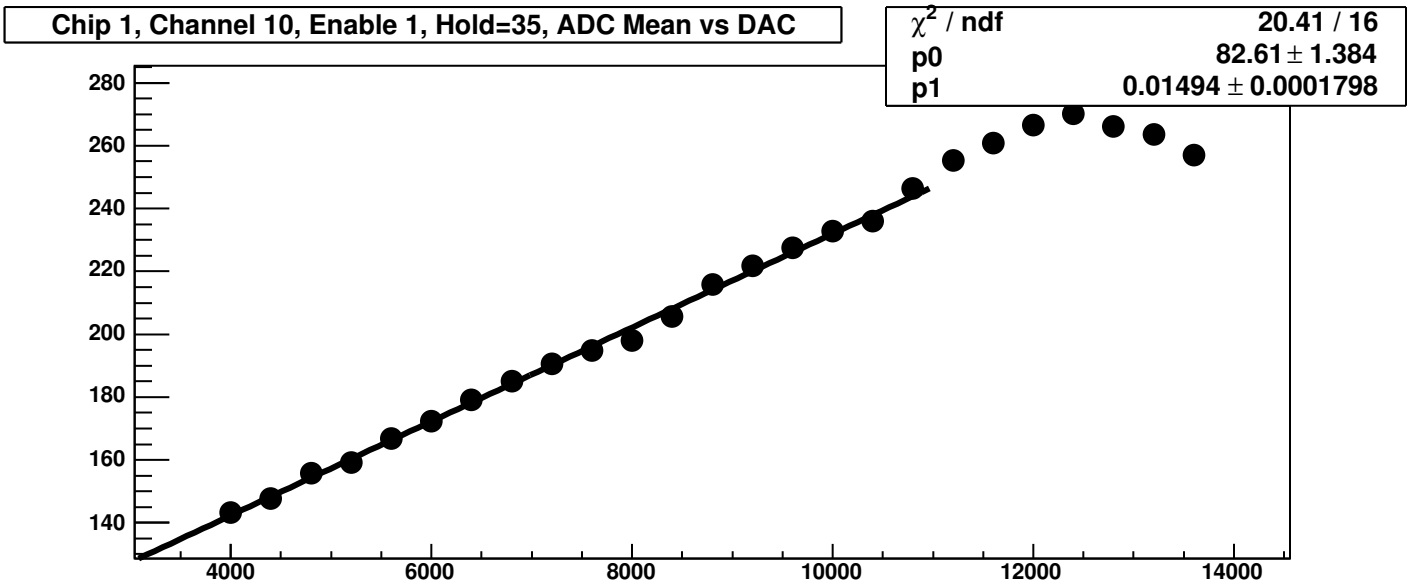

Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

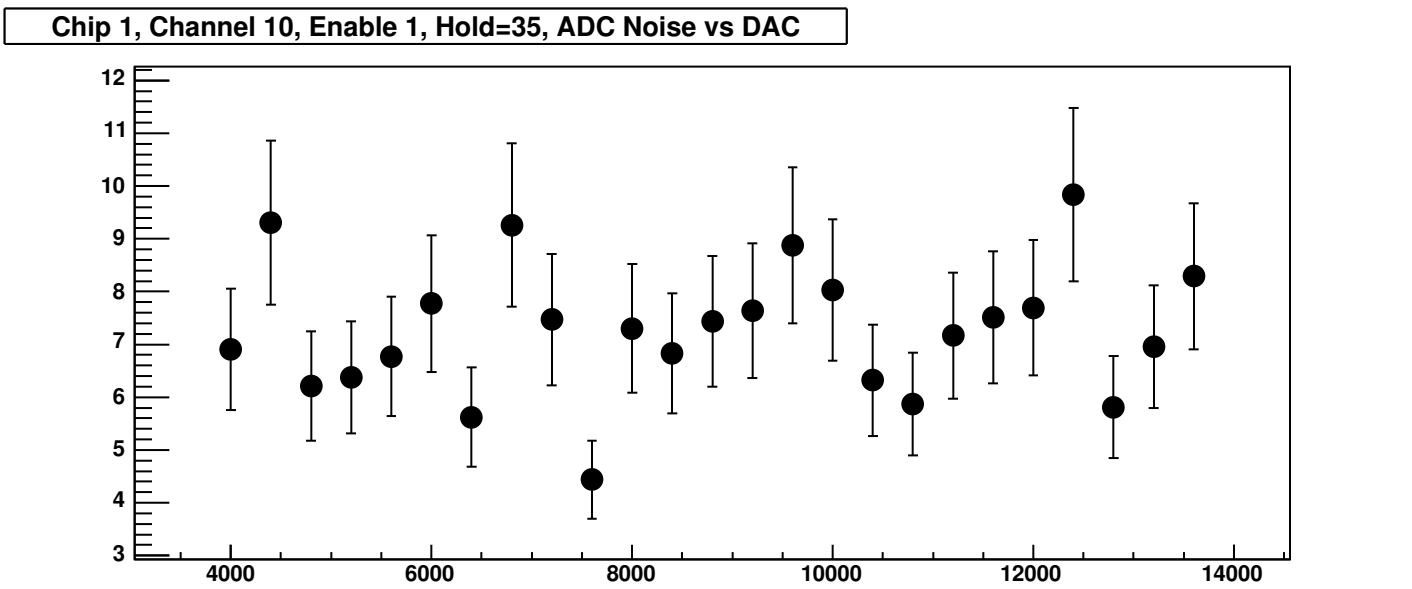

Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000

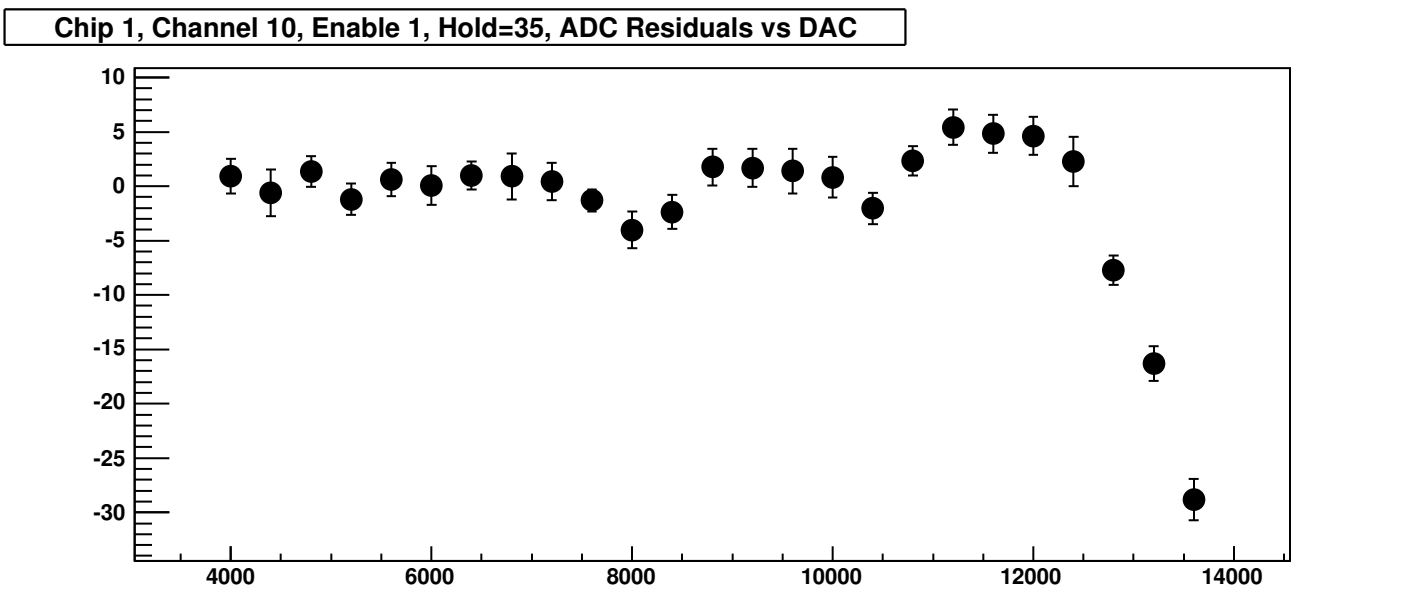

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1371 / 16
p0 290.8 ± 1.915
p1 2.514 ± 0.0002735
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

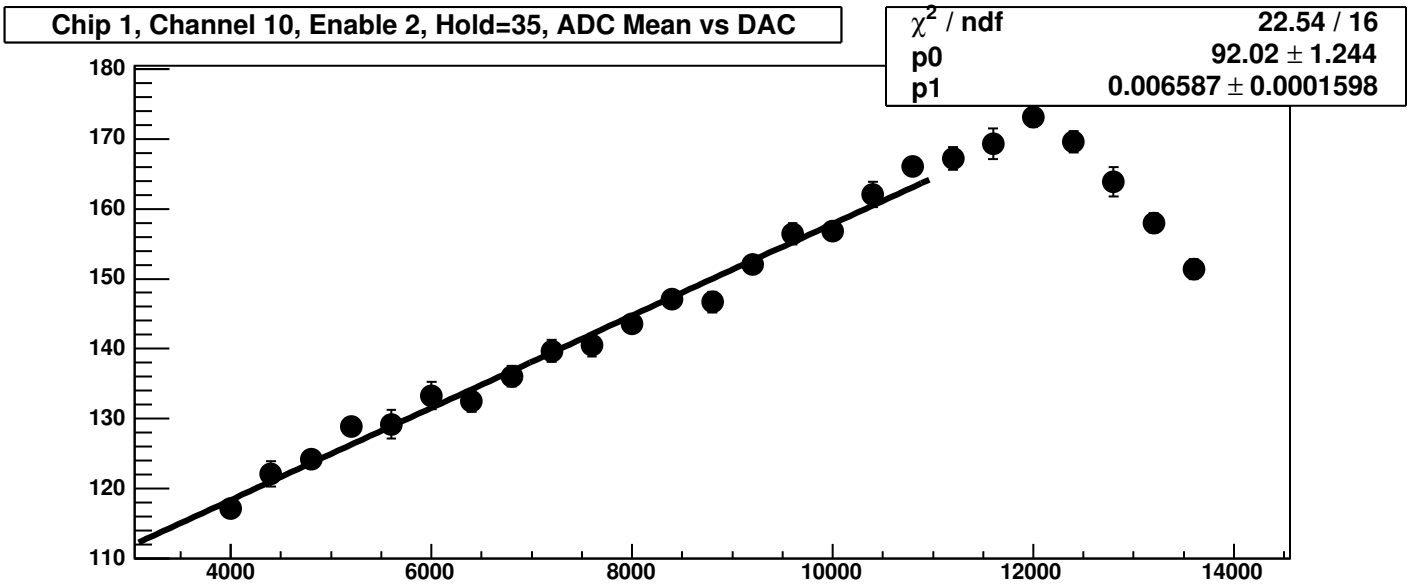

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

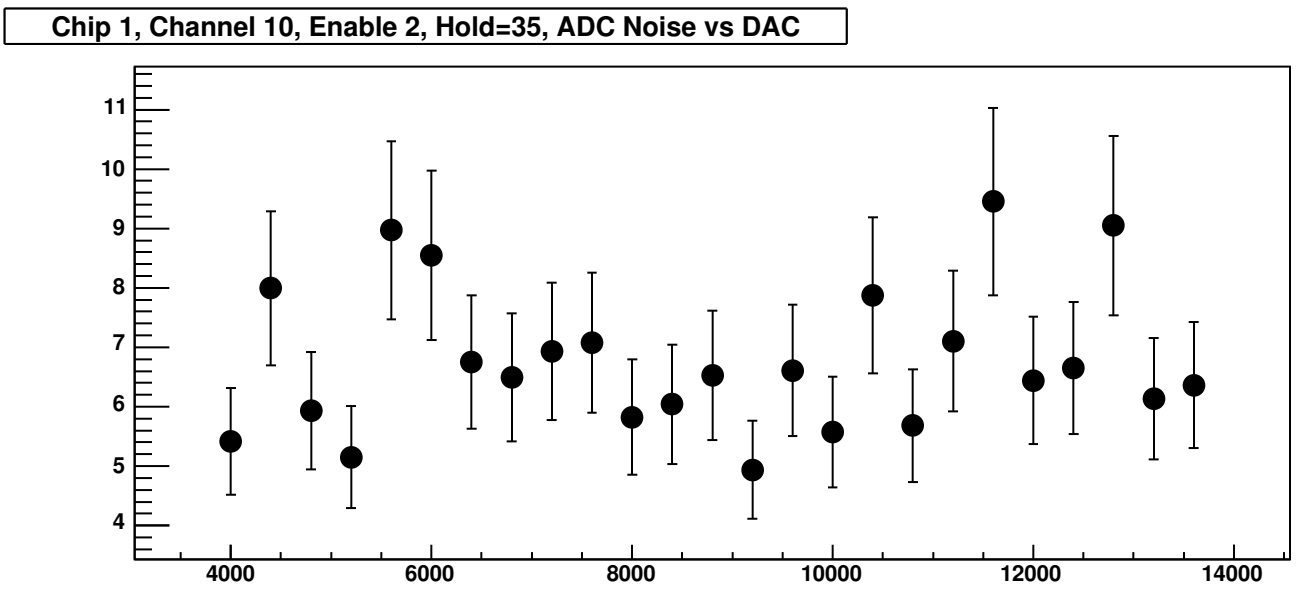

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

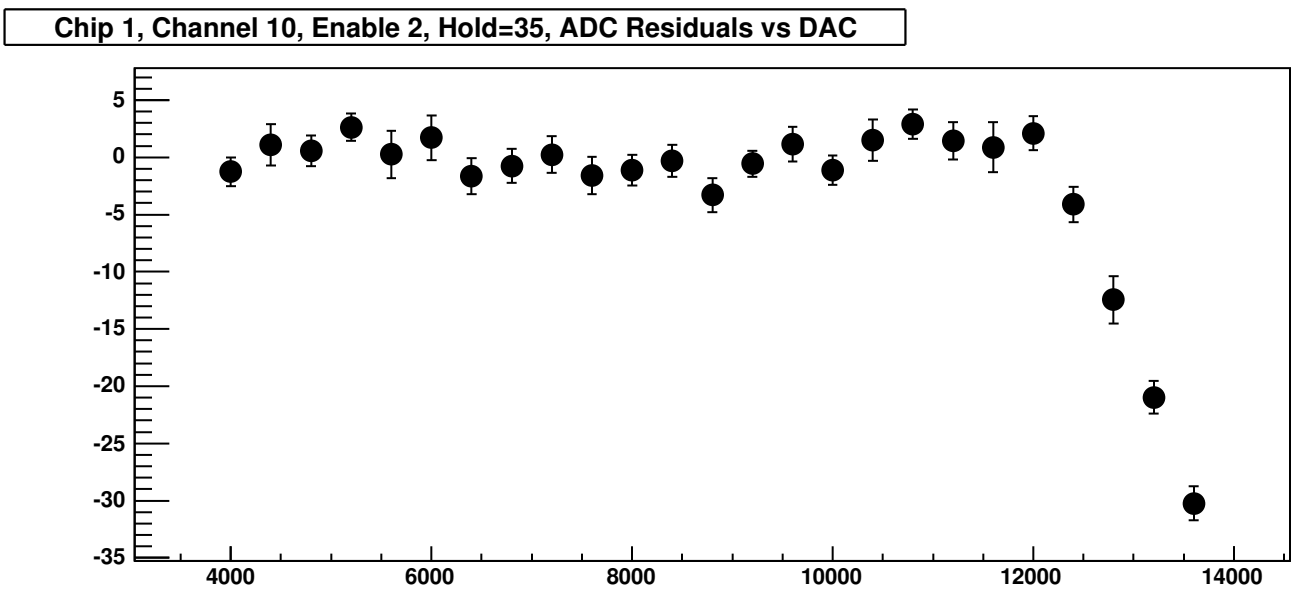

Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.41 / 16
p0 82.61 ± 1.384
p1 0.01494 ± 0.0001798
280
260
240
220
200
180
160
140
4000
6000
8000
10000
12000
14000

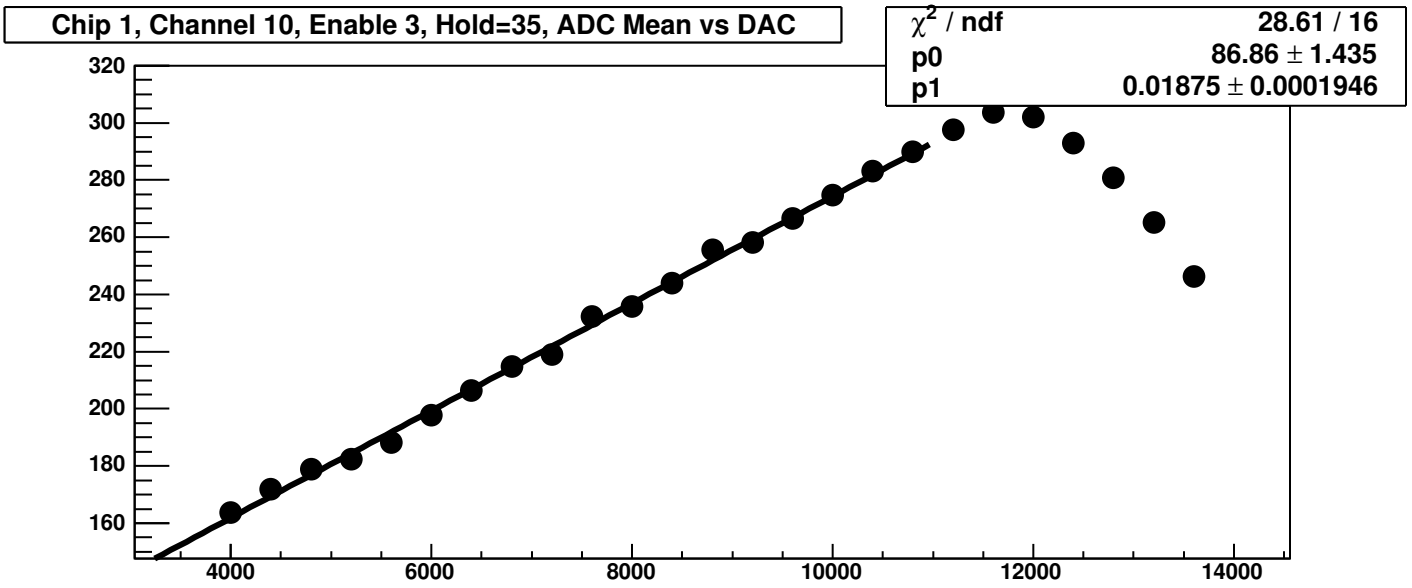

Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

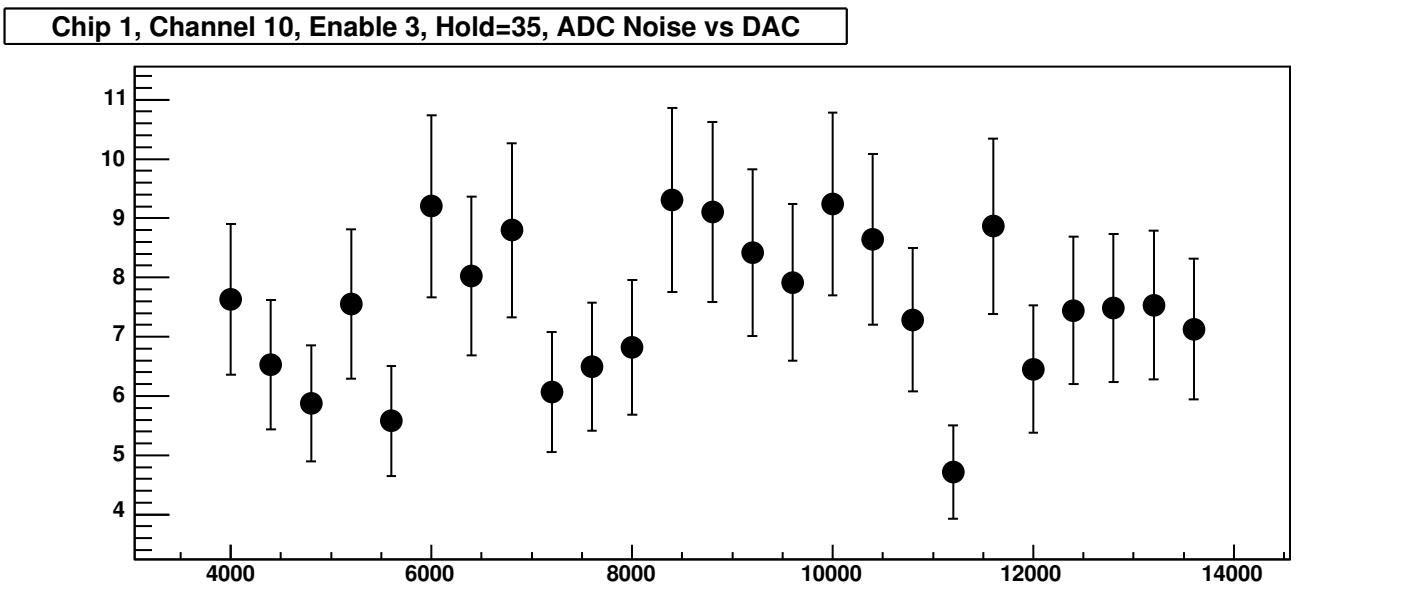

Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

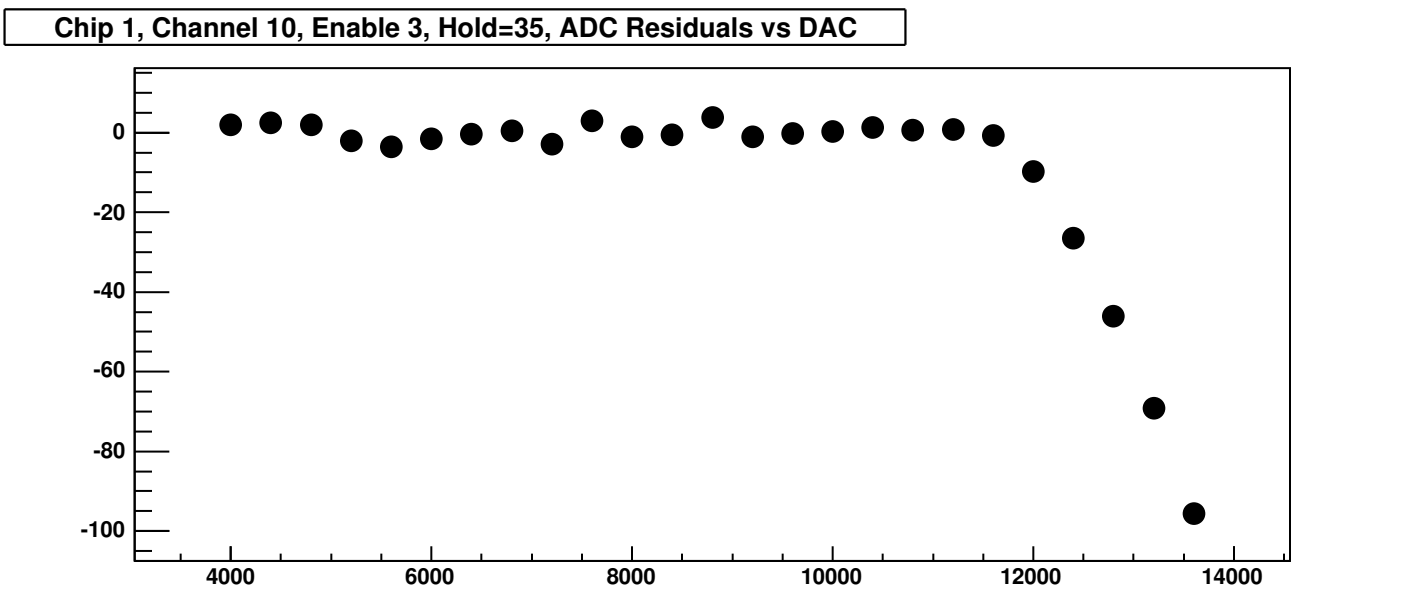

Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.54 / 16
p0 92.02 ± 1.244
p1 0.006587 ± 0.0001598
180
170
160
150
140
130
120
110
4000
6000
8000
10000
12000
14000

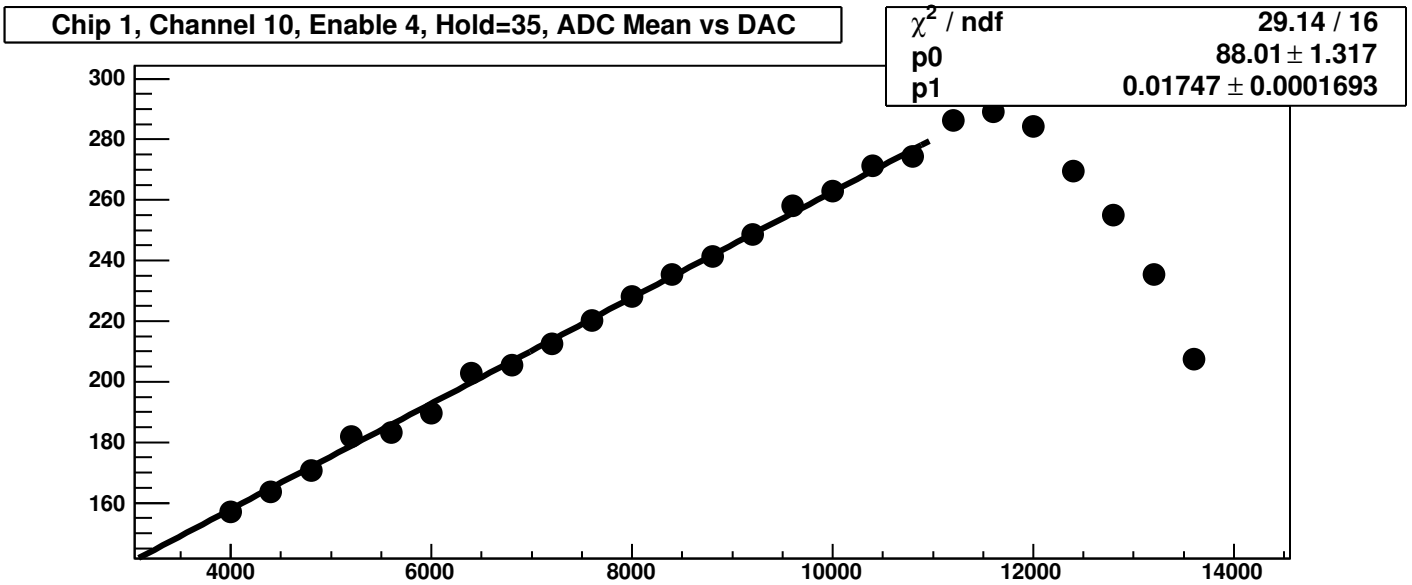

Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

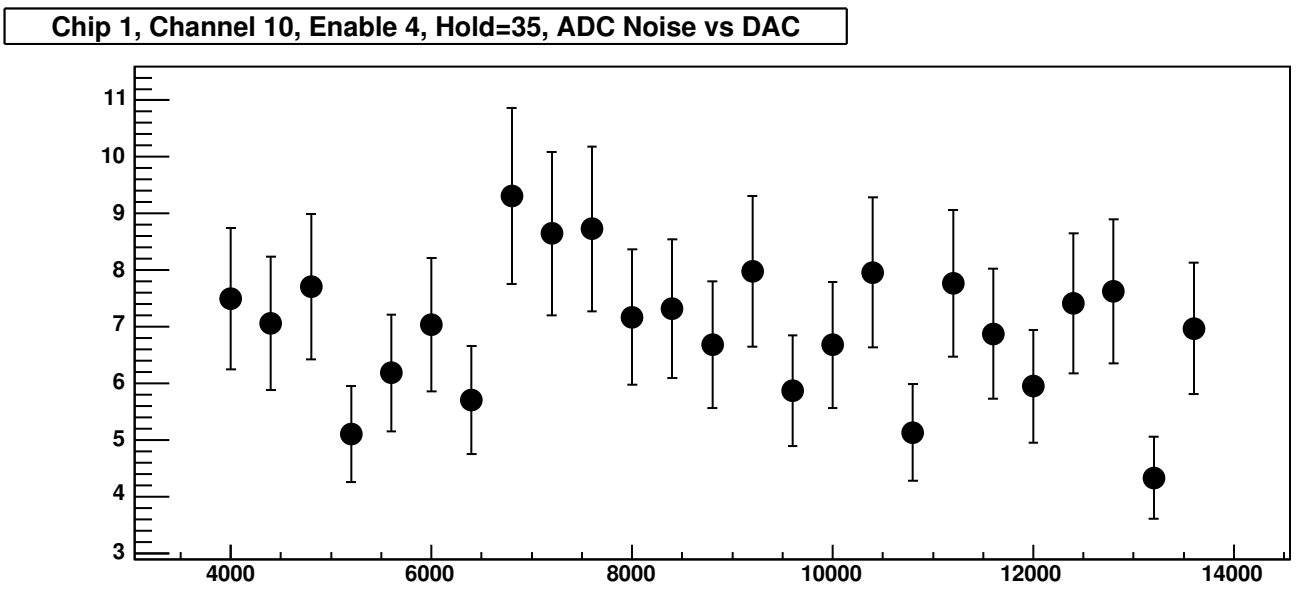

Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

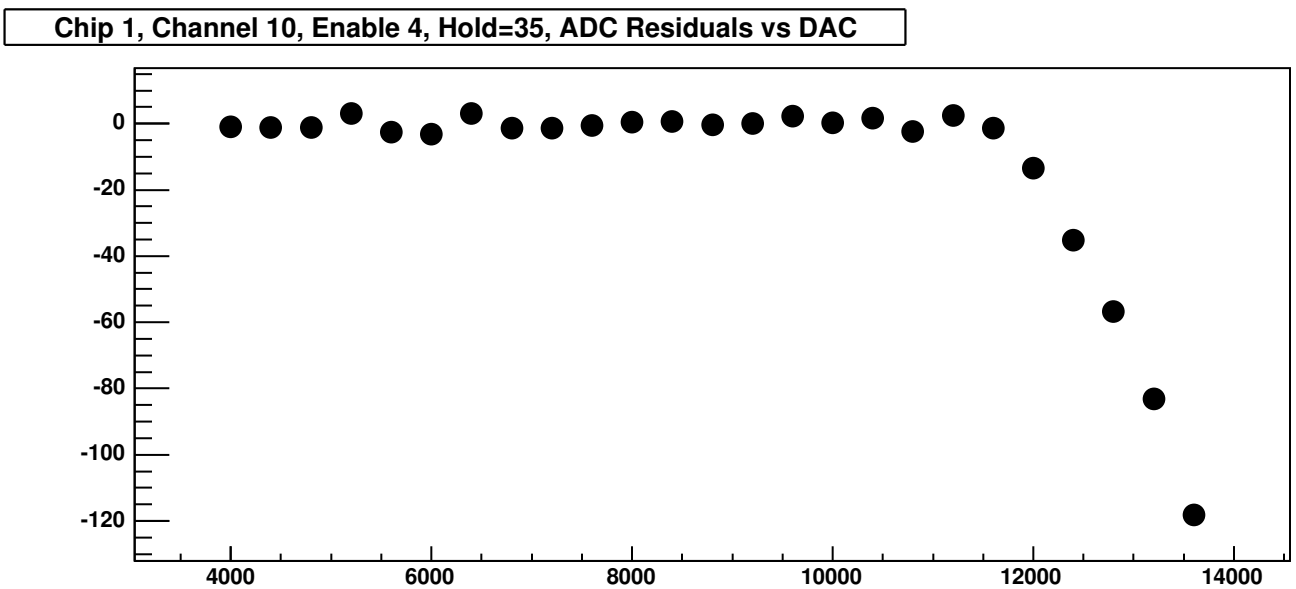

Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.61 / 16
p0 86.86 ± 1.435
p1 0.01875 ± 0.0001946
320
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

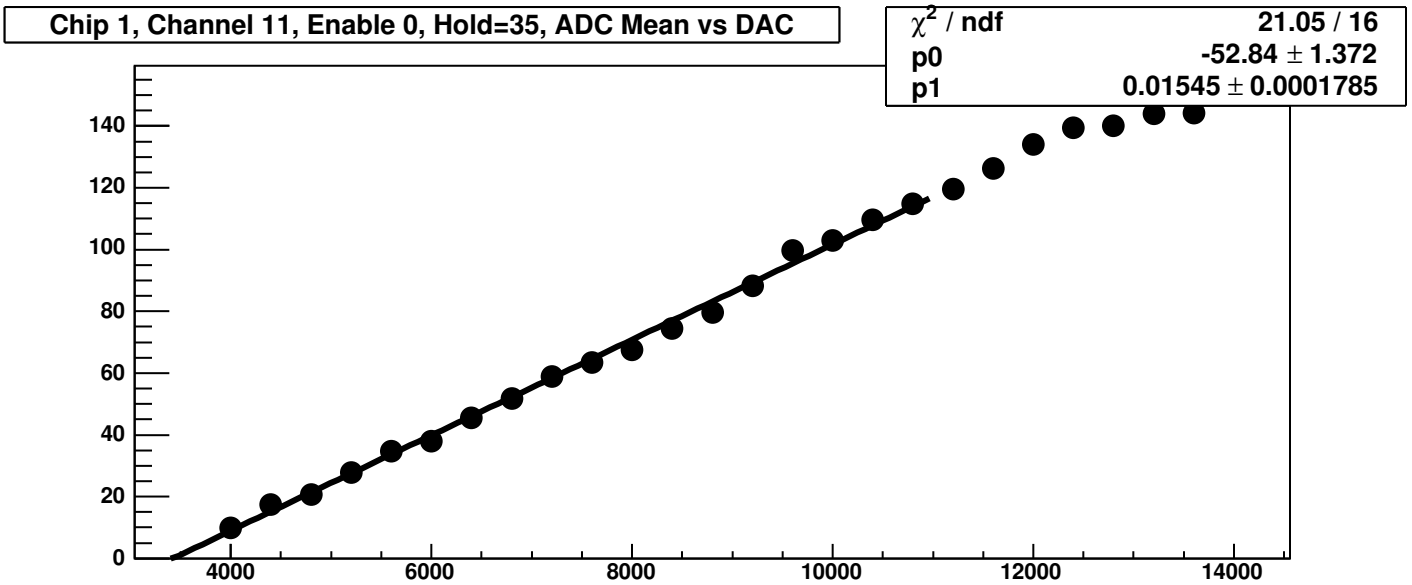

Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

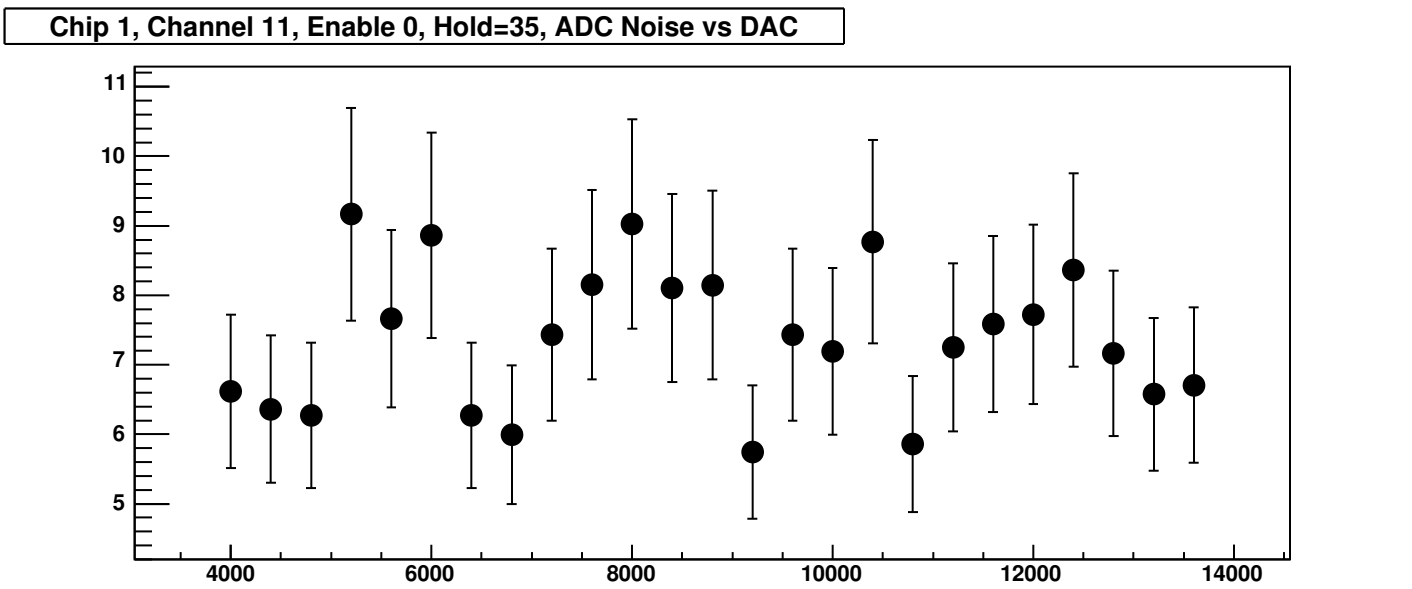

Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

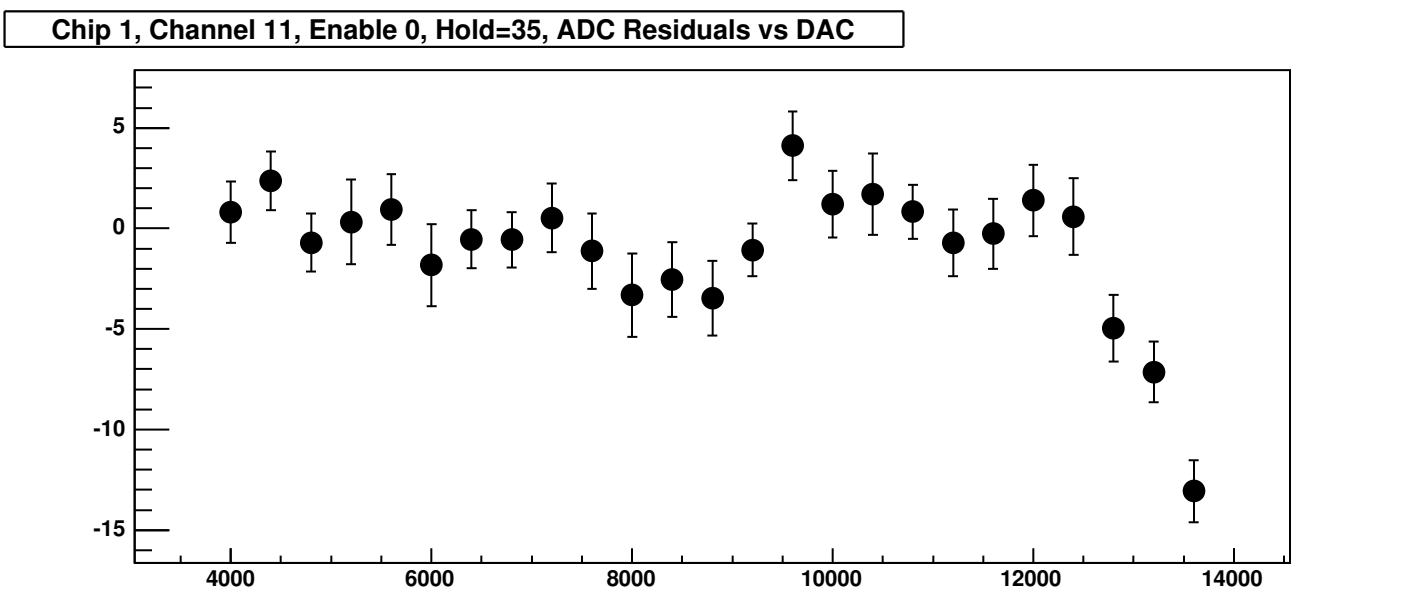

Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.14 / 16
p0 88.01 ± 1.317
p1 0.01747 ± 0.0001693
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

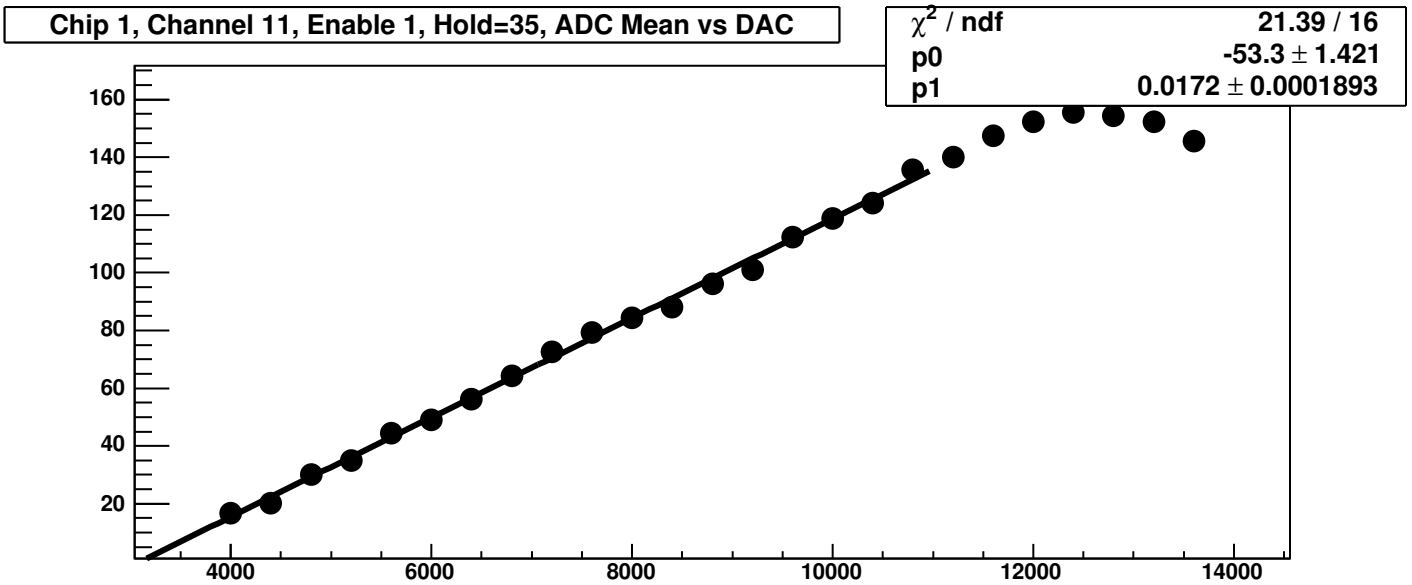

Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

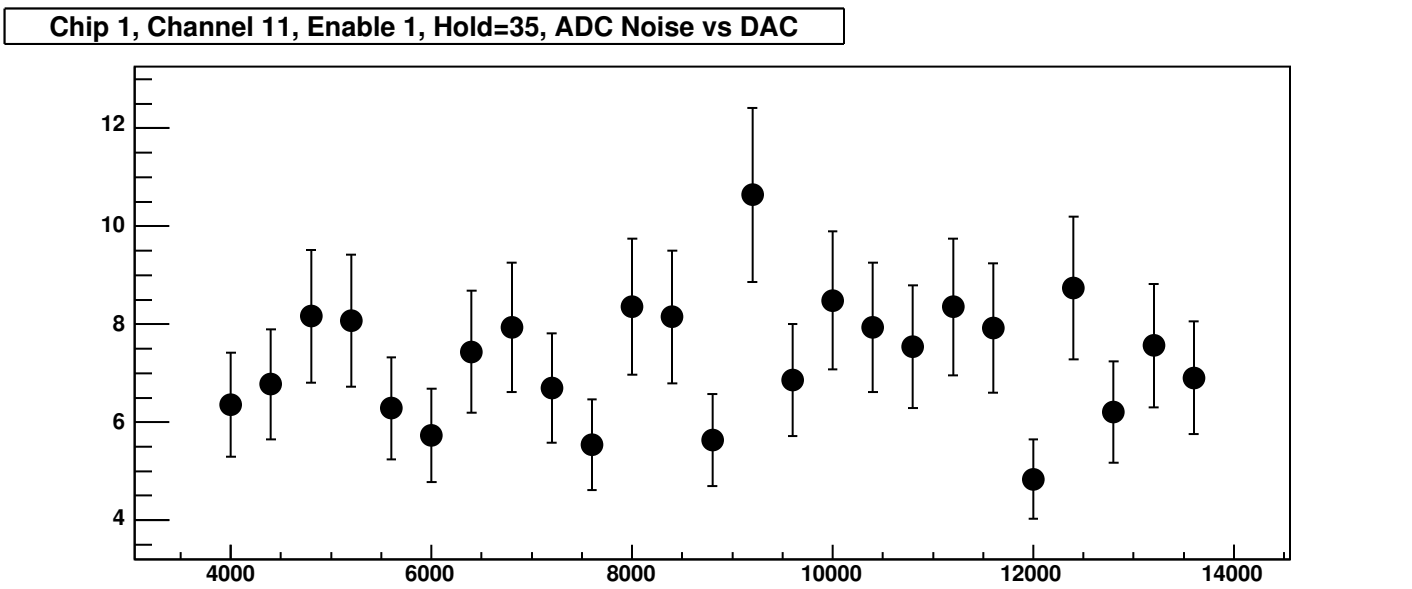

Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
-120
4000
6000
8000
10000
12000
14000

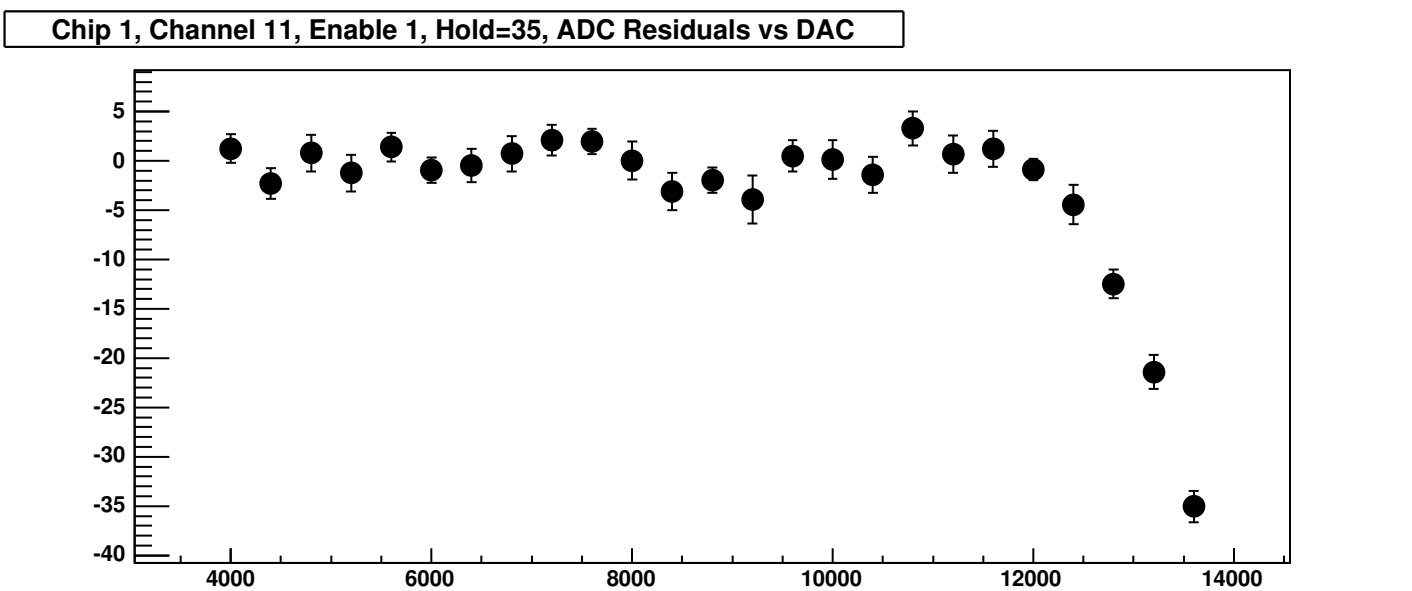

Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 21.05 / 16
p0 -52.84 ± 1.372
p1 0.01545 ± 0.0001785
0
20
40
60
80
100
120
140
4000
6000
8000
10000
12000
14000

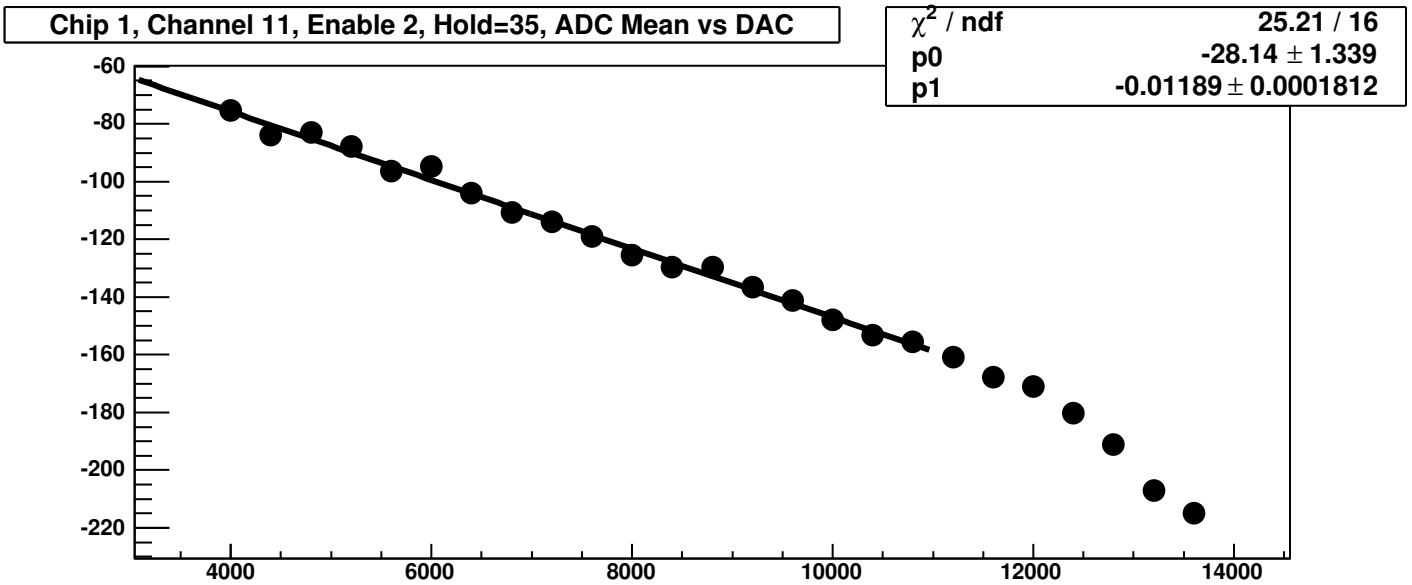

Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
5
6
7
8
9
10
11
4000
6000
8000
10000
12000
14000

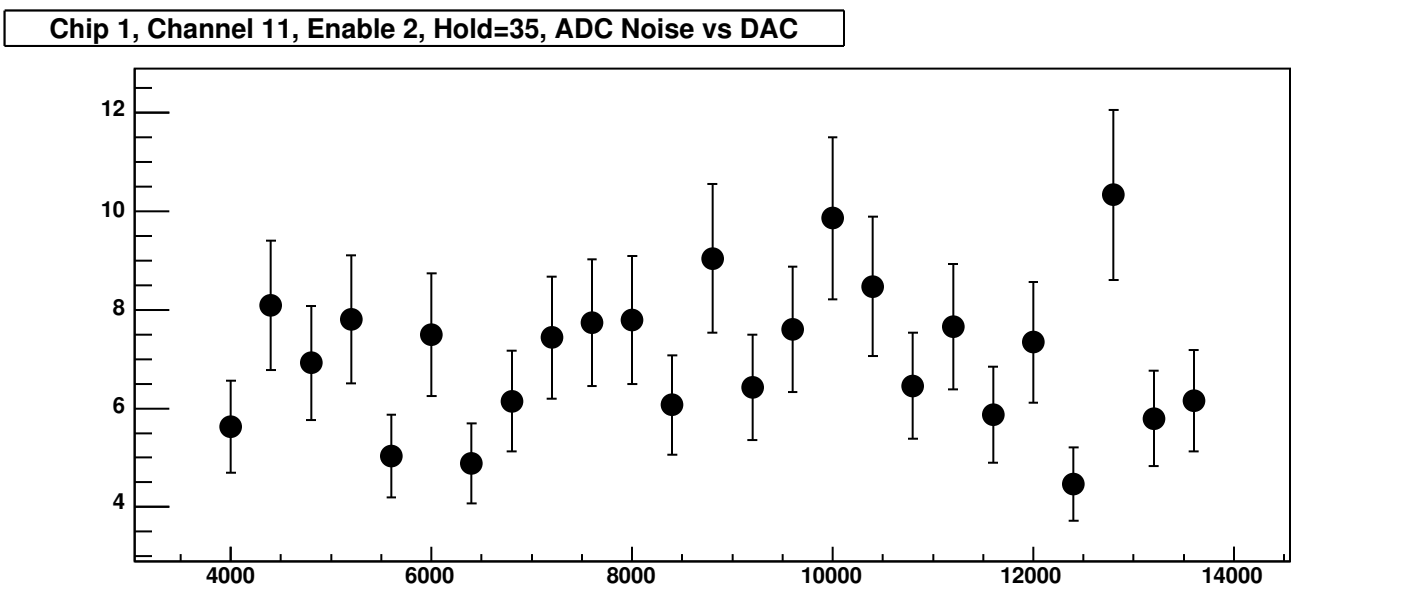

Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
-15
-10
-5
0
5
4000
6000
8000
10000
12000
14000

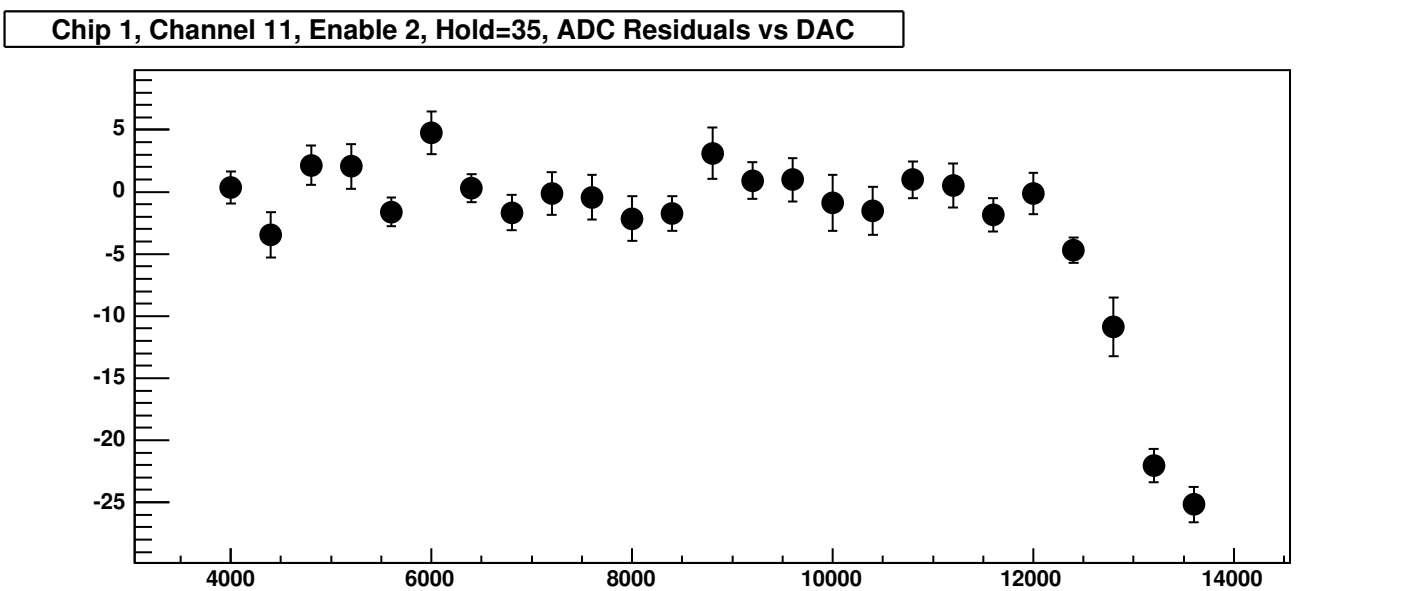

Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.39 / 16
p0 -53.3 ± 1.421
p1 0.0172 ± 0.0001893
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000

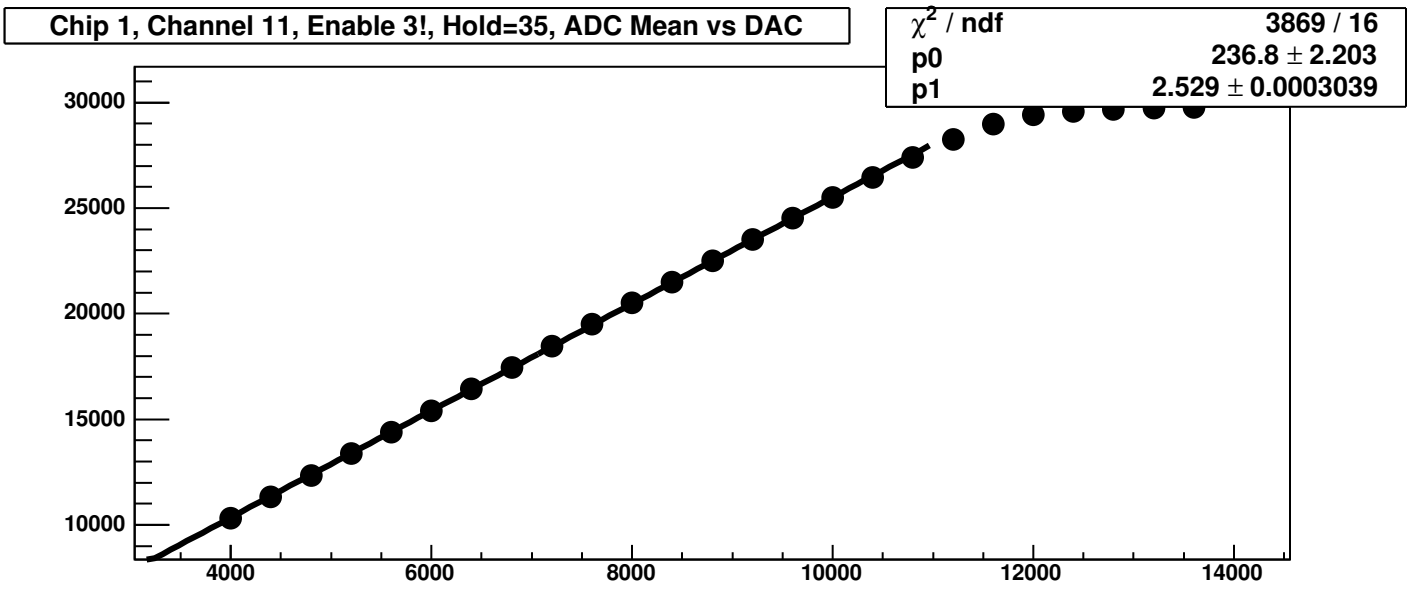

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

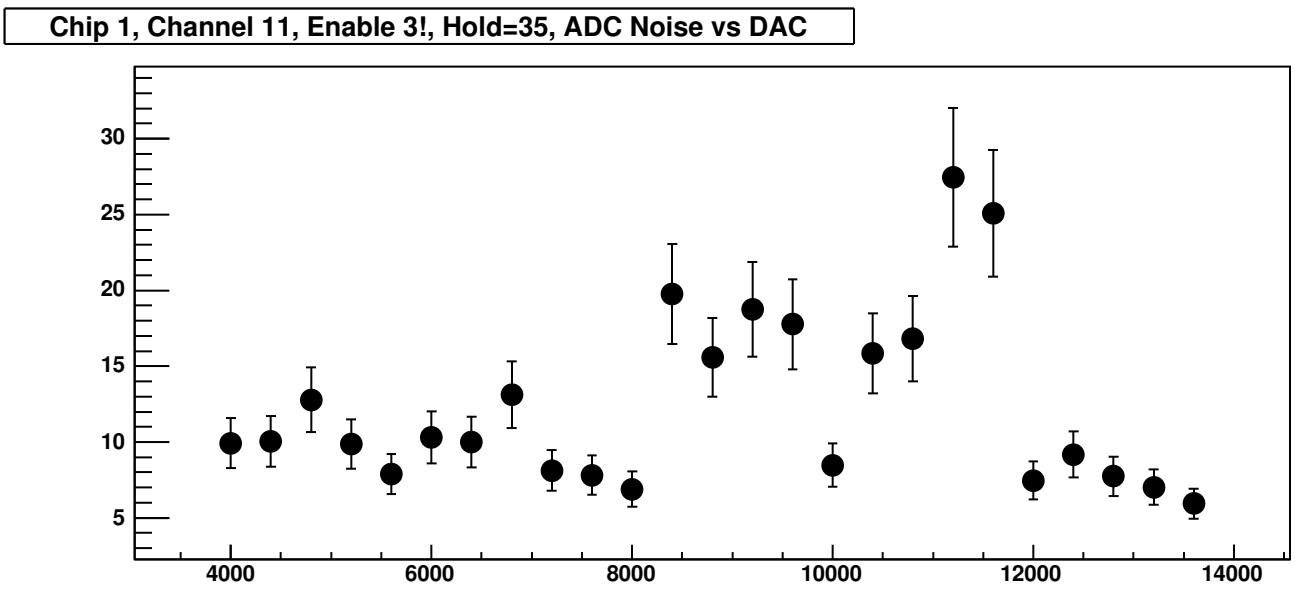

Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
-40
4000
6000
8000
10000
12000
14000

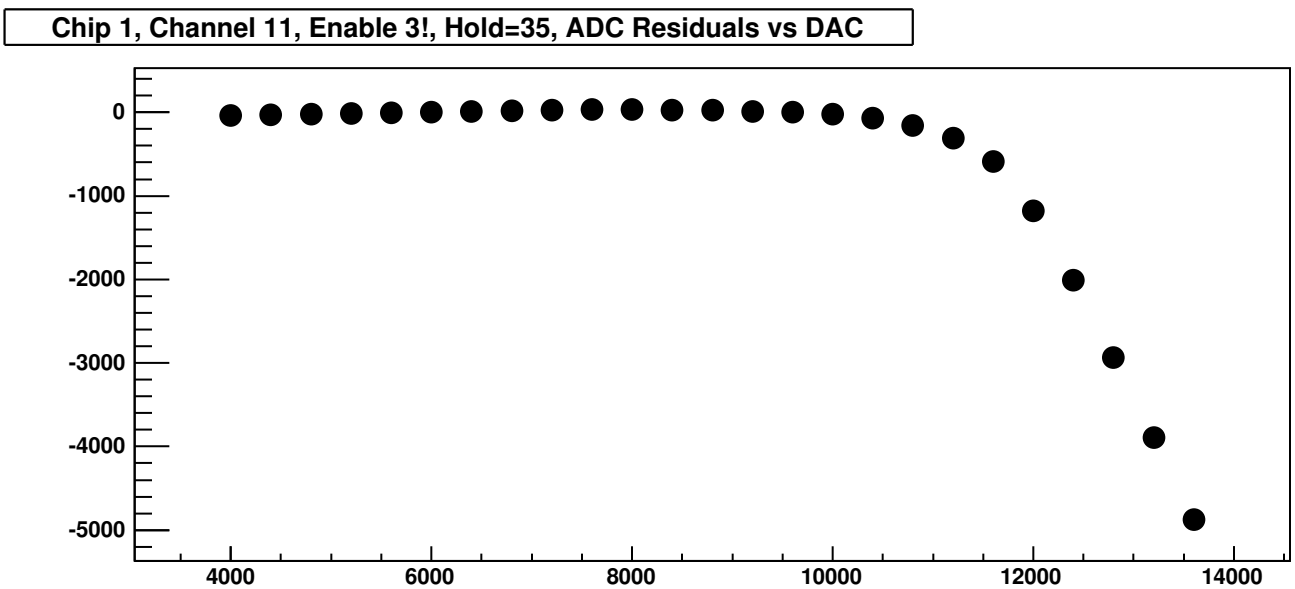

Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.21 / 16
p0 -28.14 ± 1.339
p1 -0.01189 ± 0.0001812
-60
-80
-100
-120
-140
-160
-180
-200
-220
4000
6000
8000
10000
12000
14000

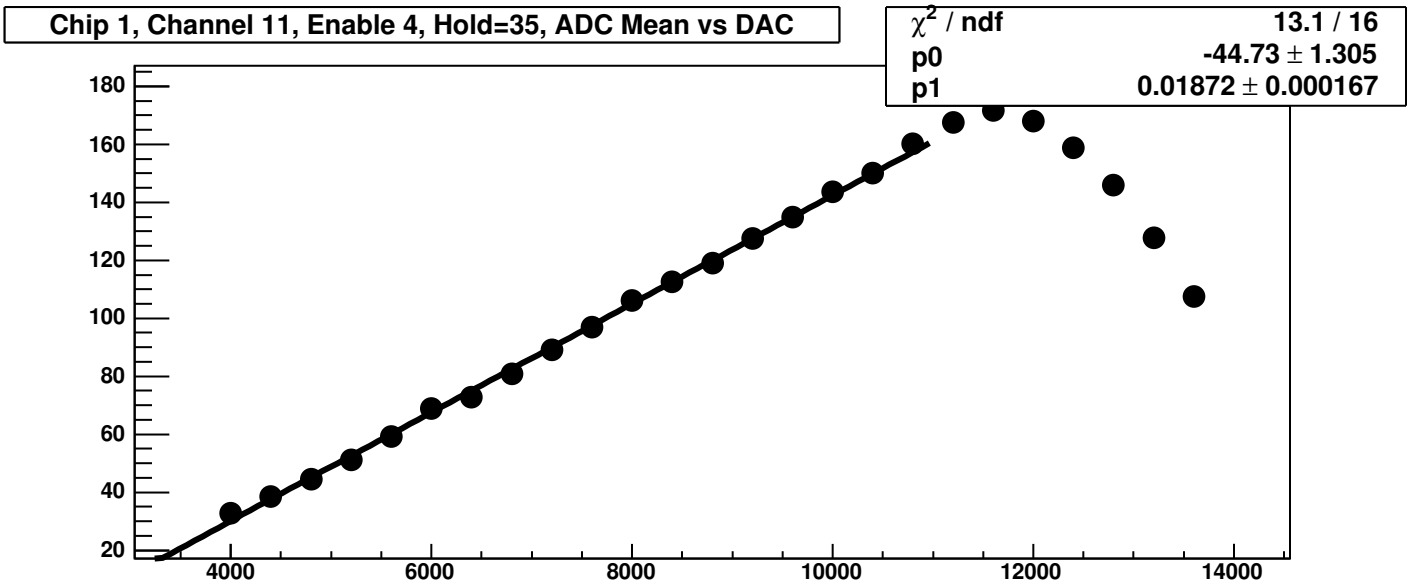

Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

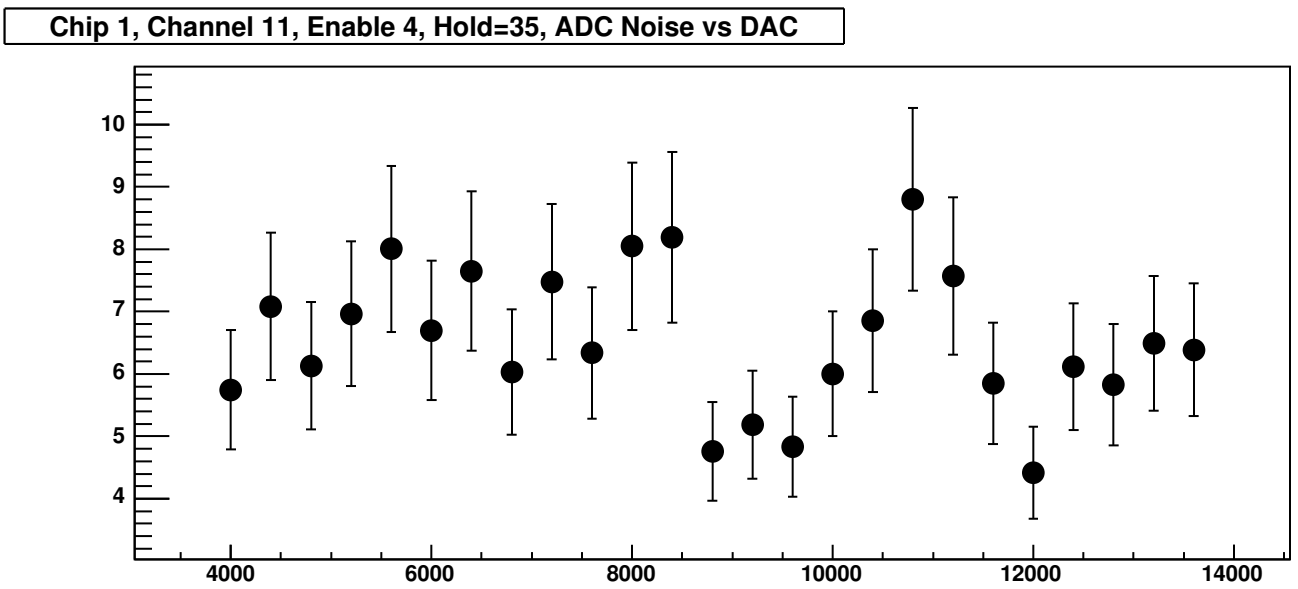

Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

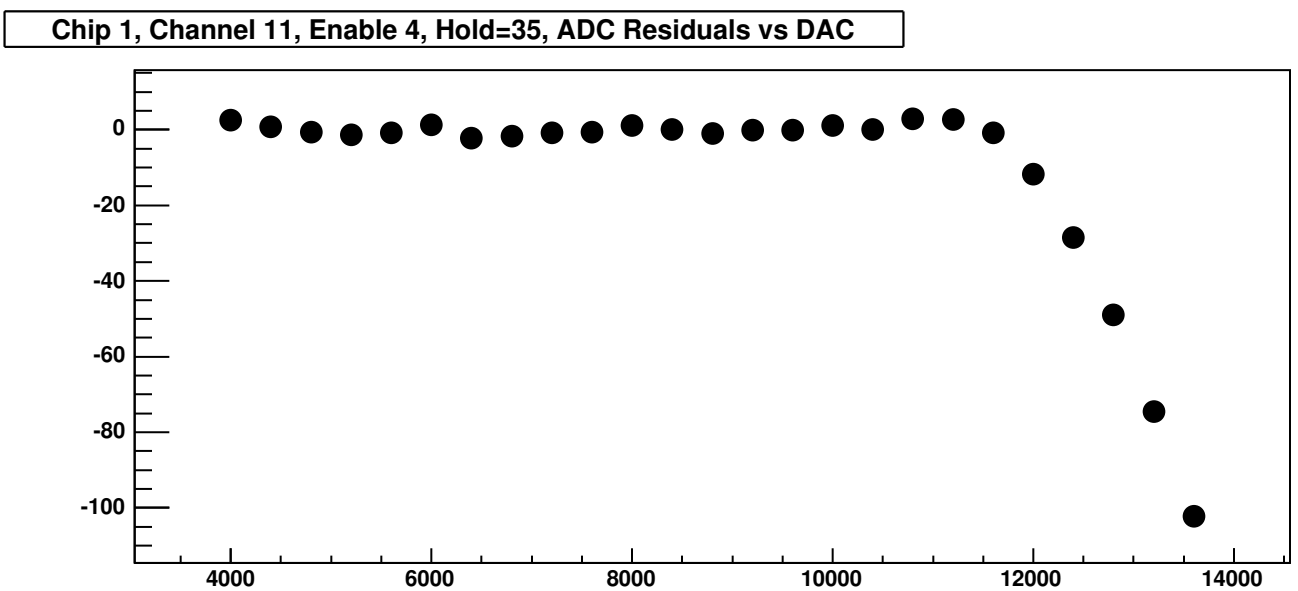

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 3869 / 16
p0 236.8 ± 2.203
p1 2.529 ± 0.0003039
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

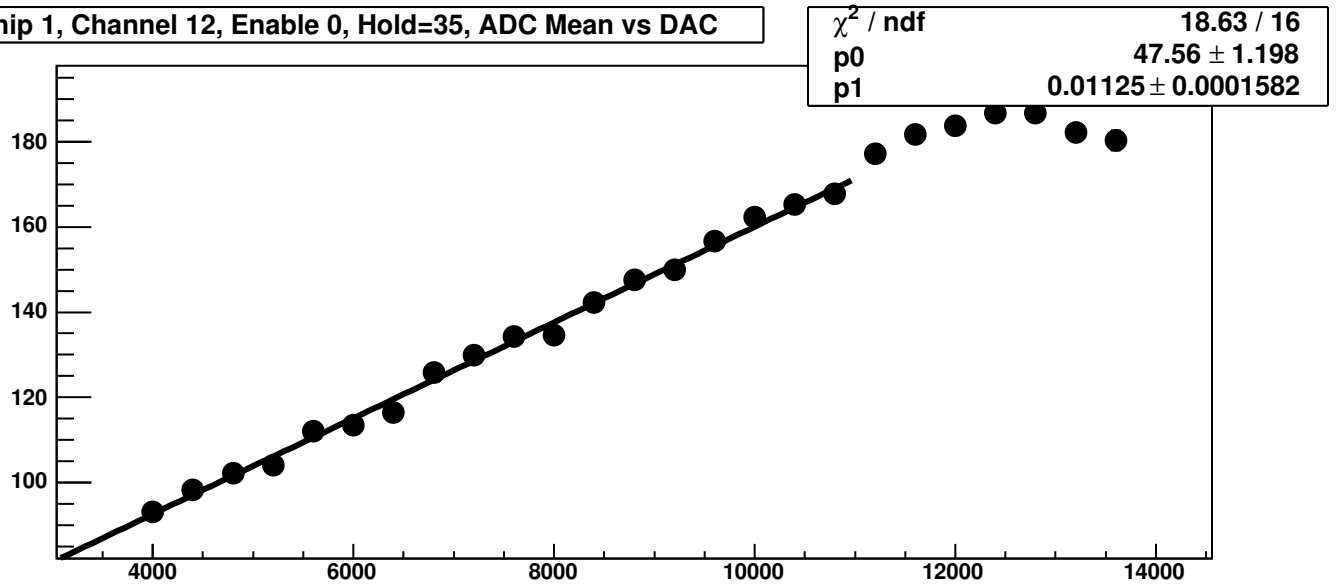

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.1 / 16
p0 -44.73 ± 1.305
p1 0.01872 ± 0.000167
180
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000

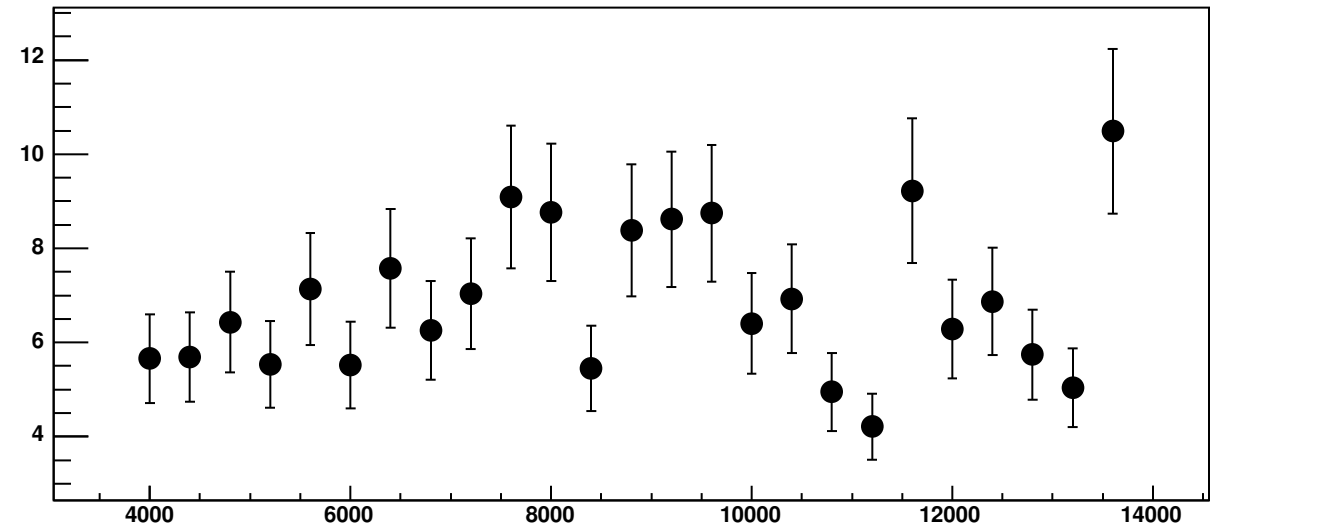

Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

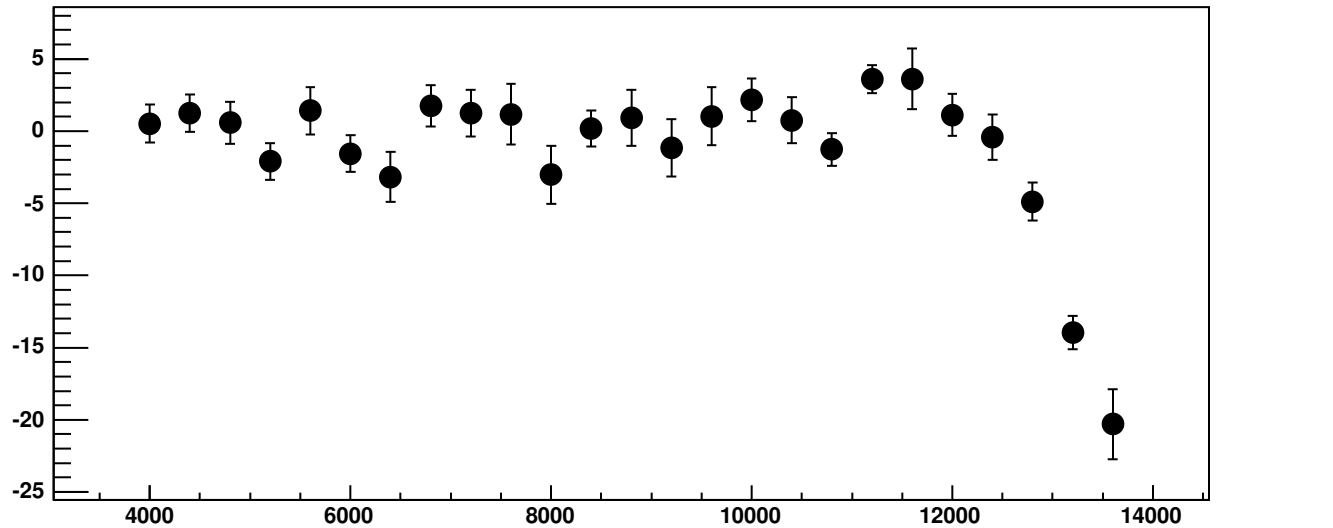
Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



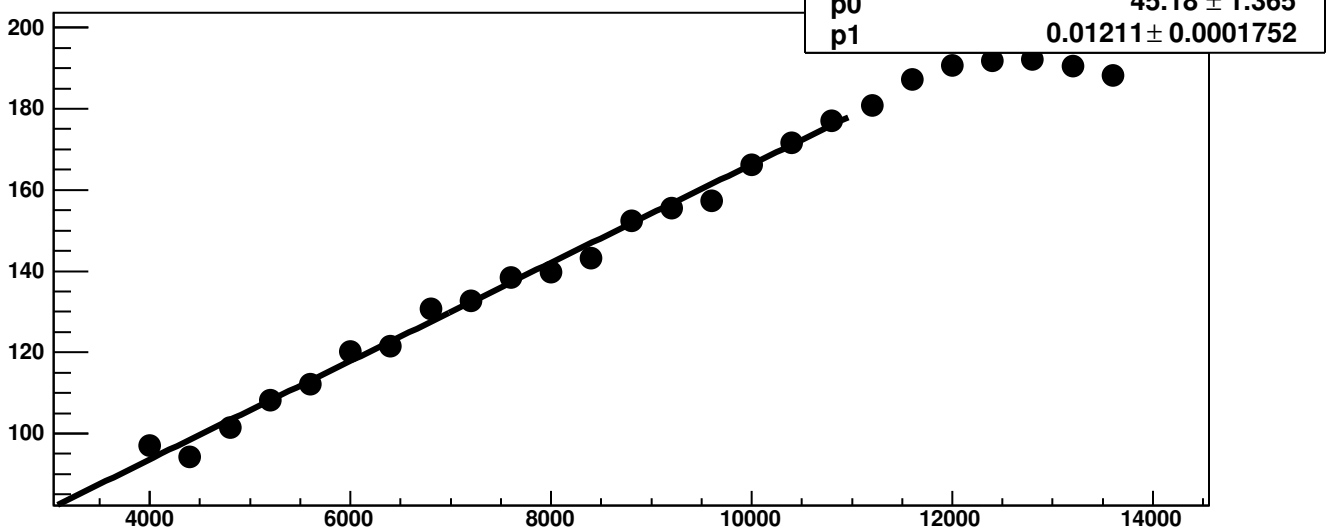
Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



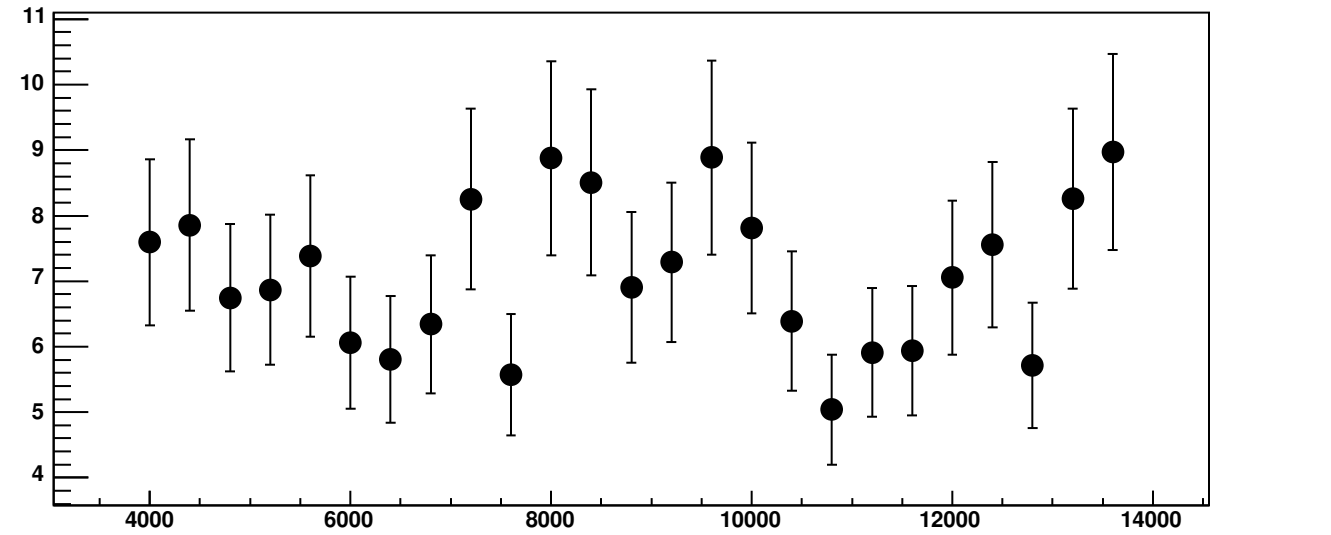
Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



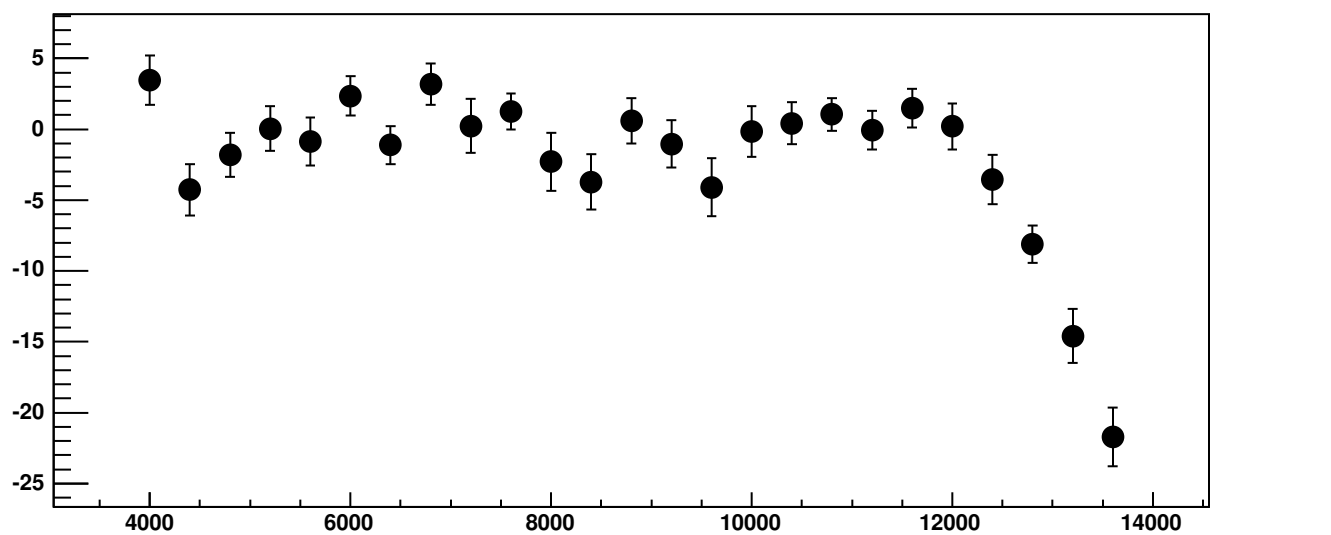
Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

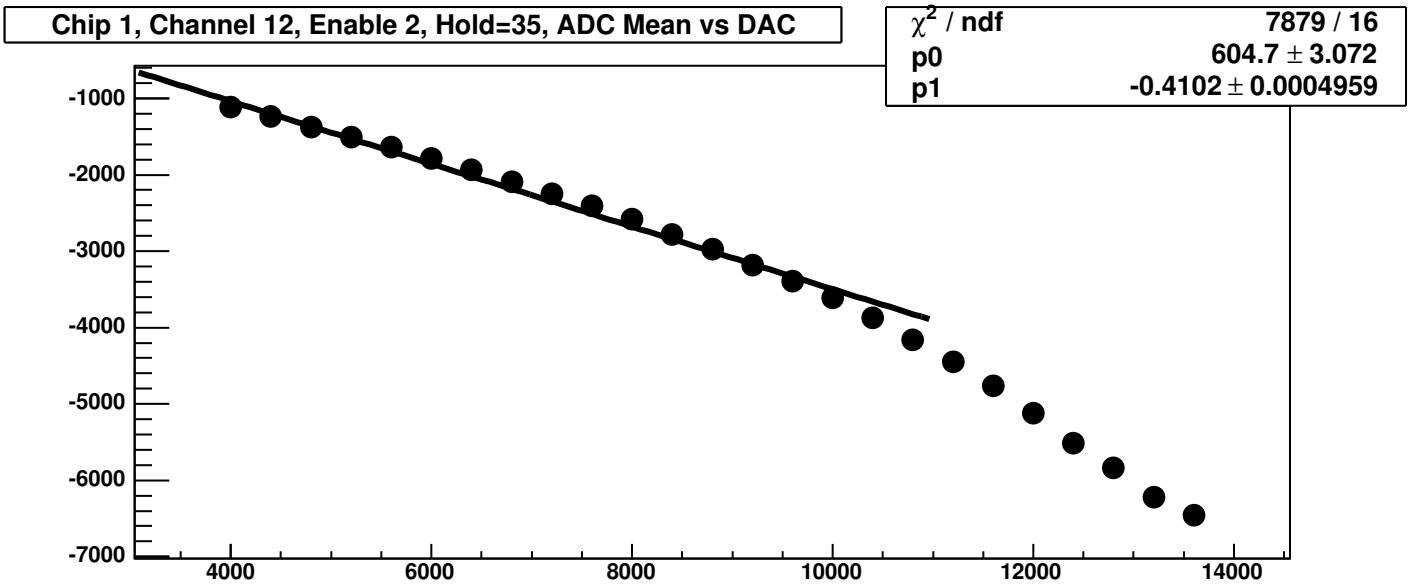


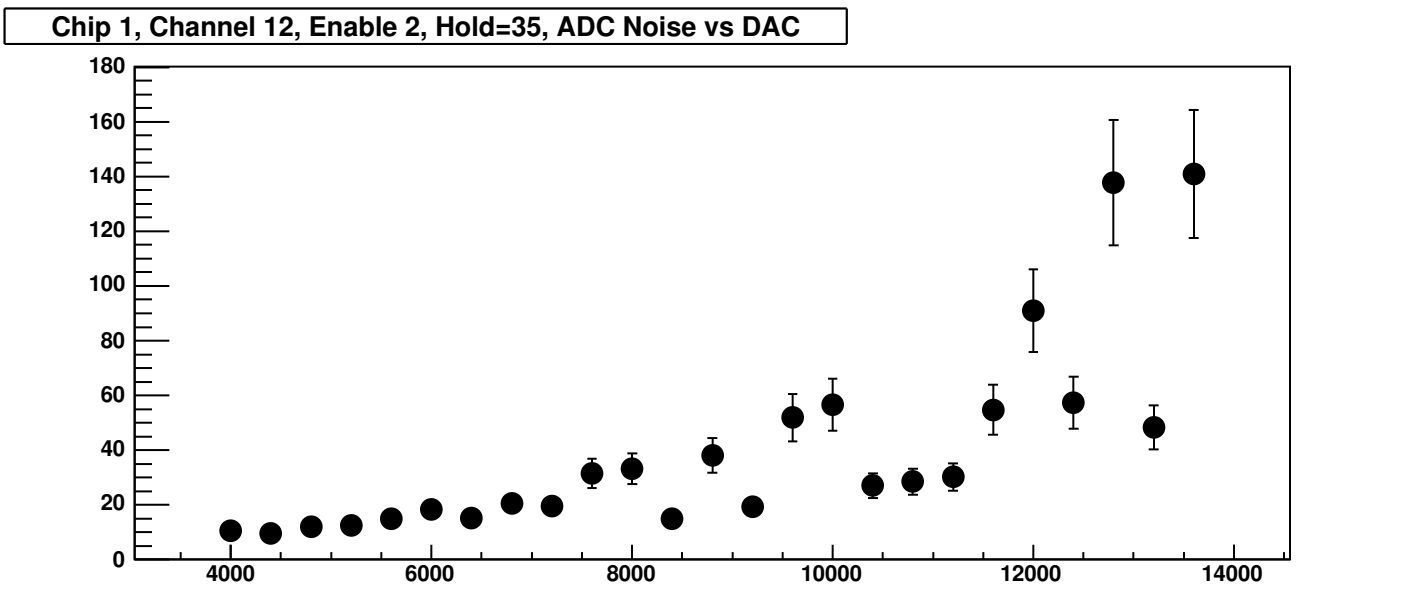
Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC

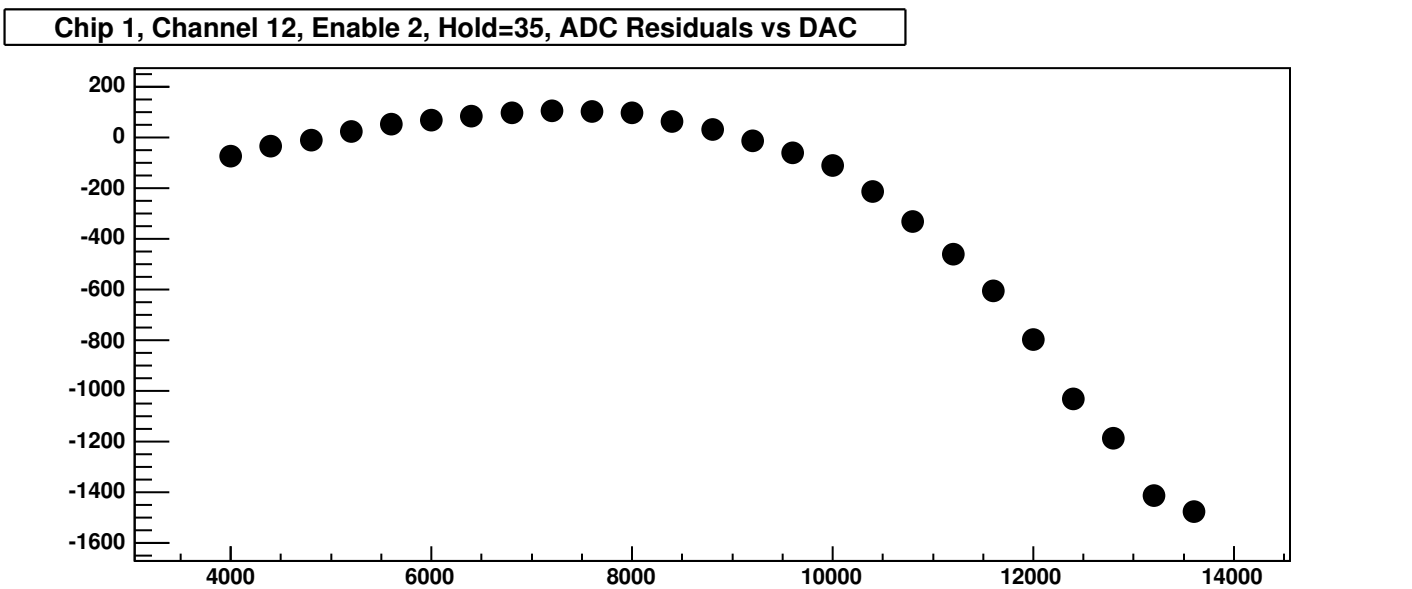


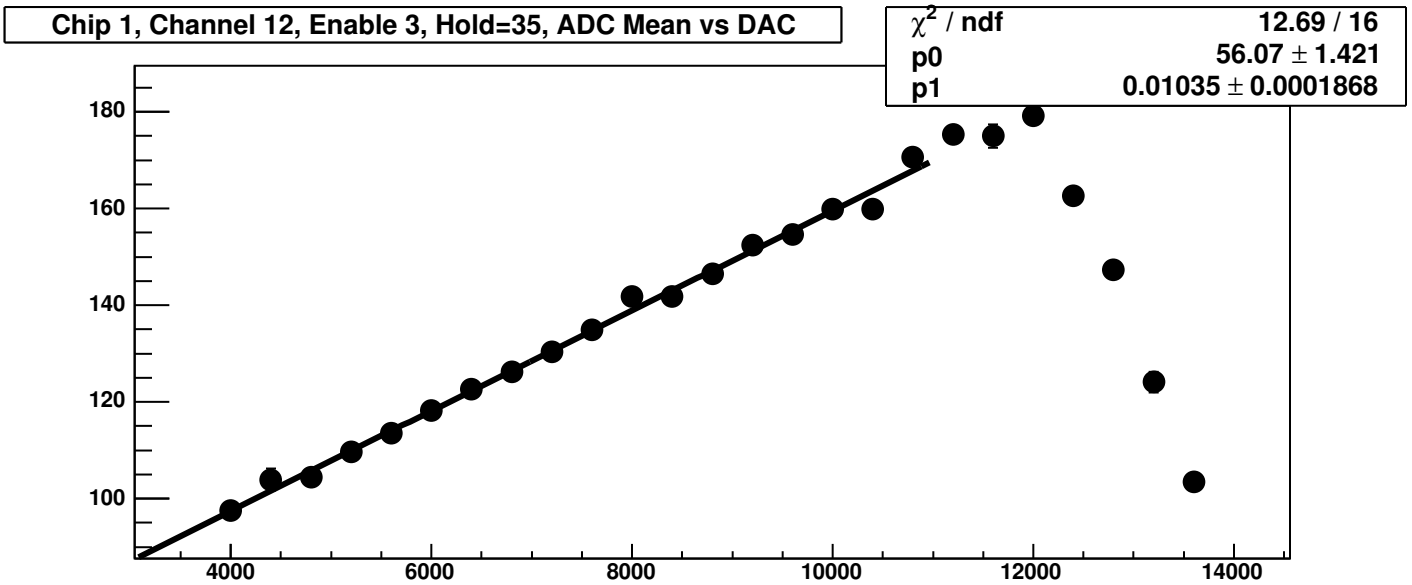
Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

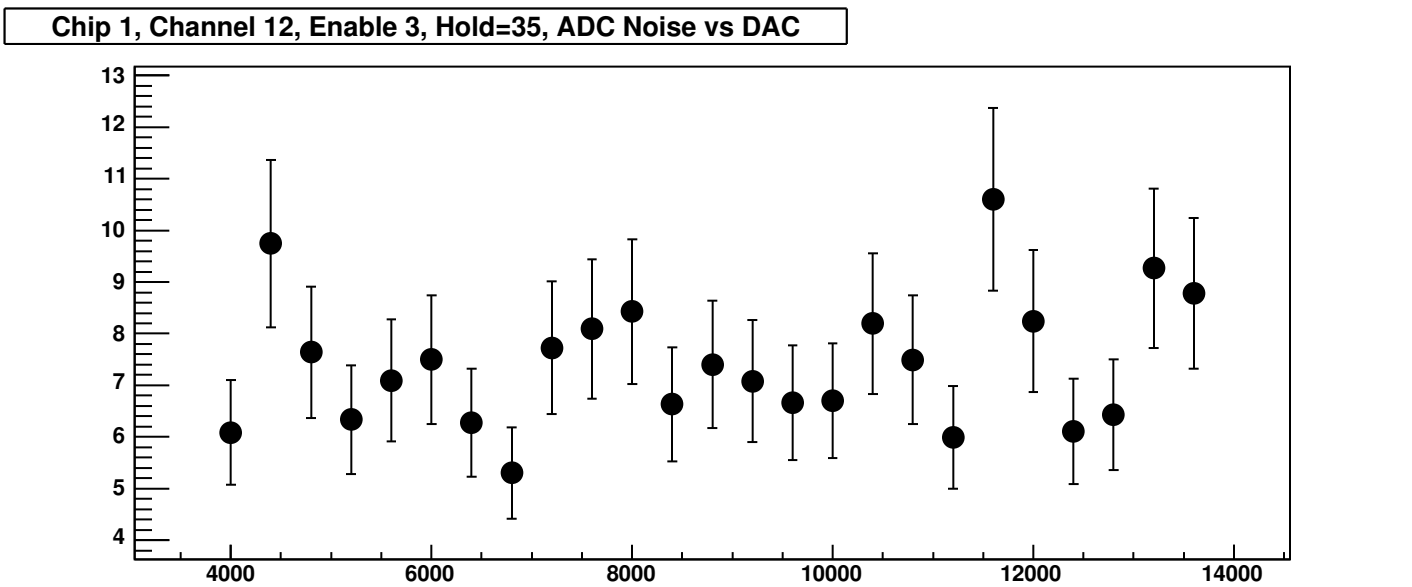


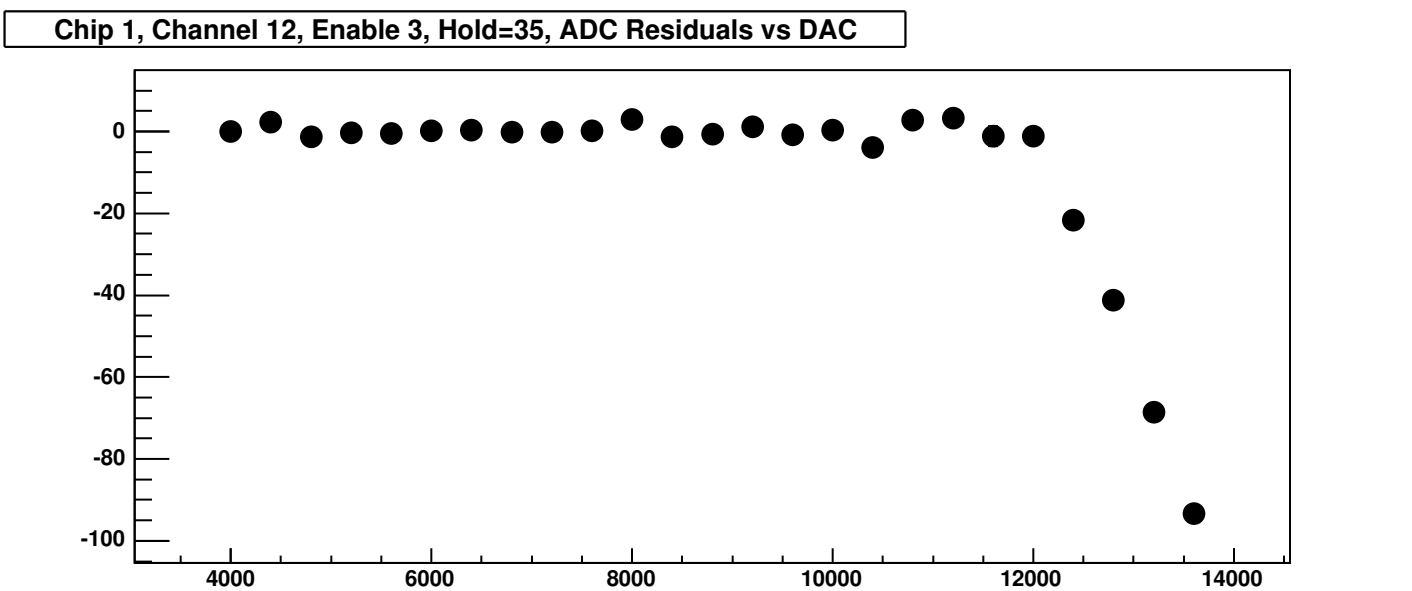

Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 7879 / 16
p0 604.7 ± 3.072
p1 -0.4102 ± 0.0004959
-1000
-2000
-3000
-4000
-5000
-6000
-7000
4000
6000
8000
10000
12000
14000

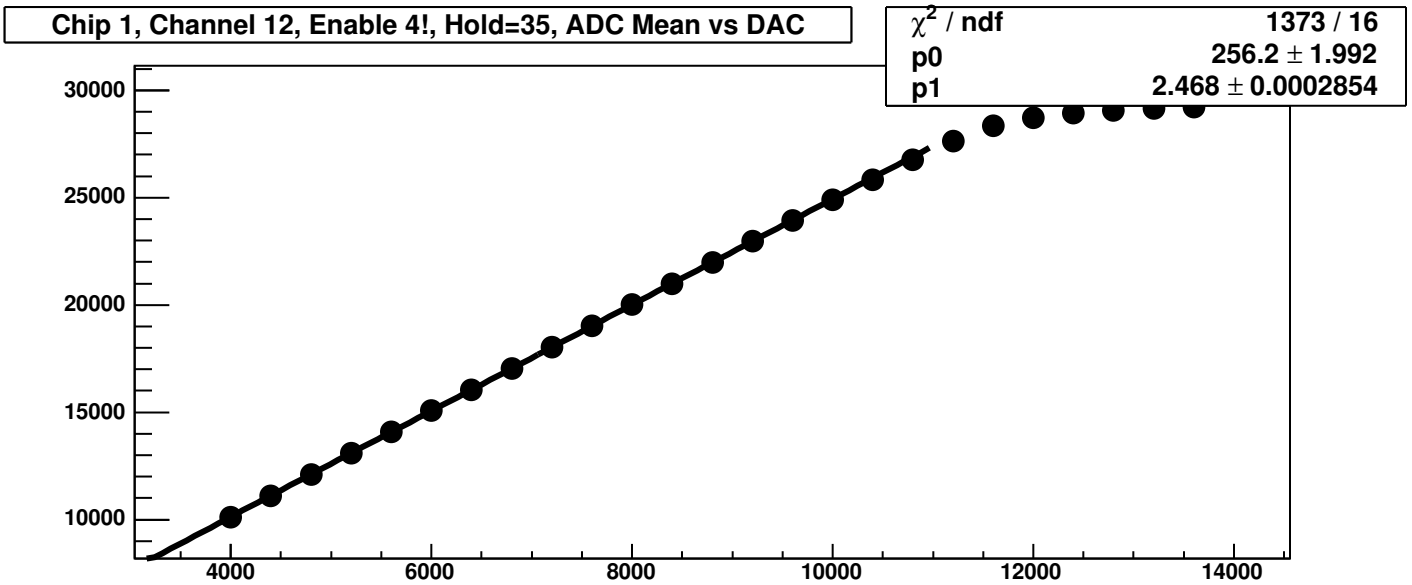

Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

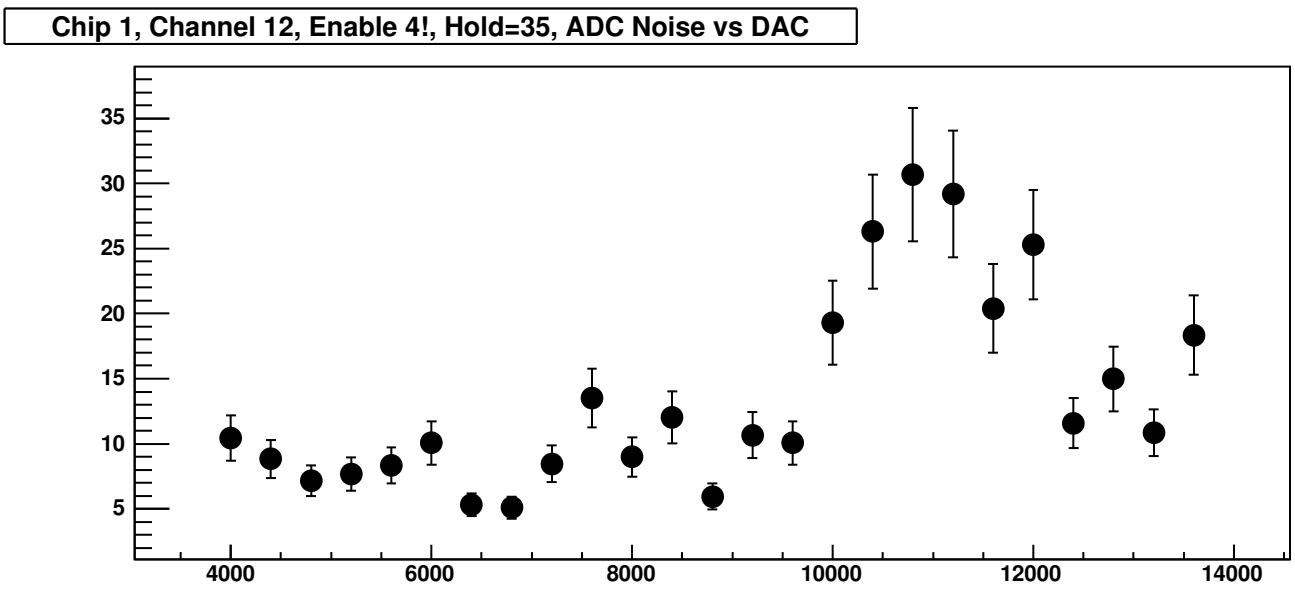

Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
4000
6000
8000
10000
12000
14000

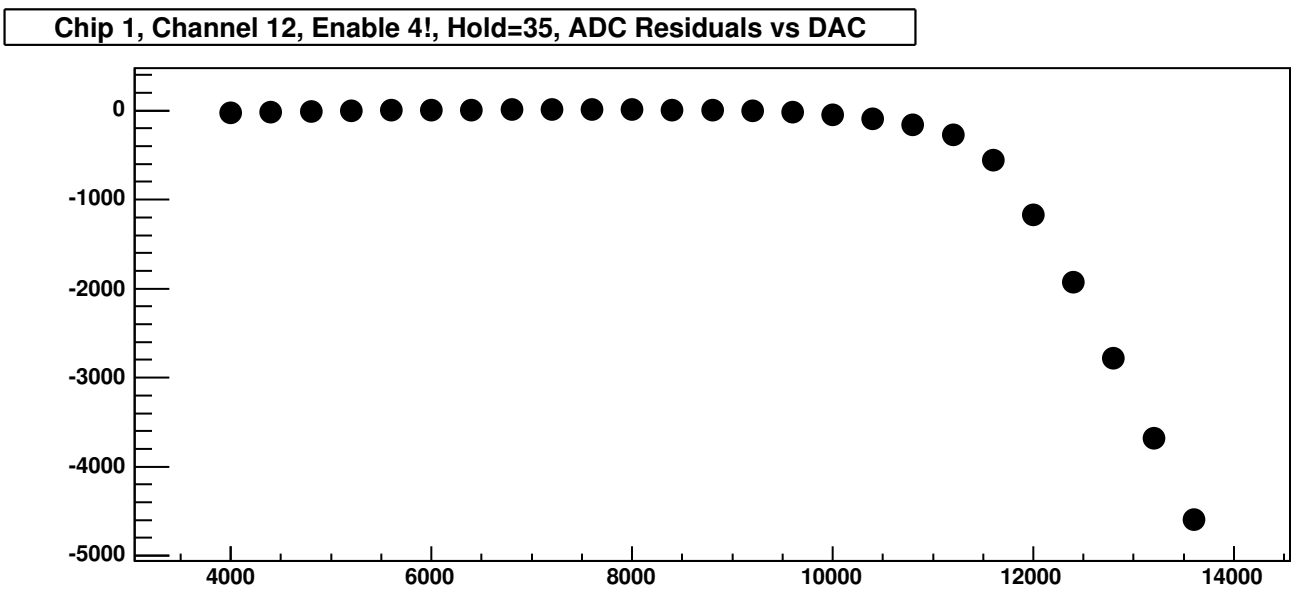

Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 12.69 / 16
p0 56.07 ± 1.421
p1 0.01035 ± 0.0001868
180
160
140
120
100
4000
6000
8000
10000
12000
14000

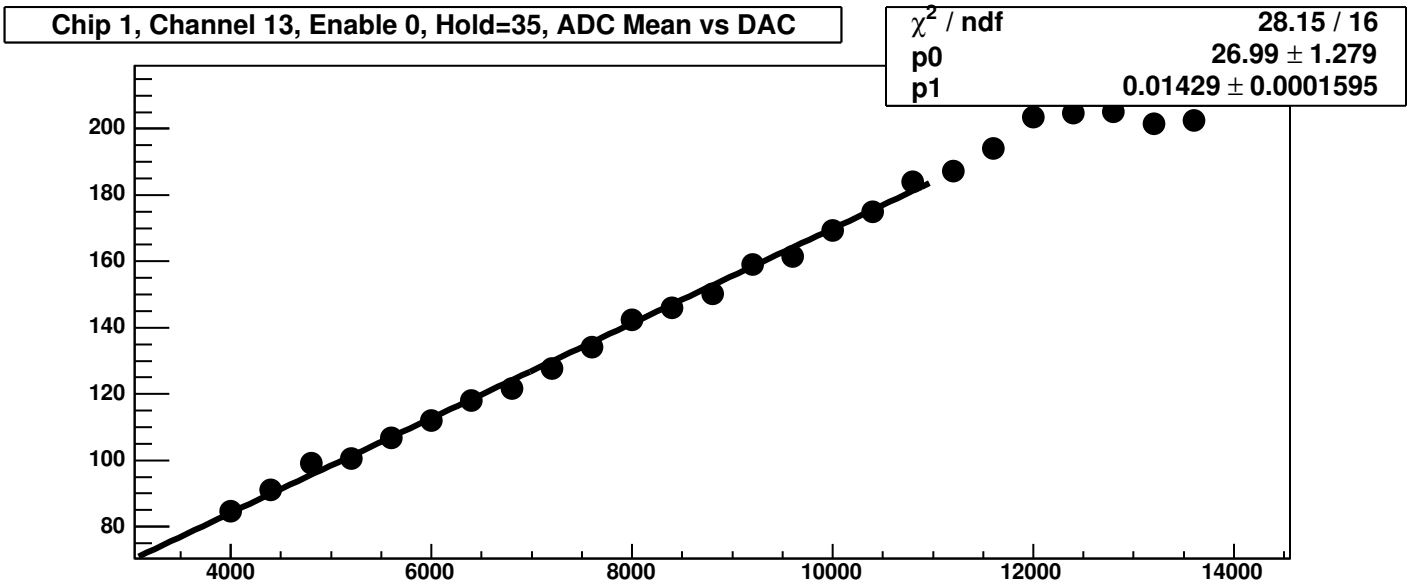

Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

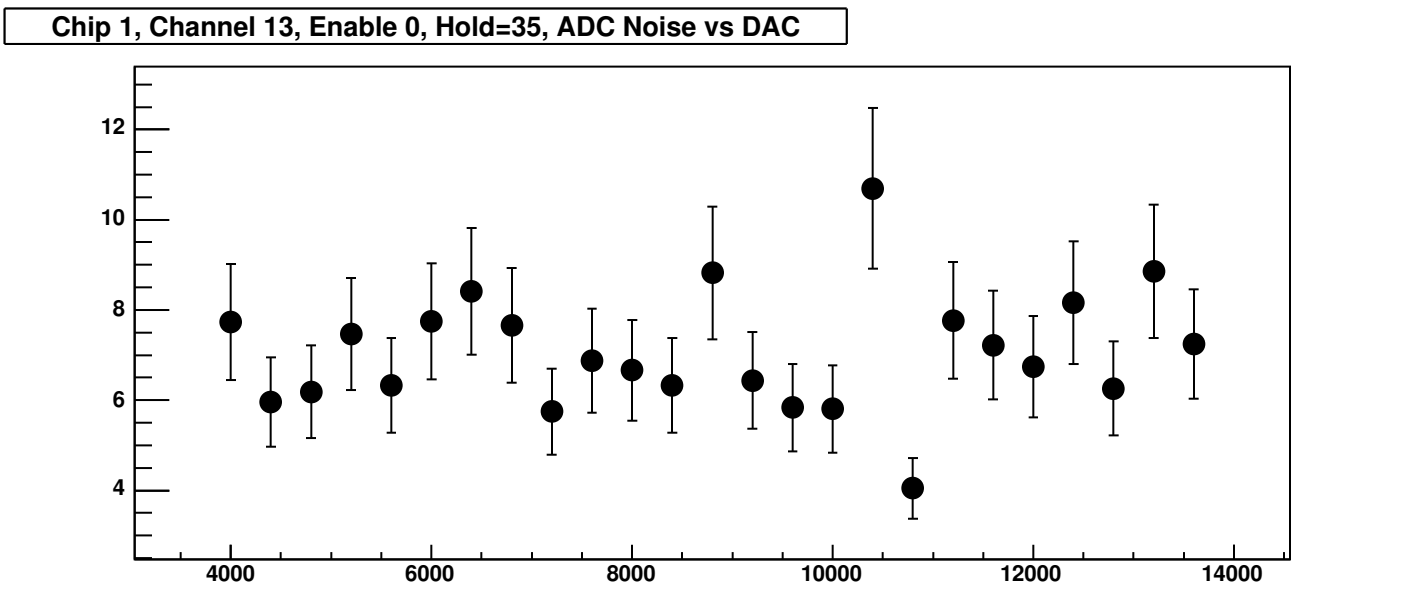

Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

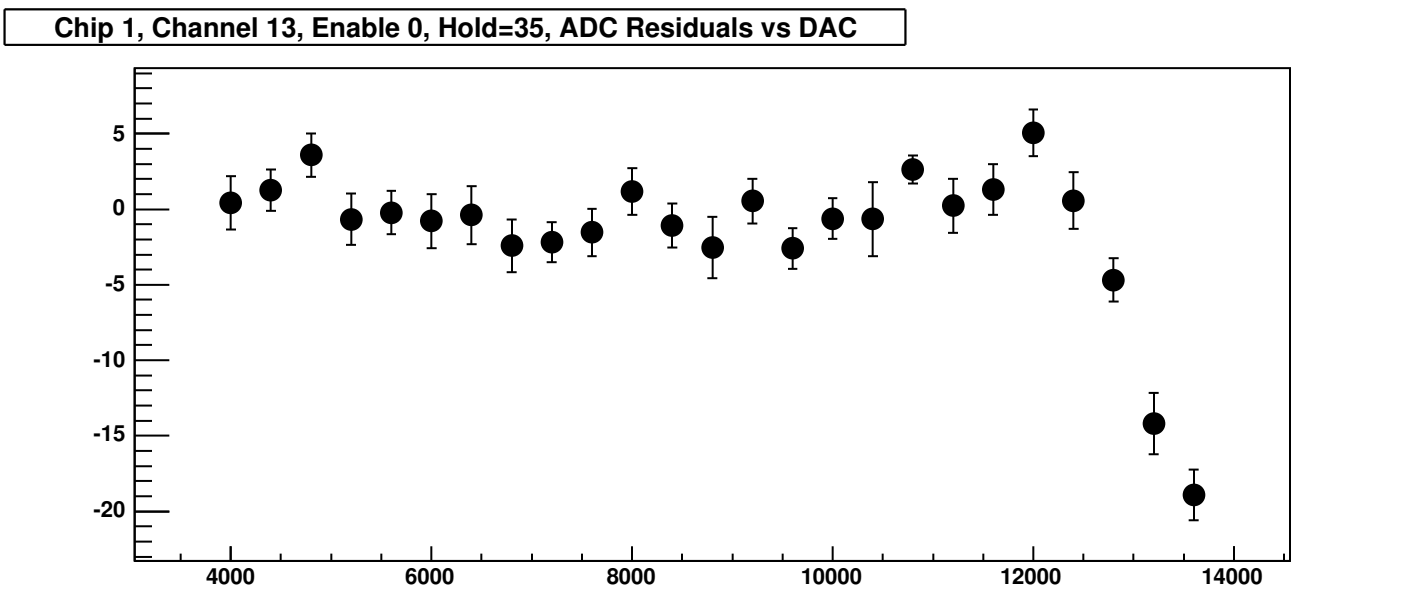

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1373 / 16
p0 256.2 ± 1.992
p1 2.468 ± 0.0002854
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

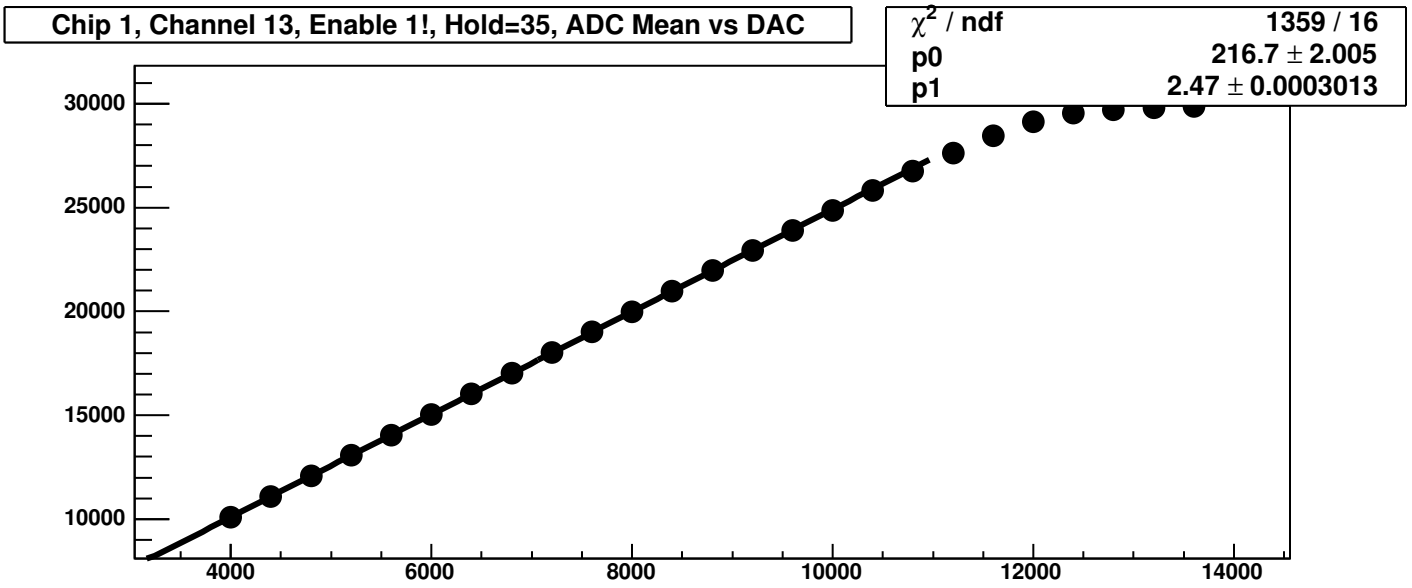

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

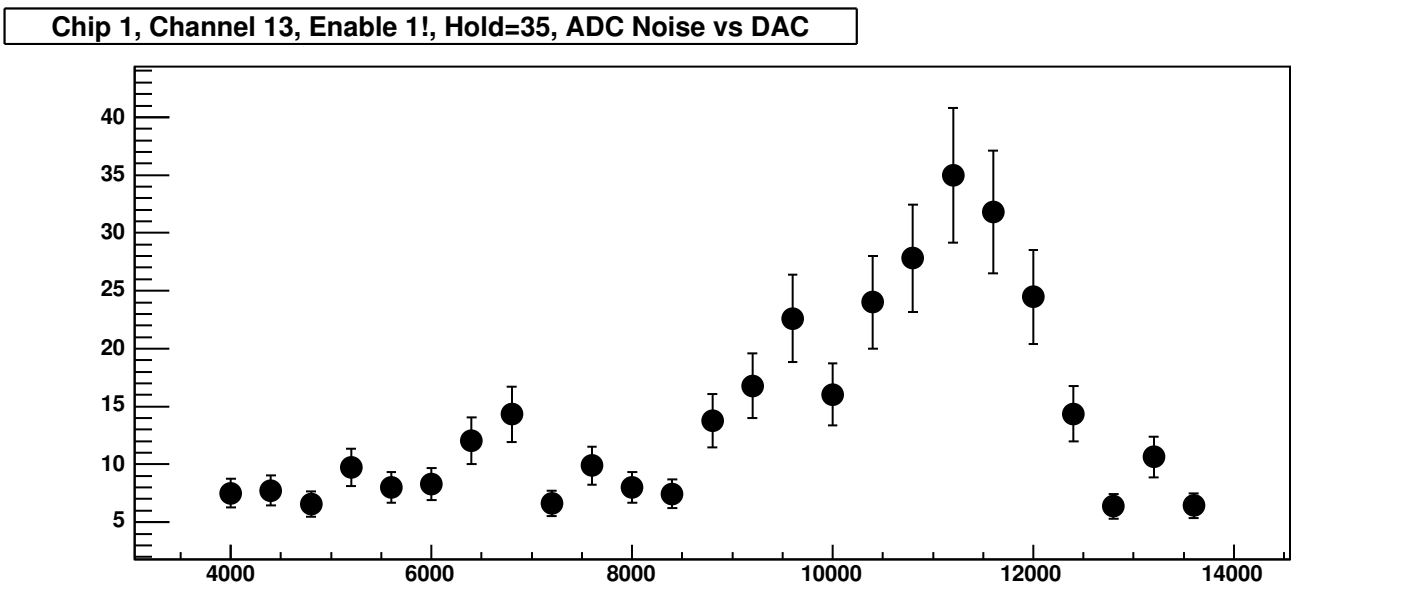

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

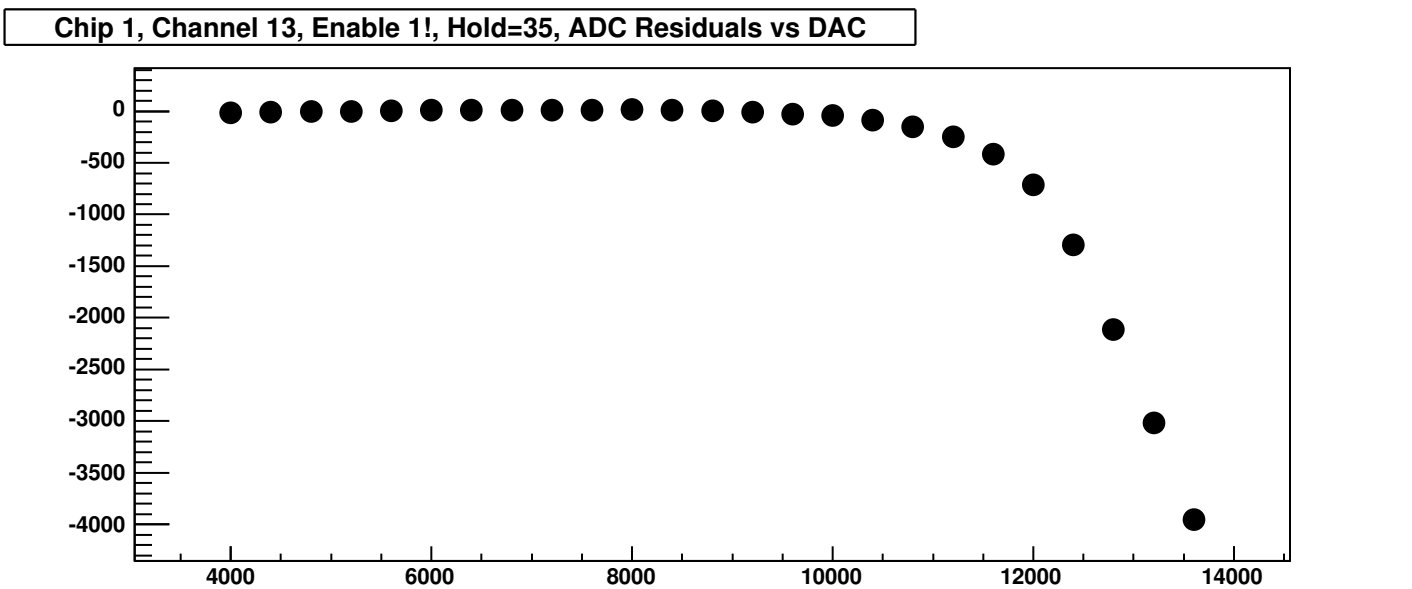

Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 28.15 / 16
p0 26.99 ± 1.279
p1 0.01429 ± 0.0001595
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

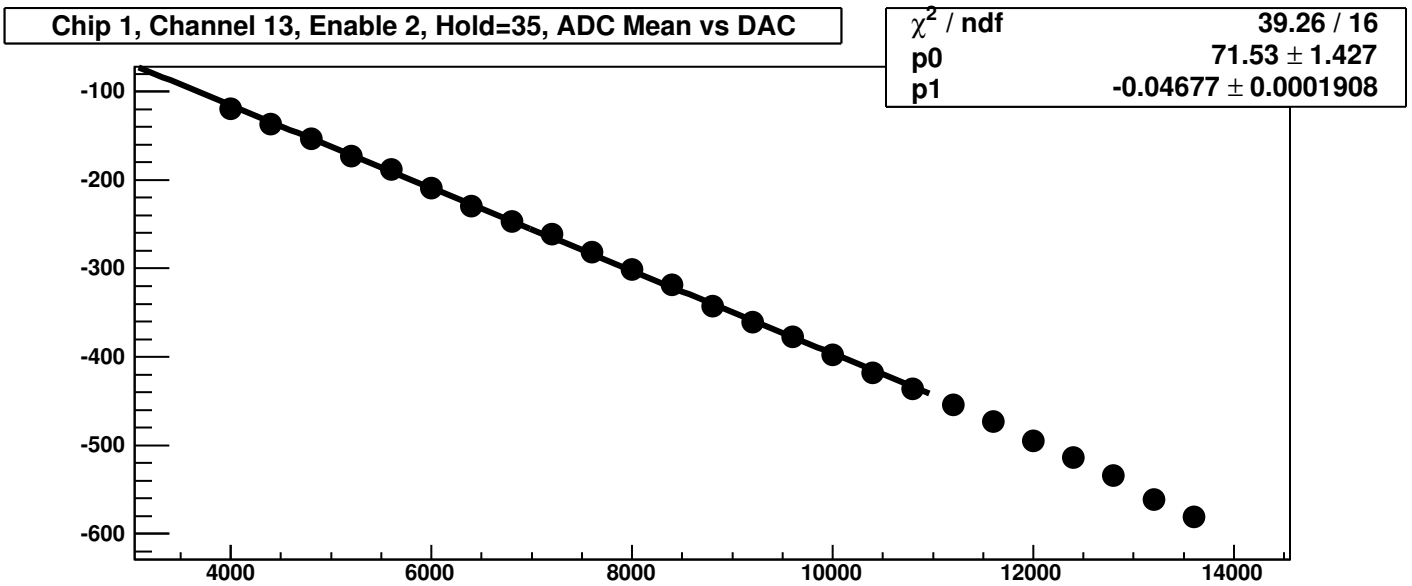

Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

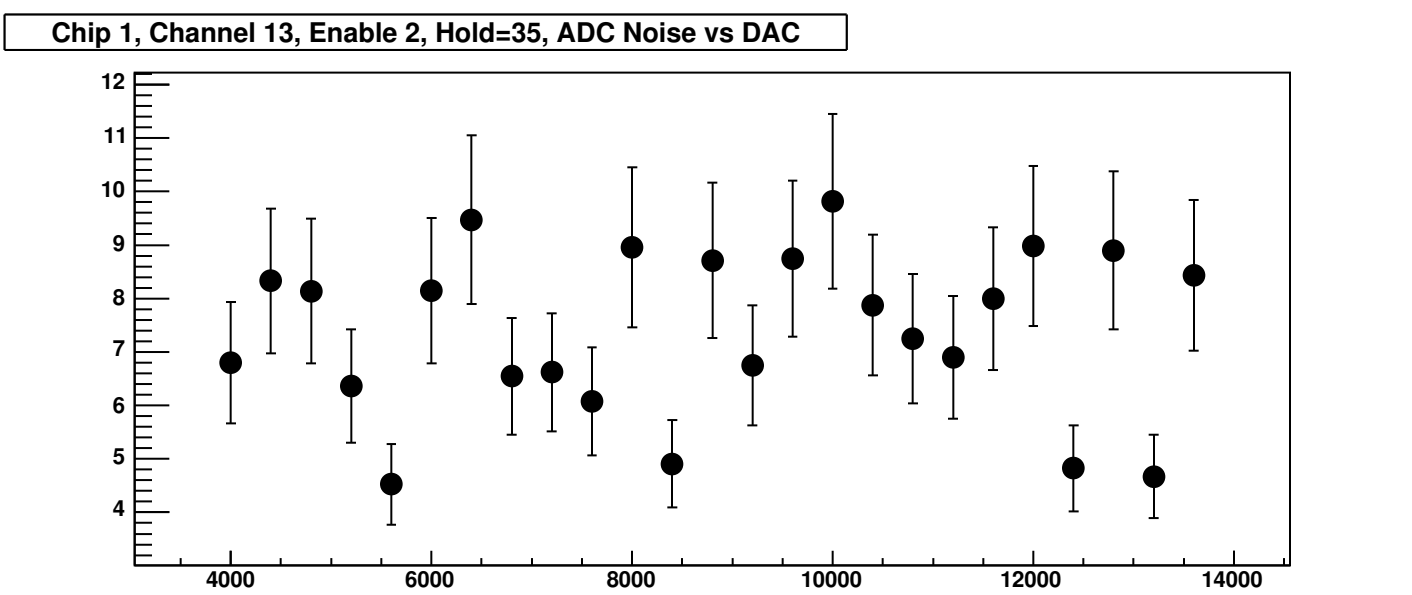

Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

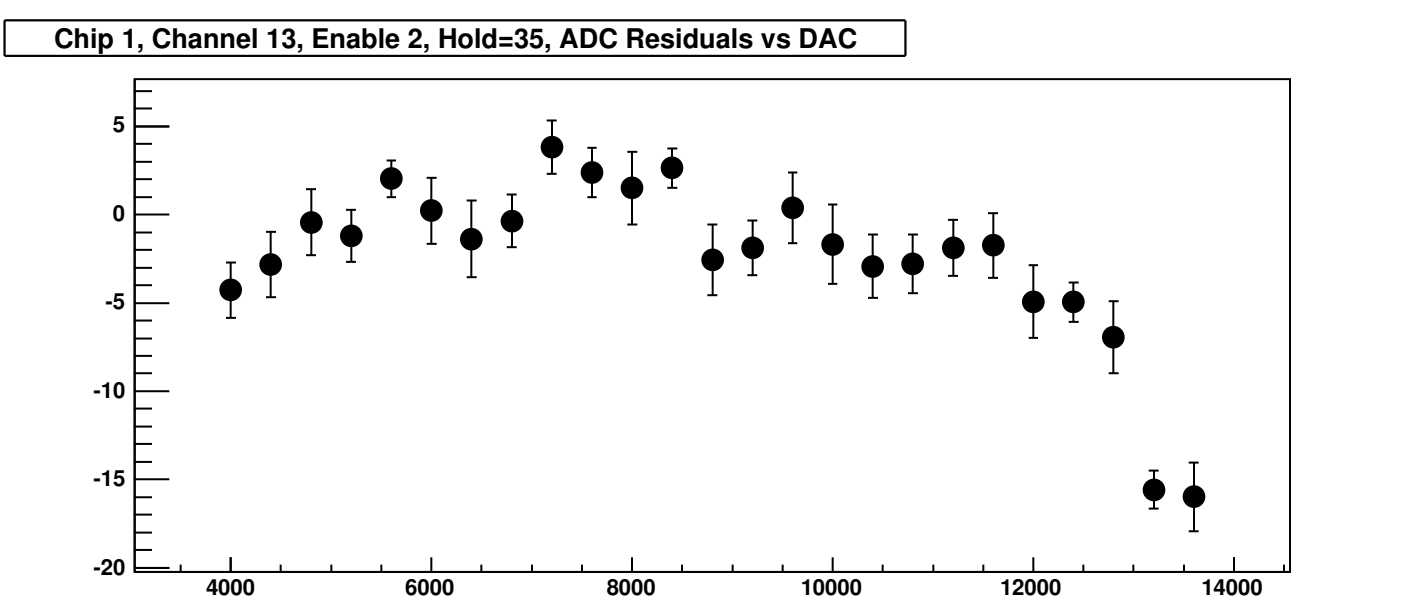

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1359 / 16
p0 216.7 ± 2.005
p1 2.47 ± 0.0003013
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

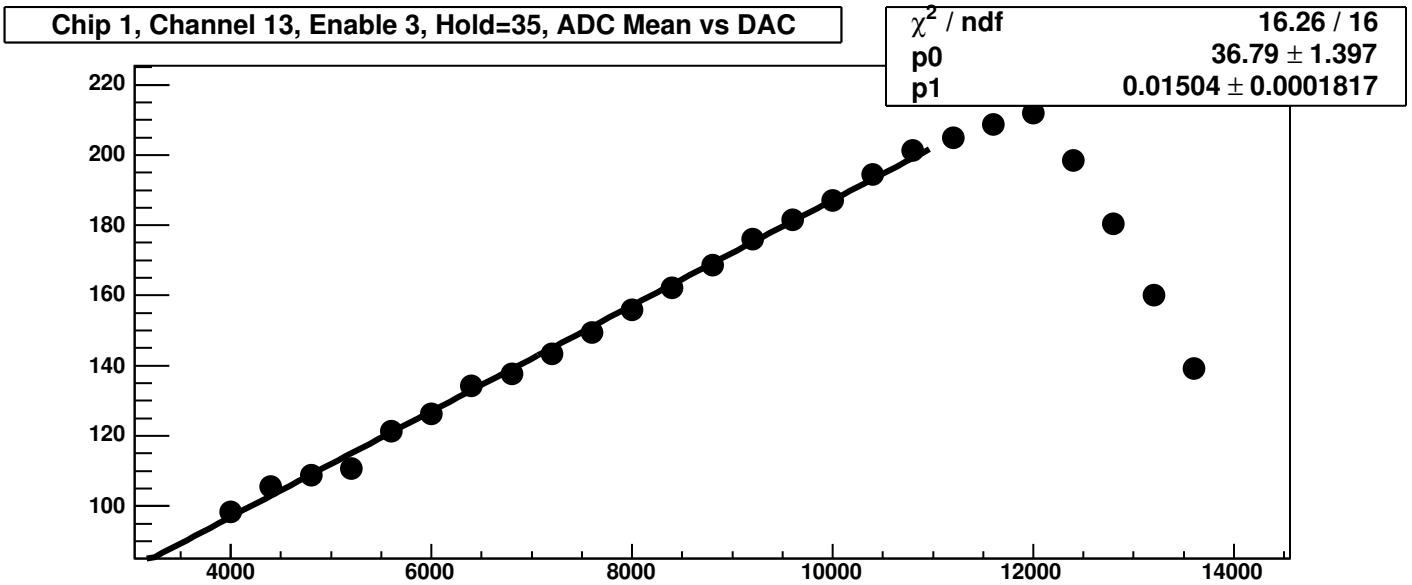

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

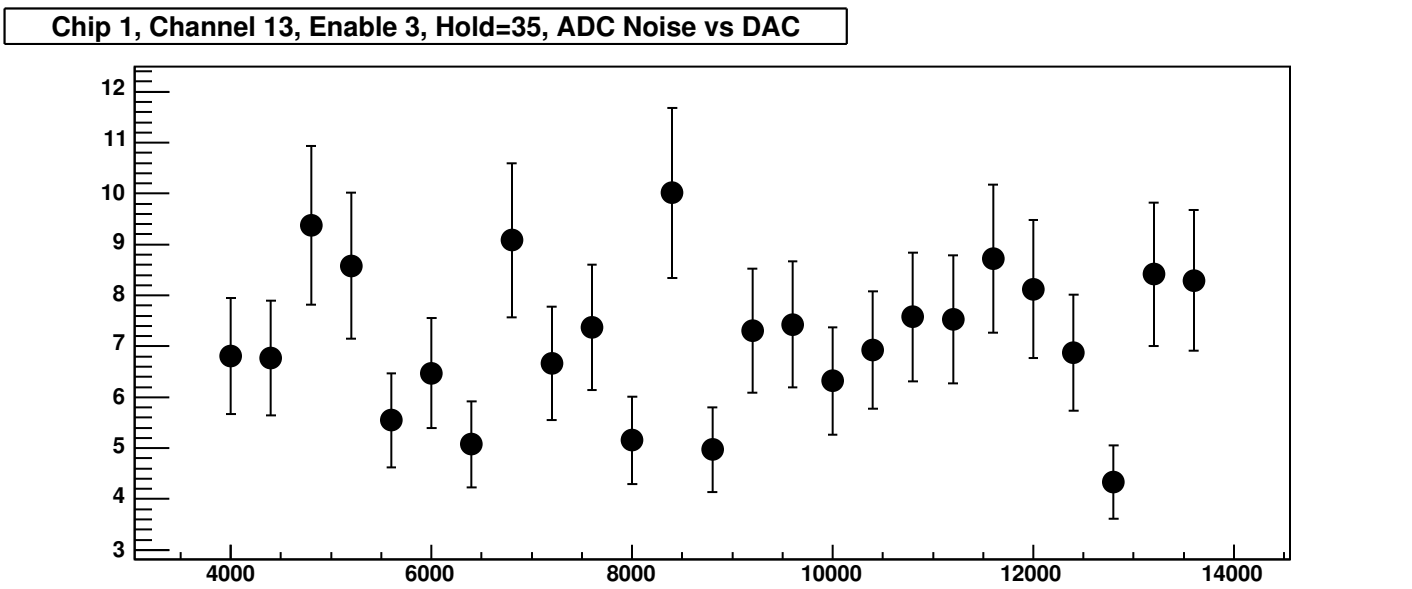

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

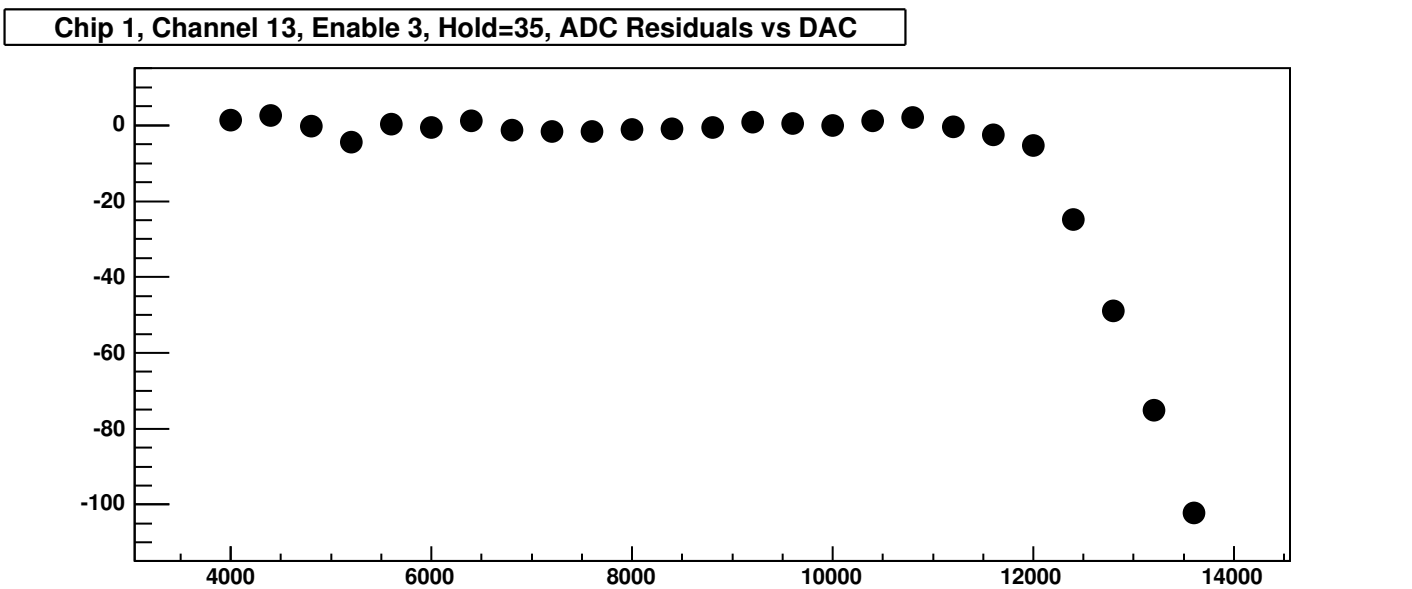

Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.26 / 16
p0 71.53 ± 1.427
p1 -0.04677 ± 0.0001908
-100
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

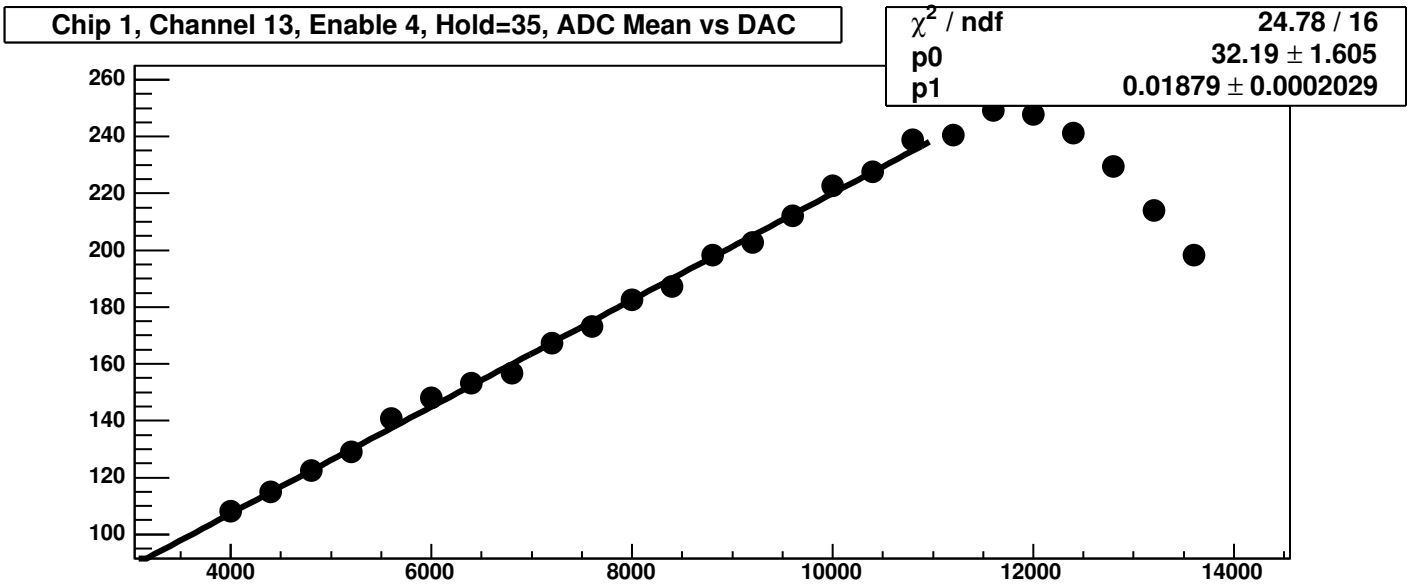

Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

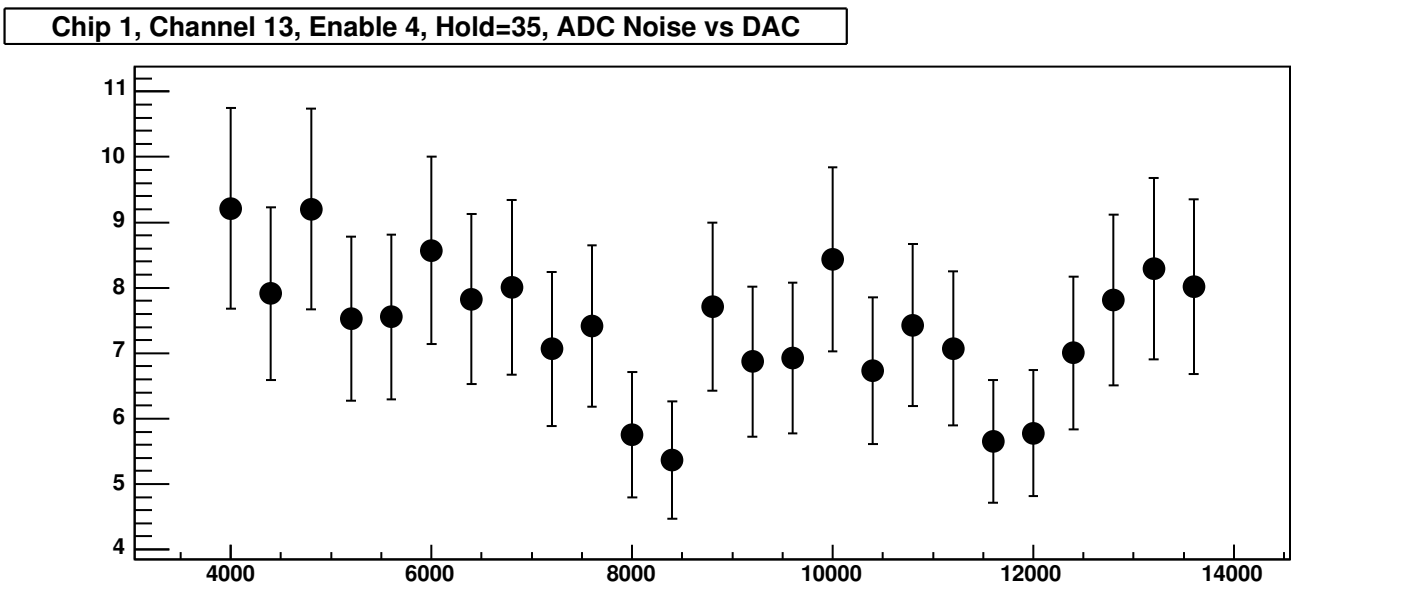

Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

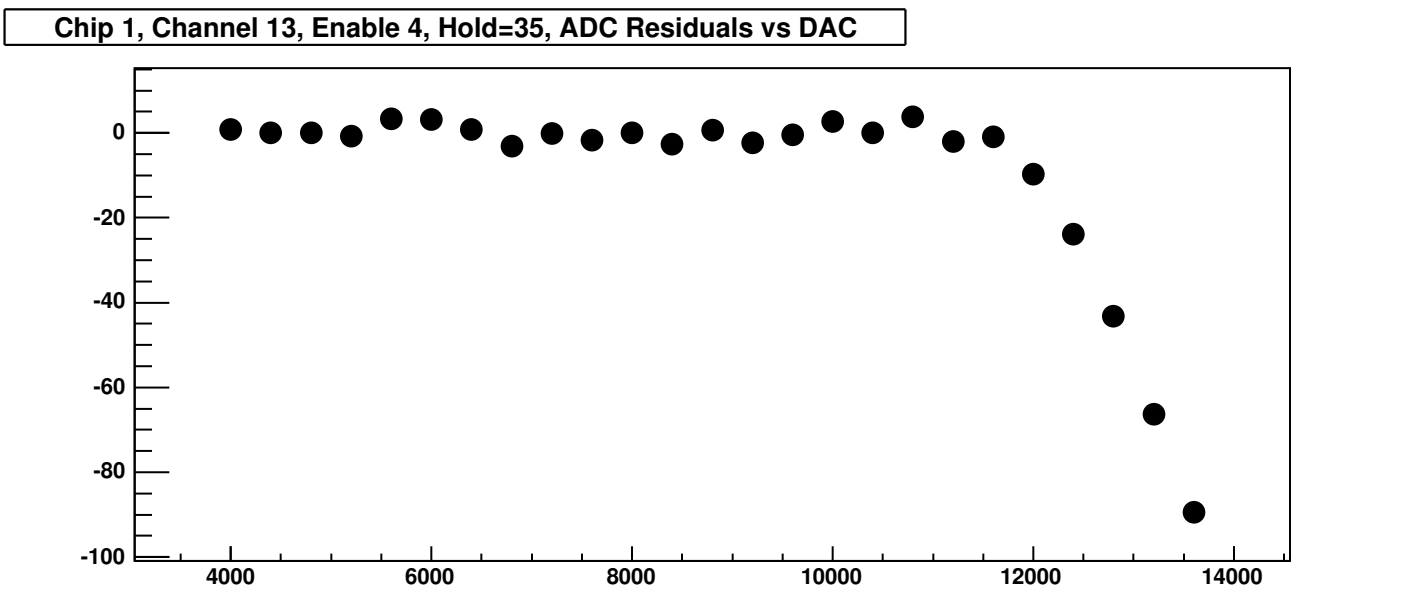

Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.26 / 16
p0 36.79 ± 1.397
p1 0.01504 ± 0.0001817
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

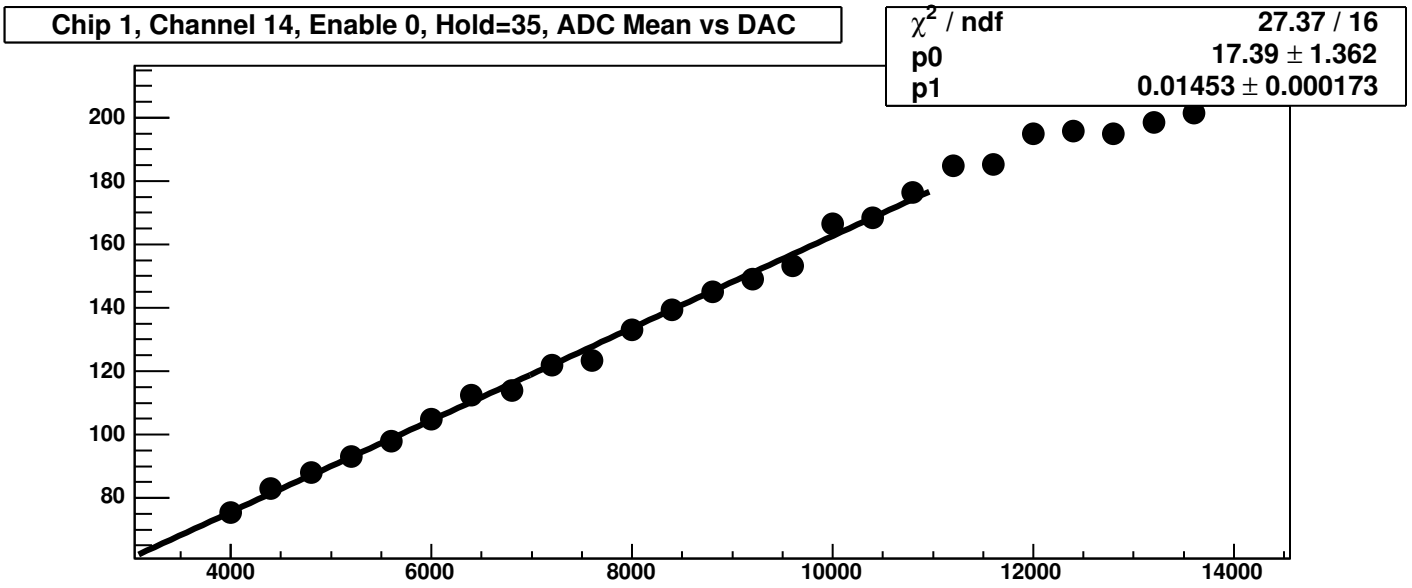

Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

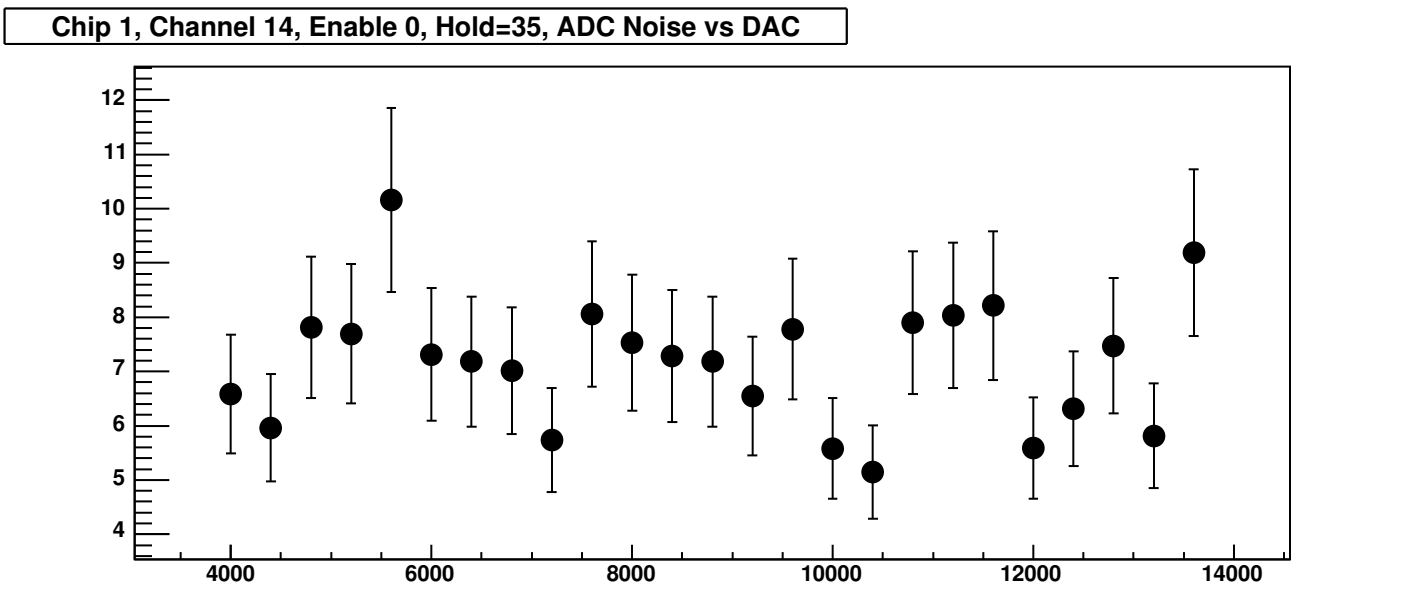

Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

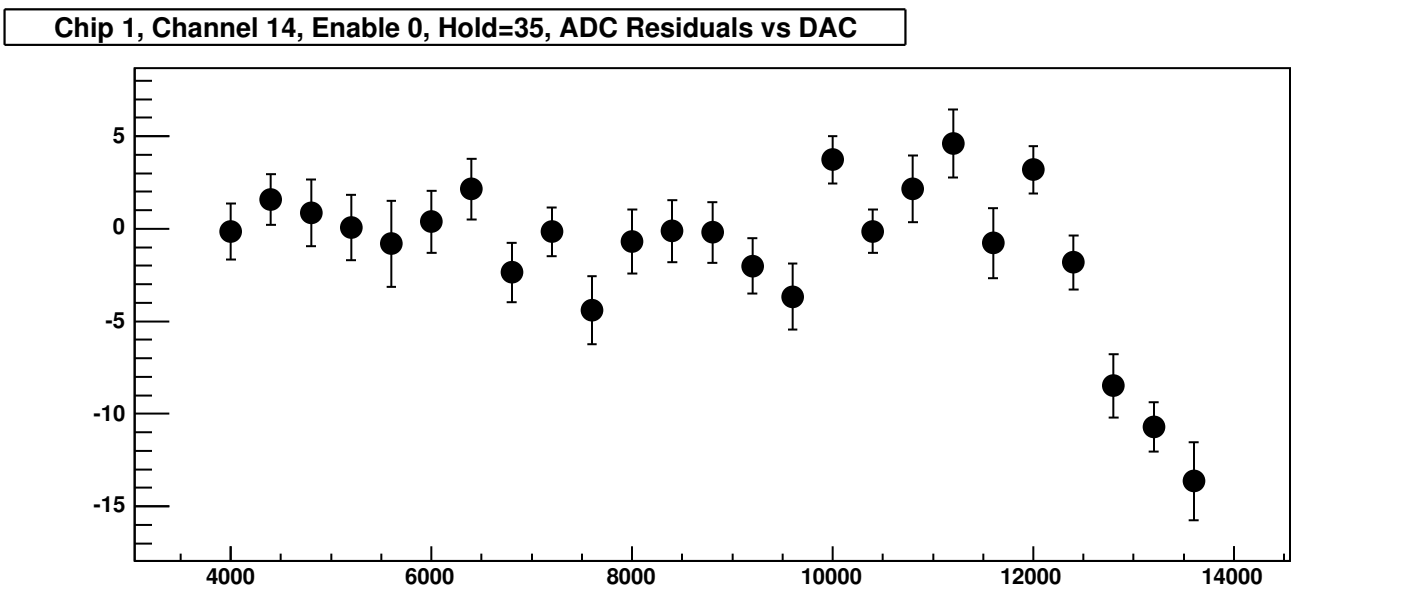

Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.78 / 16
p0 32.19 ± 1.605
p1 0.01879 ± 0.0002029
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

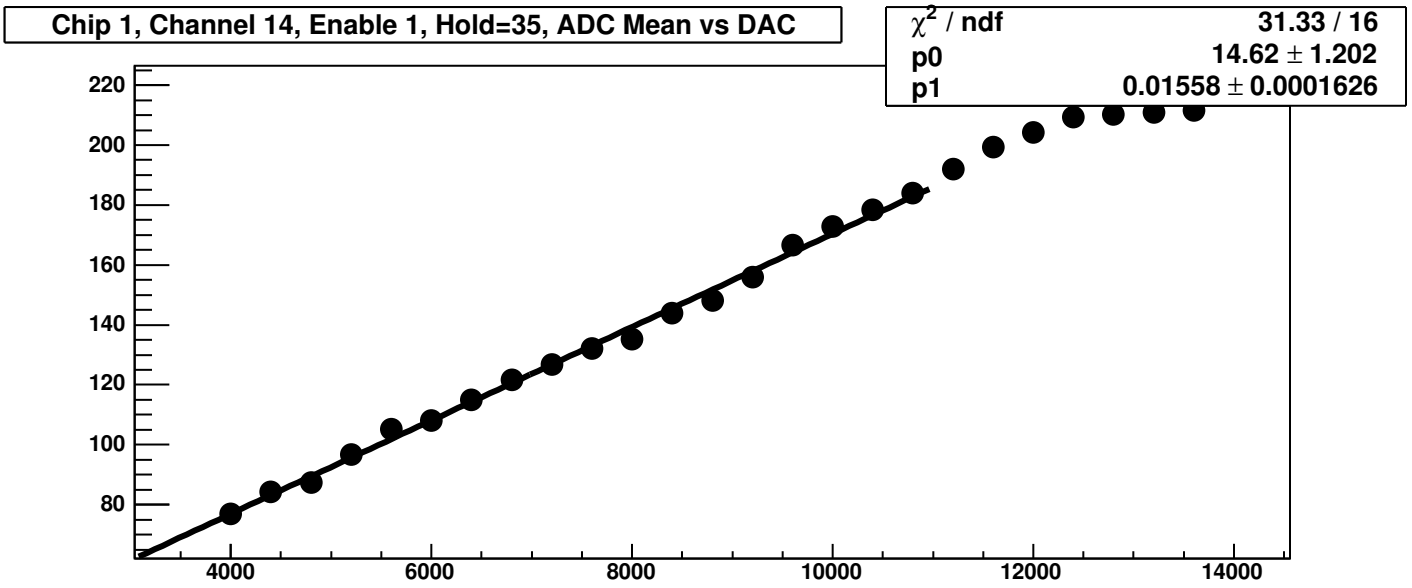

Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

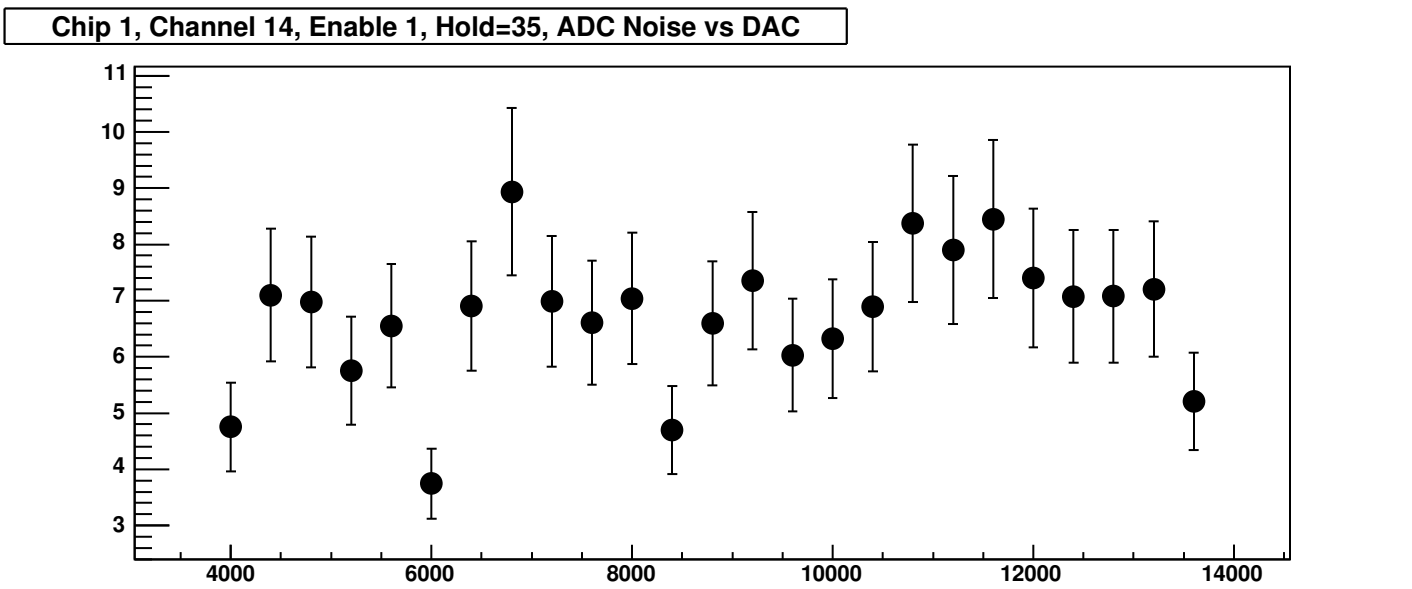

Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

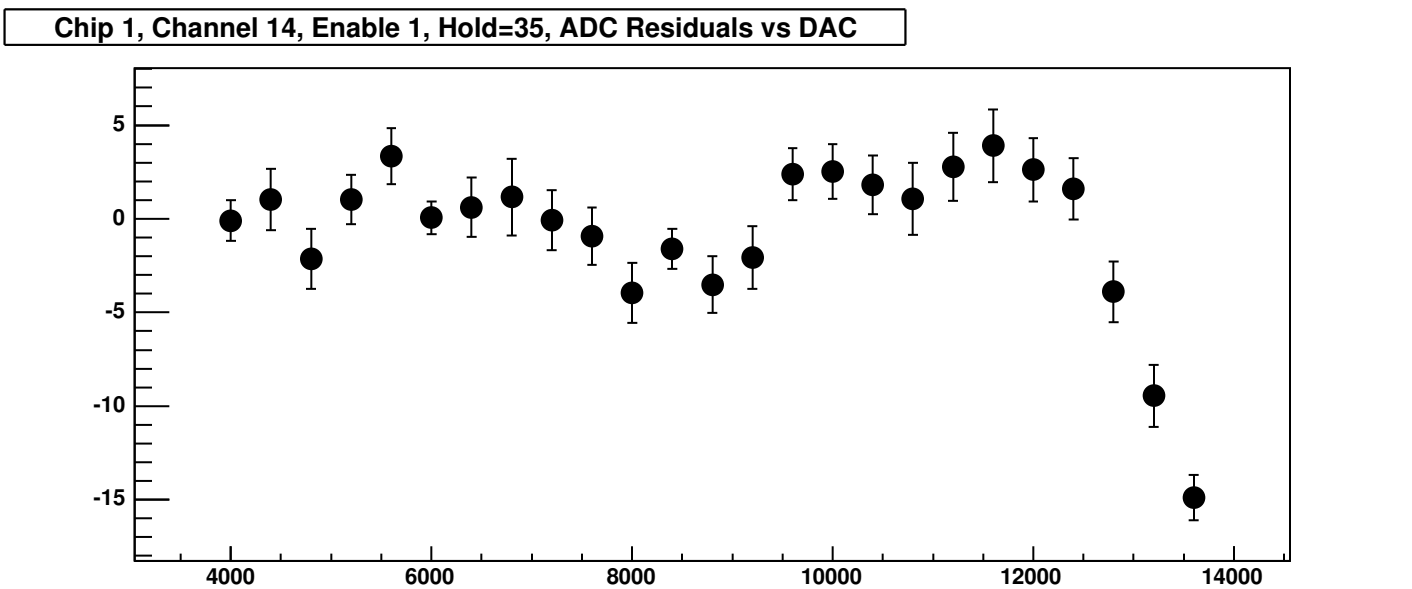

Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.37 / 16
p0 17.39 ± 1.362
p1 0.01453 ± 0.000173
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

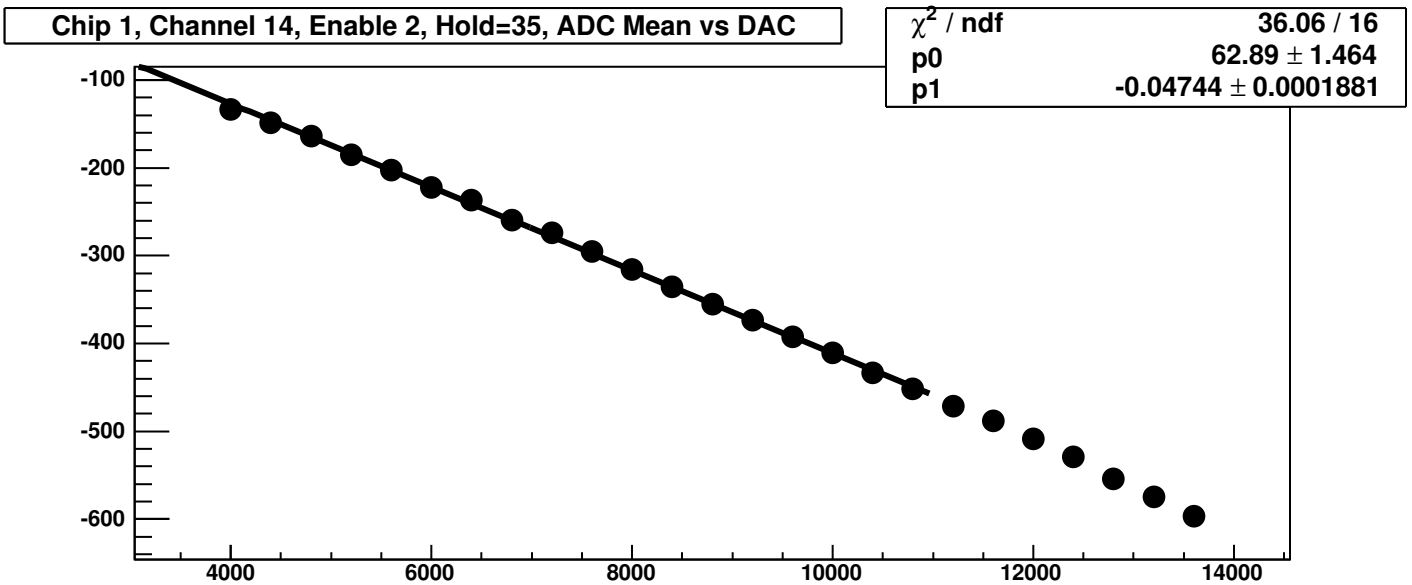

Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

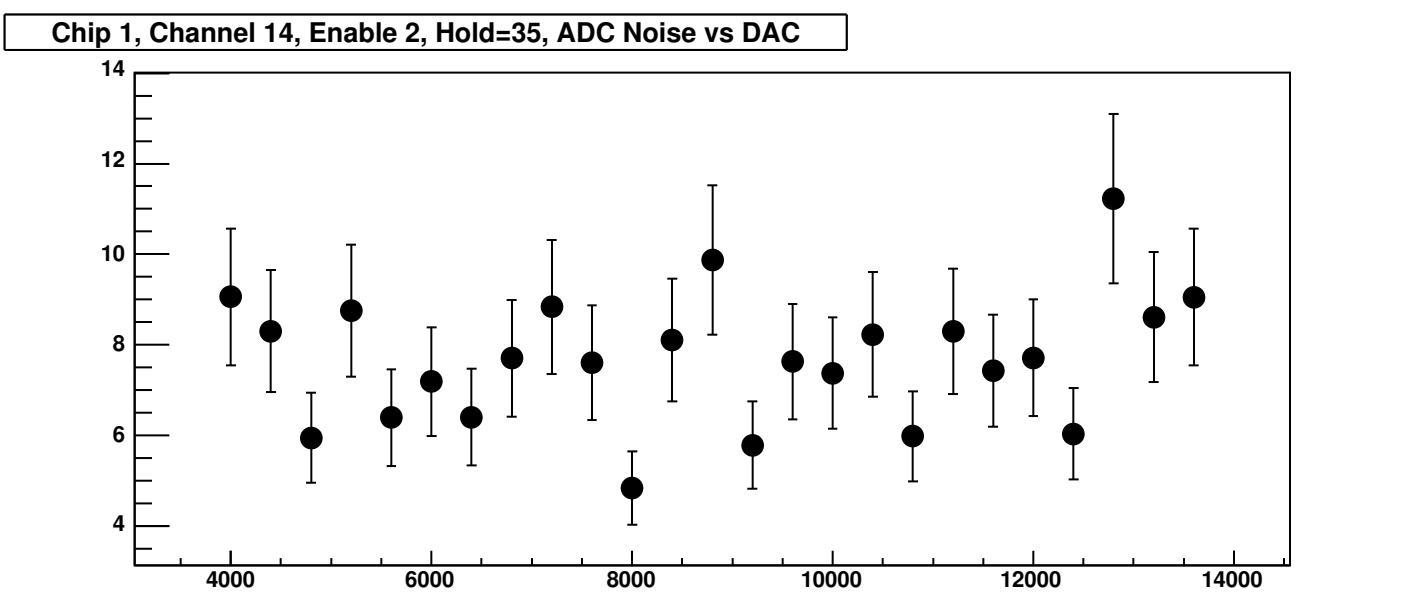

Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

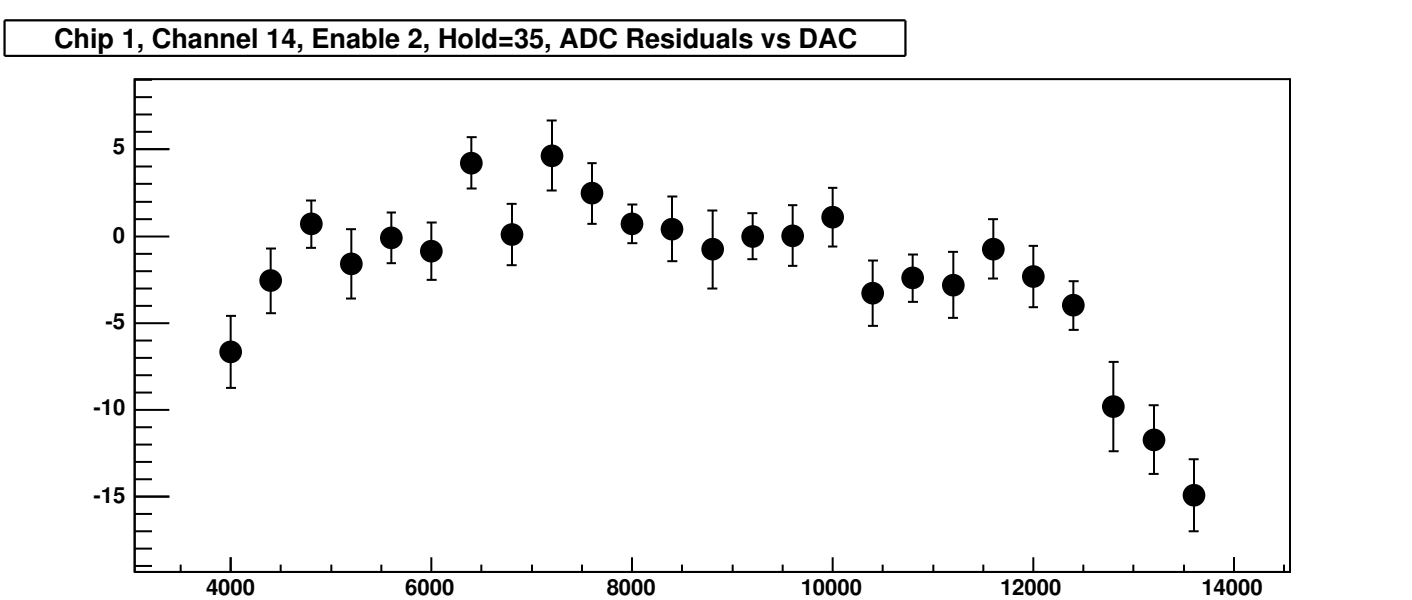

Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.33 / 16
p0 14.62 ± 1.202
p1 0.01558 ± 0.0001626
220
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

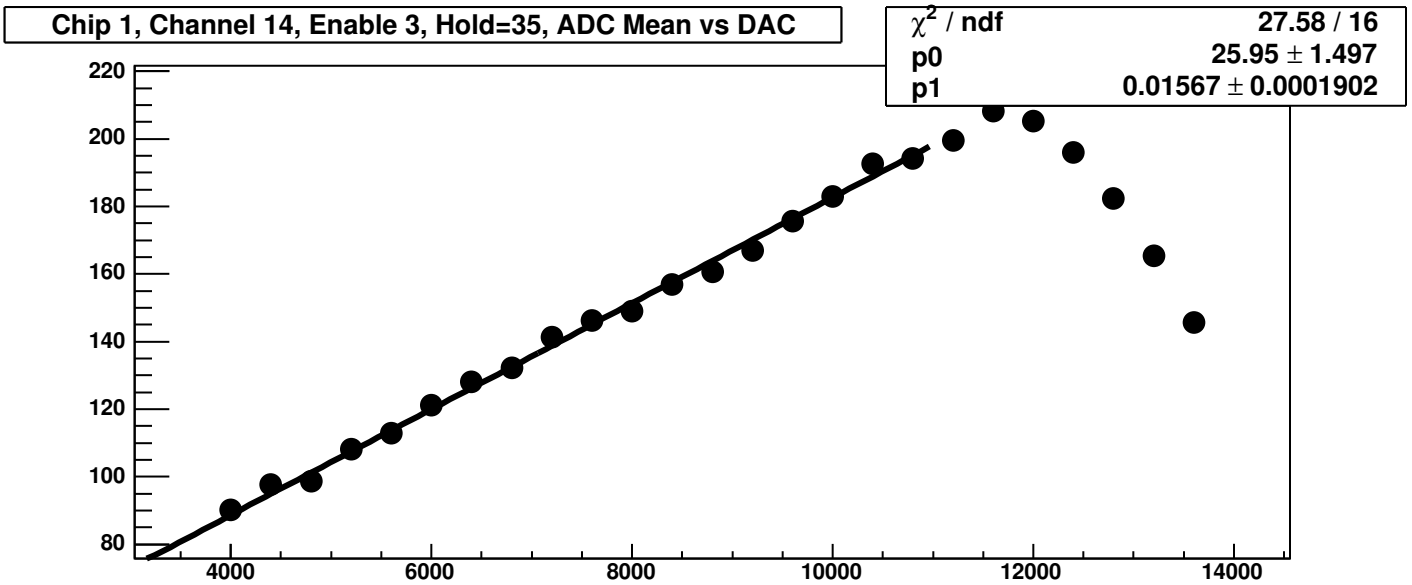

Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

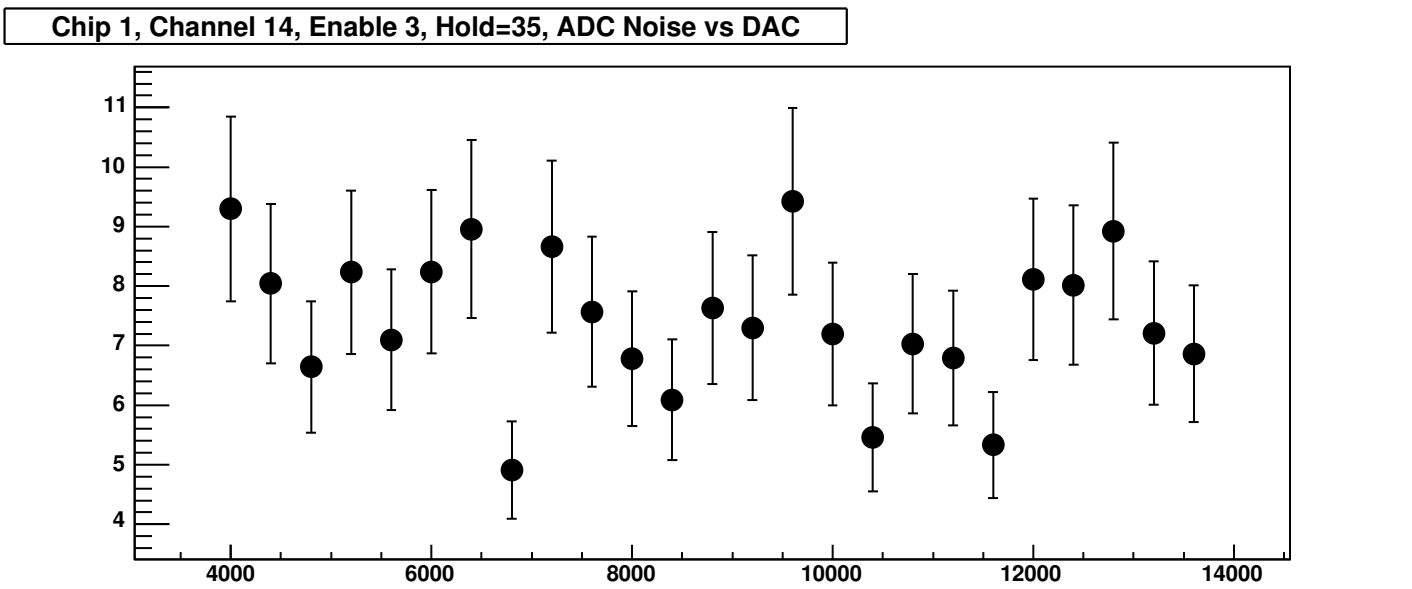

Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

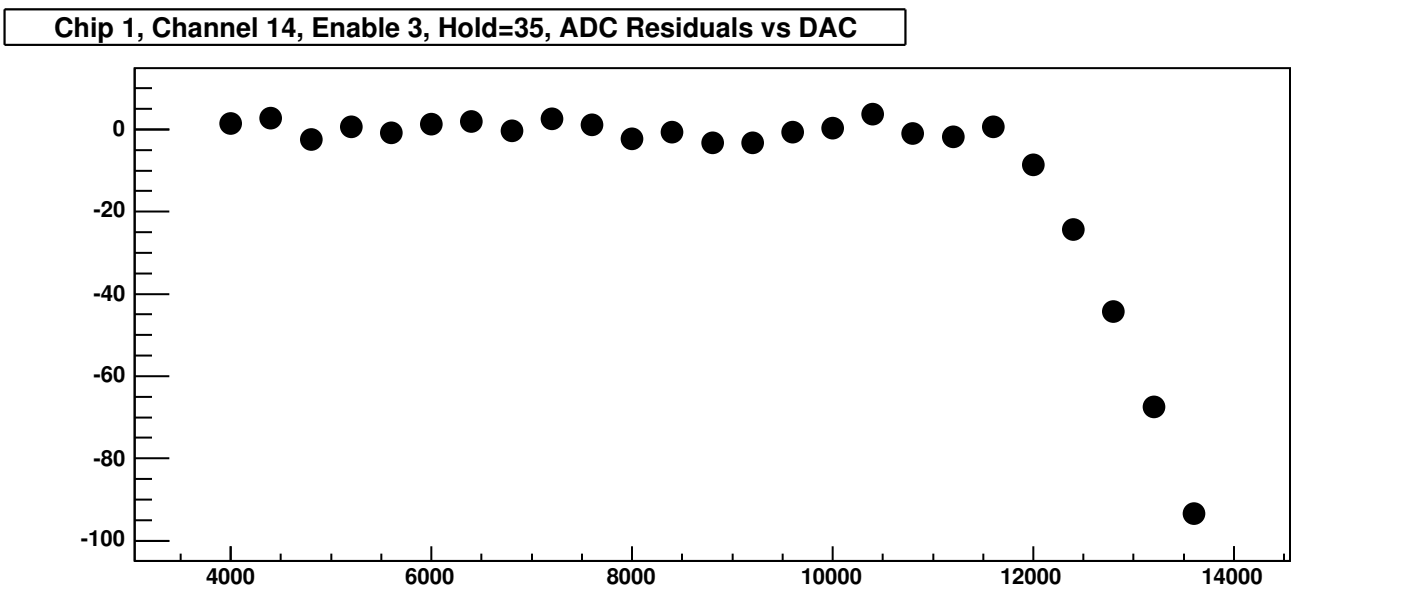

Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.06 / 16
p0 62.89 ± 1.464
p1 -0.04744 ± 0.0001881
-100
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

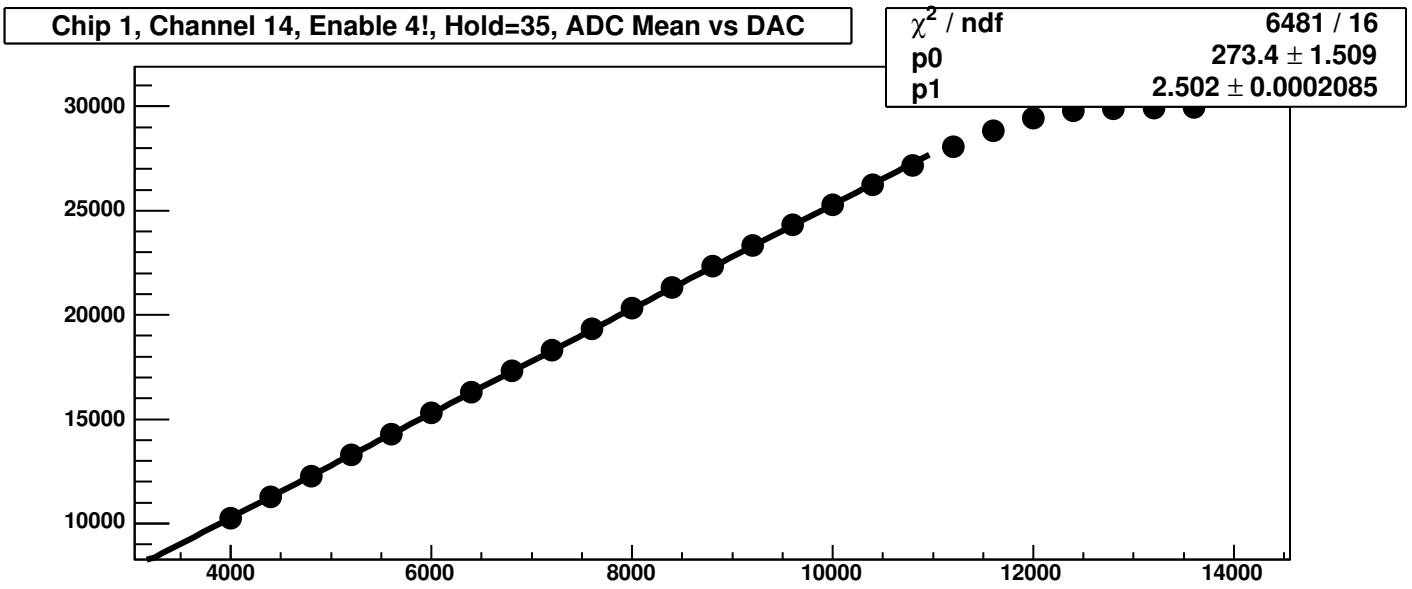

Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

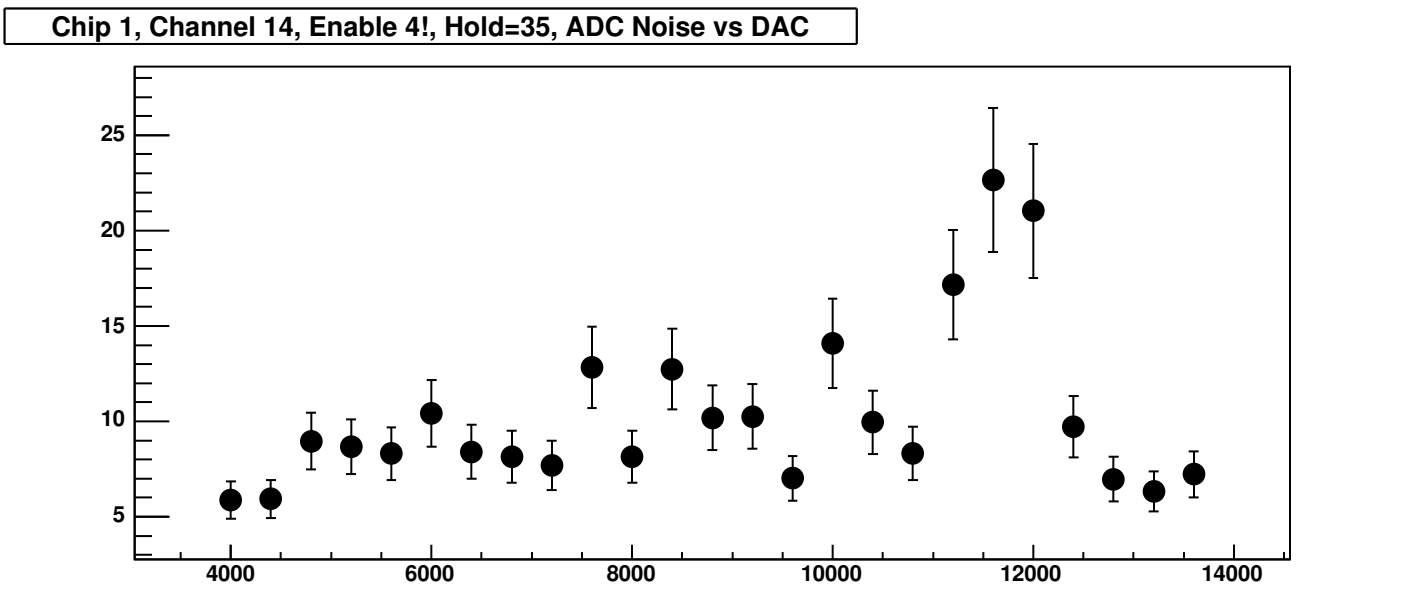

Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

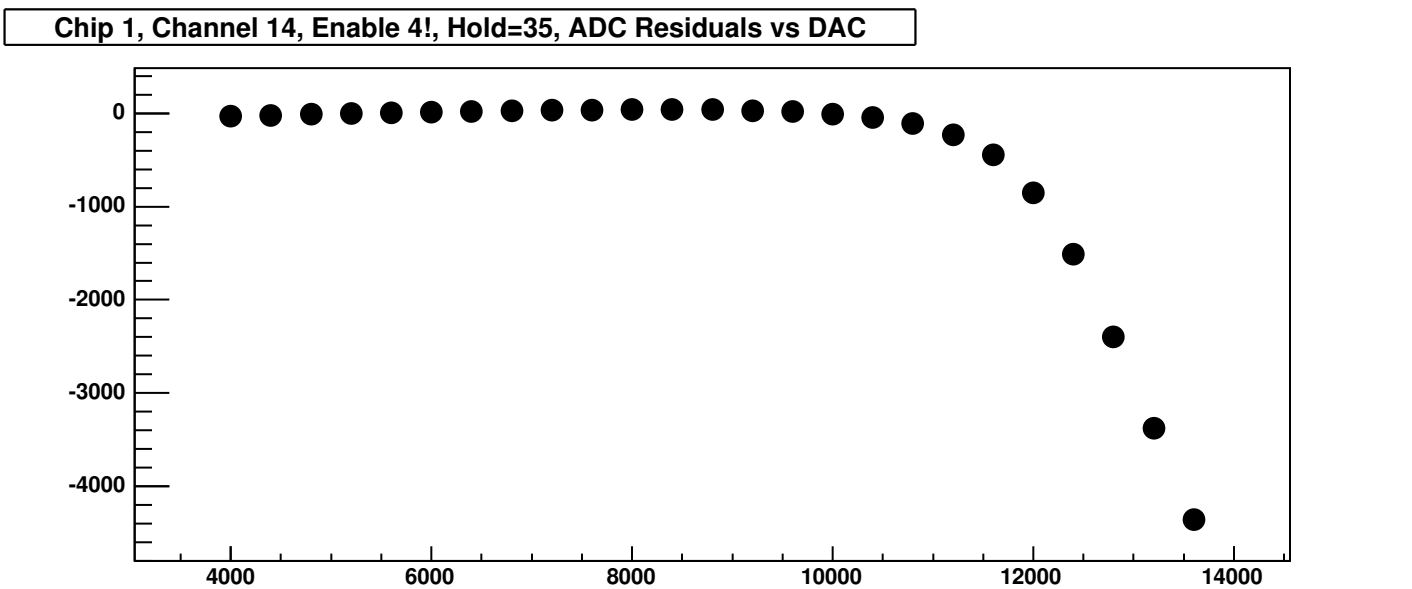

Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.58 / 16
p0 25.95 ± 1.497
p1 0.01567 ± 0.0001902
220
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

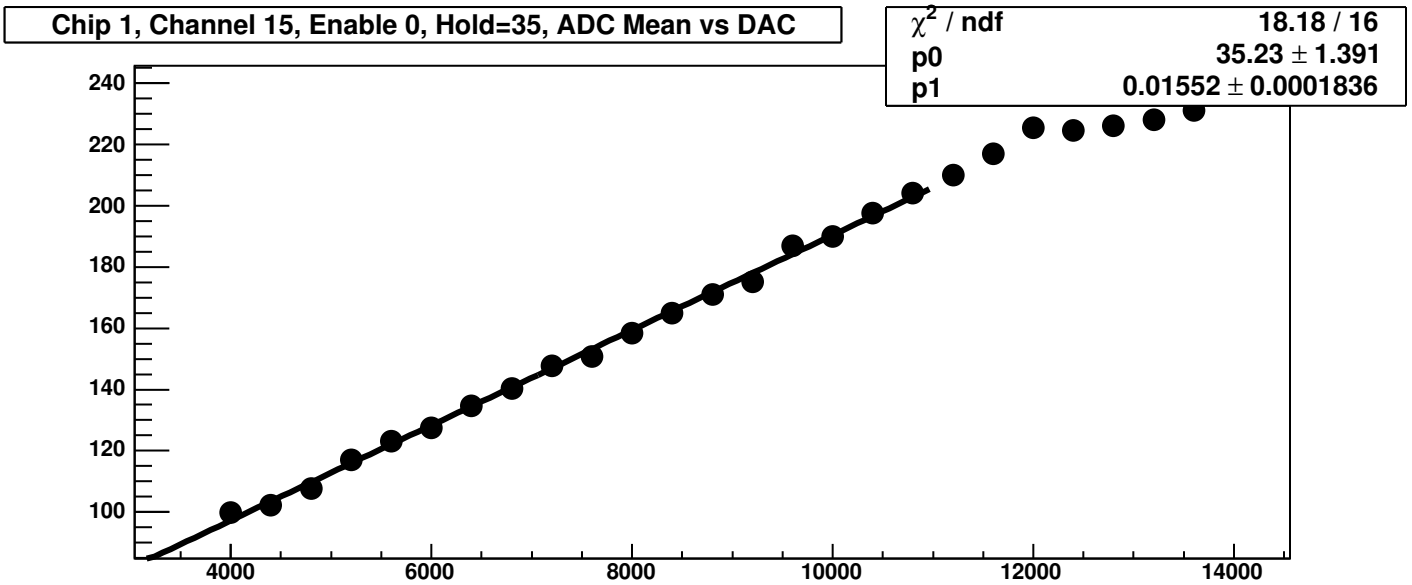

Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

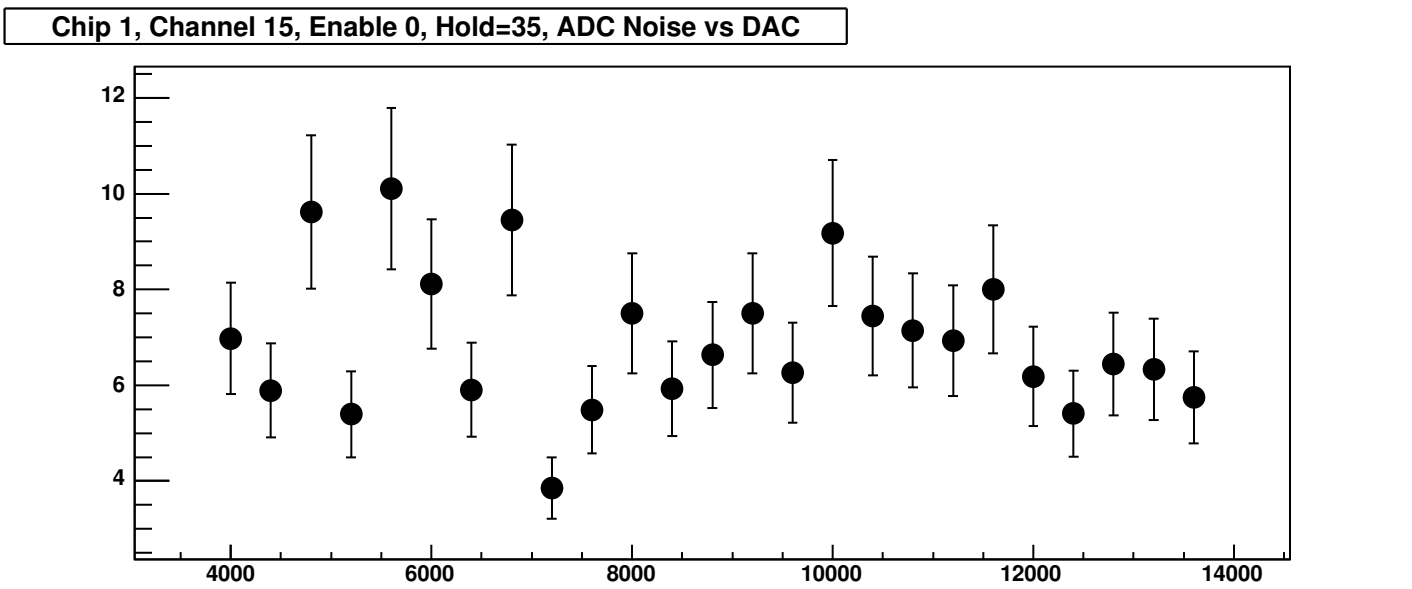

Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

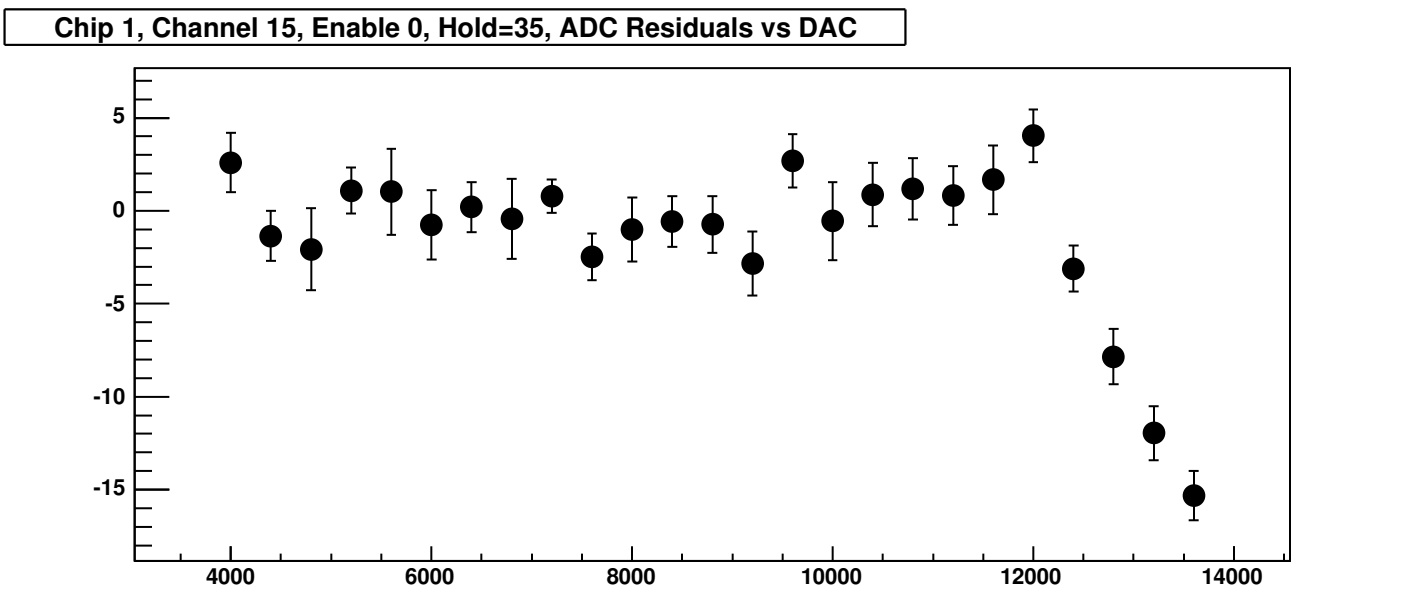

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6481 / 16
p0 273.4 ± 1.509
p1 2.502 ± 0.0002085
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

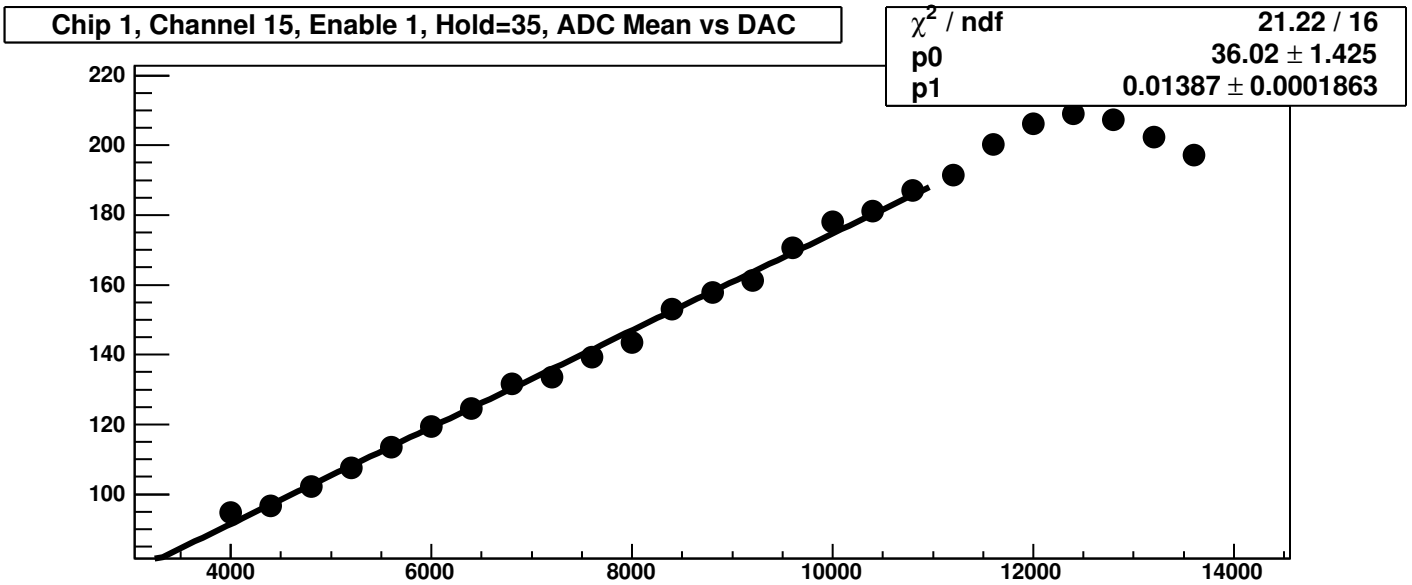

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
4000
6000
8000
10000
12000
14000

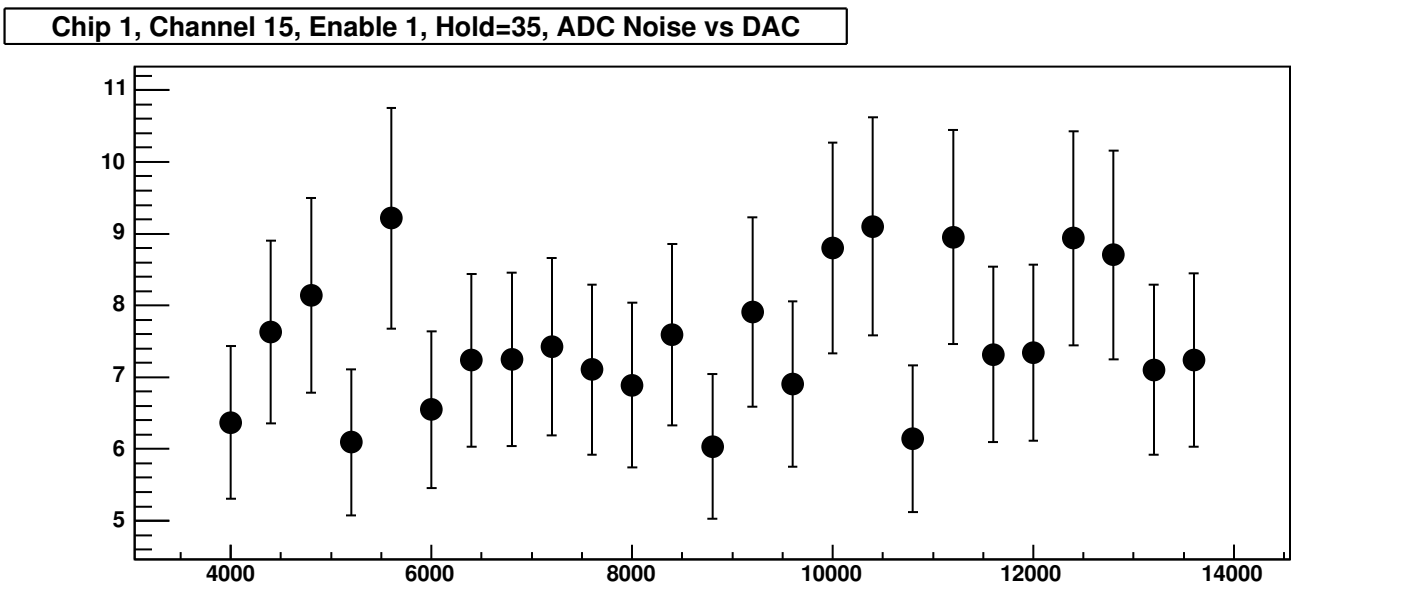

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

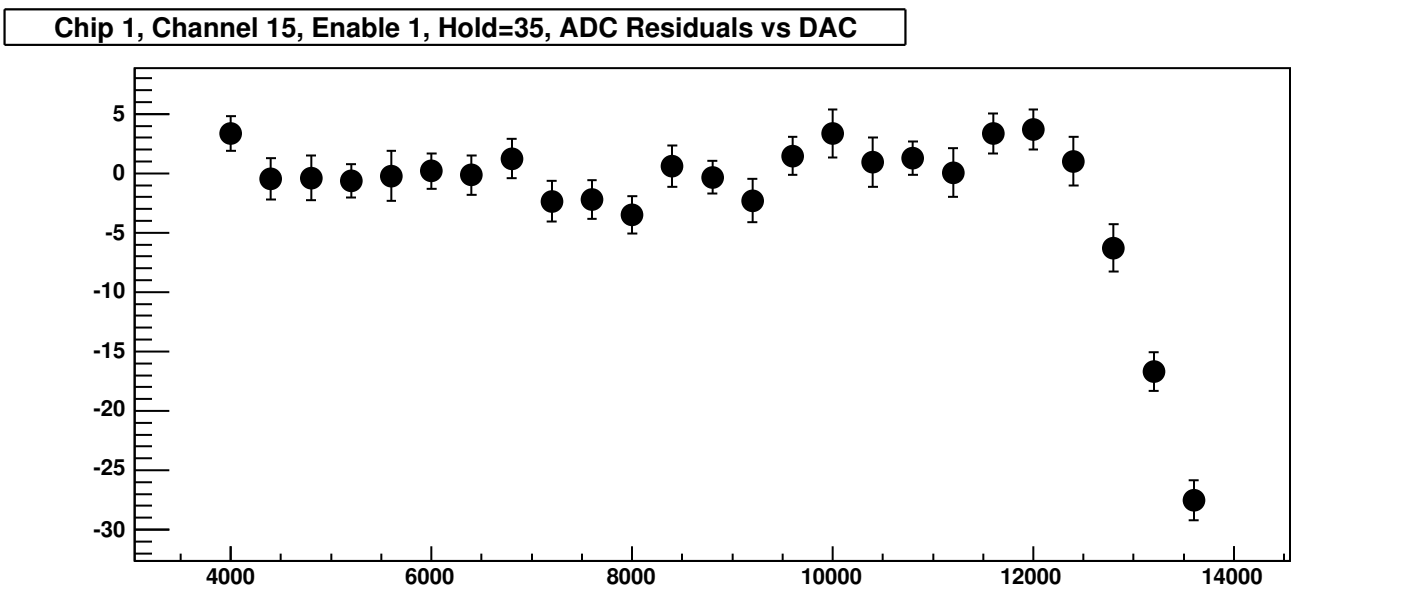

Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18.18 / 16
p0 35.23 ± 1.391
p1 0.01552 ± 0.0001836
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

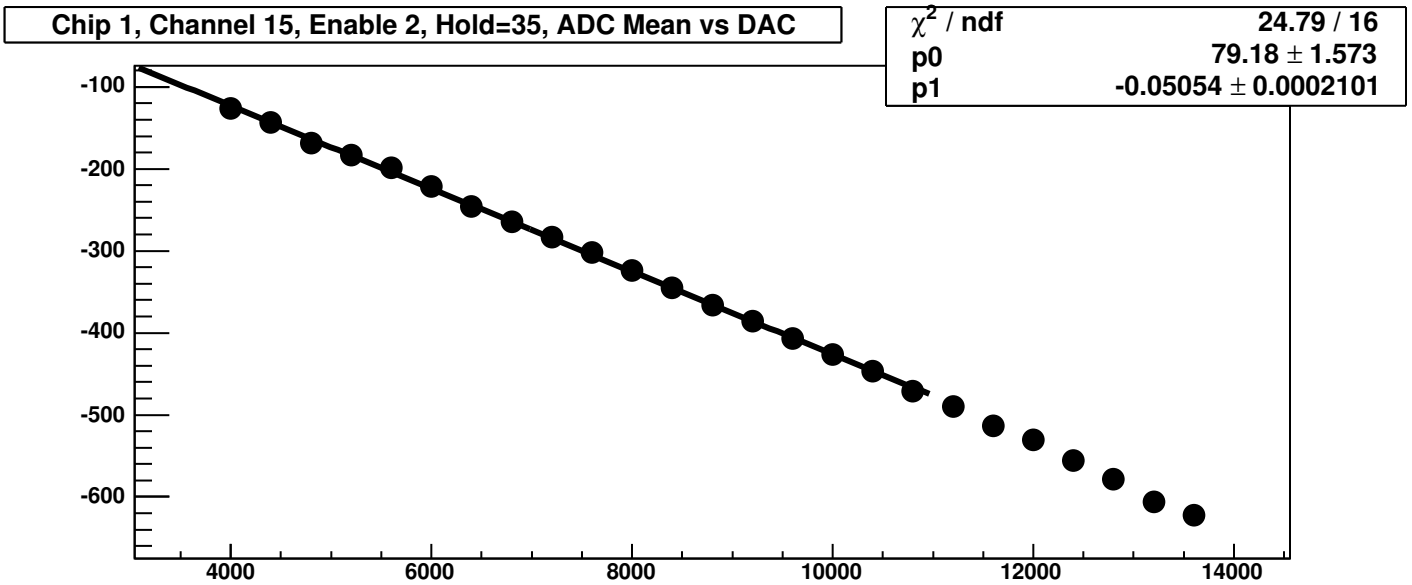

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

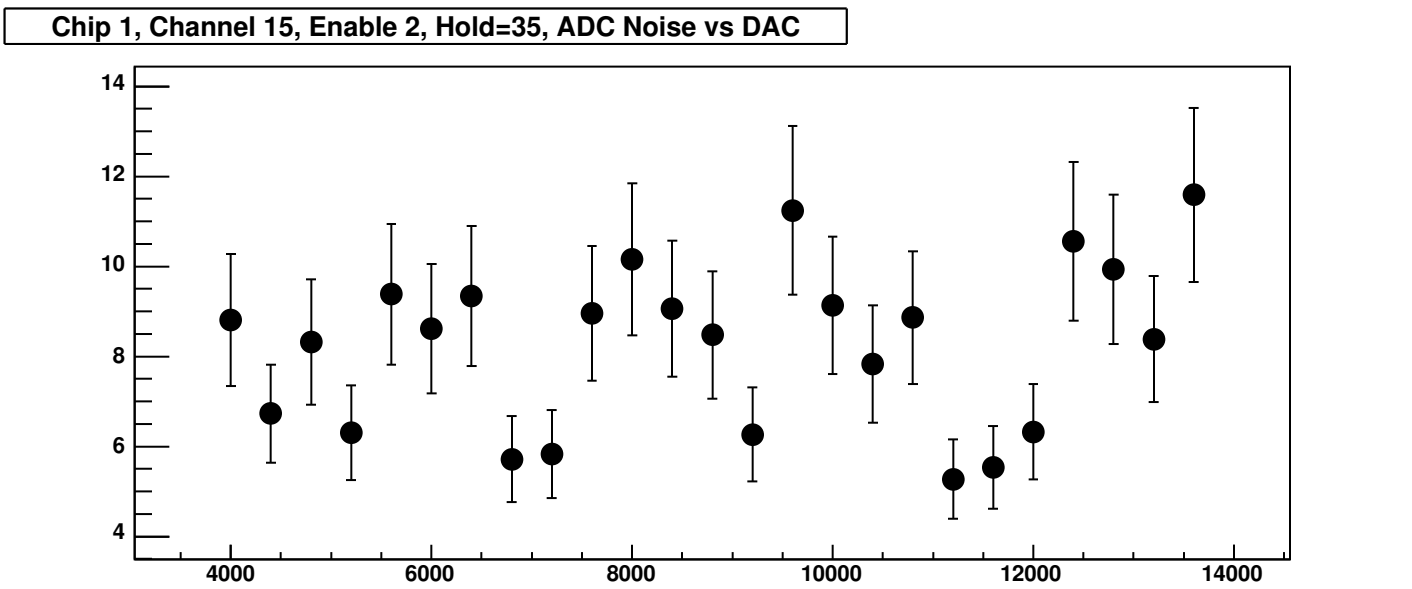

Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

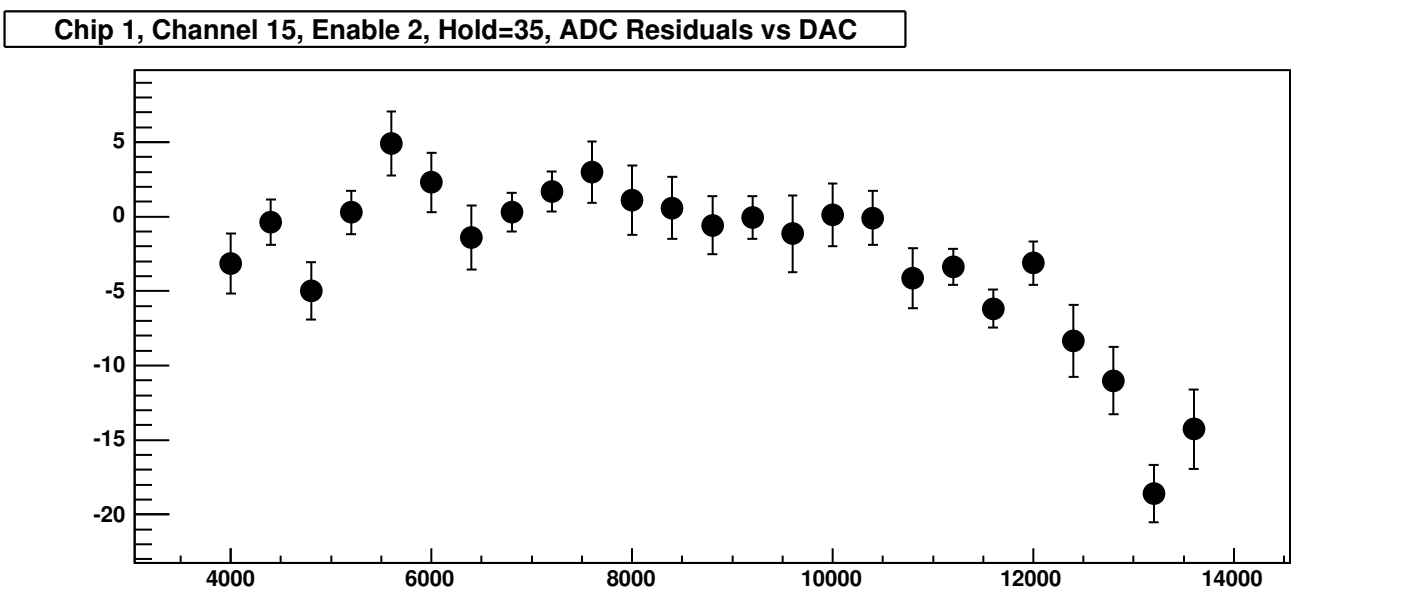

Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.22 / 16
p0 36.02 ± 1.425
p1 0.01387 ± 0.0001863
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

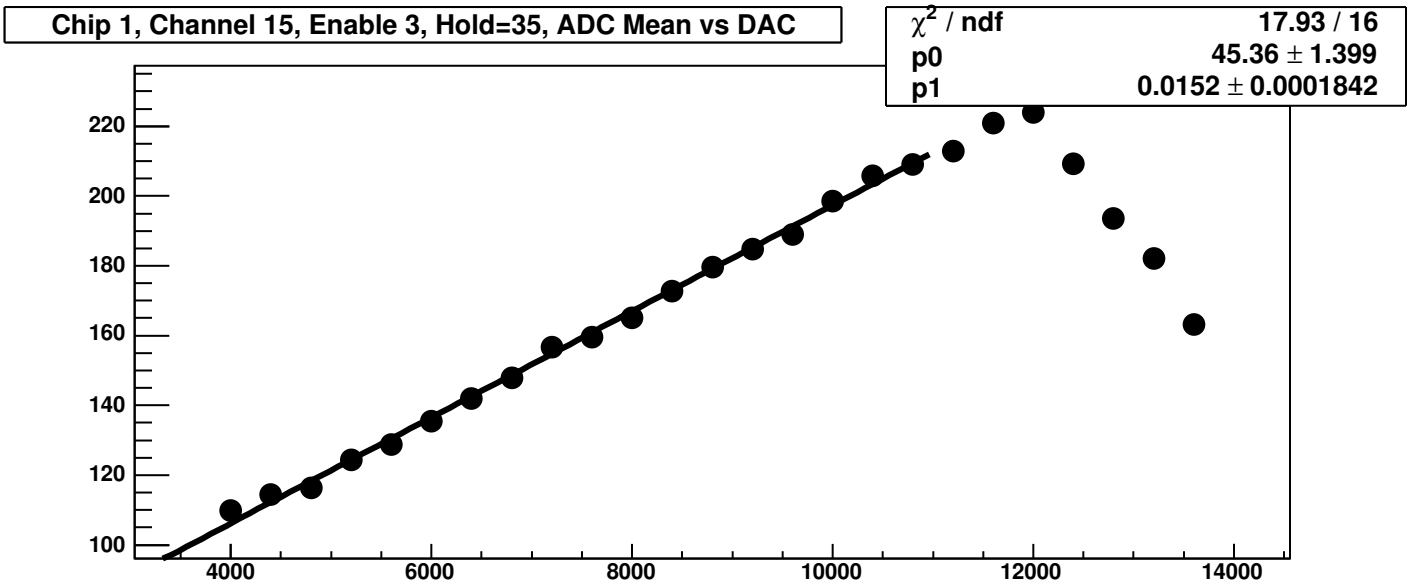

Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

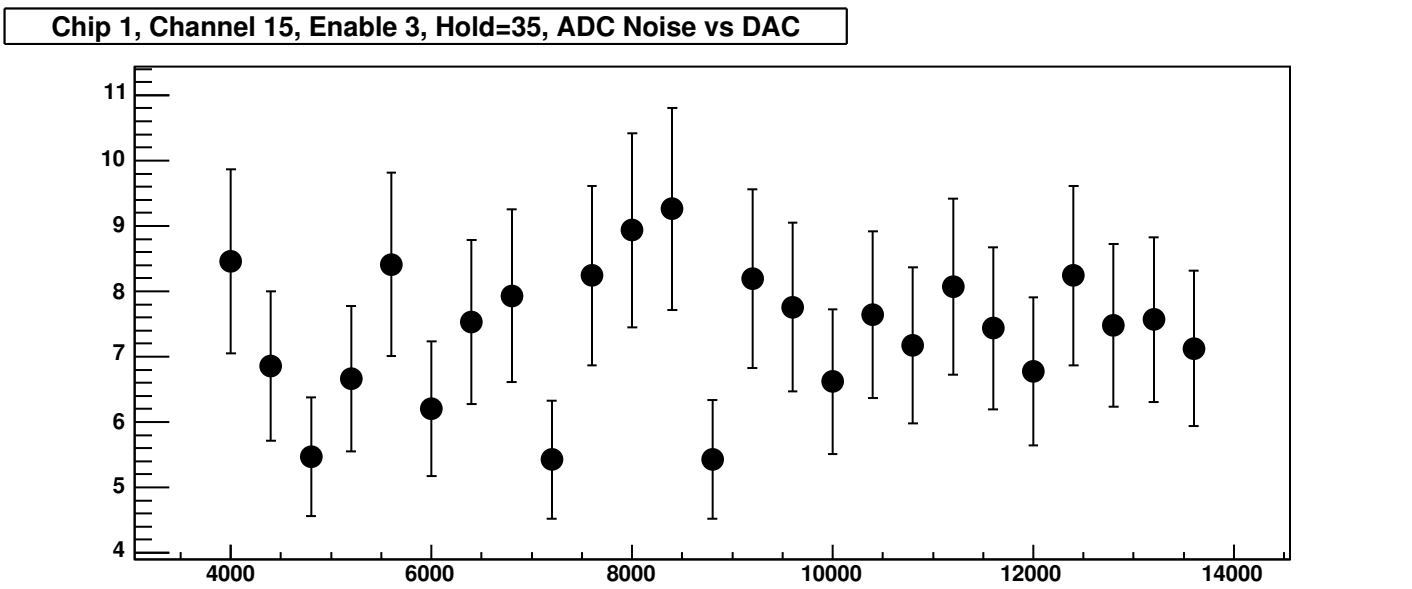

Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

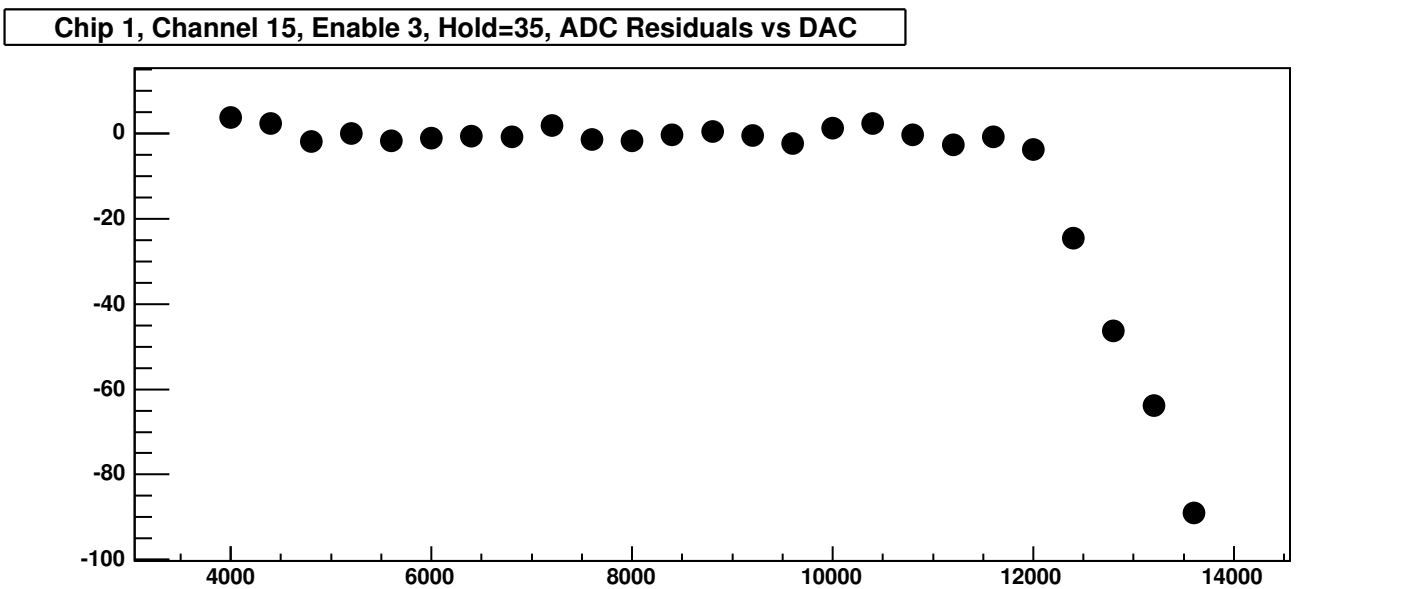

Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.79 / 16
p0 79.18 ± 1.573
p1 -0.05054 ± 0.0002101
-100
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

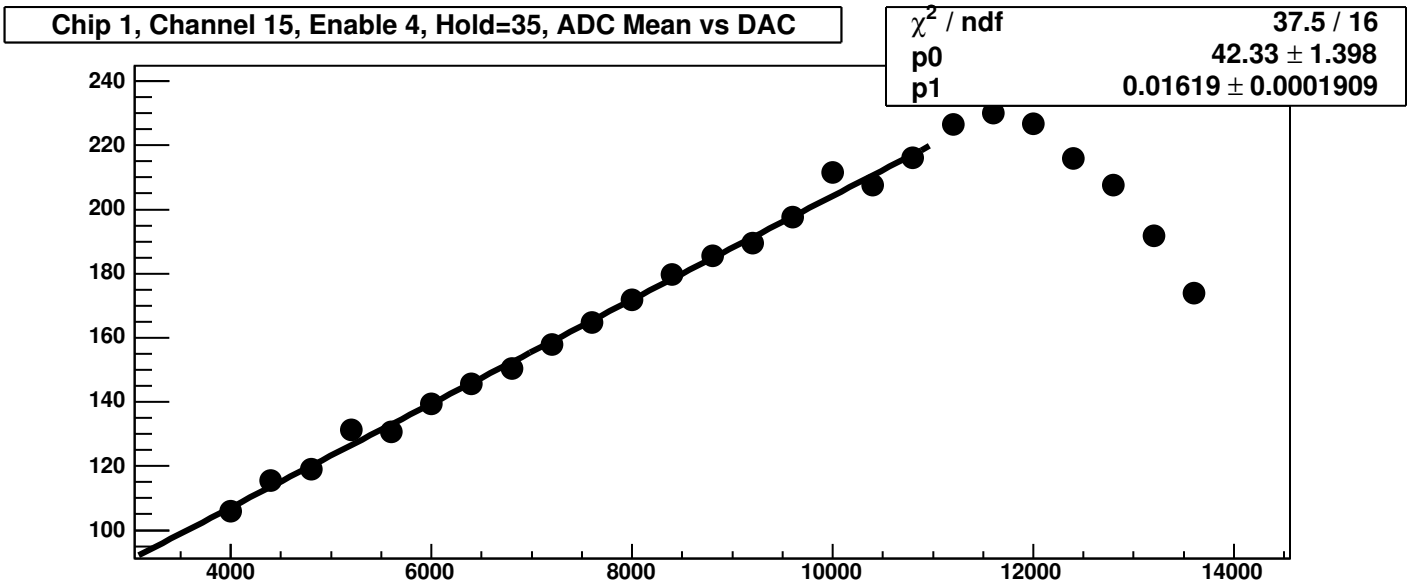

Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

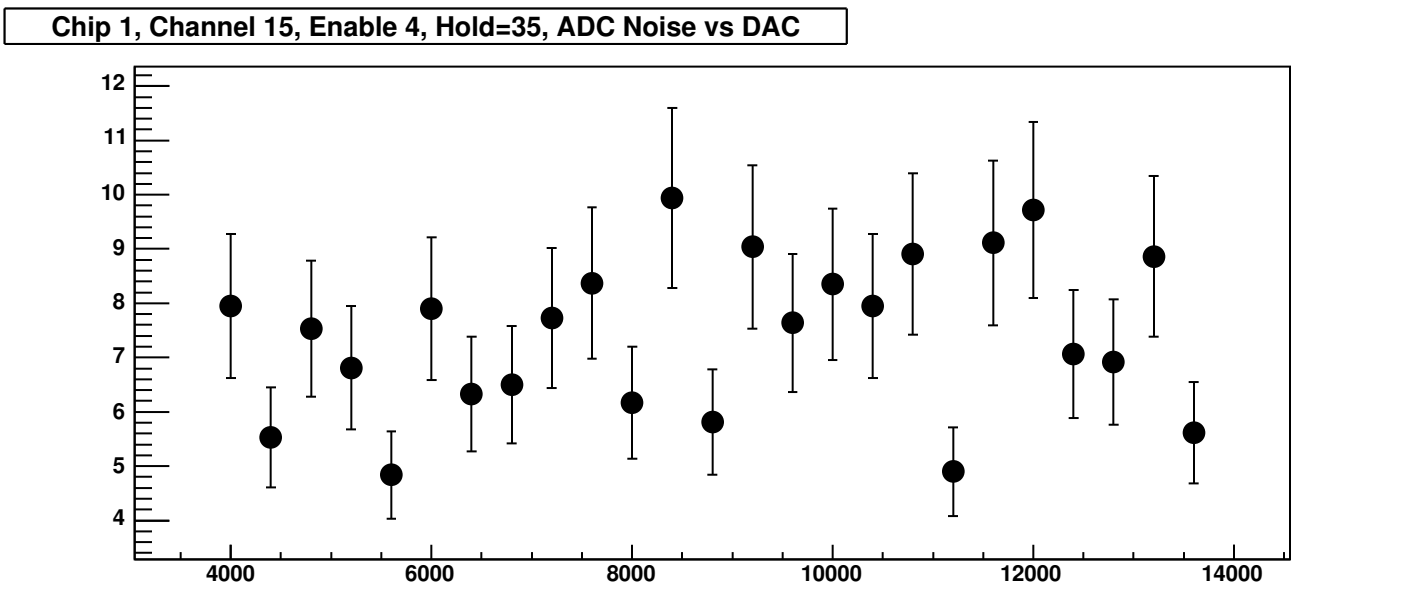

Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

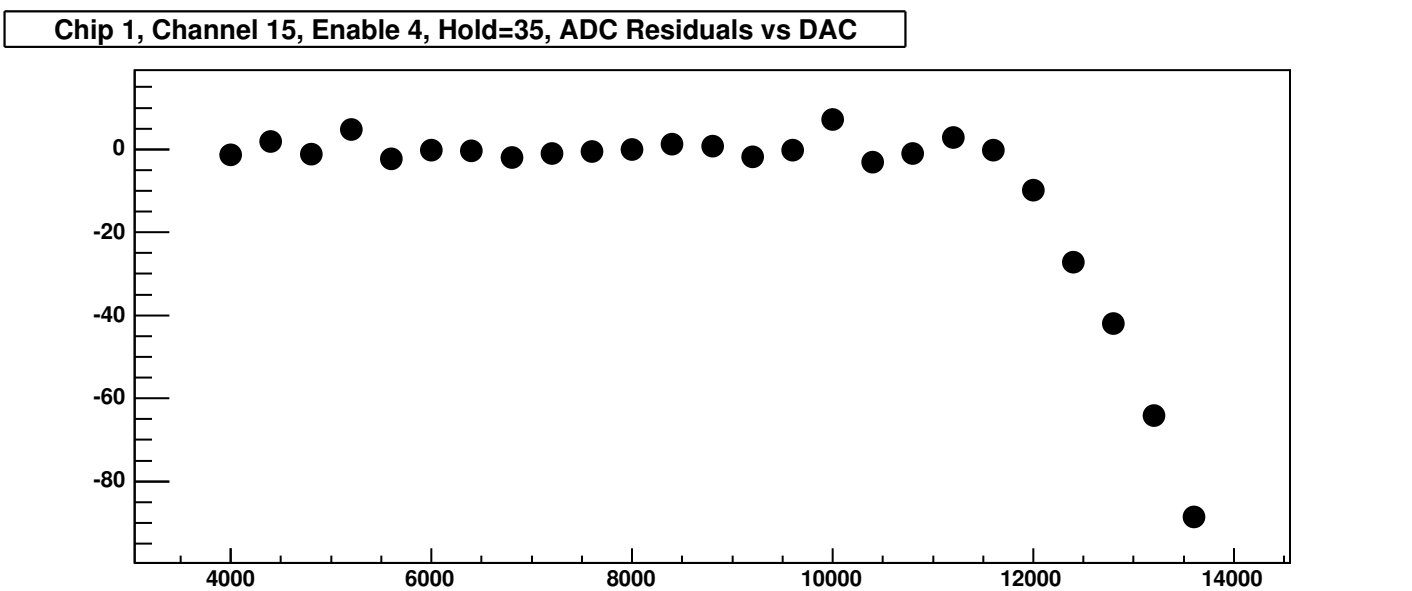

Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.93 / 16
p0 45.36 ± 1.399
p1 0.0152 ± 0.0001842
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

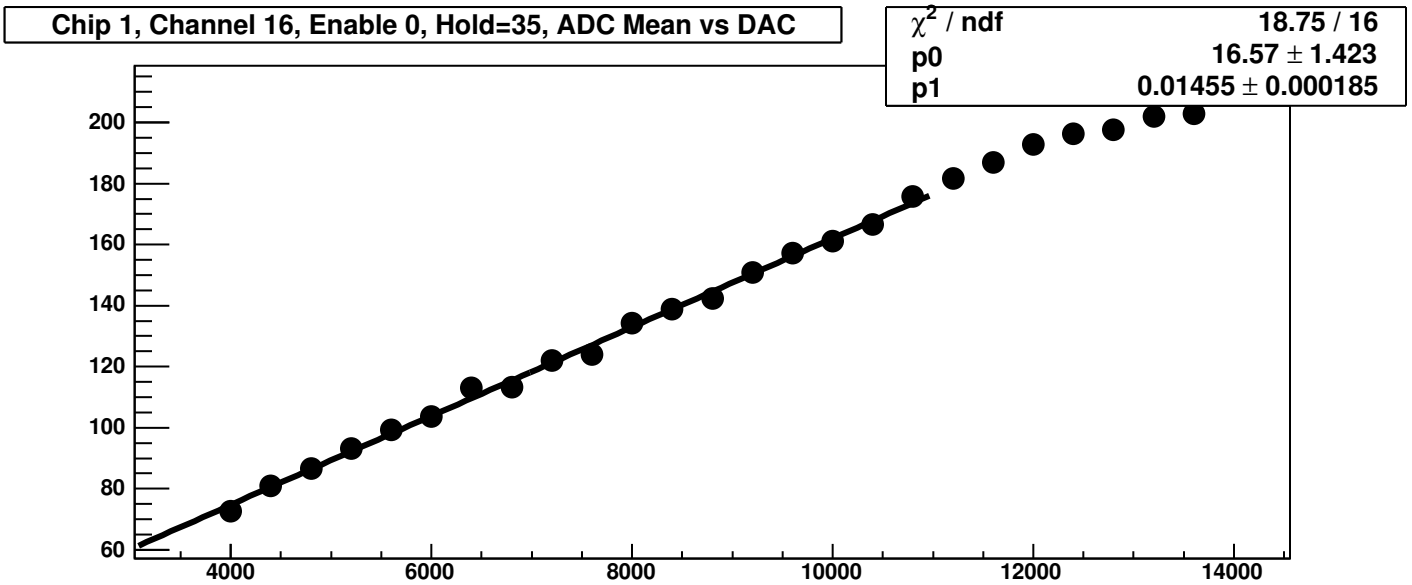

Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

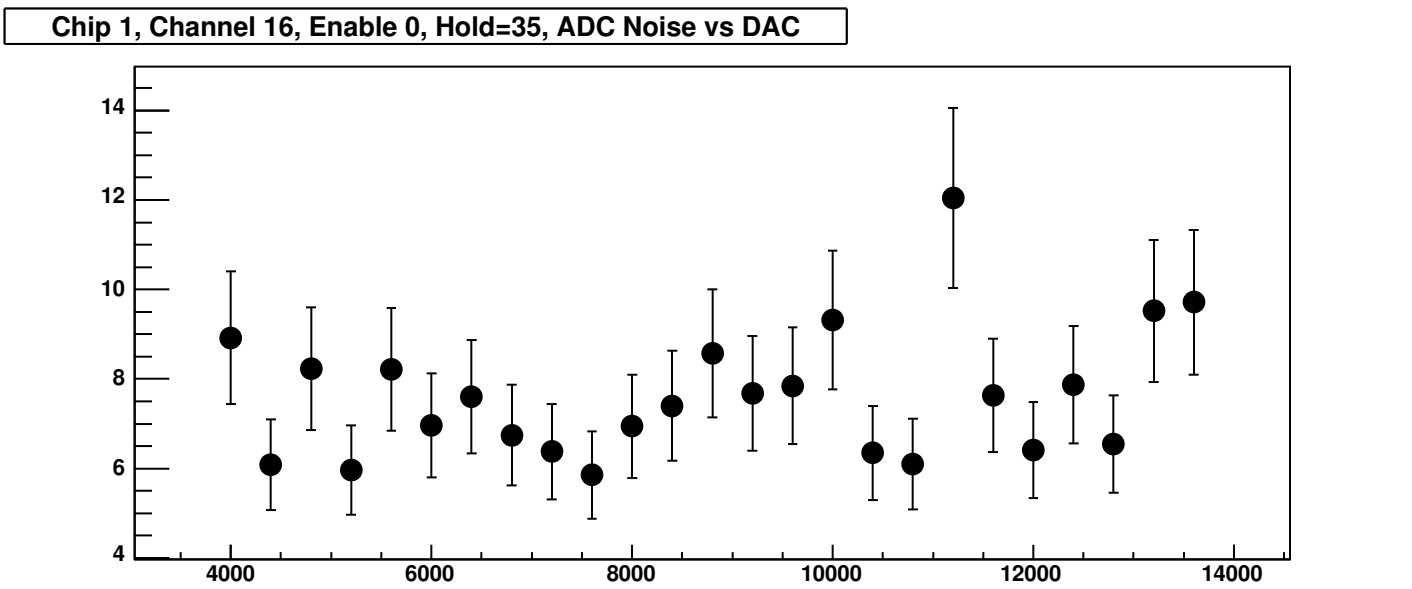

Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
-100
4000
6000
8000
10000
12000
14000

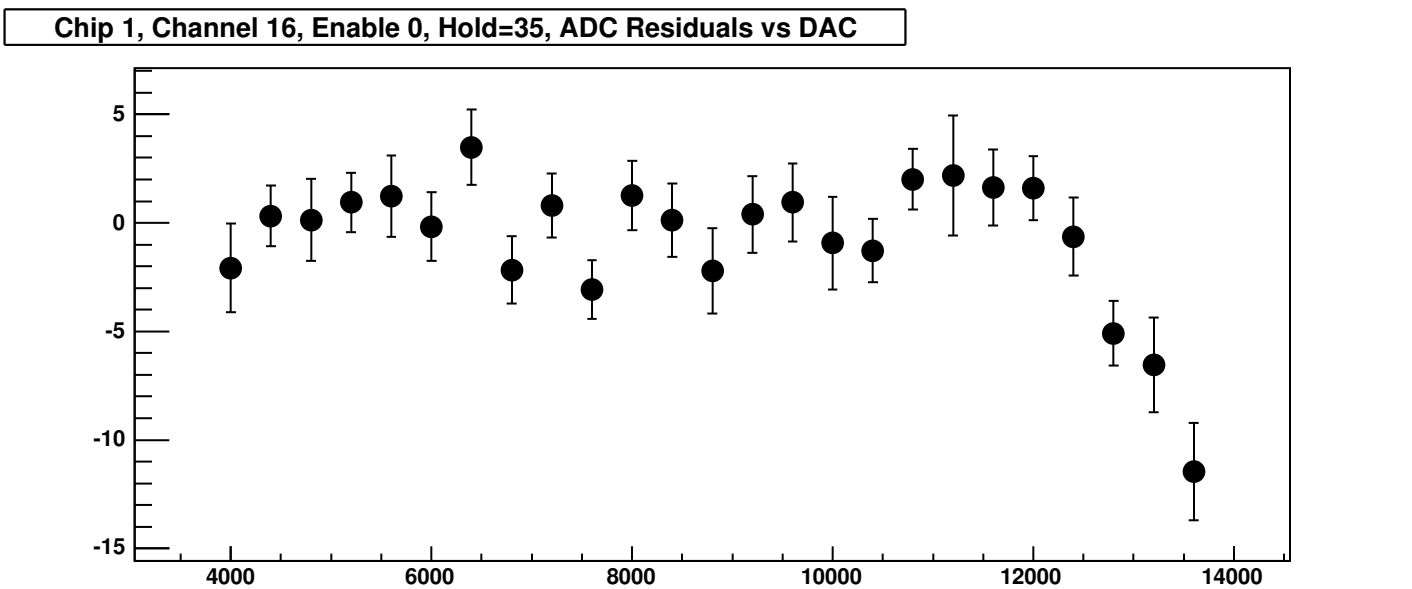

Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.5 / 16
p0 42.33 ± 1.398
p1 0.01619 ± 0.0001909
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000


Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
4000
6000
8000
10000
12000
14000

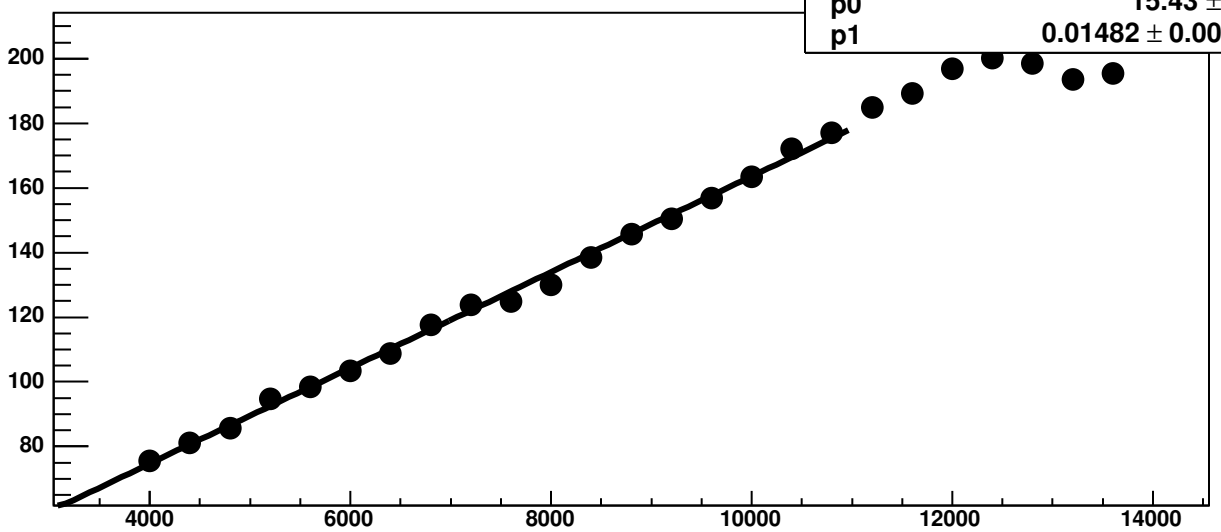

Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18.75 / 16
p0 16.57 ± 1.423
p1 0.01455 ± 0.000185
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000


Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

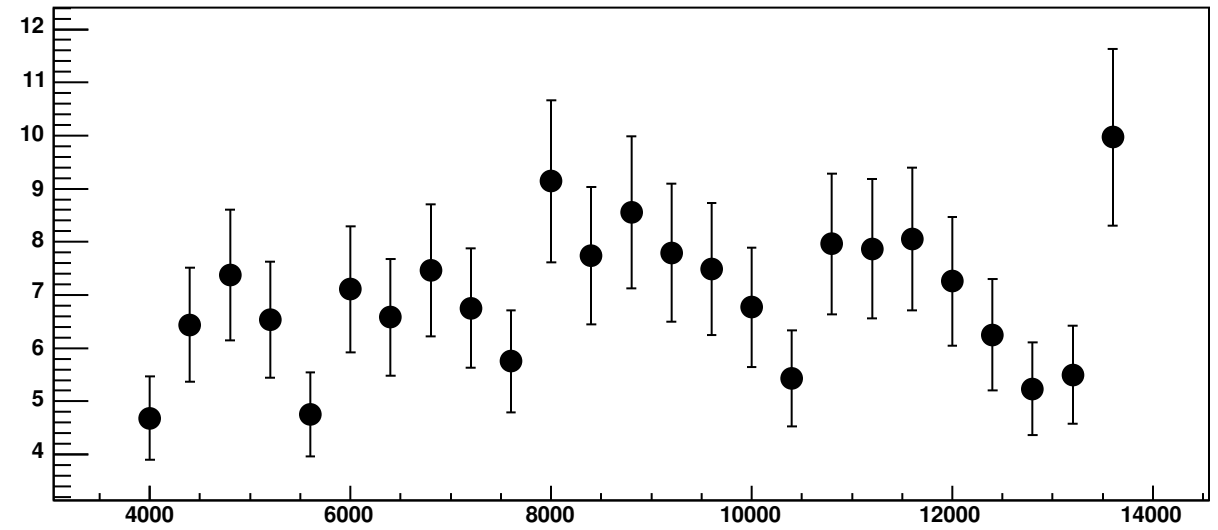

Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

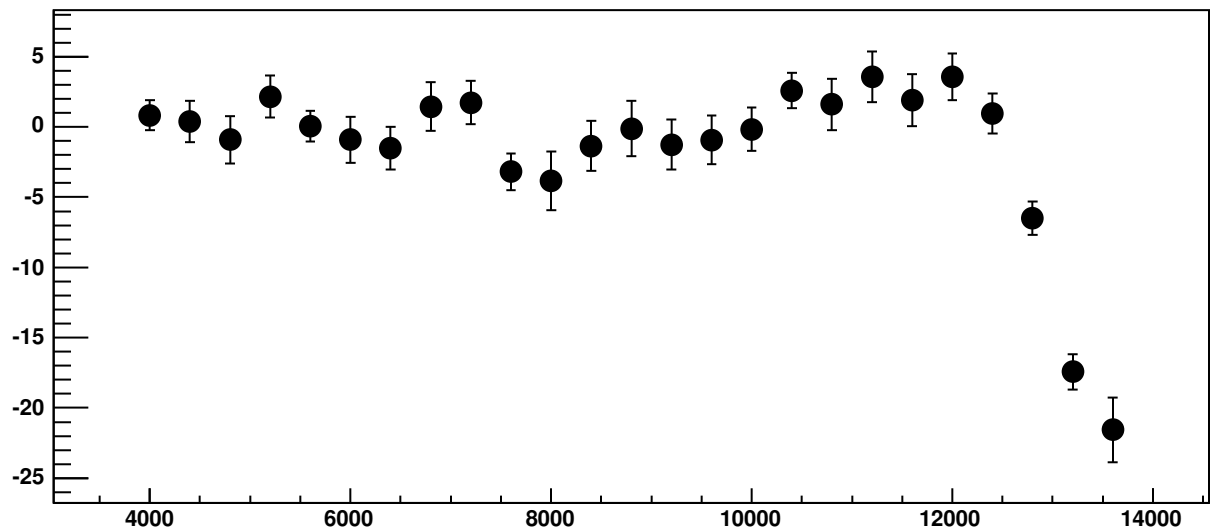
χ^2 / ndf	22.02 / 16
p0	15.43 ± 1.207
p1	0.01482 ± 0.0001635

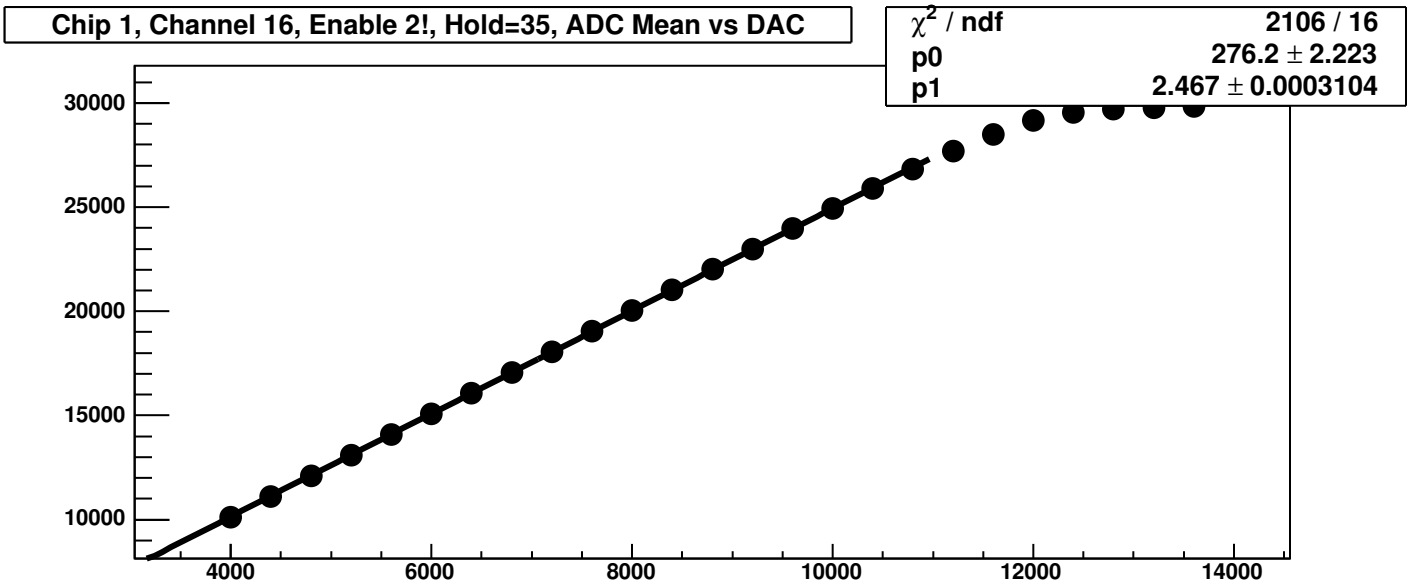


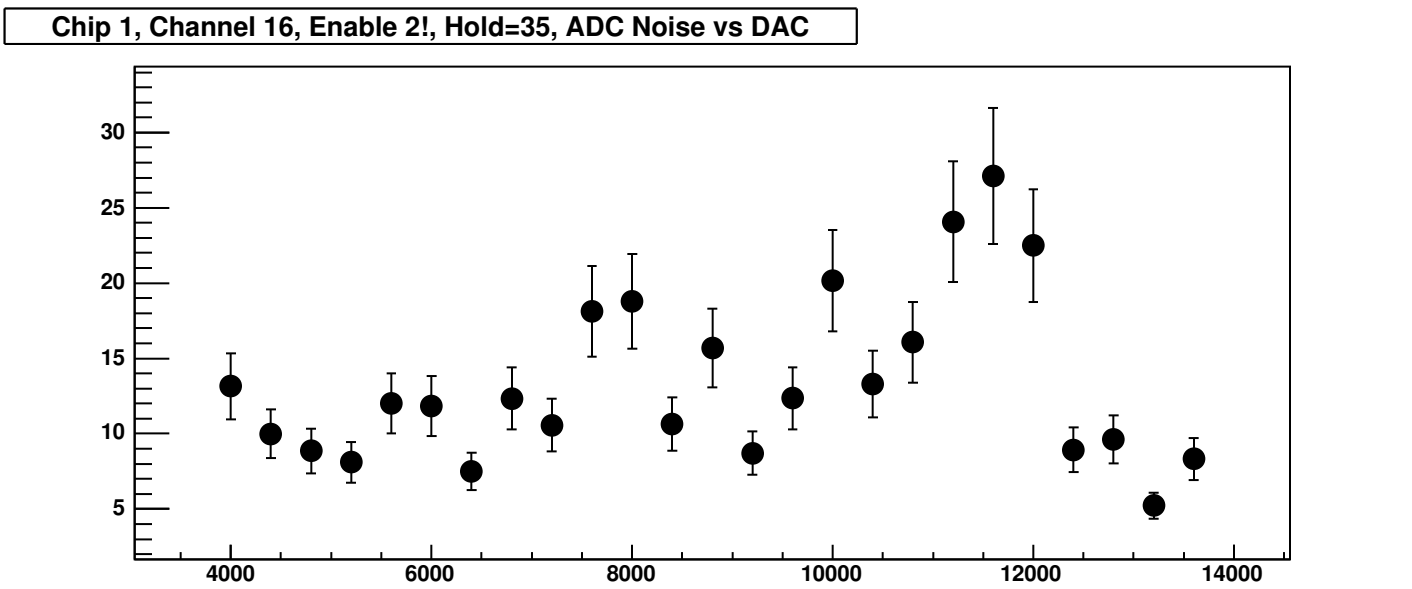
Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

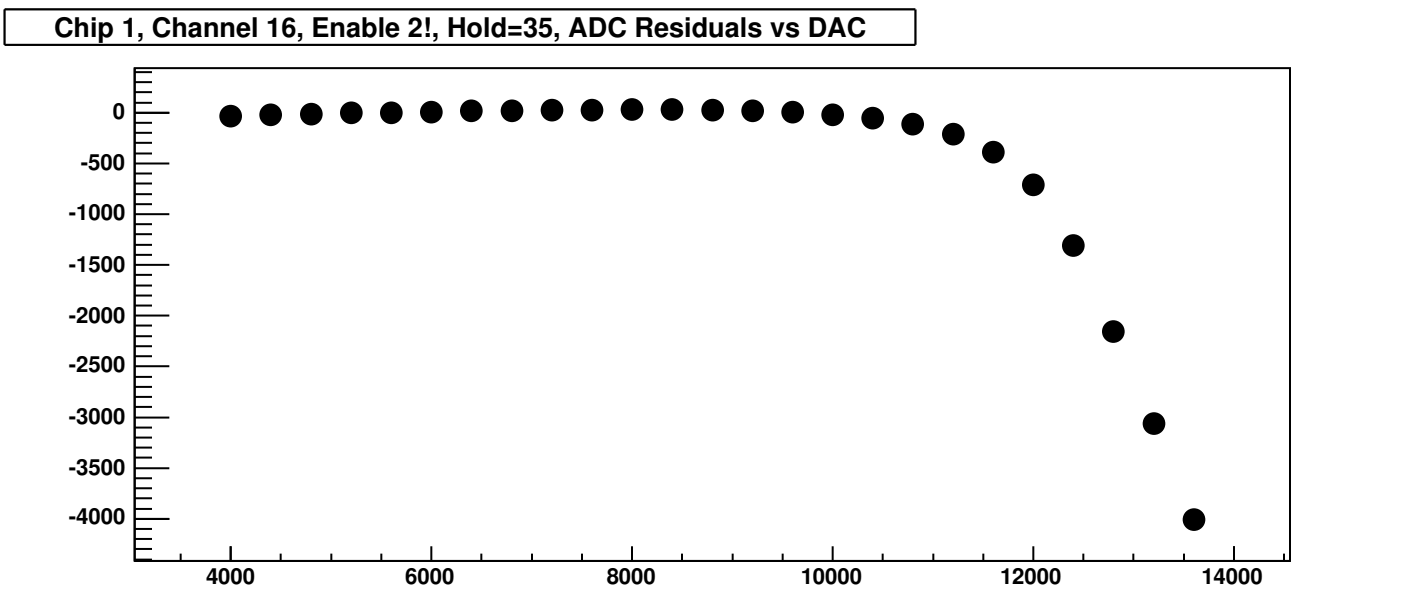


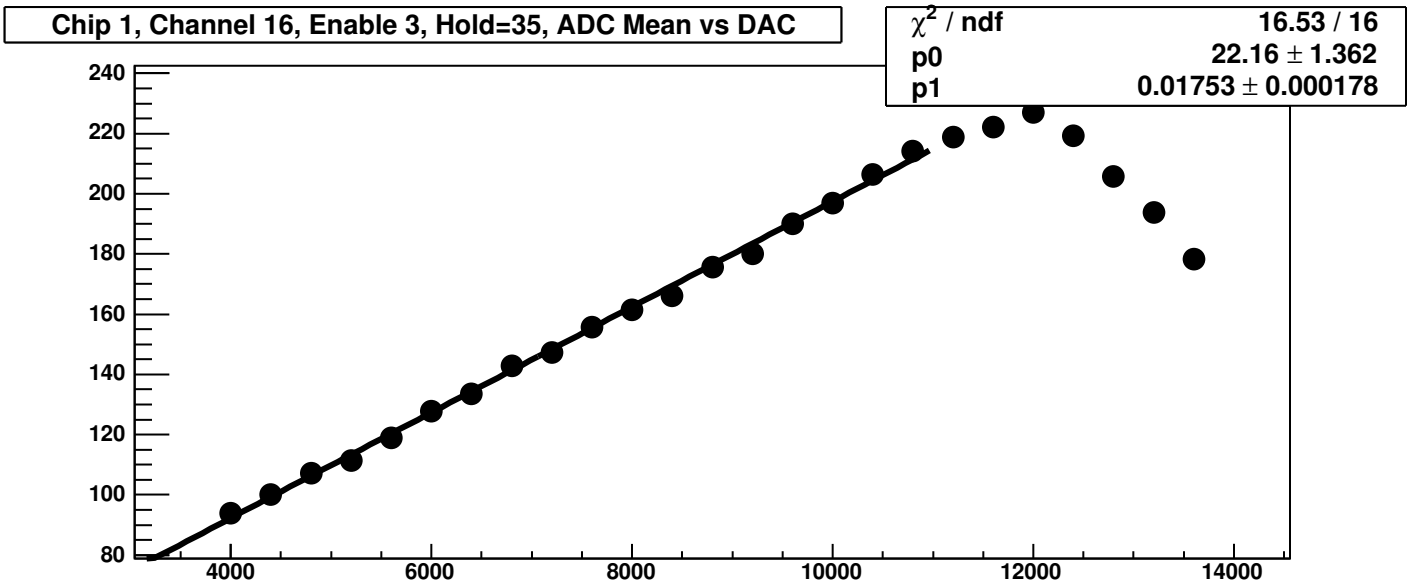
Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

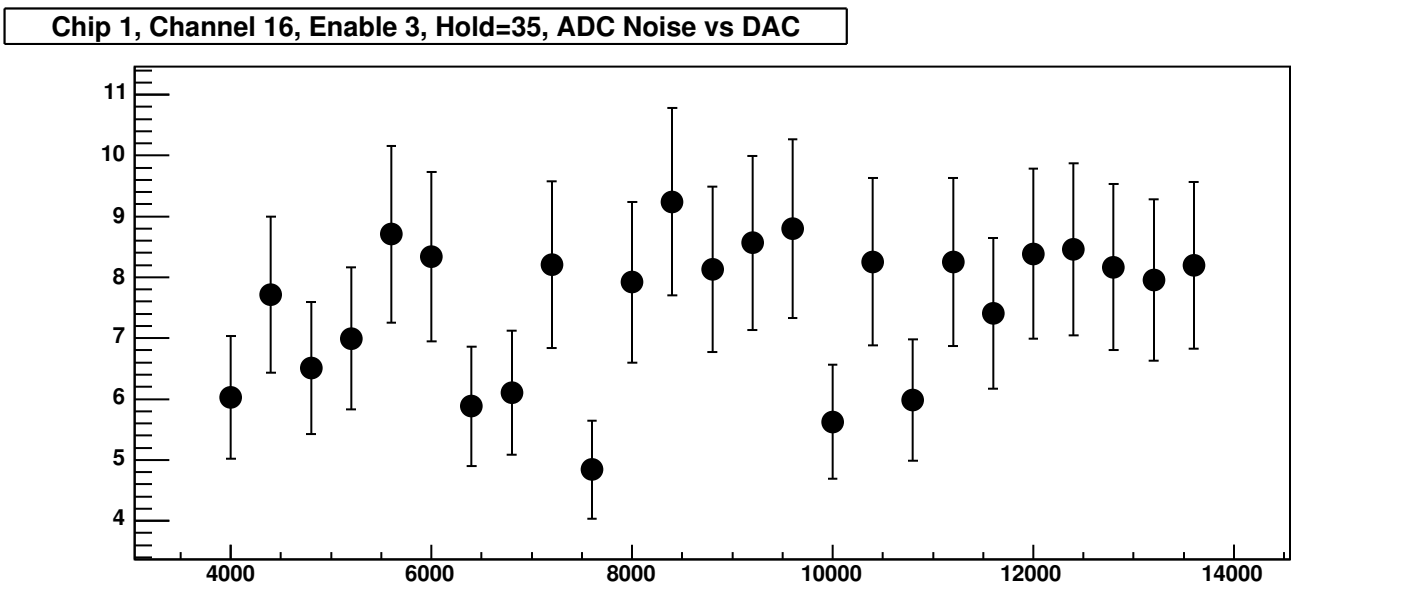


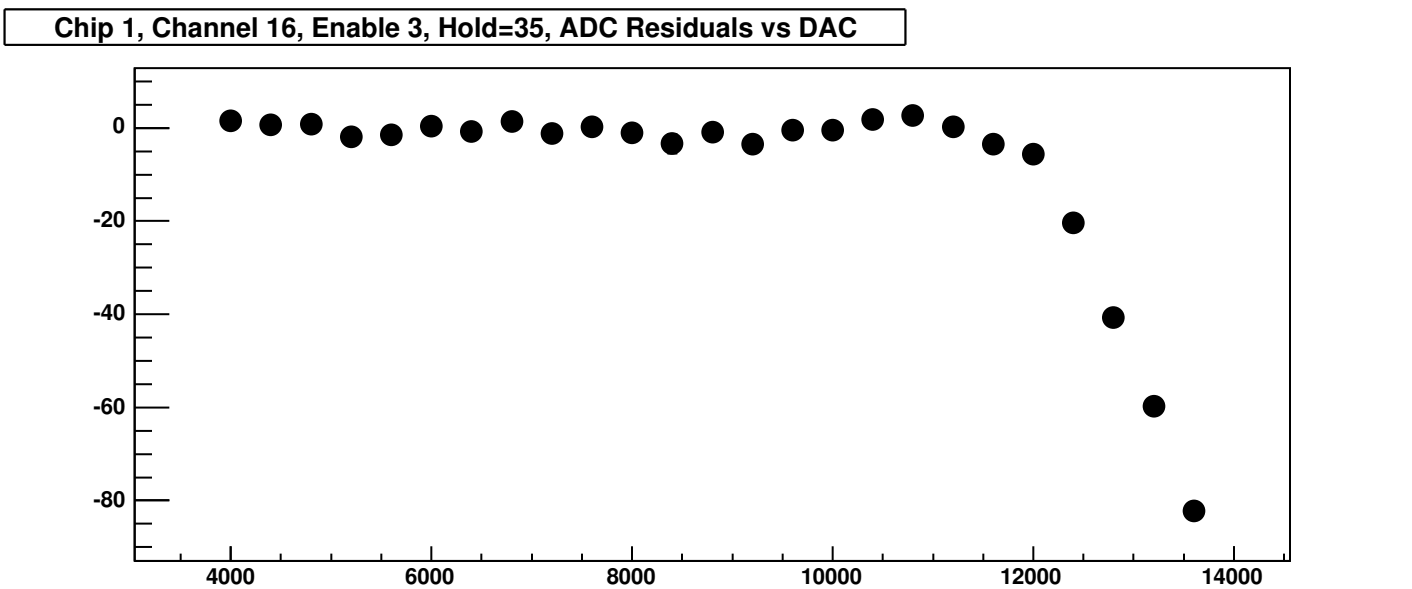

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 2106 / 16
p0 276.2 ± 2.223
p1 2.467 ± 0.0003104
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

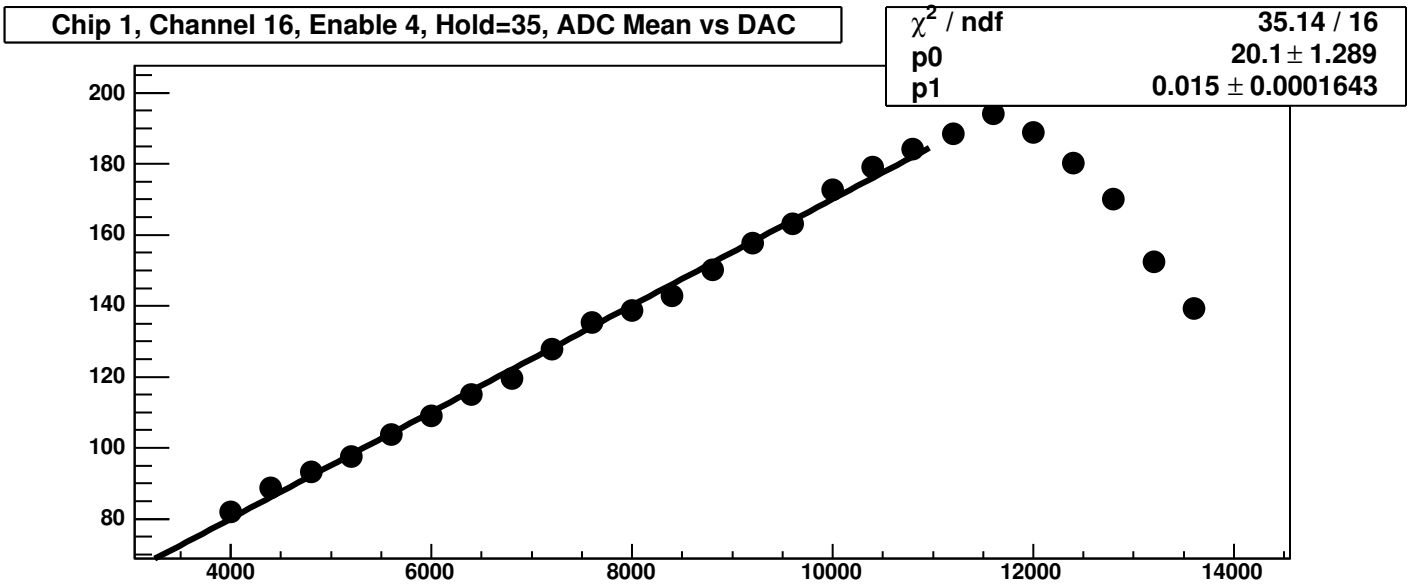

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

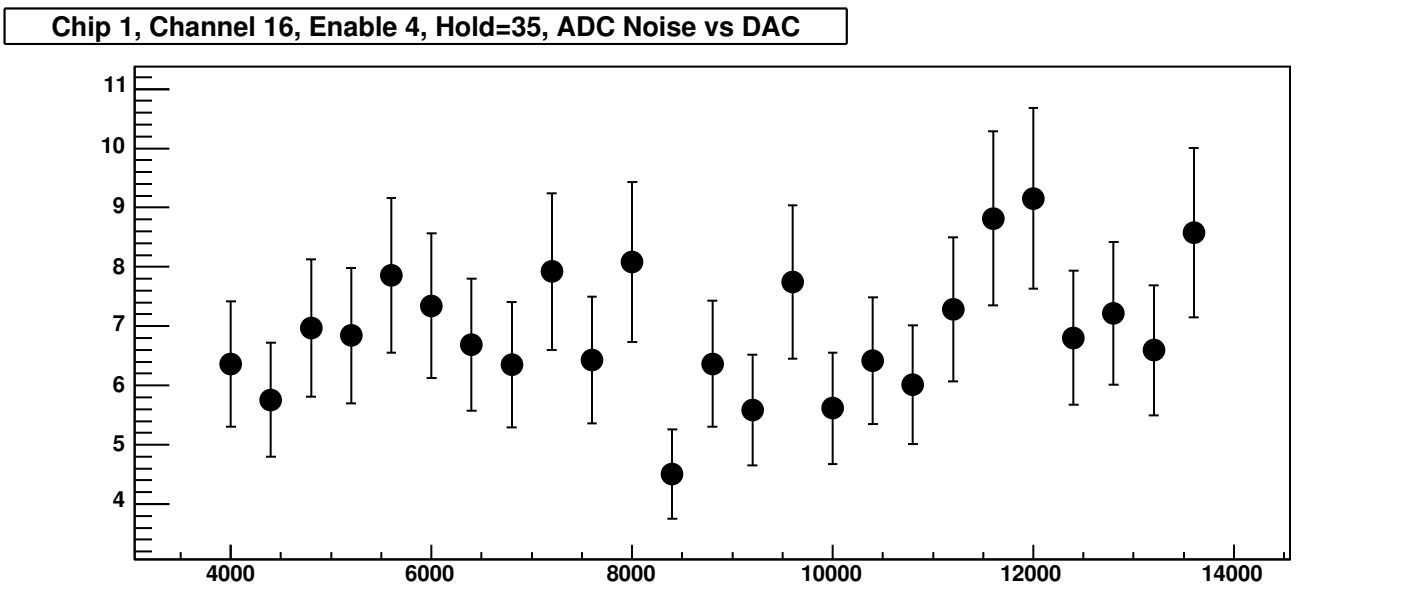

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

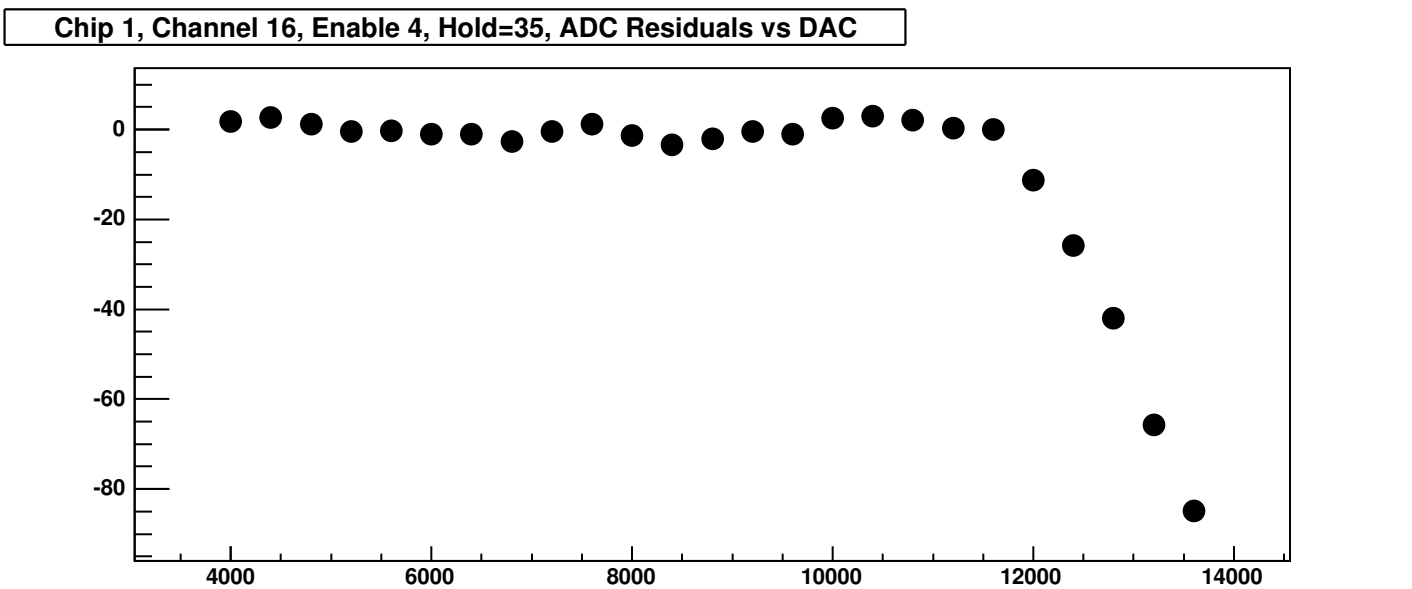

Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.53 / 16
p0 22.16 ± 1.362
p1 0.01753 ± 0.000178
240
220
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

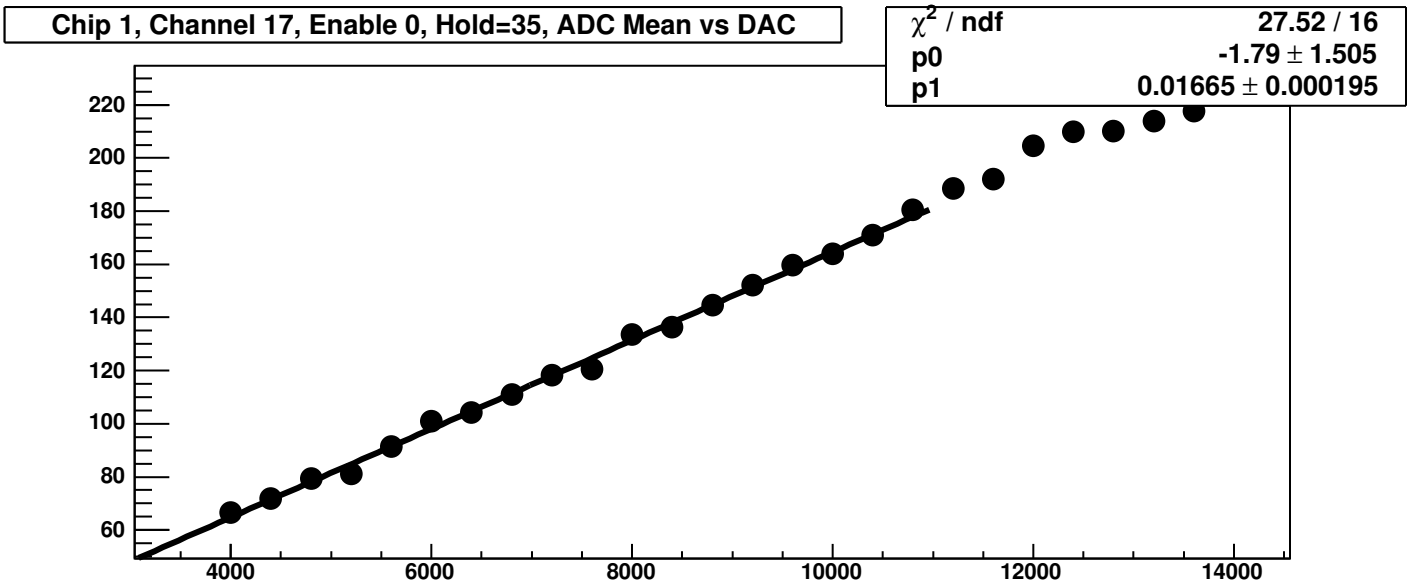

Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

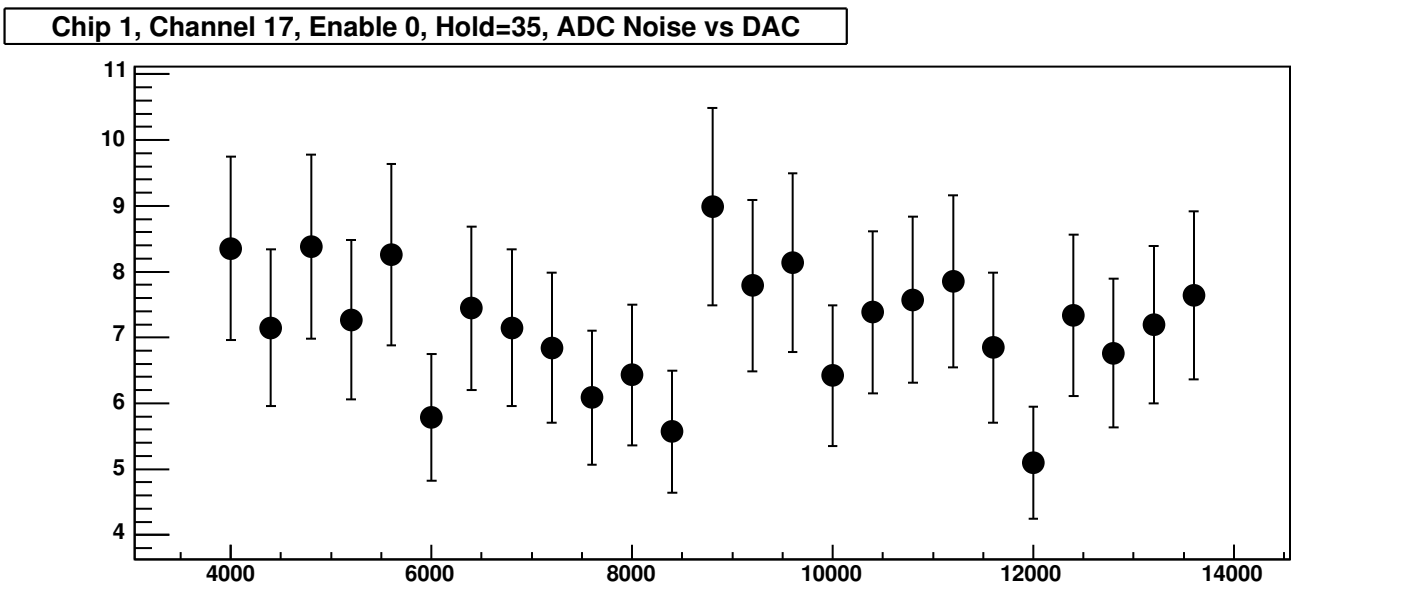

Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
4000
6000
8000
10000
12000
14000

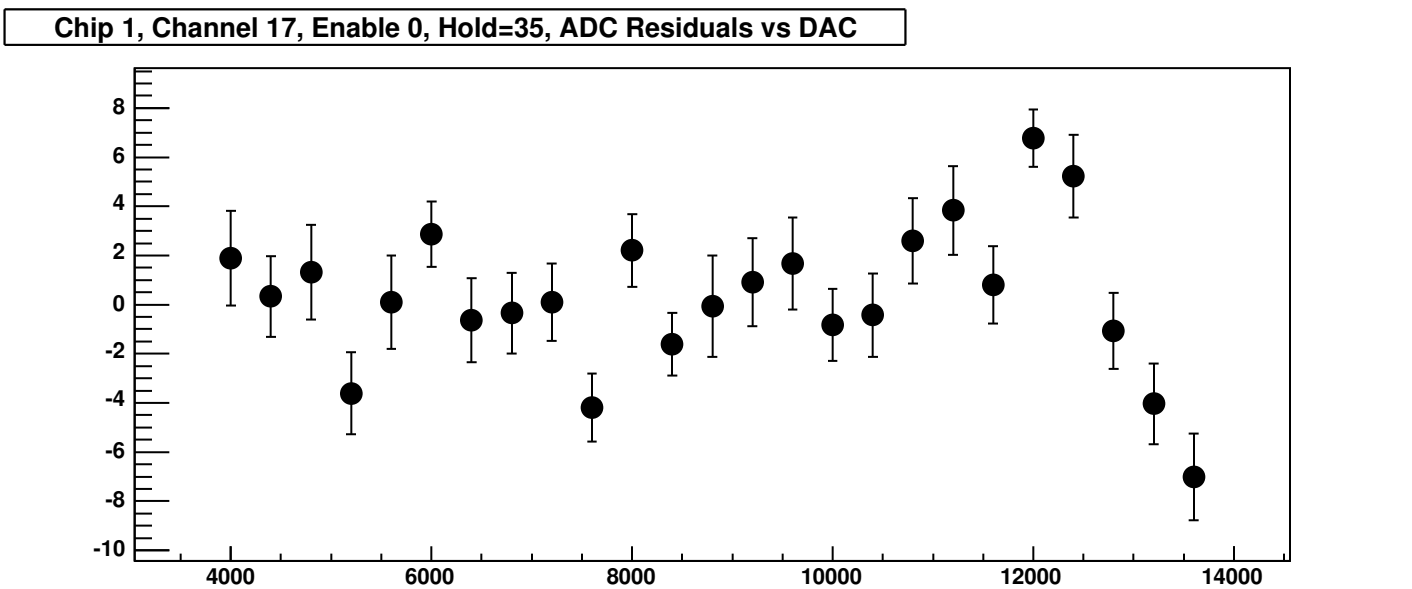

Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.14 / 16
p0 20.1 ± 1.289
p1 0.015 ± 0.0001643
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

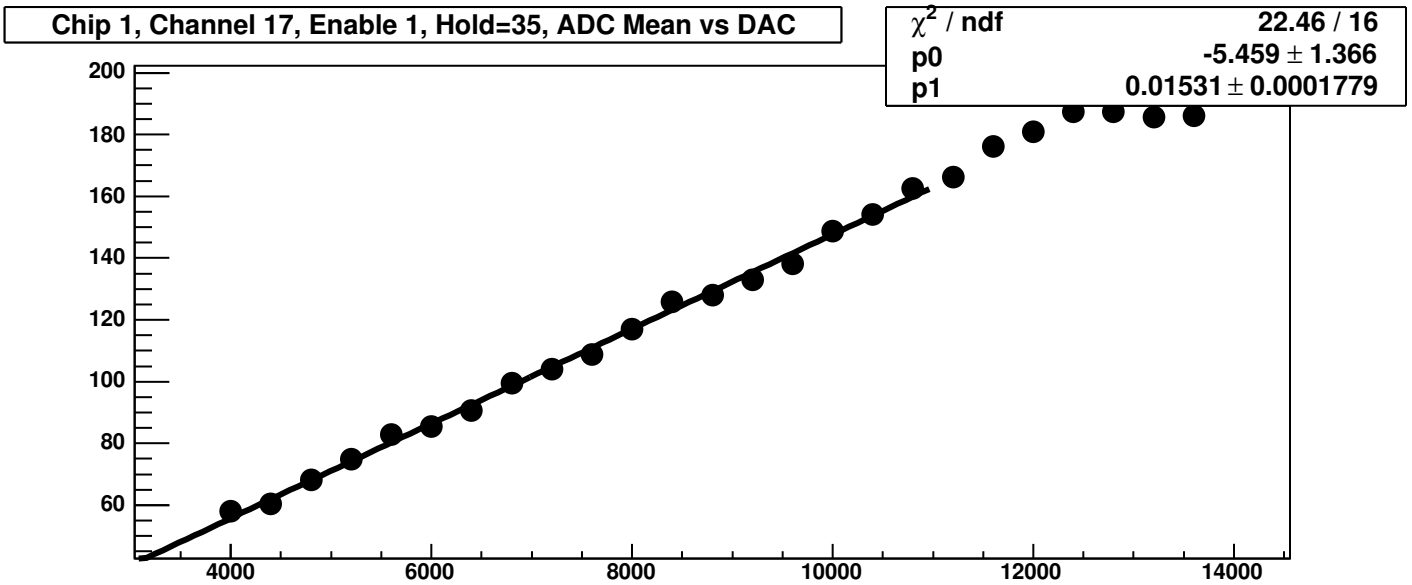

Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

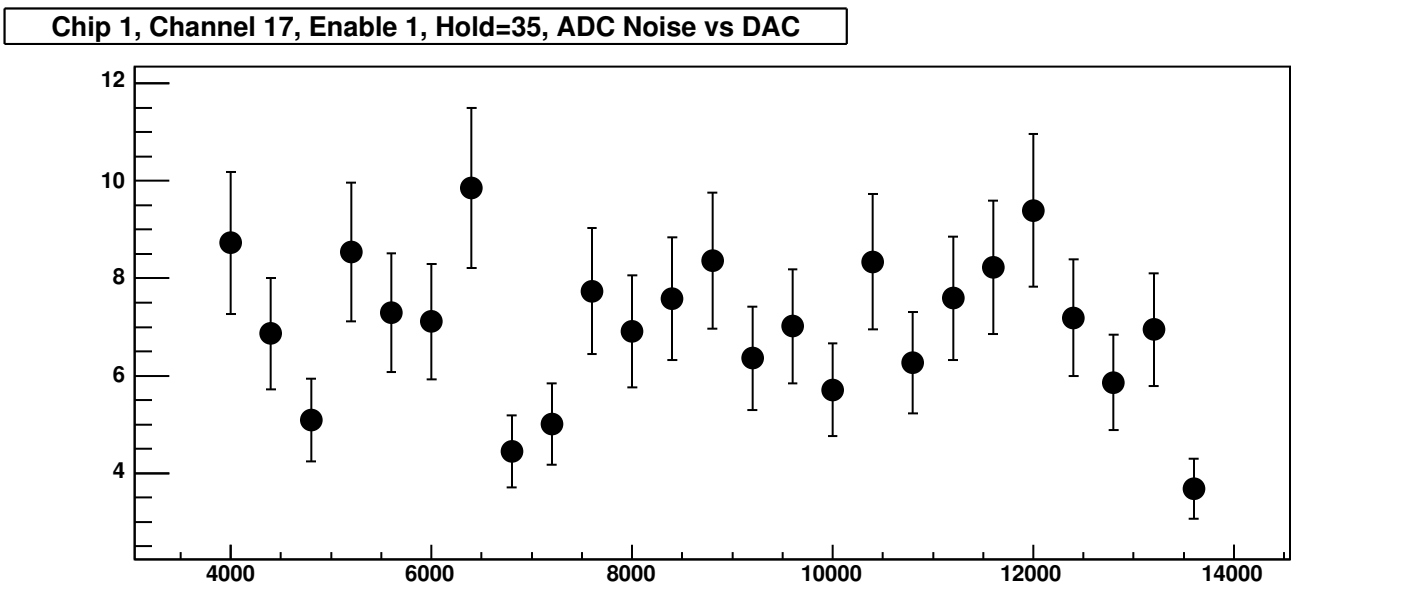

Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
4000
6000
8000
10000
12000
14000

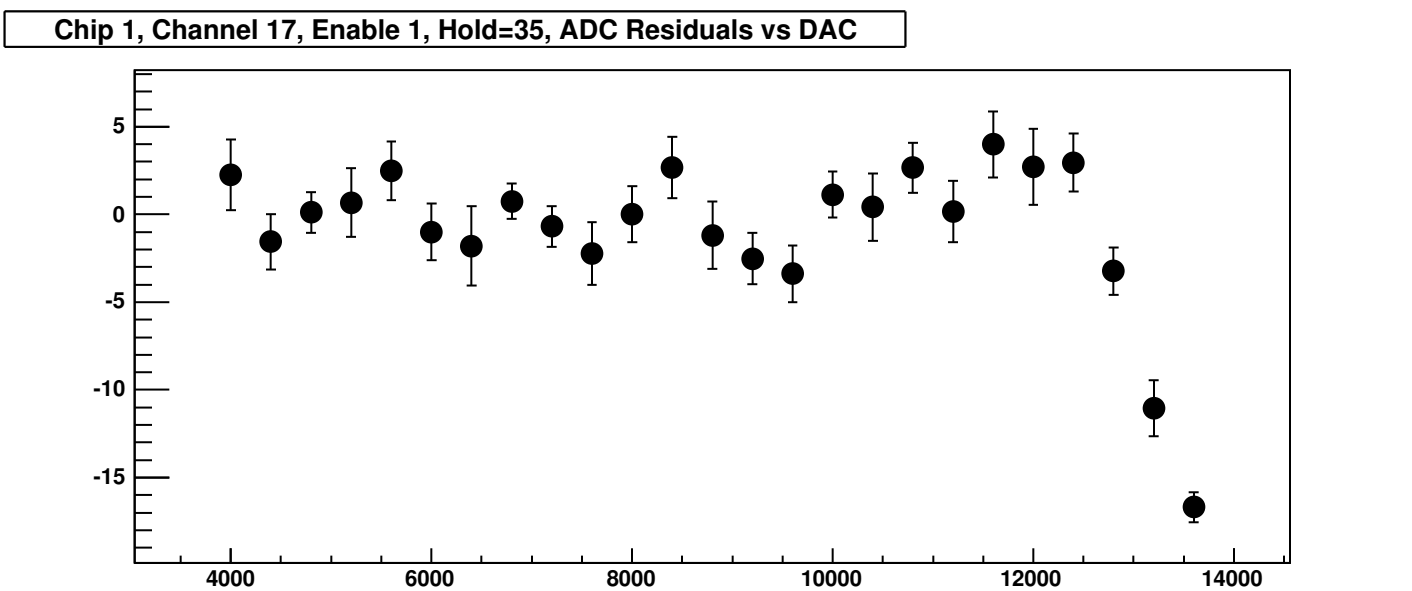

Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.52 / 16
p0 -1.79 ± 1.505
p1 0.01665 ± 0.000195
220
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

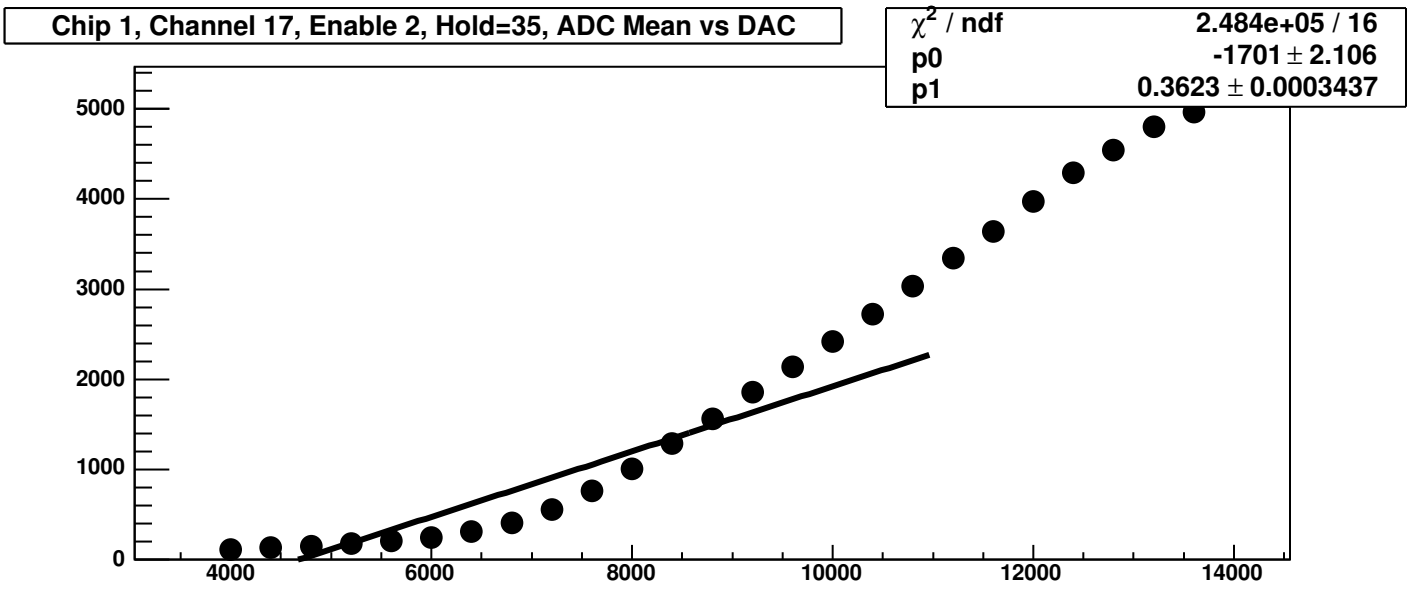

Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

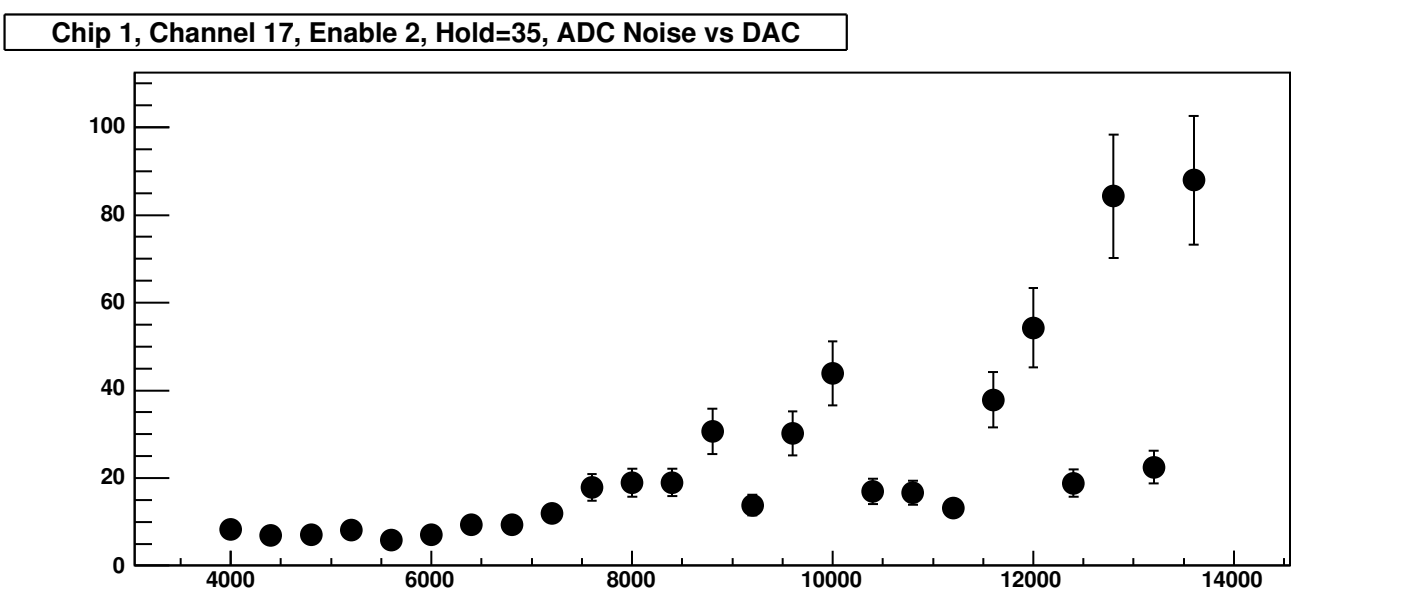

Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

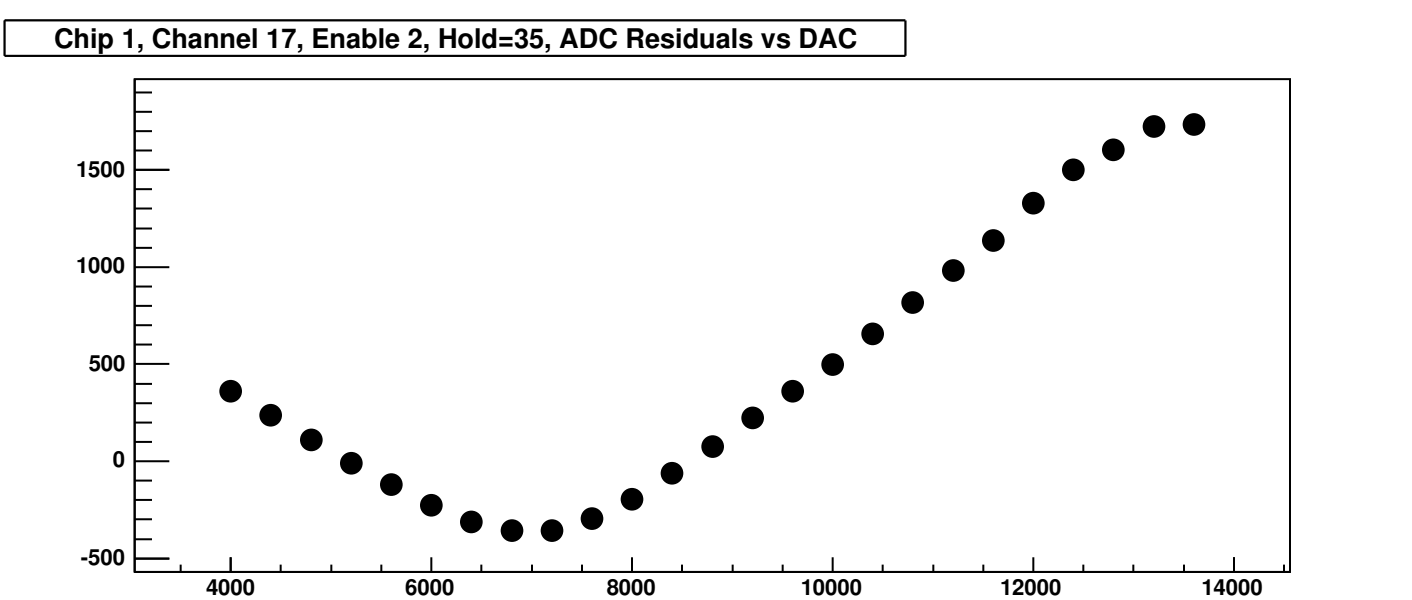

Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.46 / 16
p0 -5.459 ± 1.366
p1 0.01531 ± 0.0001779
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

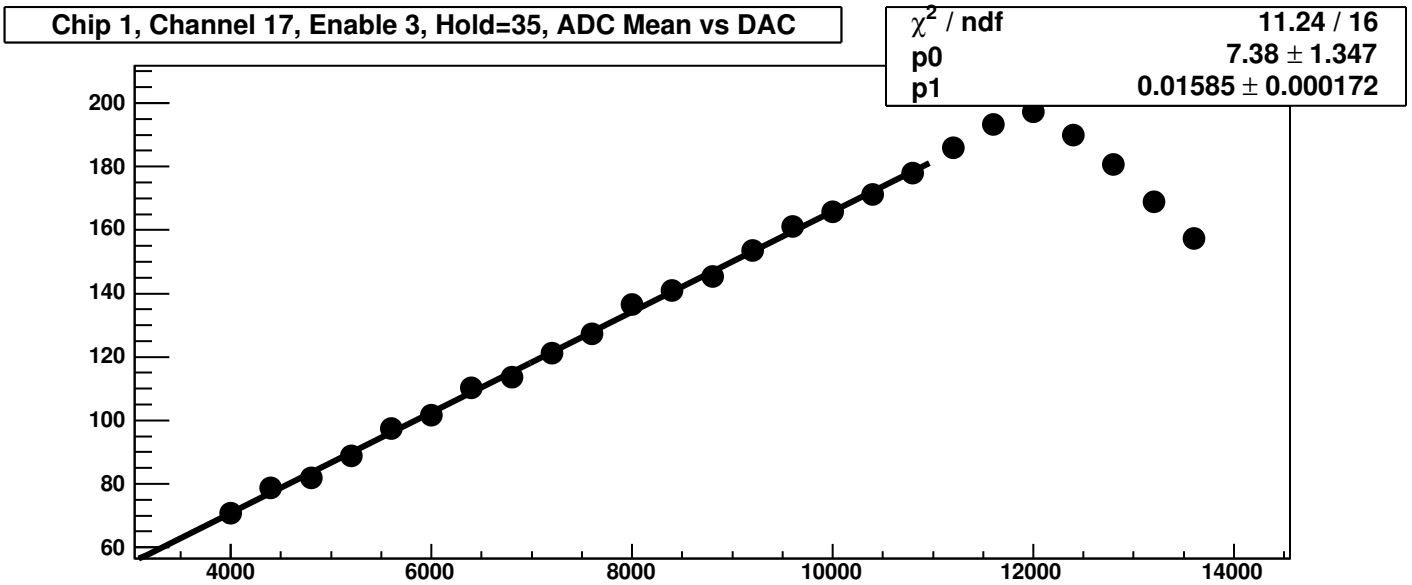

Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

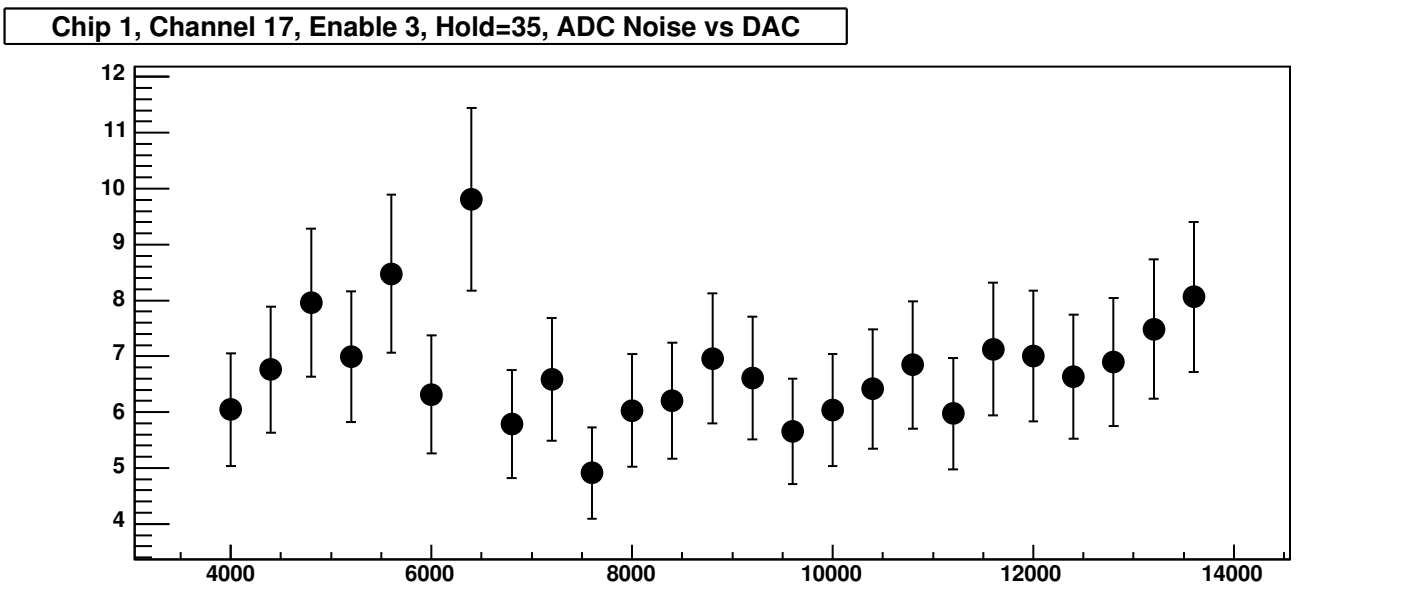

Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

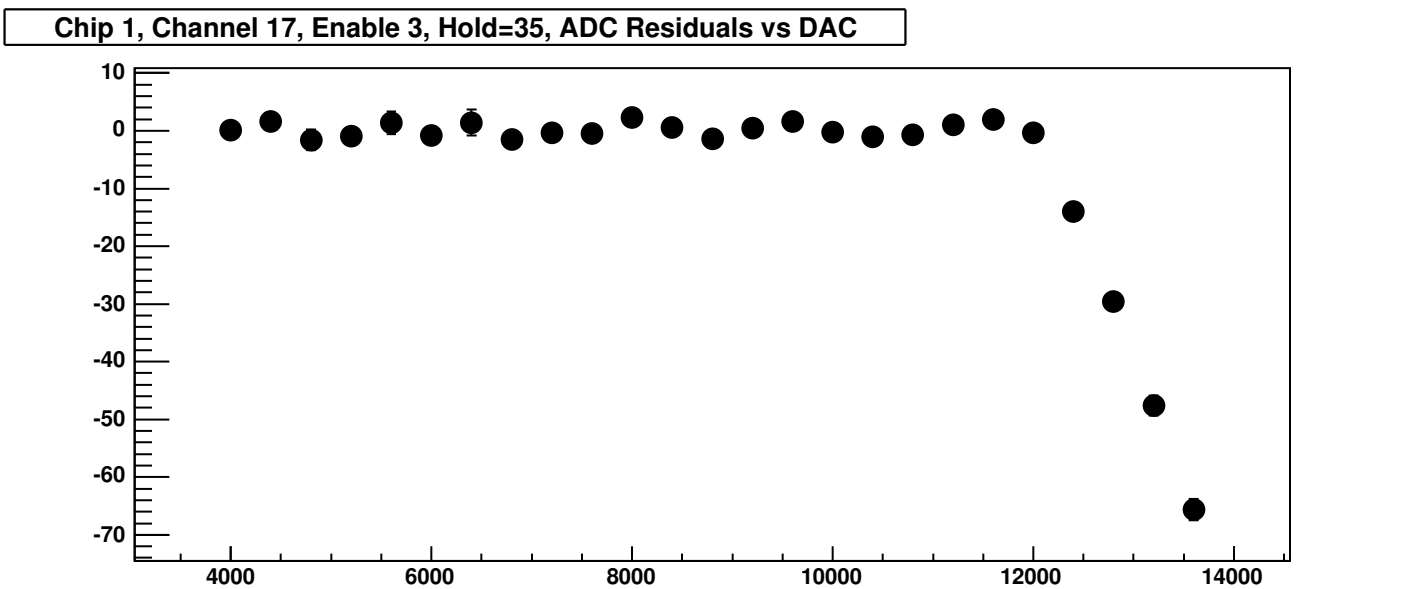

Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2.484e+05 / 16
p0 -1701 ± 2.106
p1 0.3623 ± 0.0003437
5000
4000
3000
2000
1000
0
4000
6000
8000
10000
12000
14000

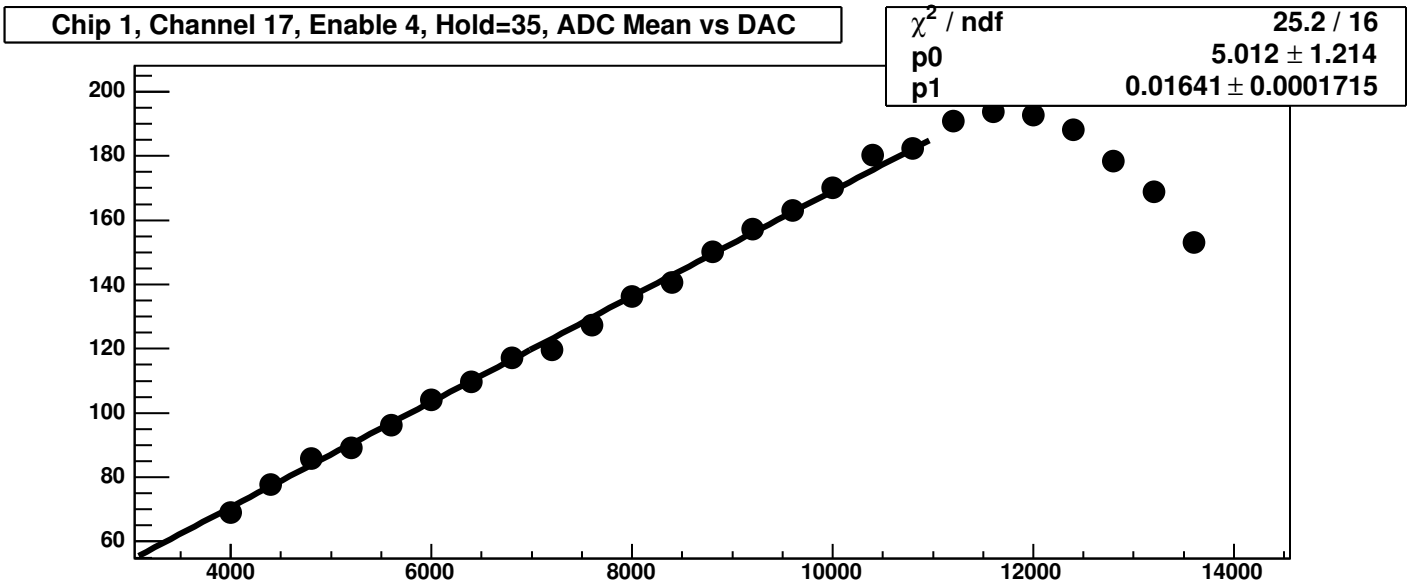

Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

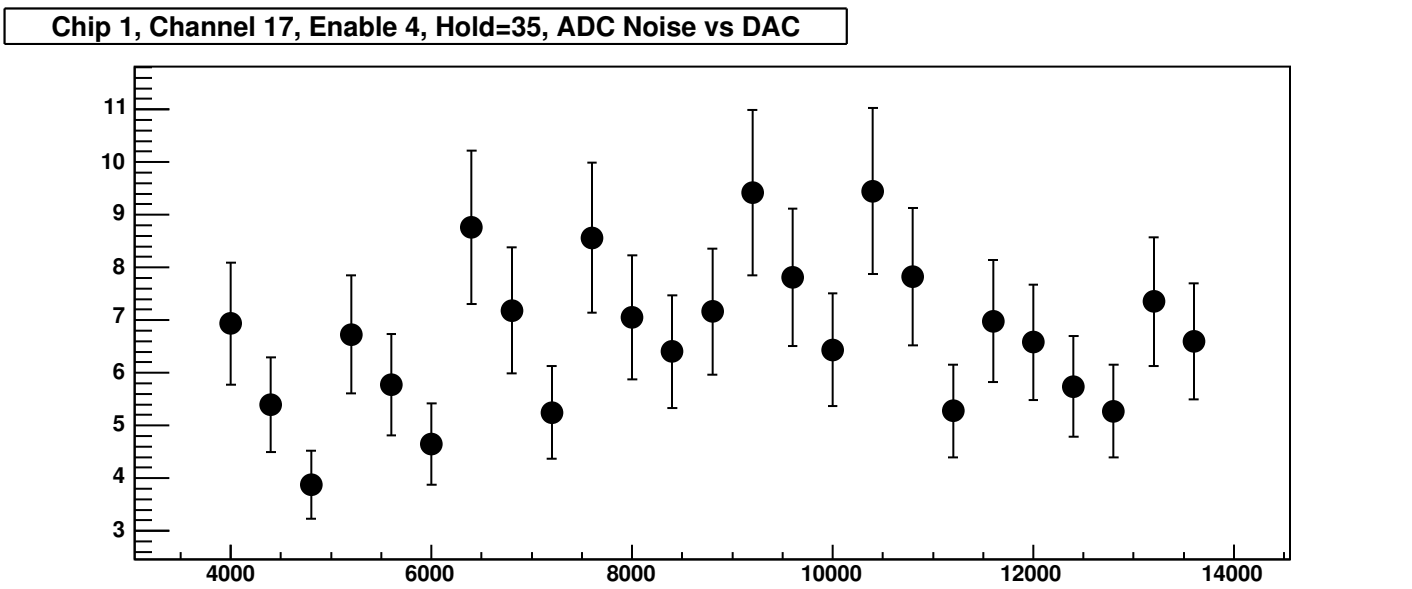

Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
4000
6000
8000
10000
12000
14000

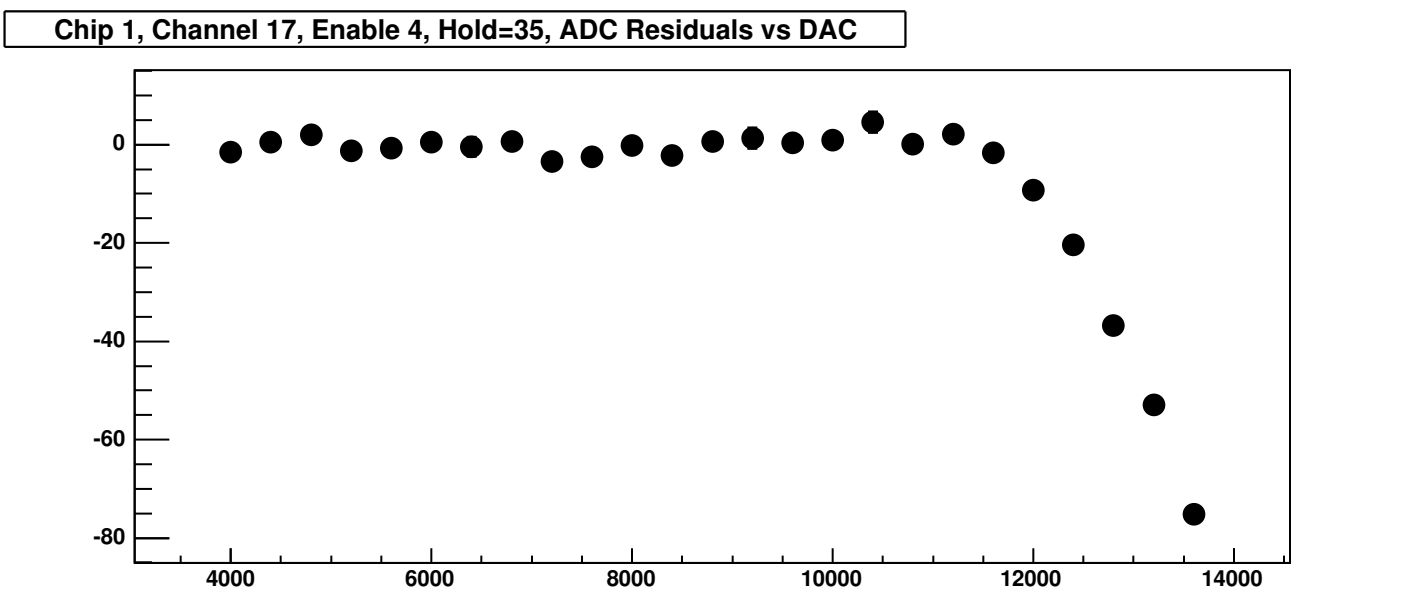

Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 11.24 / 16
p0 7.38 ± 1.347
p1 0.01585 ± 0.000172
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

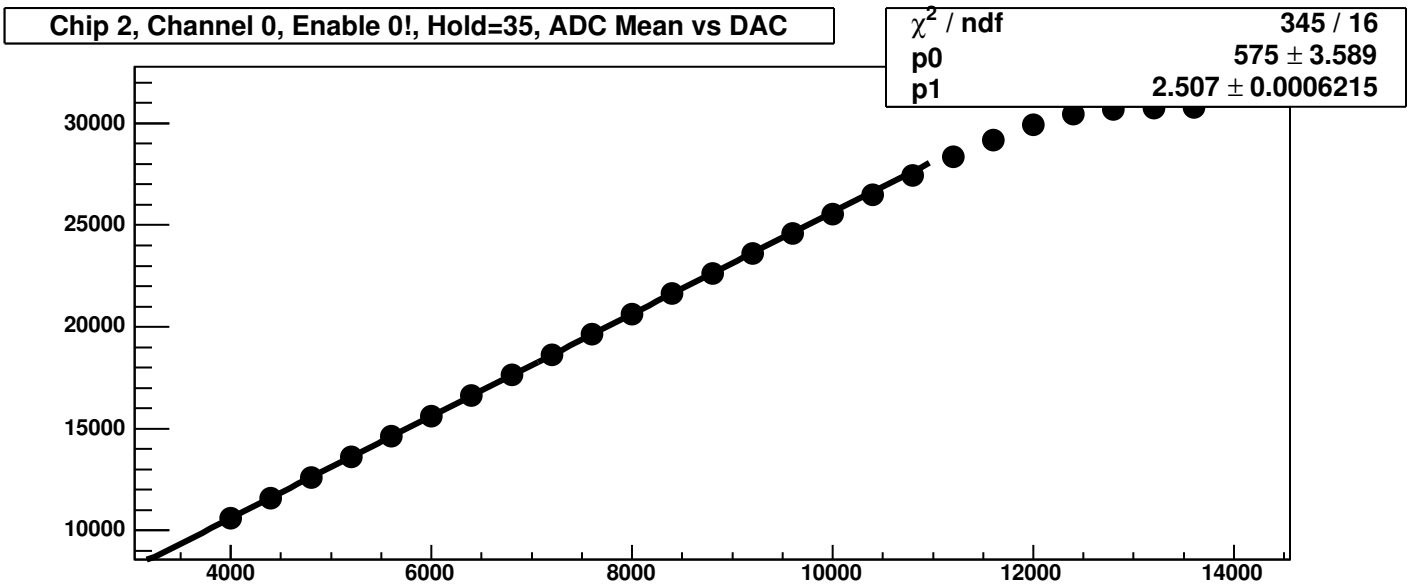

Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

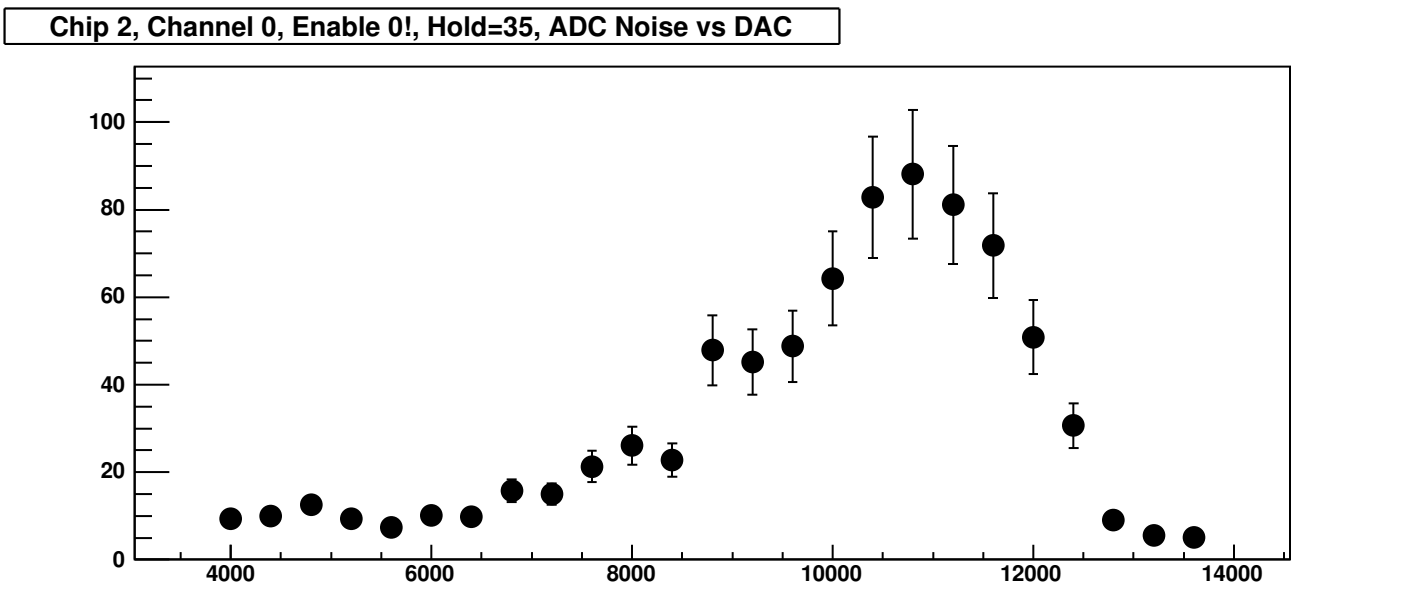

Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
4000
6000
8000
10000
12000
14000

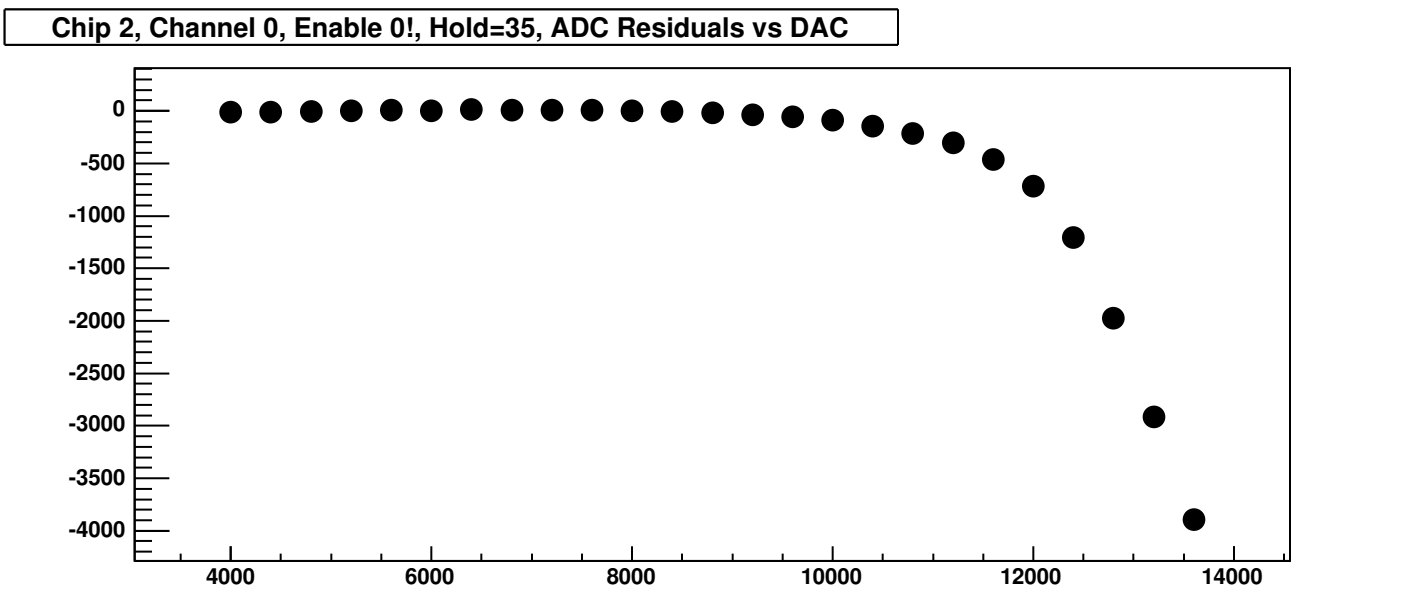

Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.2 / 16
p0 5.012 ± 1.214
p1 0.01641 ± 0.0001715
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

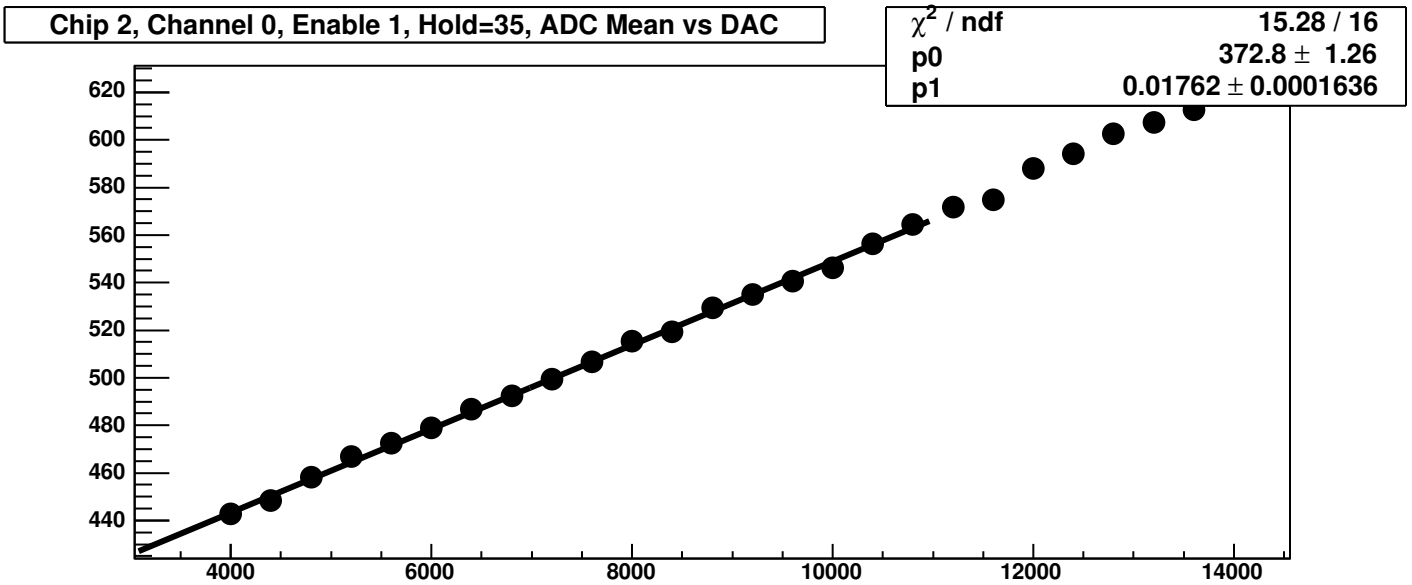

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

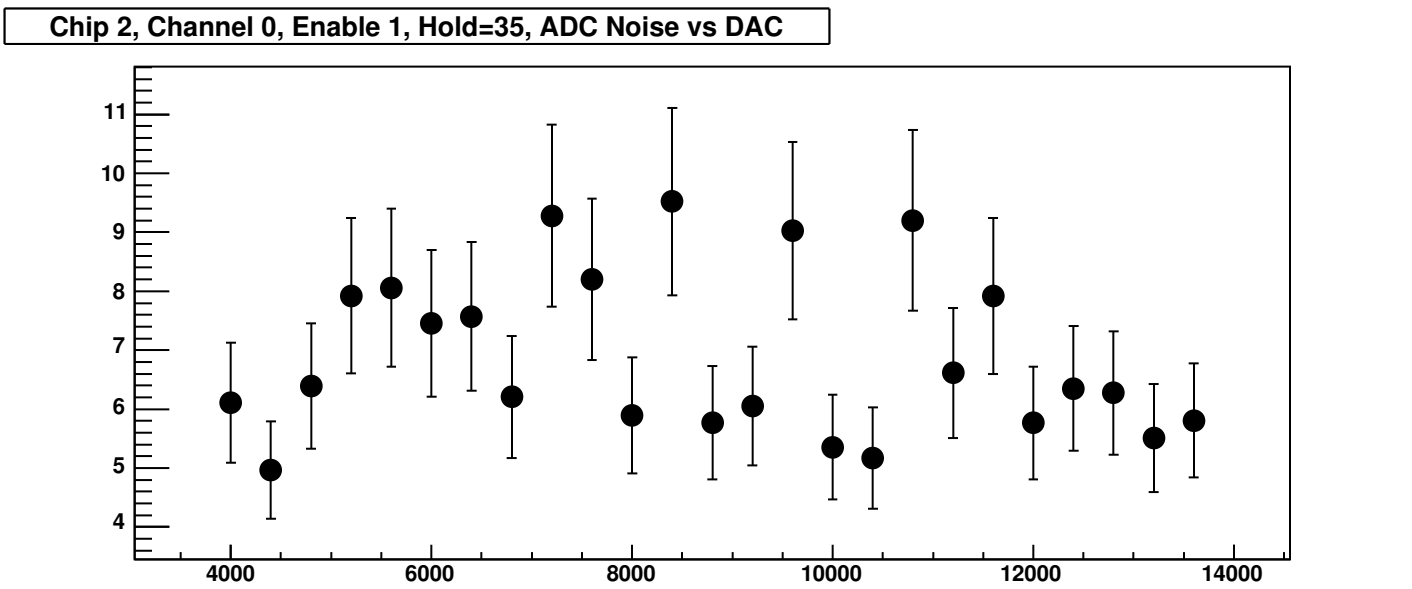

Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
4000
6000
8000
10000
12000
14000

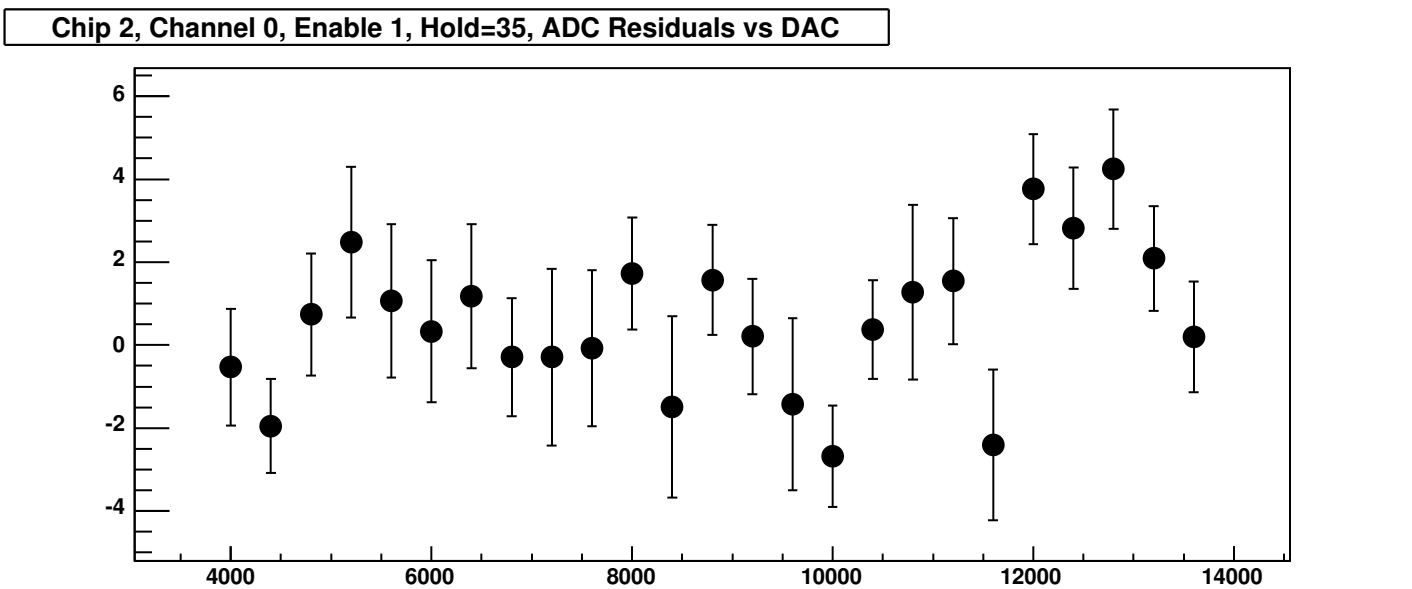

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 345 / 16
p0 575 ± 3.589
p1 2.507 ± 0.0006215
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

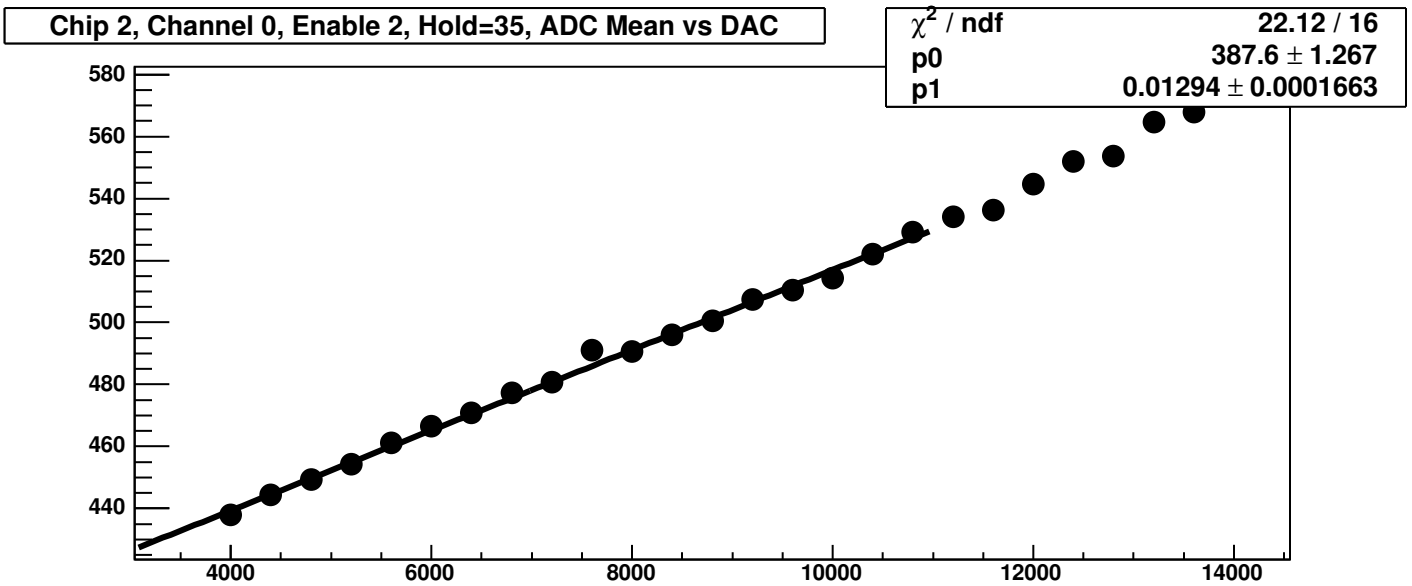

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

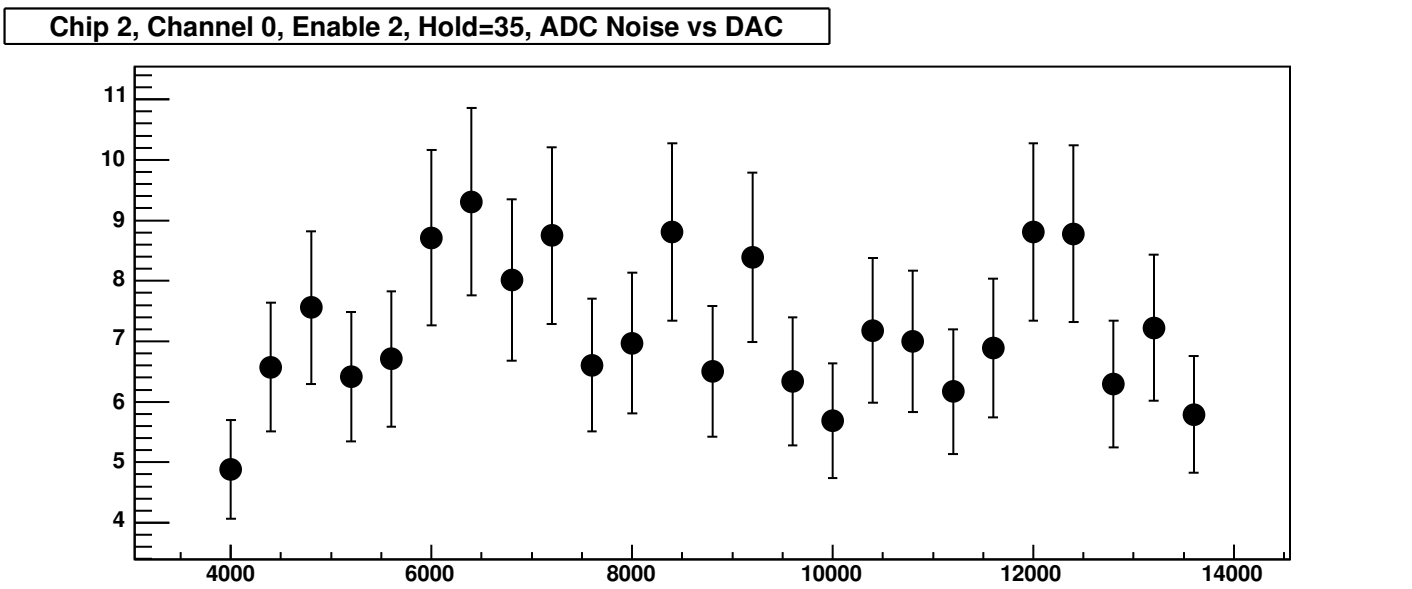

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

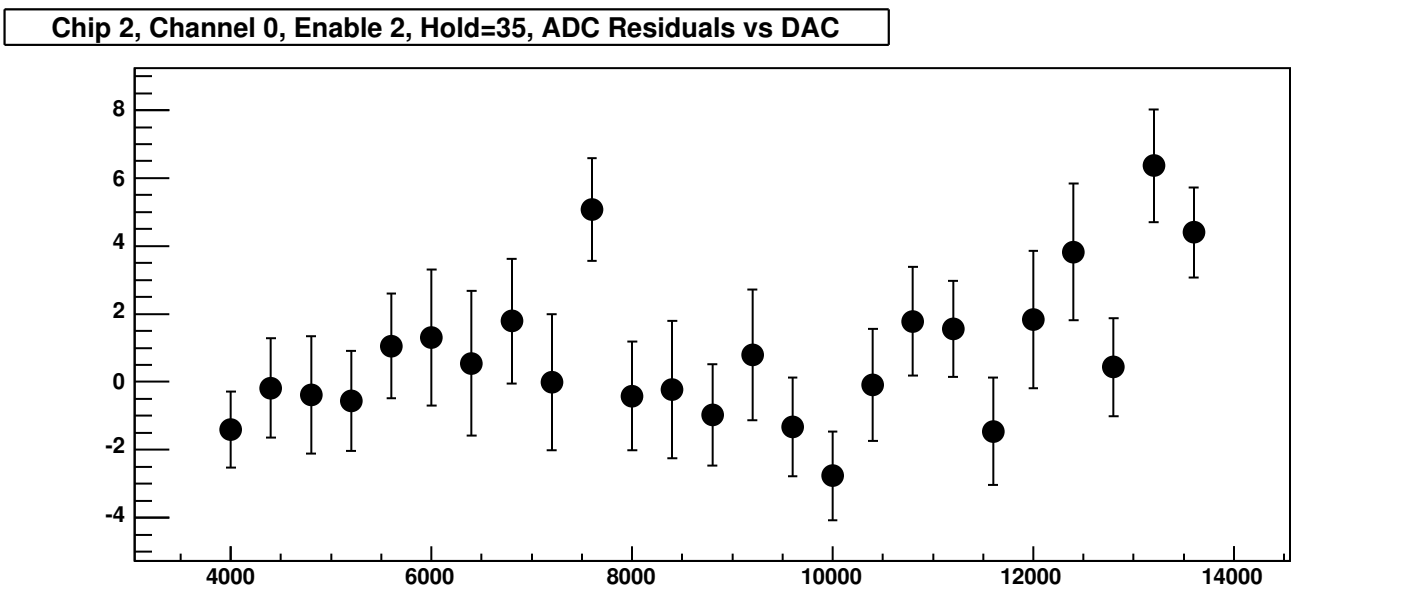

Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 15.28 / 16
p0 372.8 ± 1.26
p1 0.01762 ± 0.0001636
620
600
580
560
540
520
500
480
460
440
4000
6000
8000
10000
12000
14000

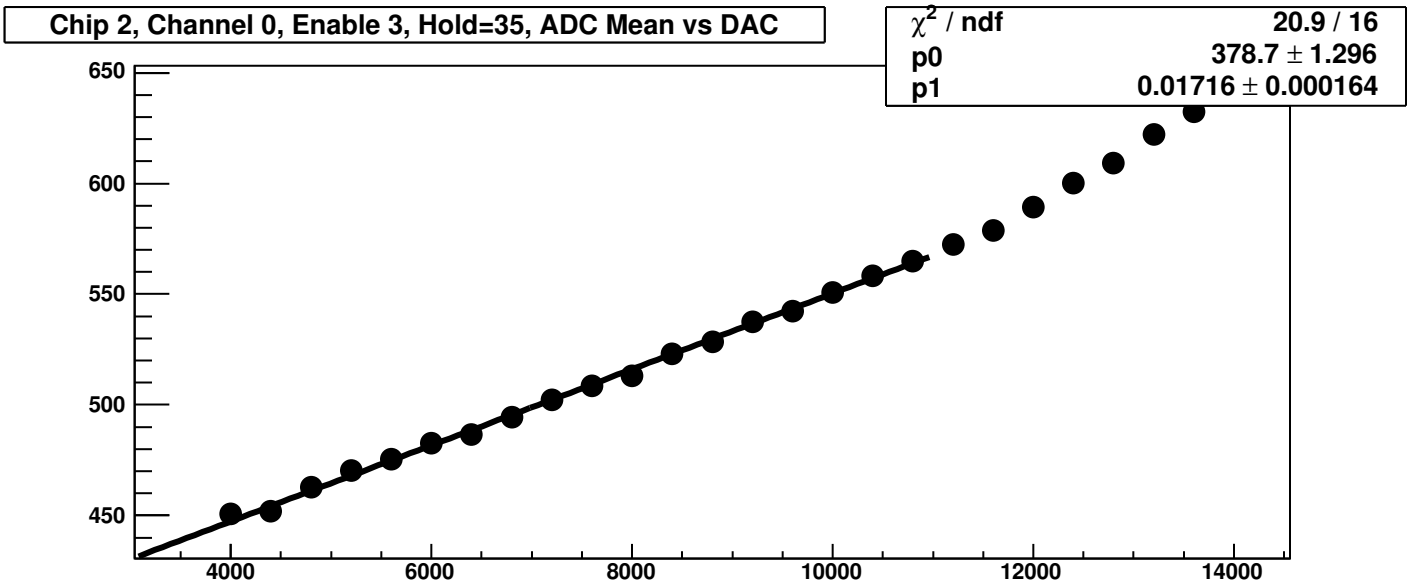

Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

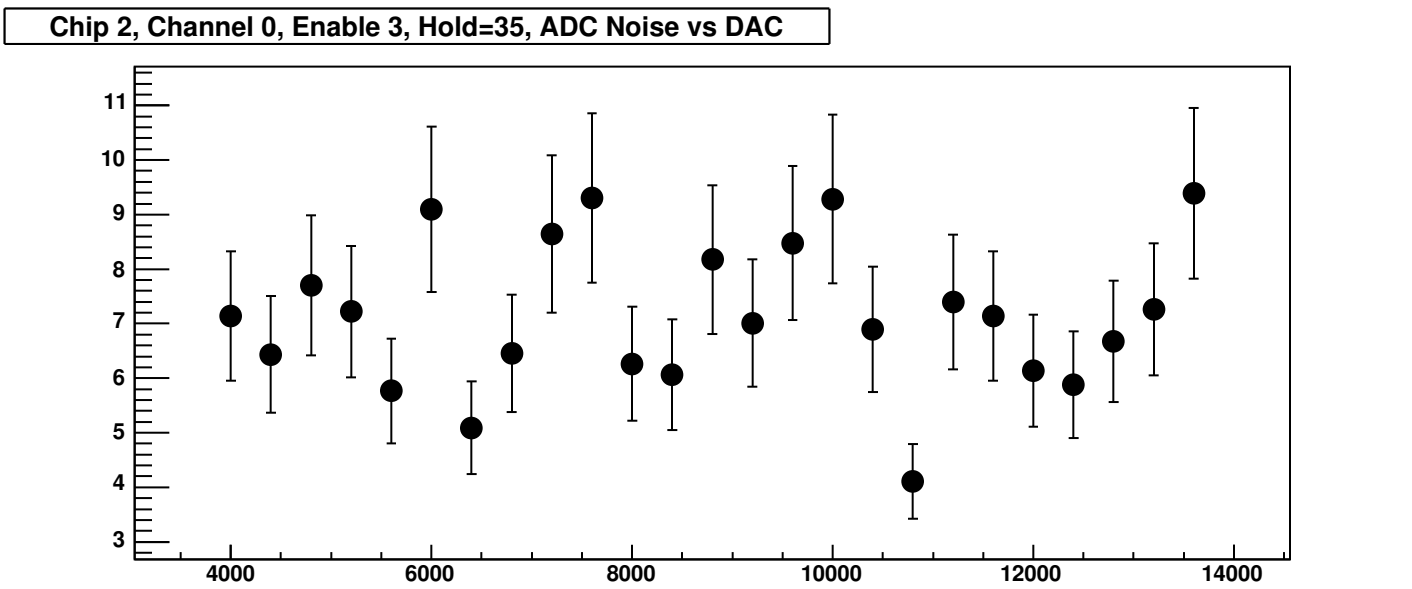

Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

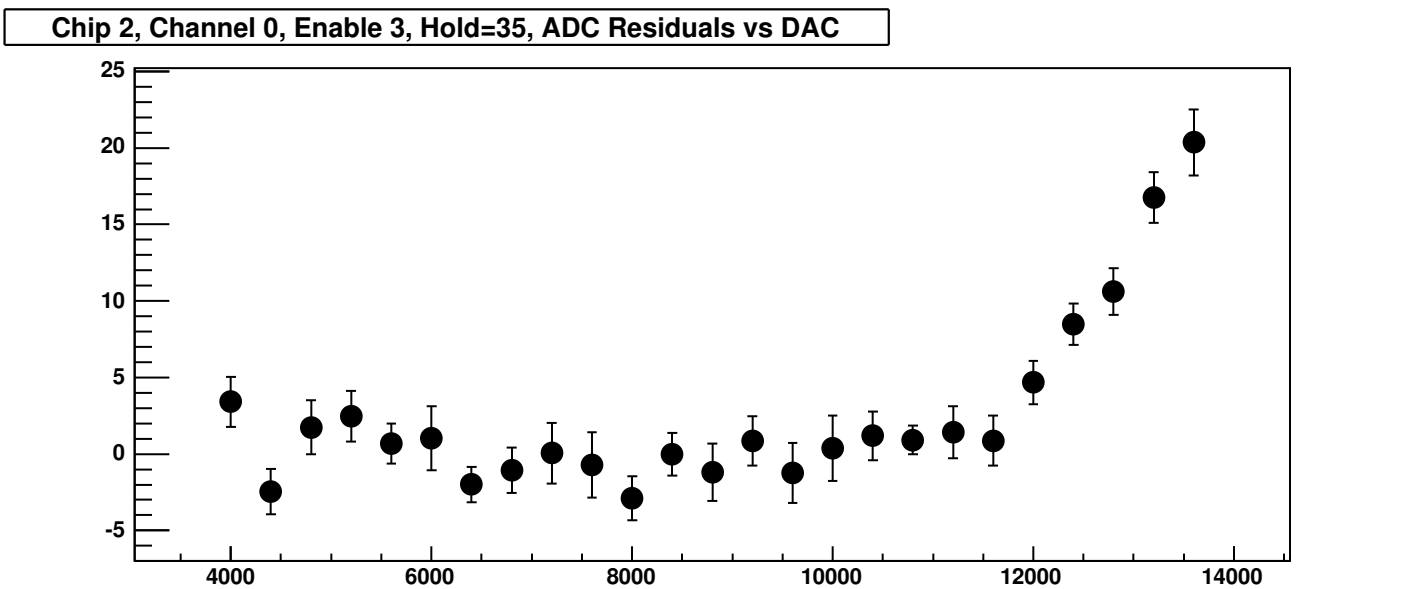

Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.12 / 16
p0 387.6 ± 1.267
p1 0.01294 ± 0.0001663
580
560
540
520
500
480
460
440
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

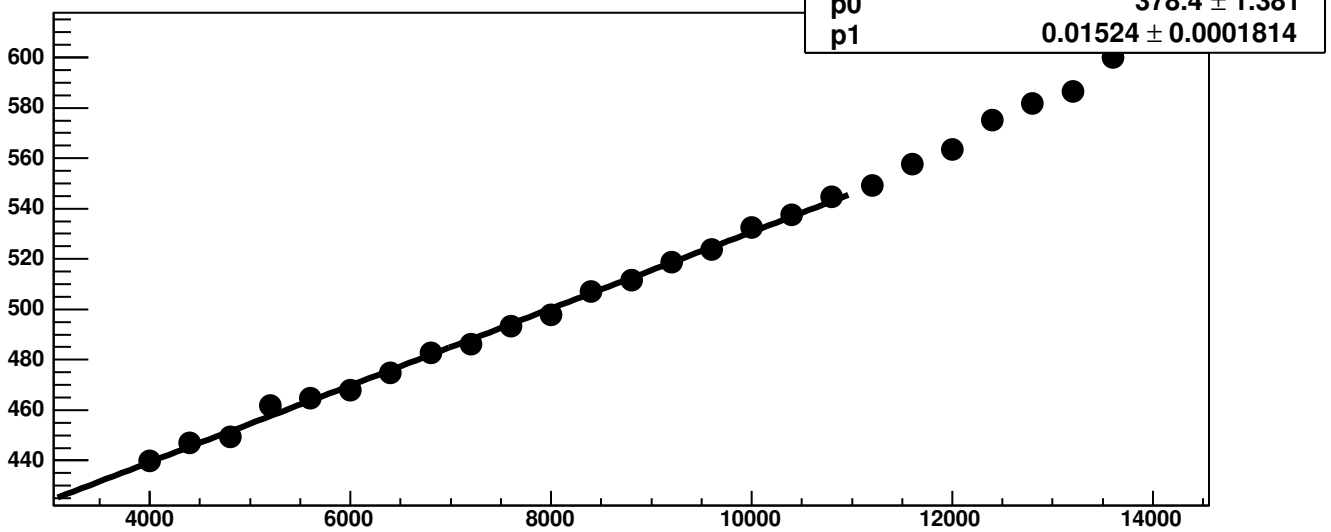

Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.9 / 16
p0 378.7 ± 1.296
p1 0.01716 ± 0.000164
650
600
550
500
450
4000
6000
8000
10000
12000
14000

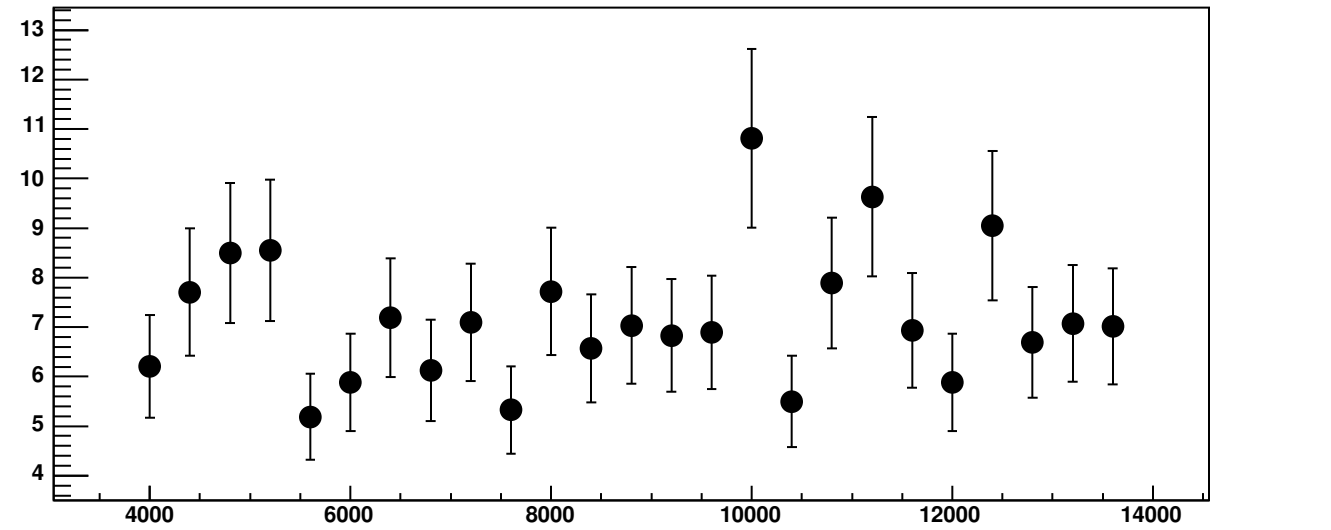

Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

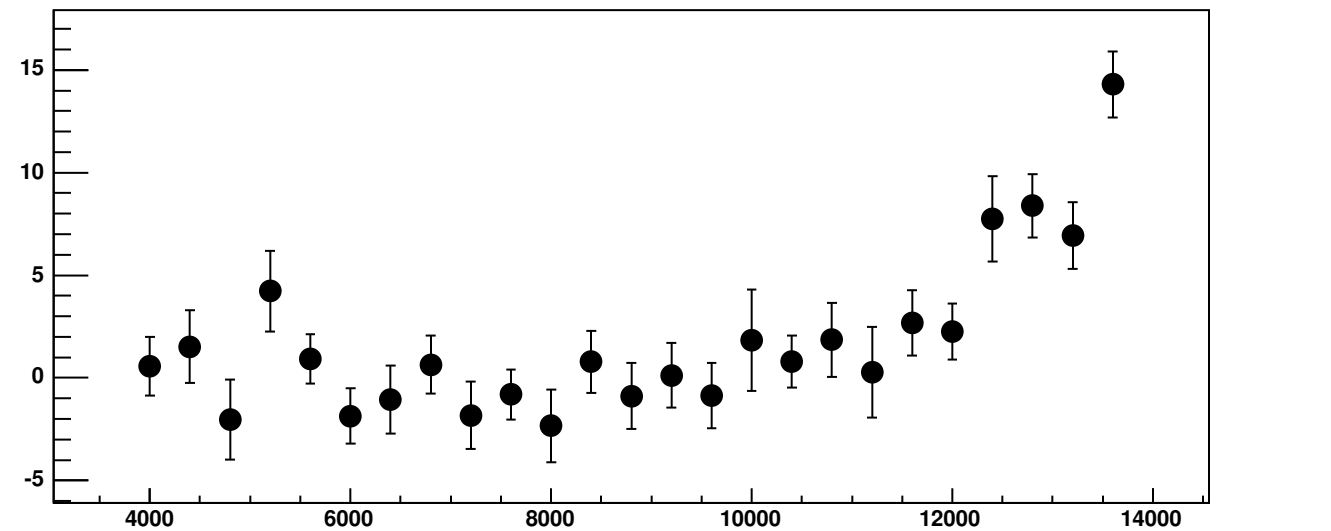
Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

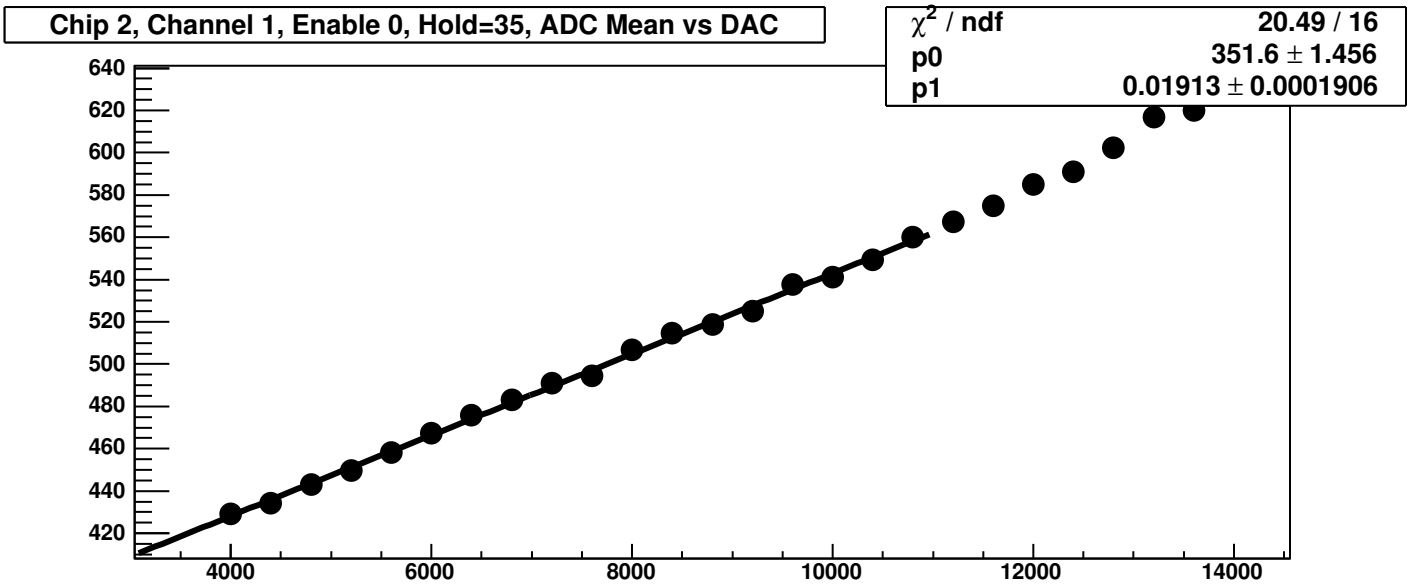


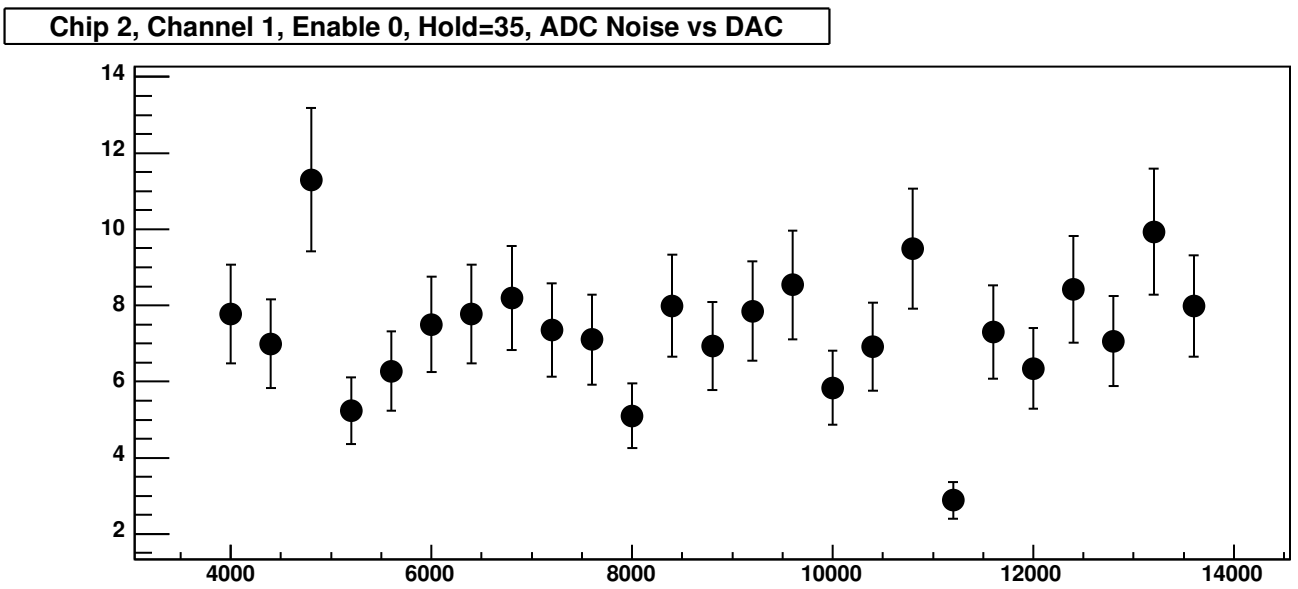
Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

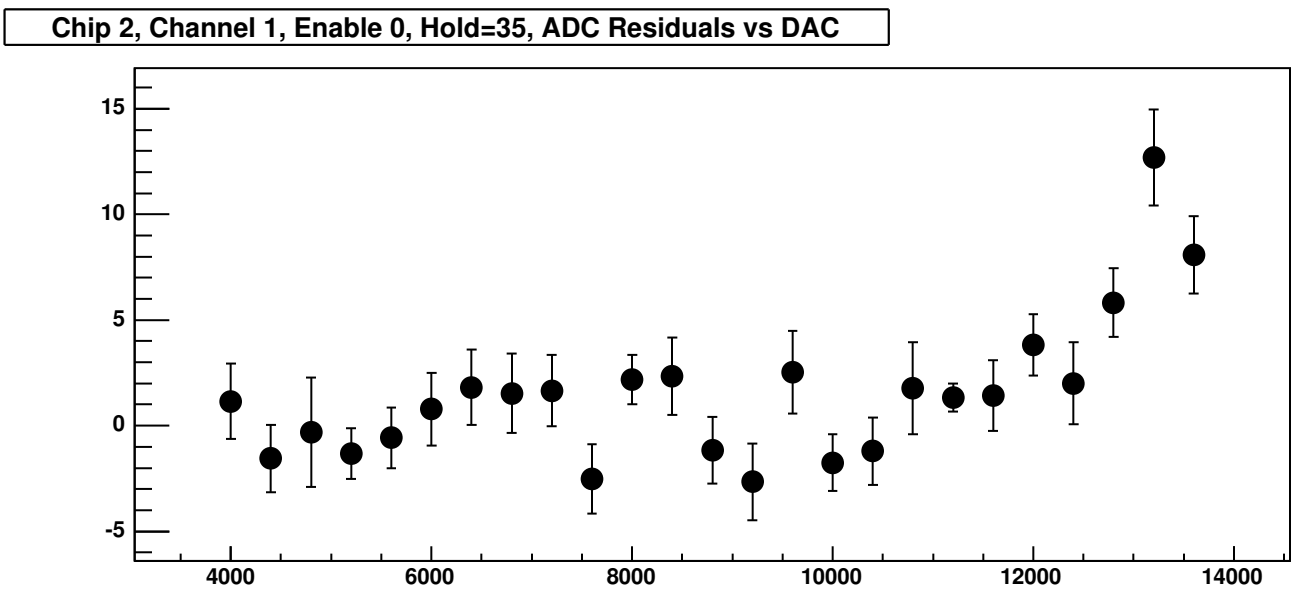


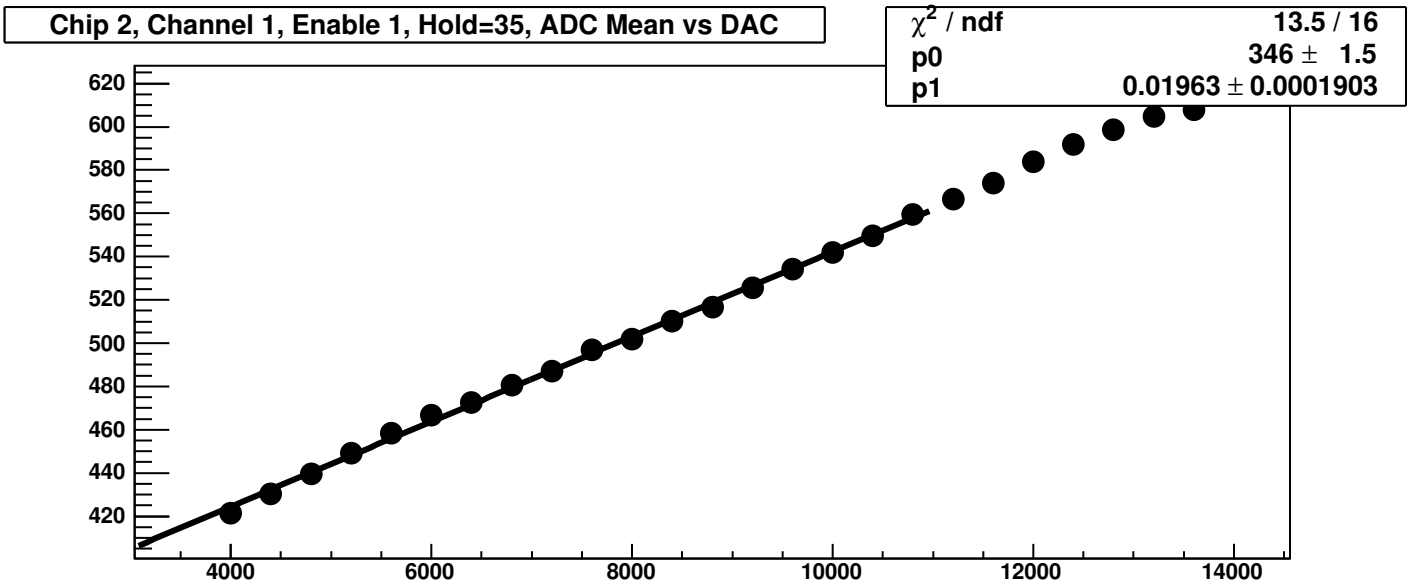
Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

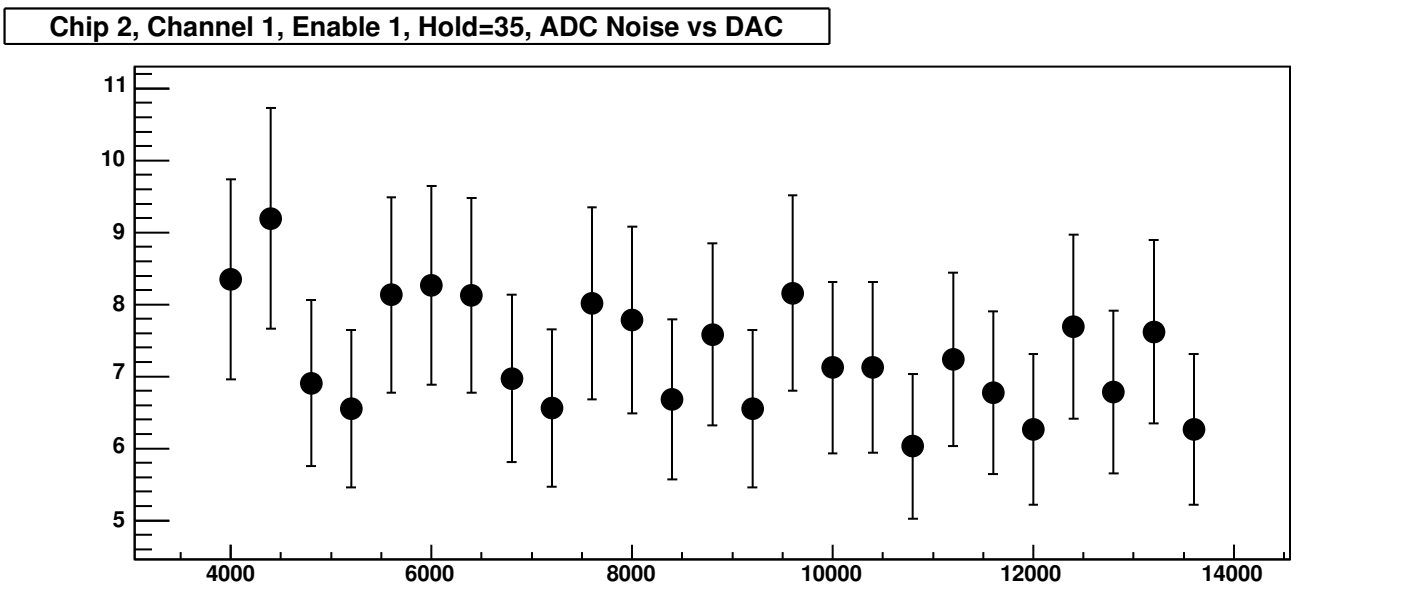


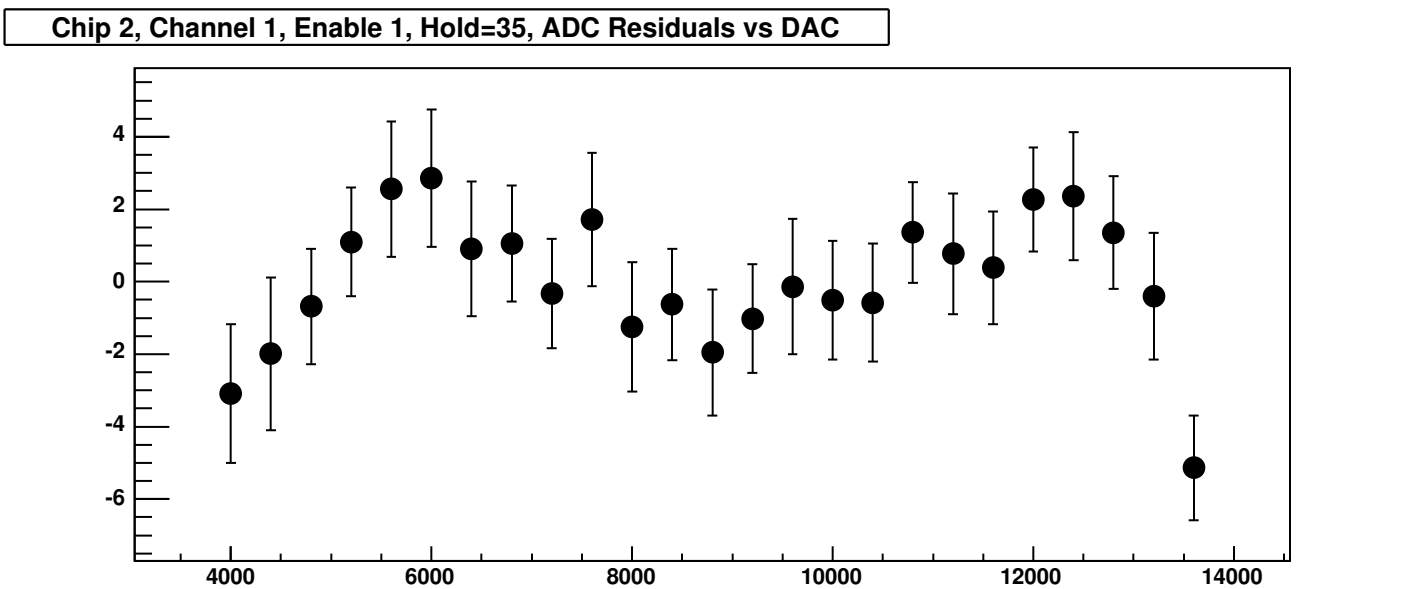

Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20.49 / 16
p0 351.6 ± 1.456
p1 0.01913 ± 0.0001906
640
620
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

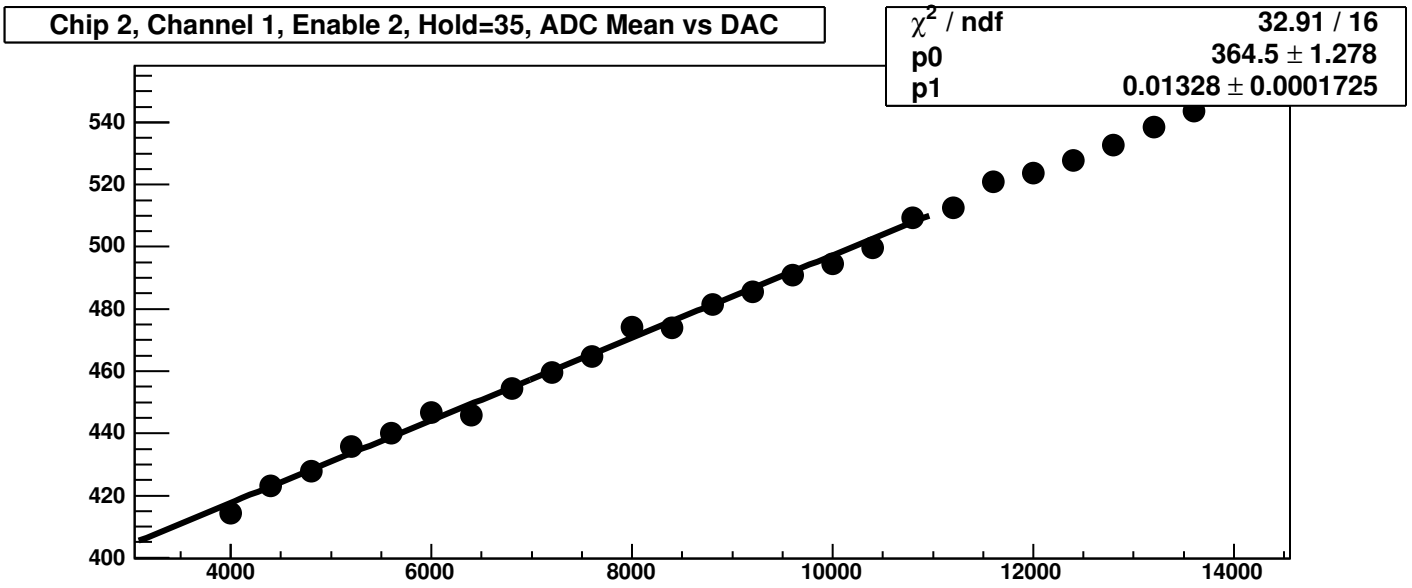

Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
4000
6000
8000
10000
12000
14000

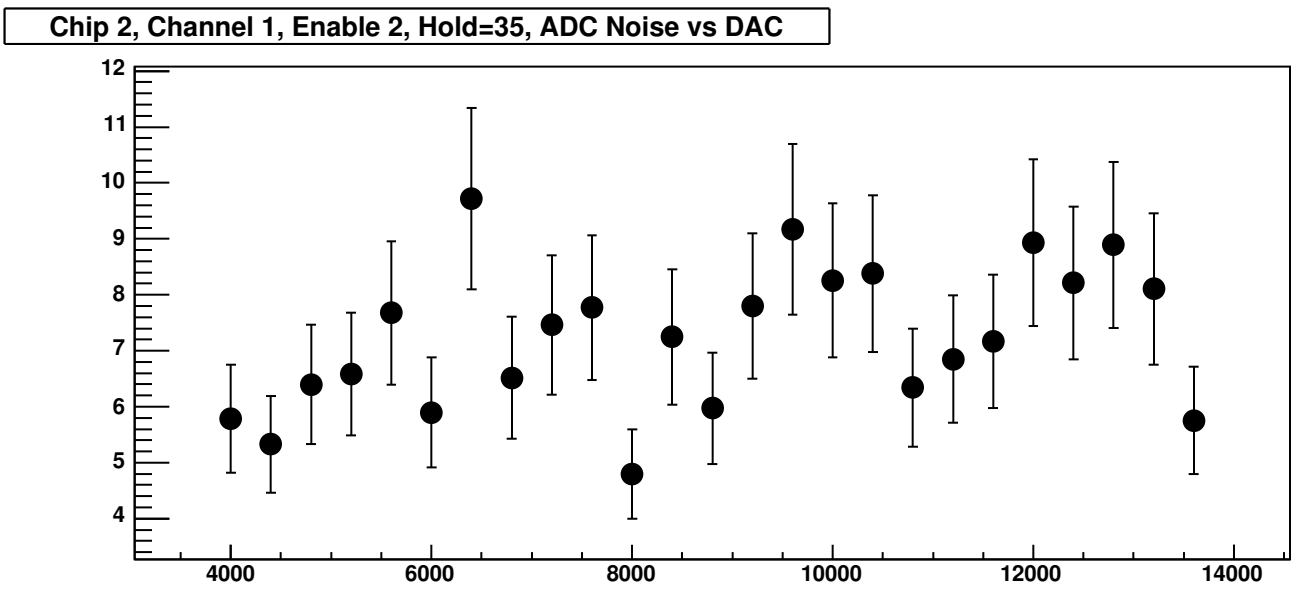

Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

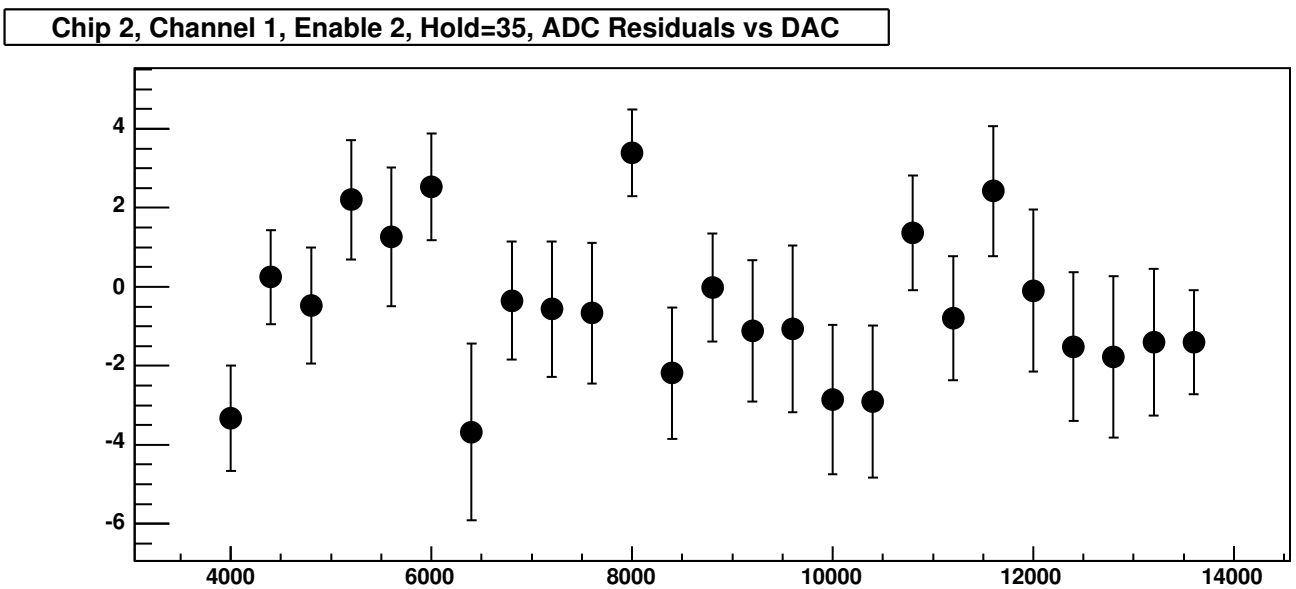

Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 13.5 / 16
p0 346 ± 1.5
p1 0.01963 ± 0.0001903
620
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

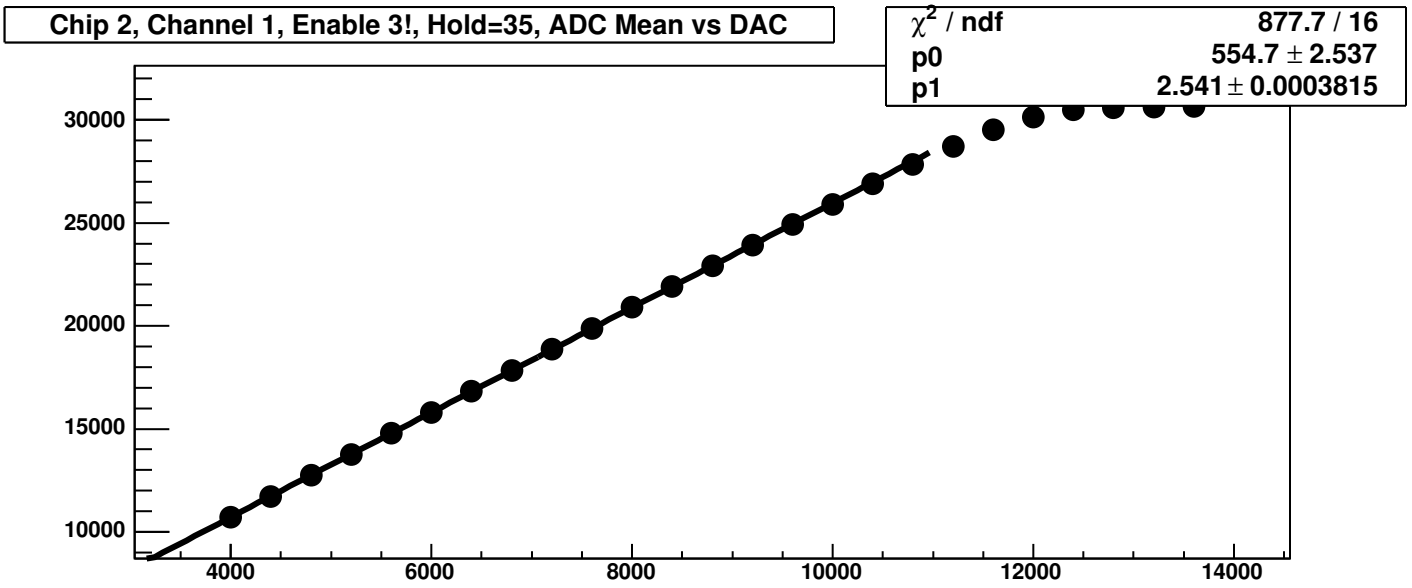

Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

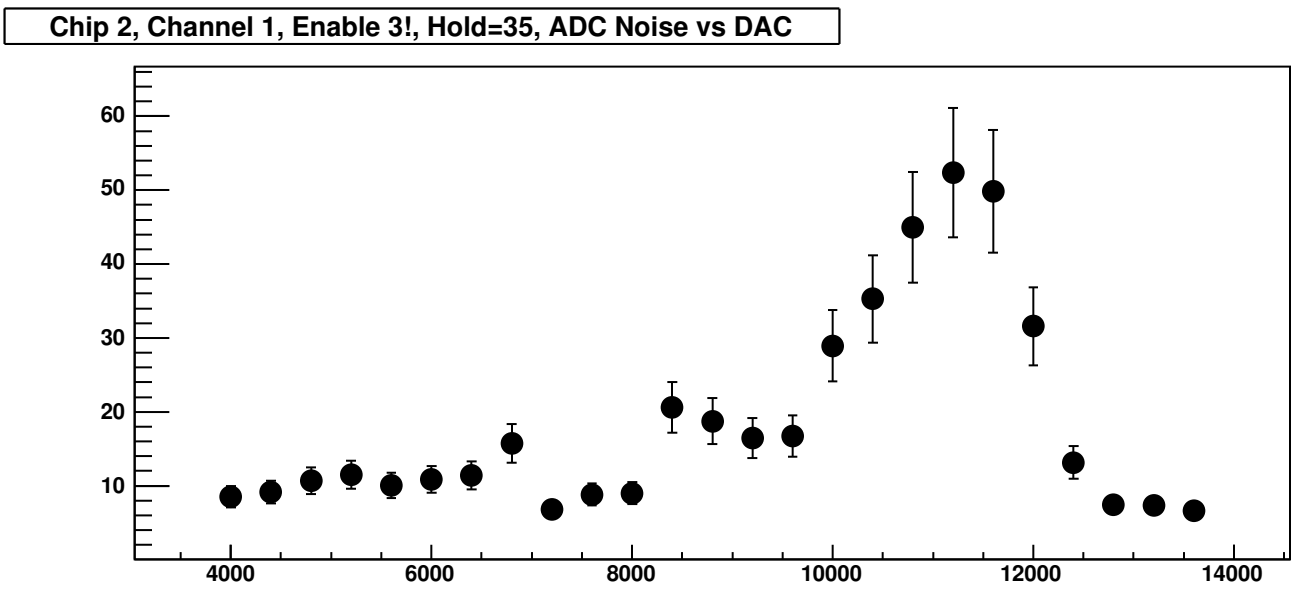

Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

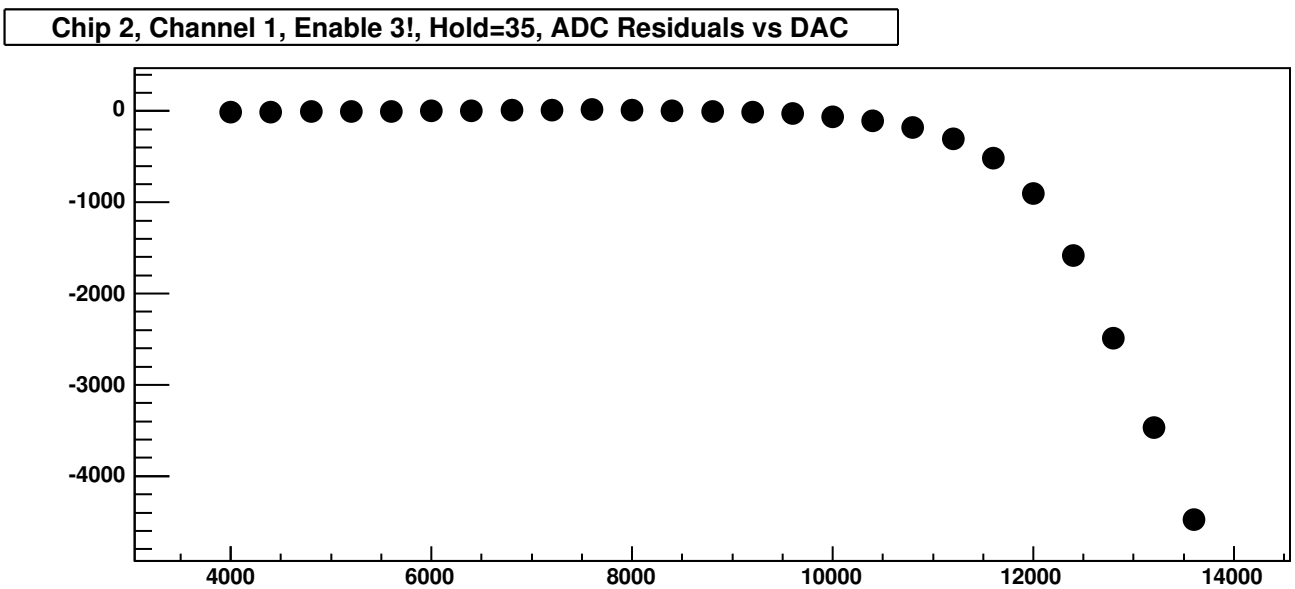

Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.91 / 16
p0 364.5 ± 1.278
p1 0.01328 ± 0.0001725
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

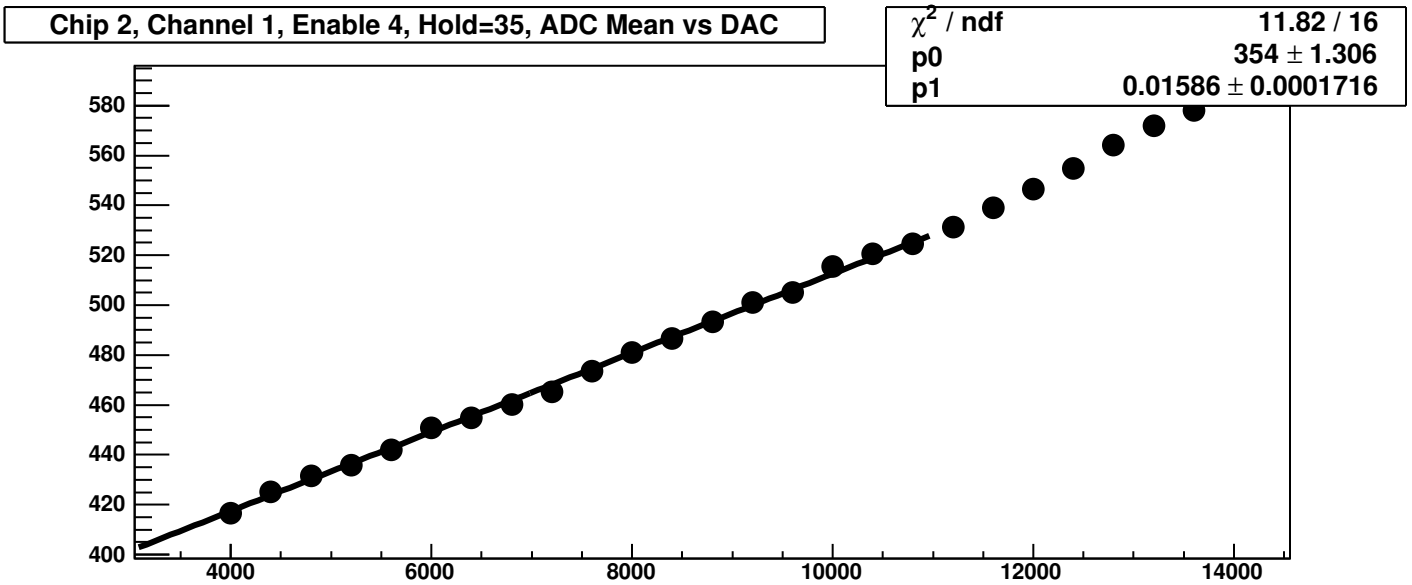

Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

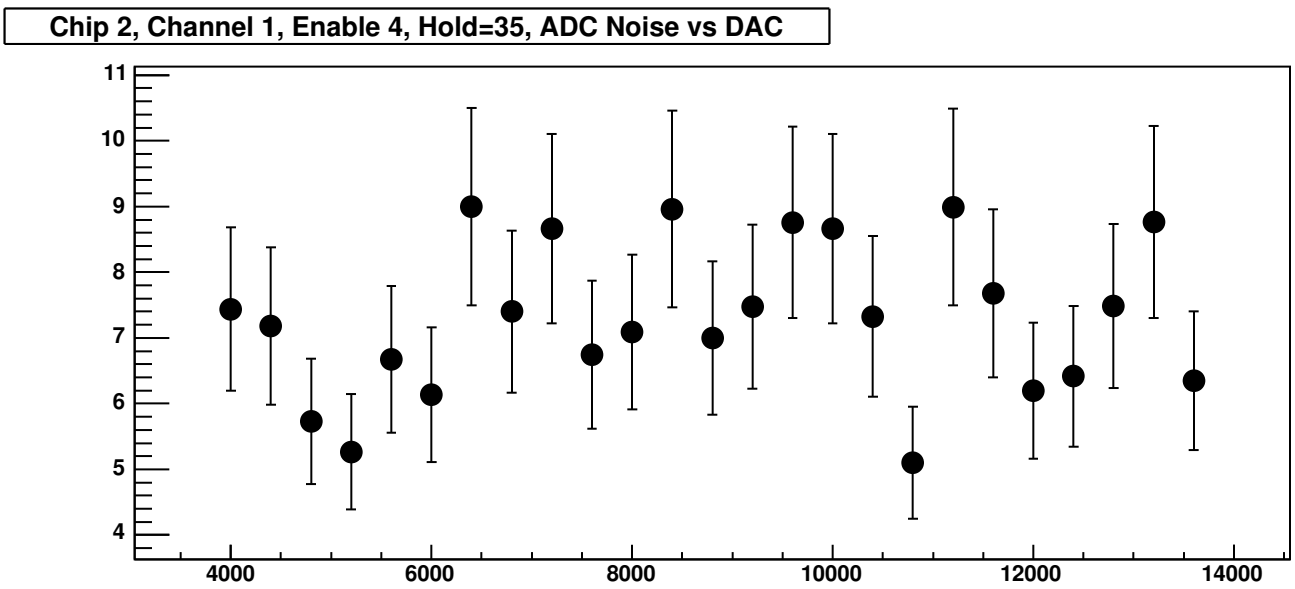

Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

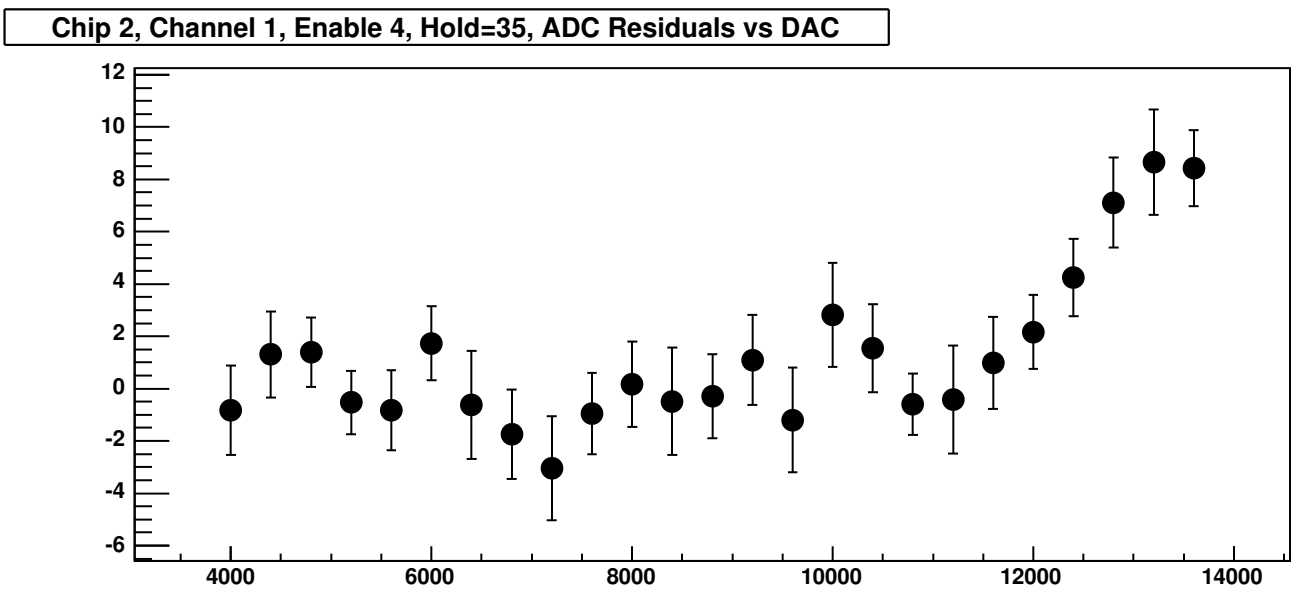

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 877.7 / 16
p0 554.7 ± 2.537
p1 2.541 ± 0.0003815
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

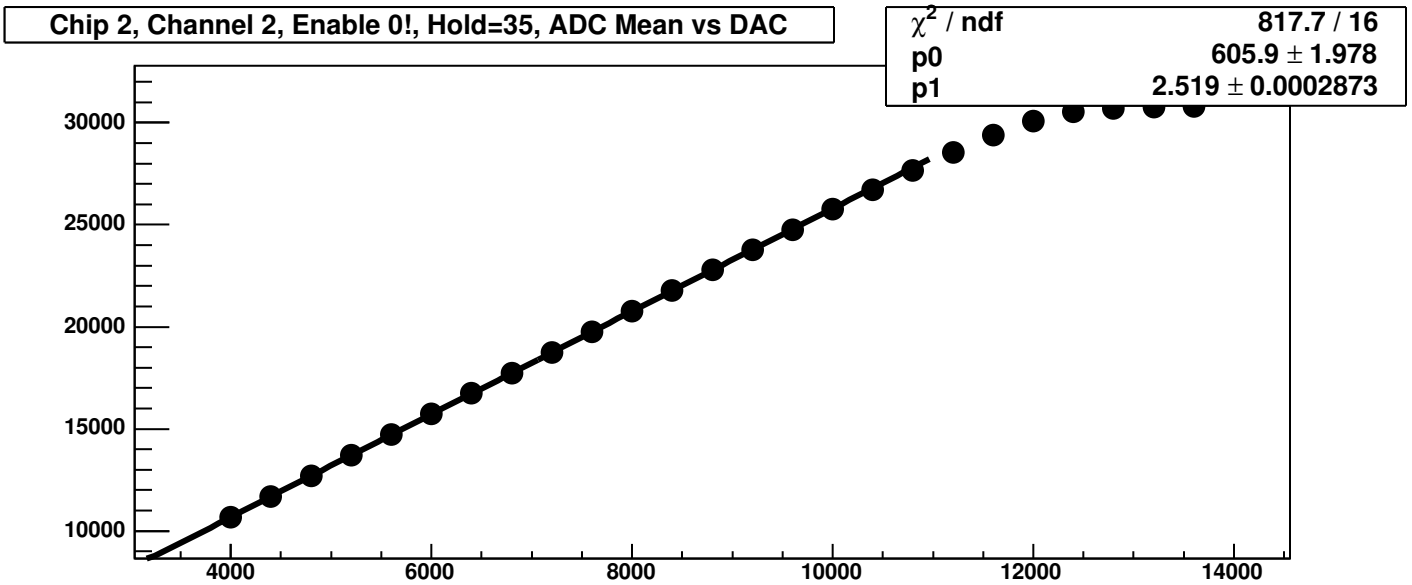

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
4000
6000
8000
10000
12000
14000

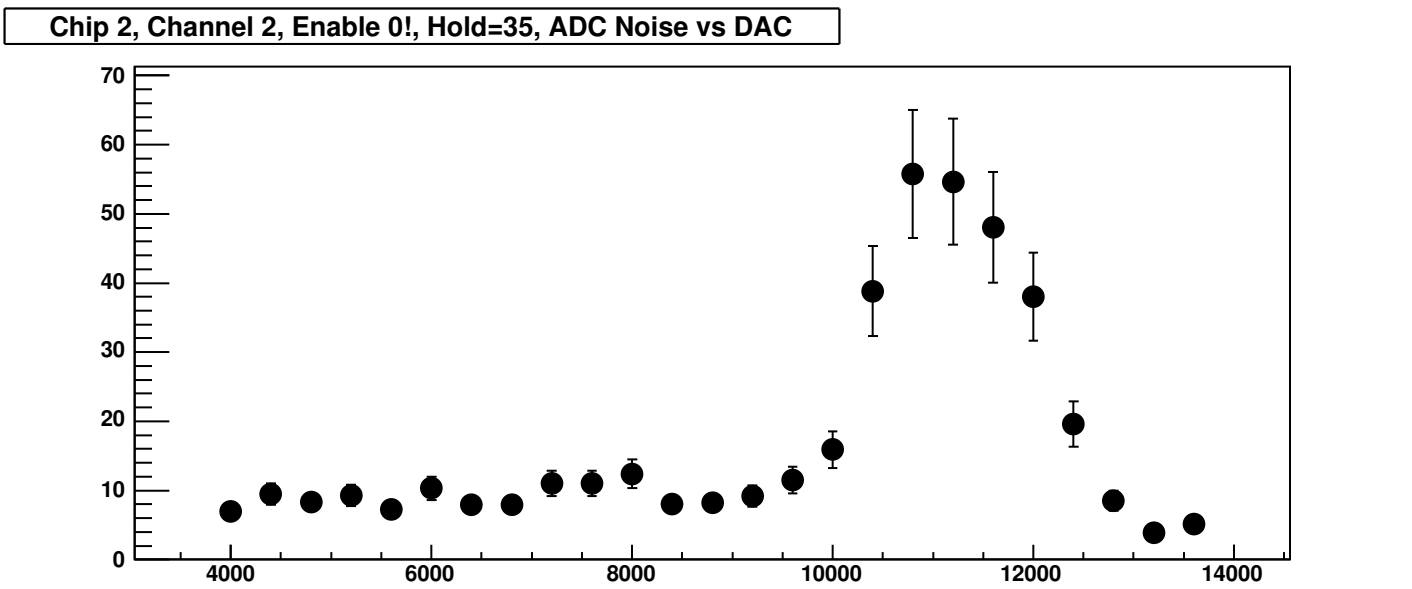

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

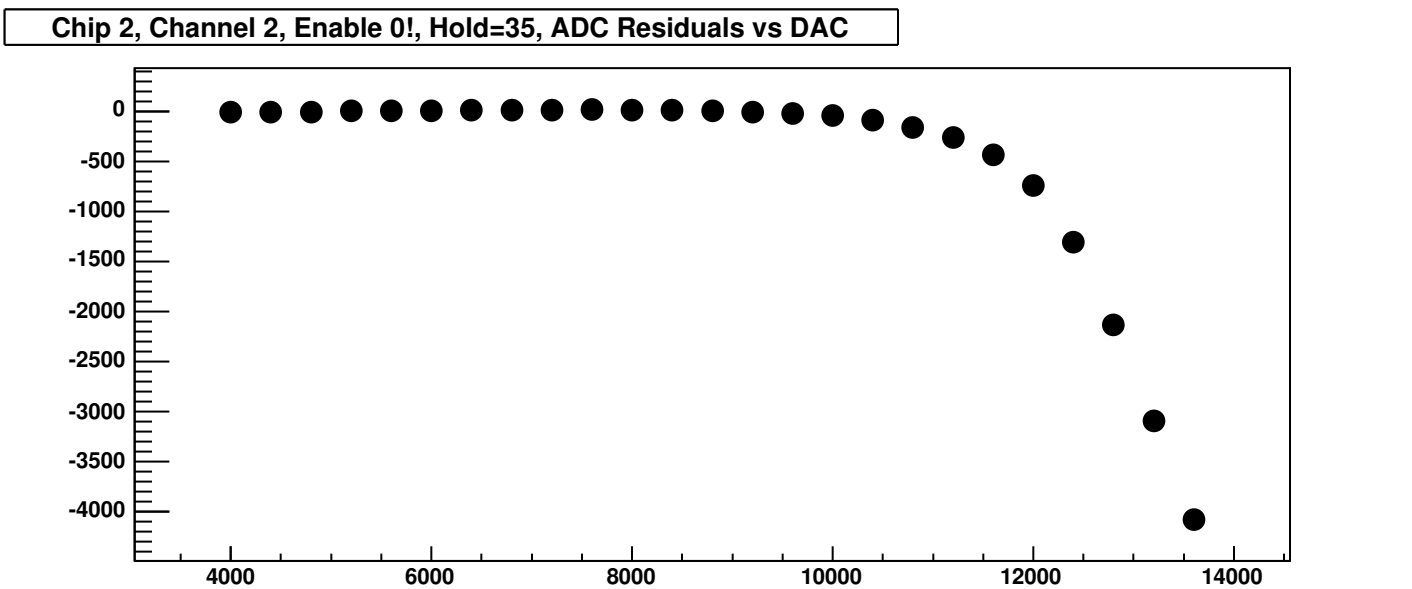

Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.82 / 16
p0 354 ± 1.306
p1 0.01586 ± 0.0001716
580
560
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

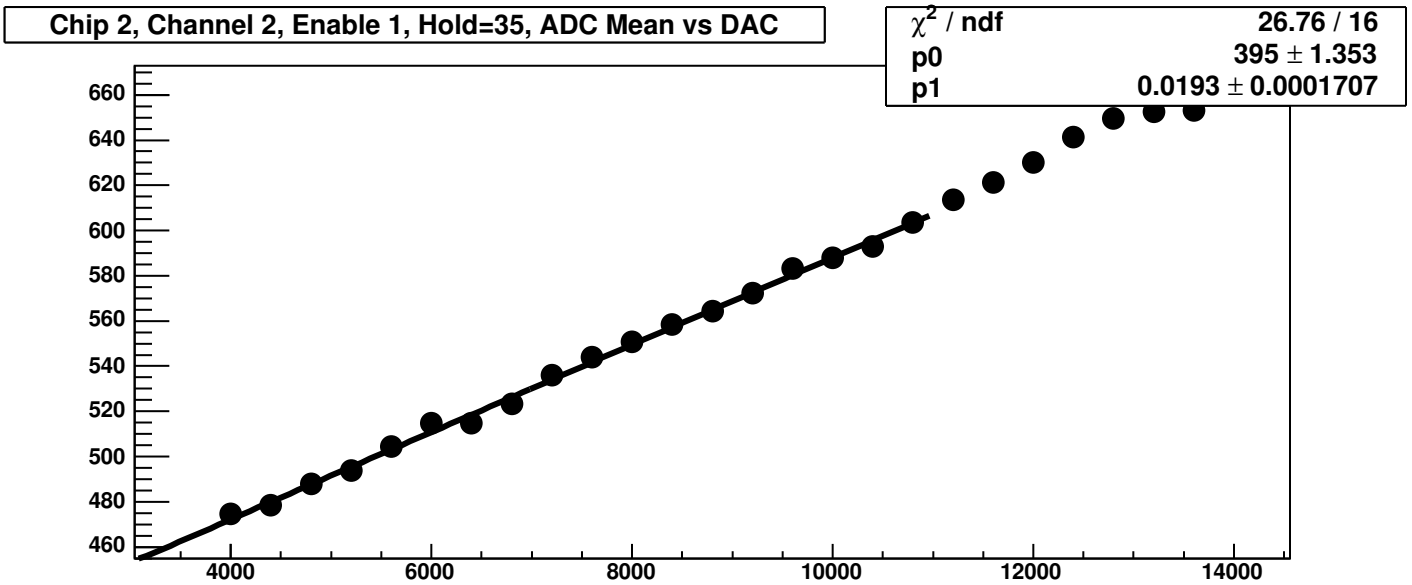

Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

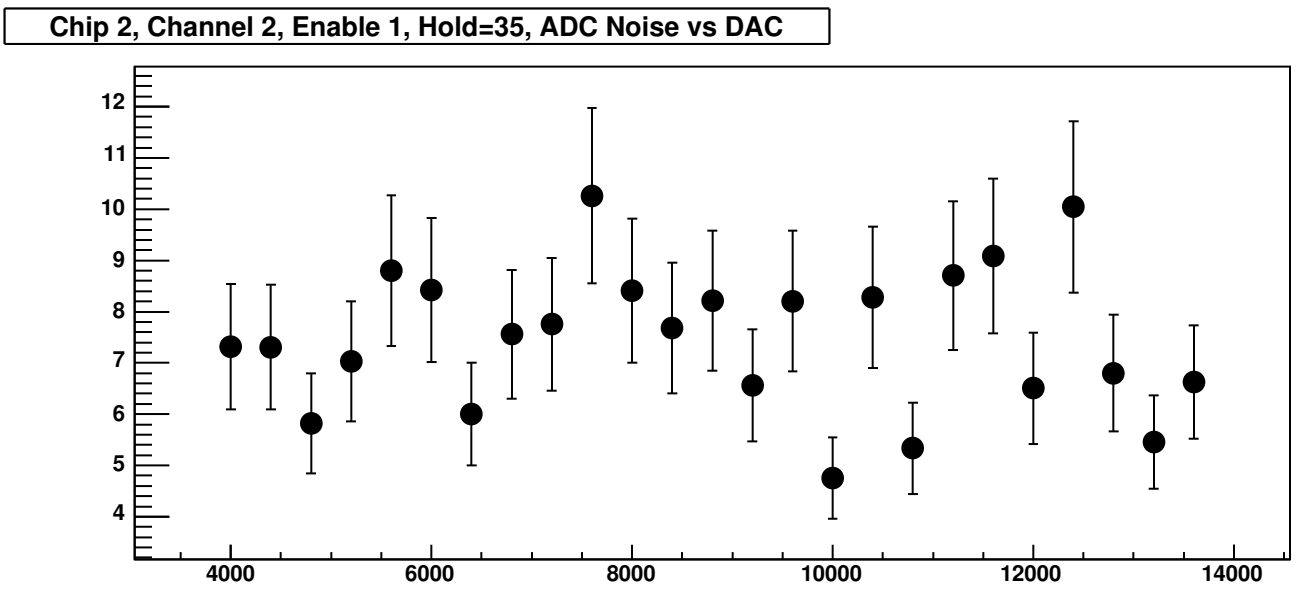

Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

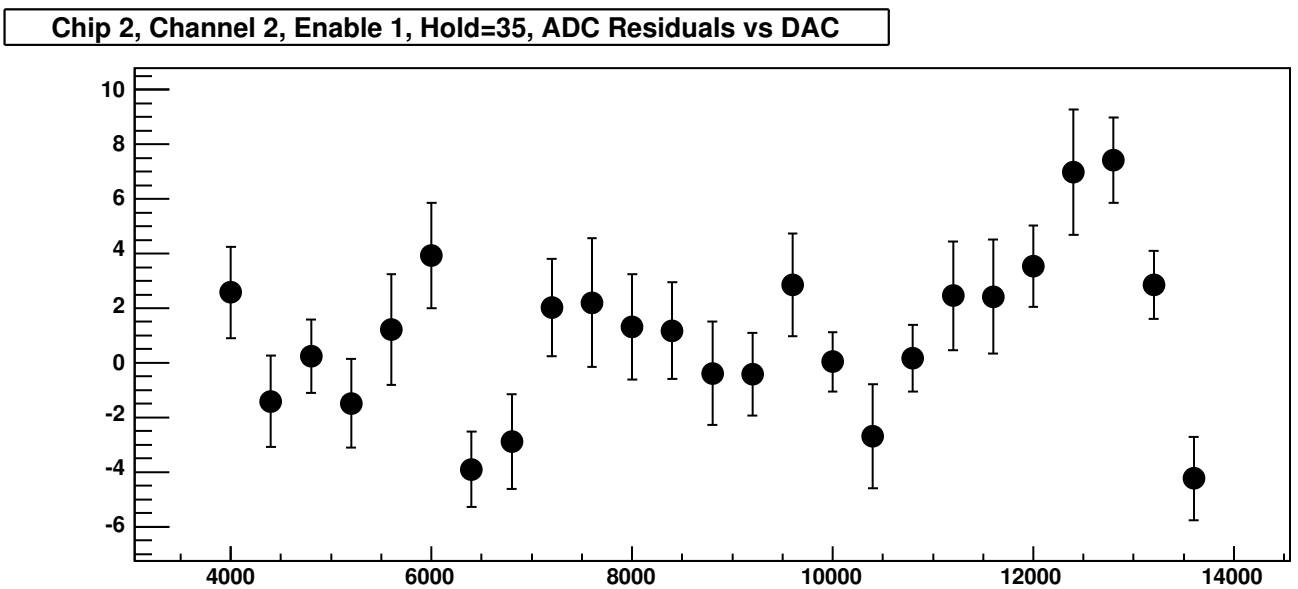

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 817.7 / 16
p0 605.9 ± 1.978
p1 2.519 ± 0.0002873
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

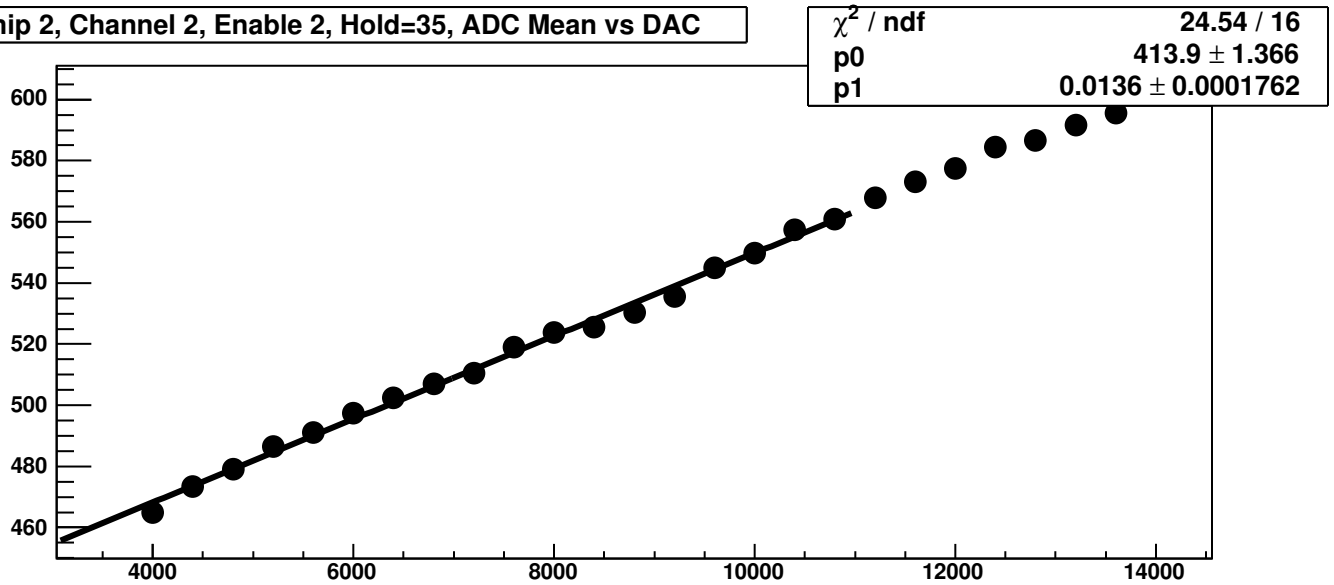

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.76 / 16
p0 395 ± 1.353
p1 0.0193 ± 0.0001707
660
640
620
600
580
560
540
520
500
480
460
4000
6000
8000
10000
12000
14000

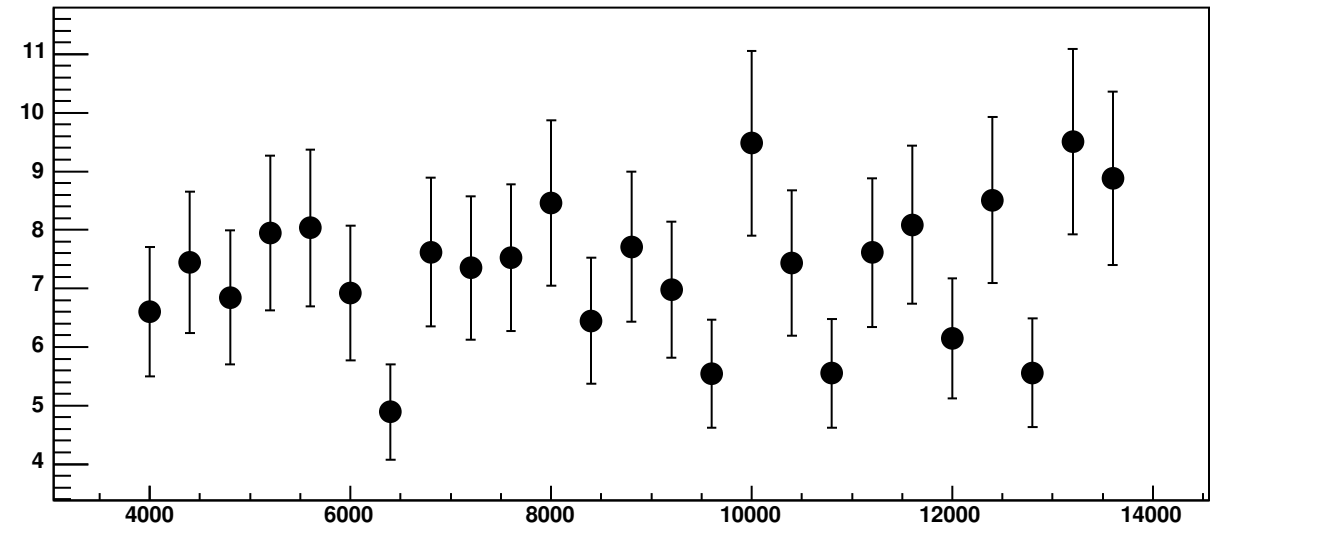

Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

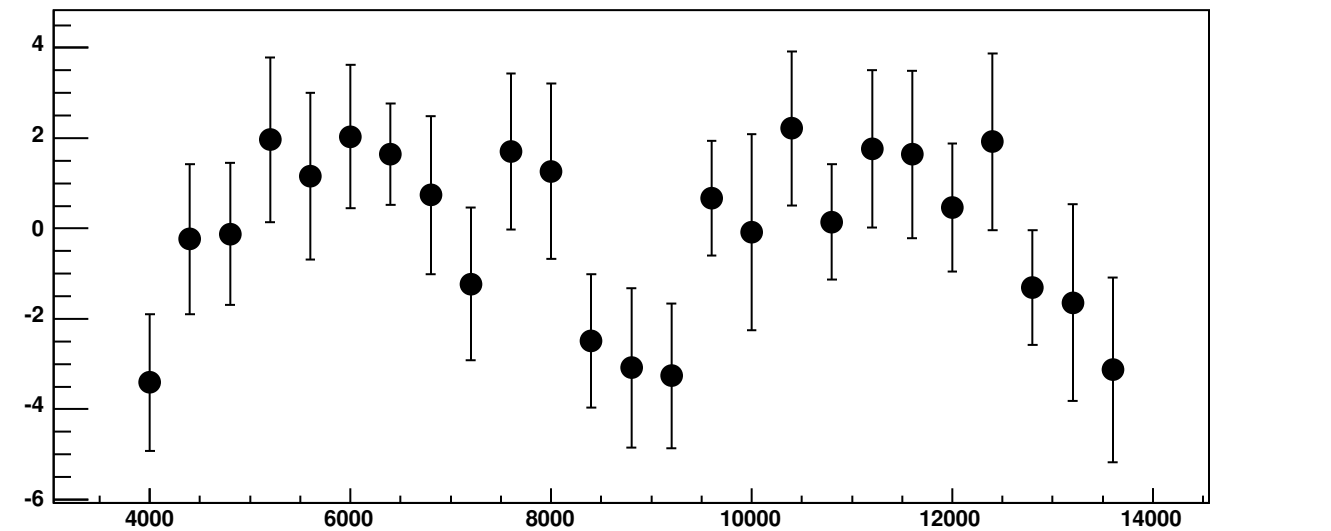
Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



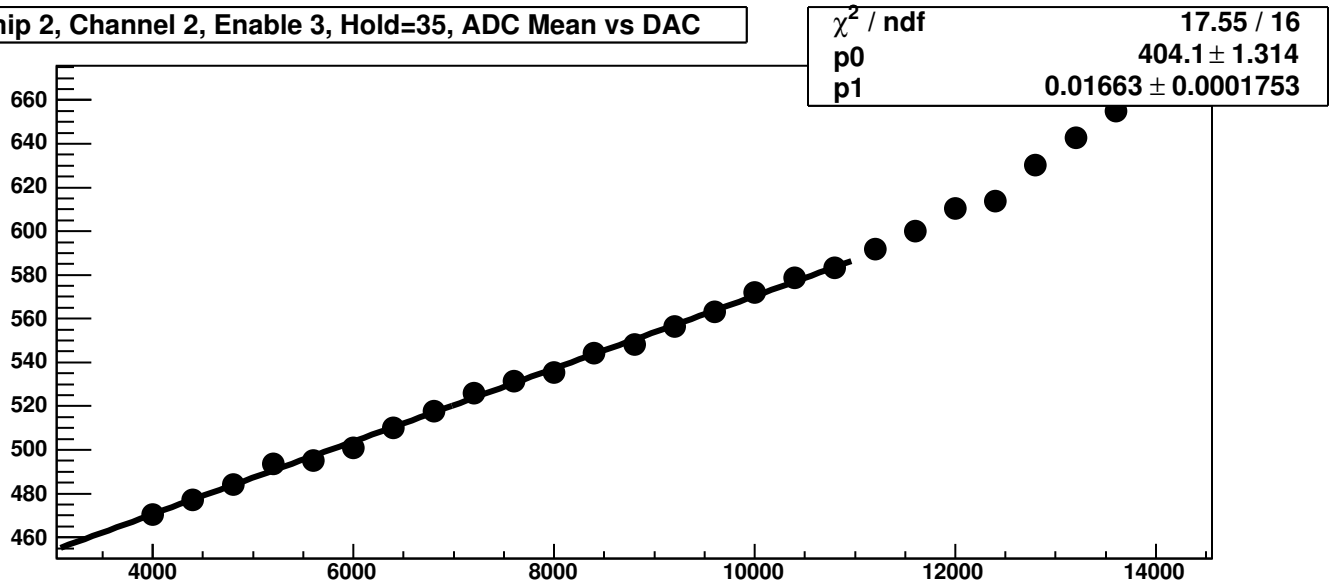
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



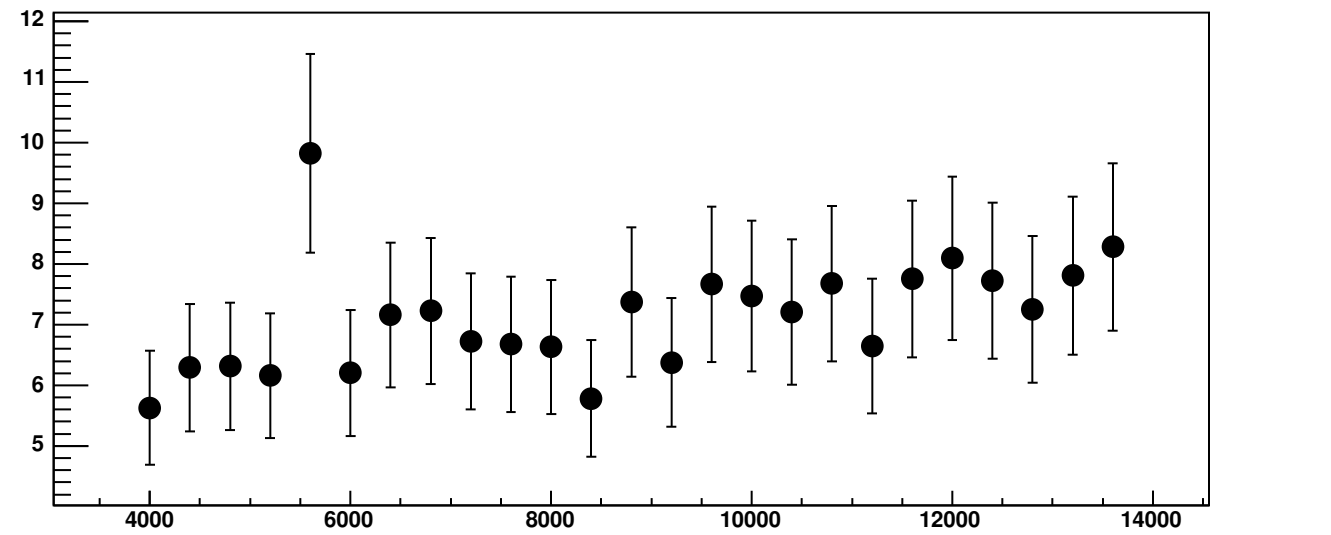
Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



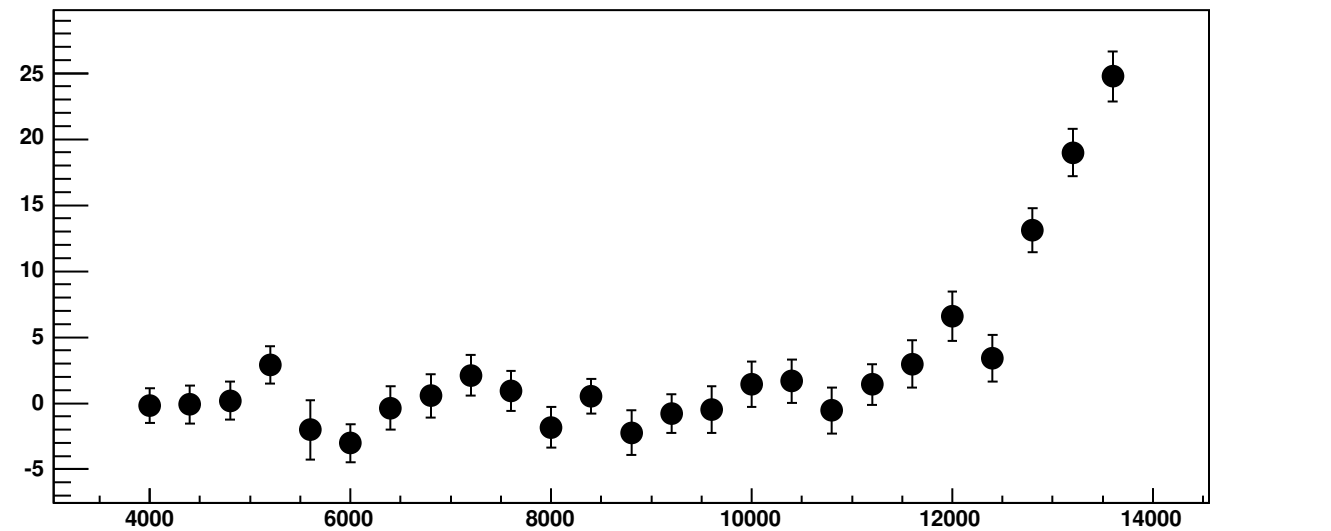
Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

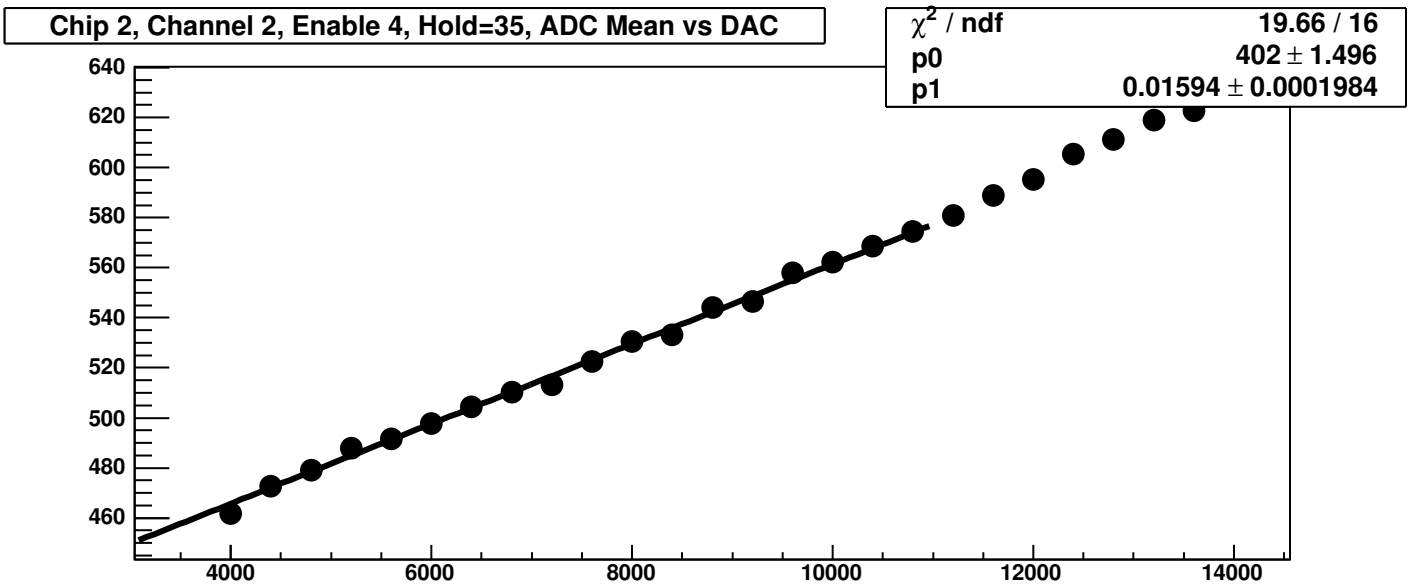


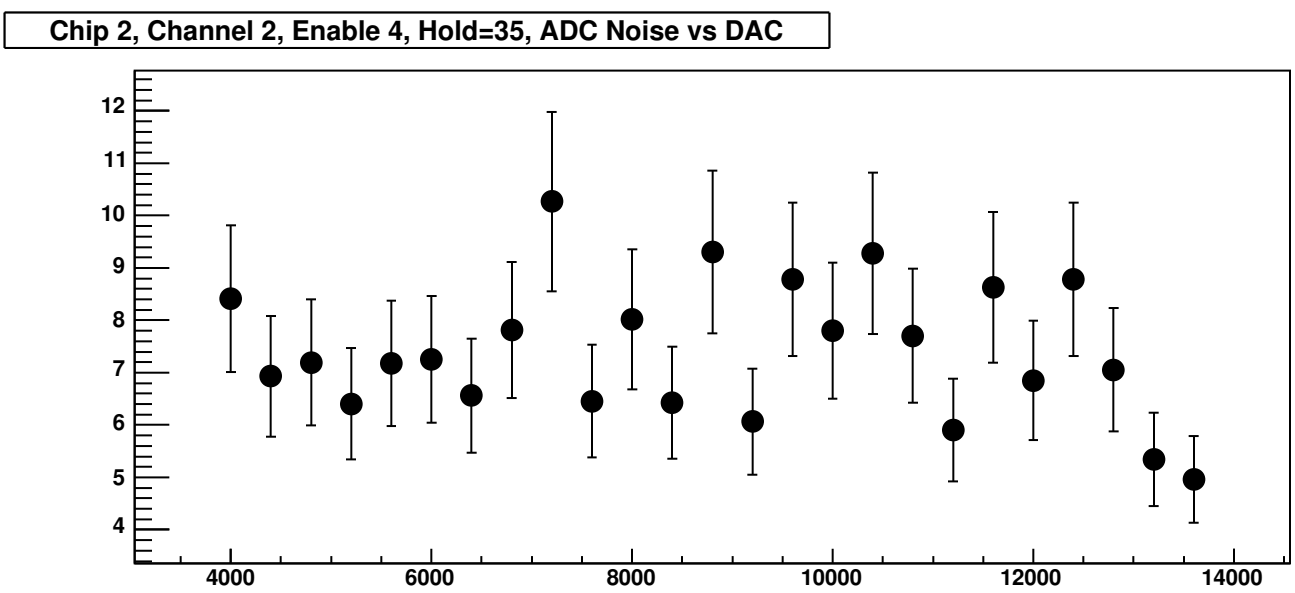
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC

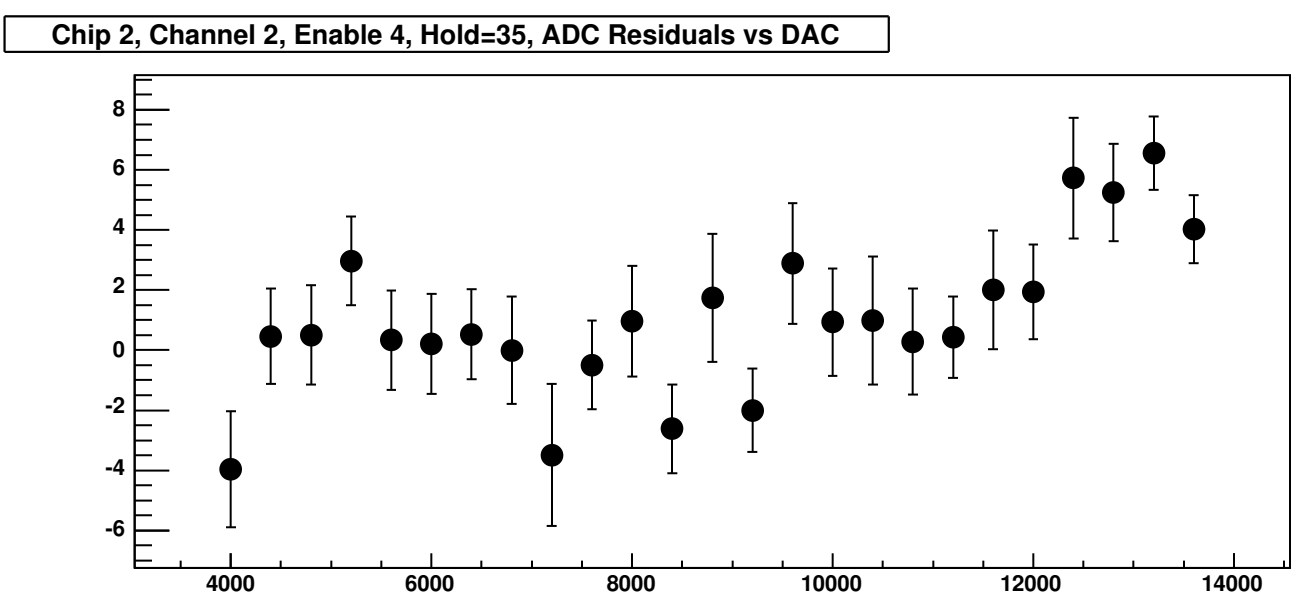


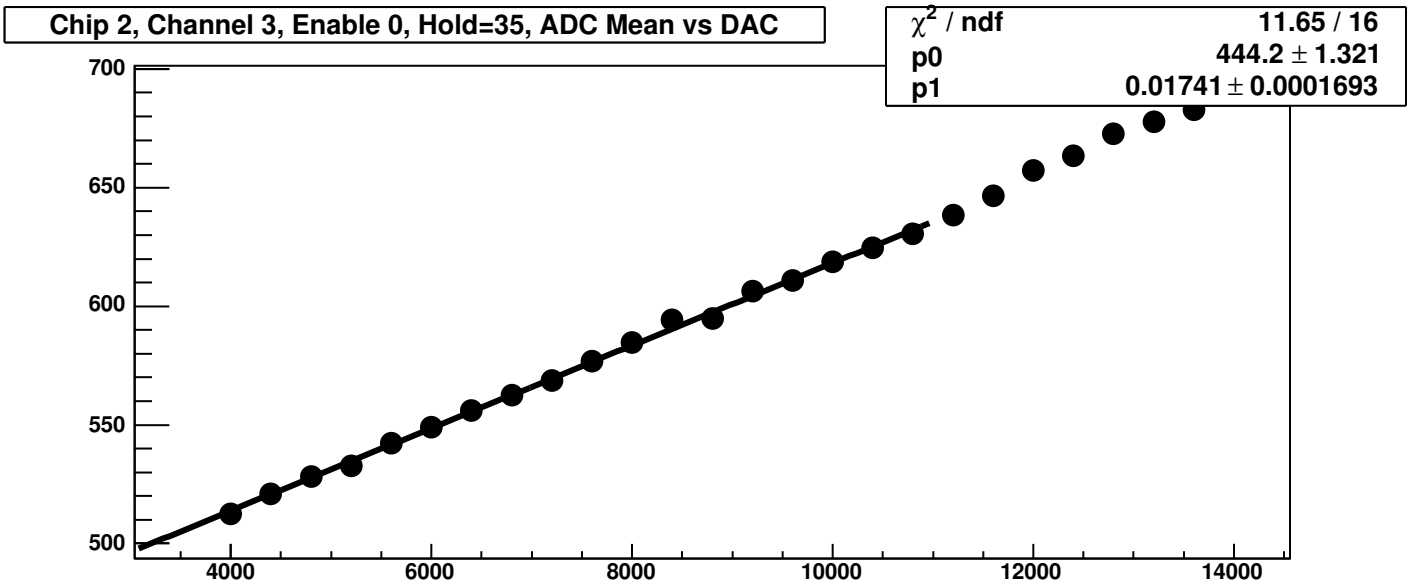
Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

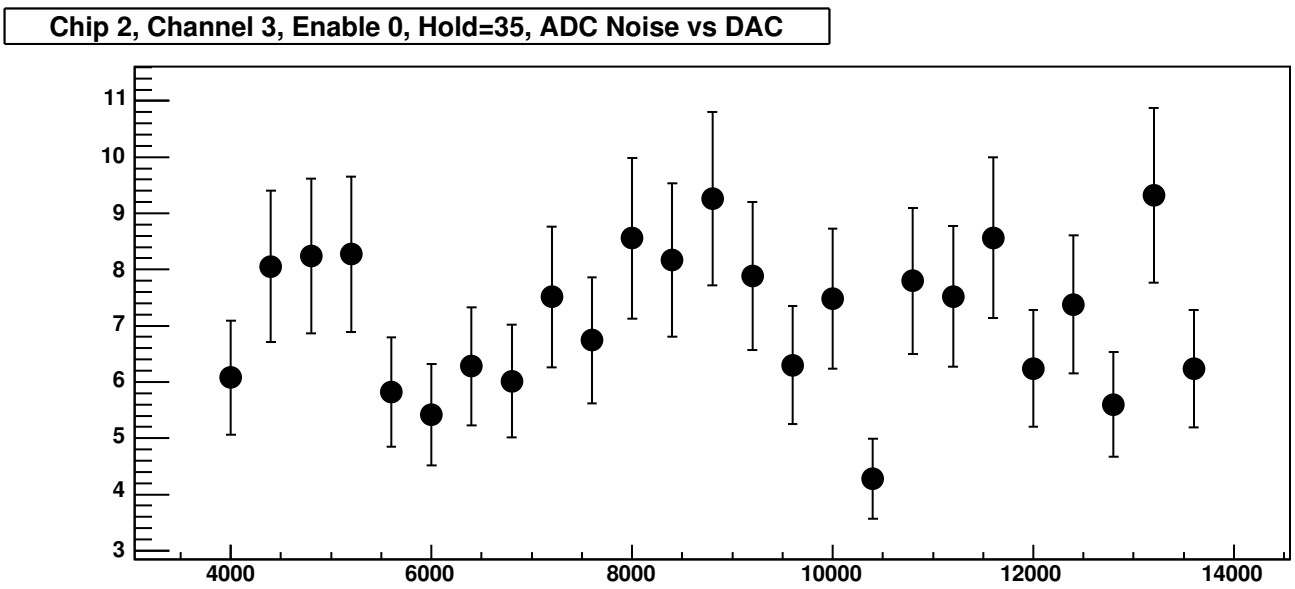


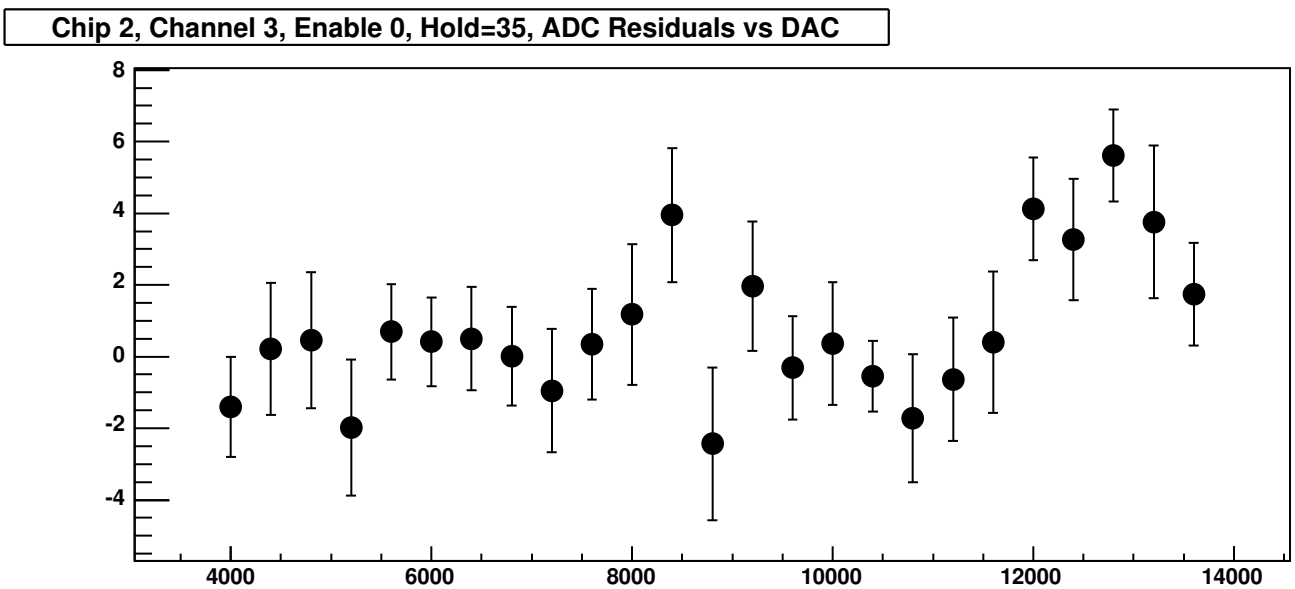

Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.66 / 16
p0 402 ± 1.496
p1 0.01594 ± 0.0001984
640
620
600
580
560
540
520
500
480
460
4000
6000
8000
10000
12000
14000

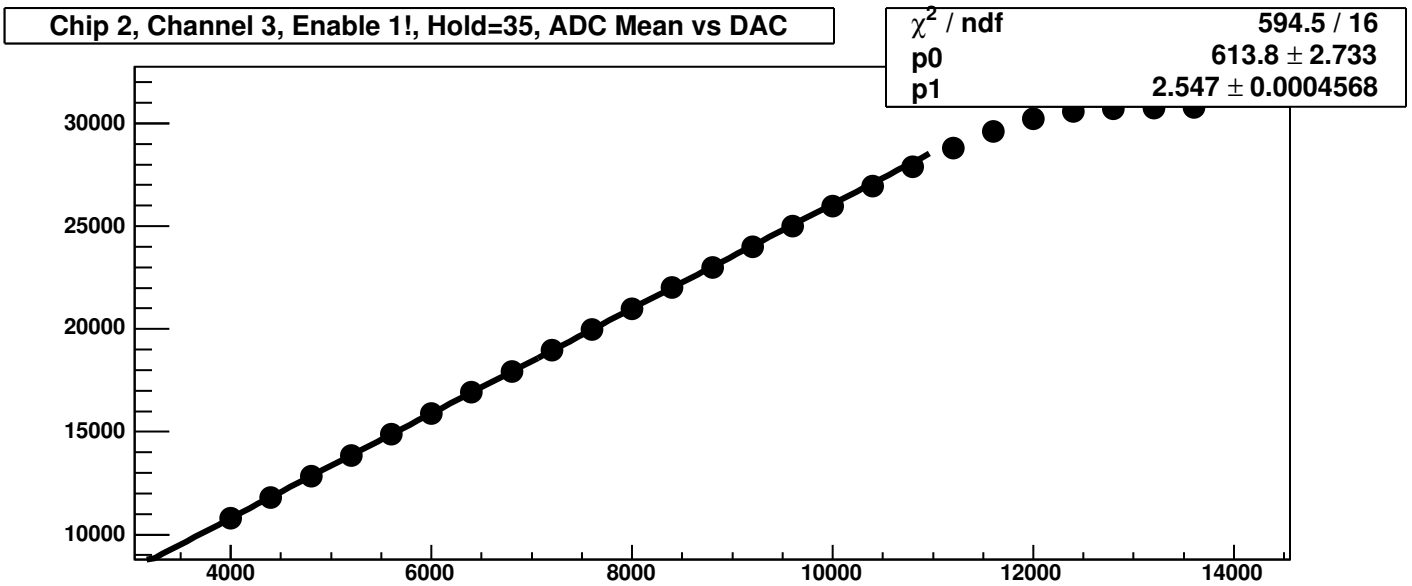

Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

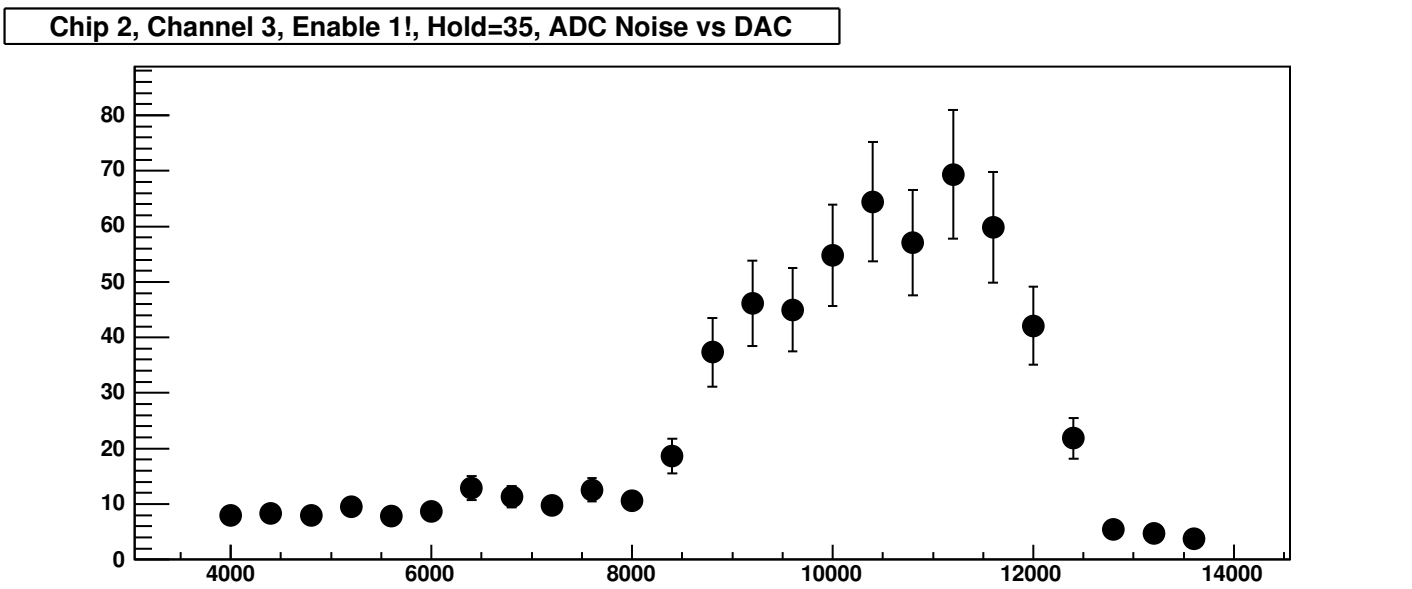

Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

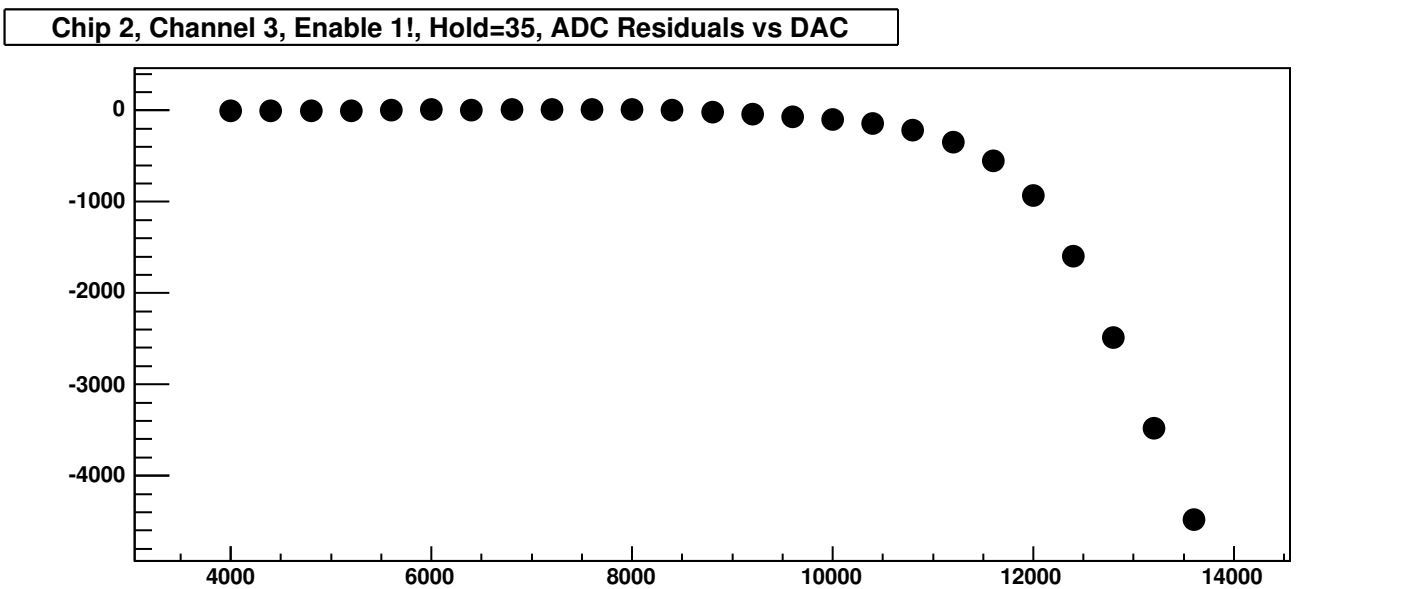

Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 11.65 / 16
p0 444.2 ± 1.321
p1 0.01741 ± 0.0001693
700
650
600
550
500
4000
6000
8000
10000
12000
14000

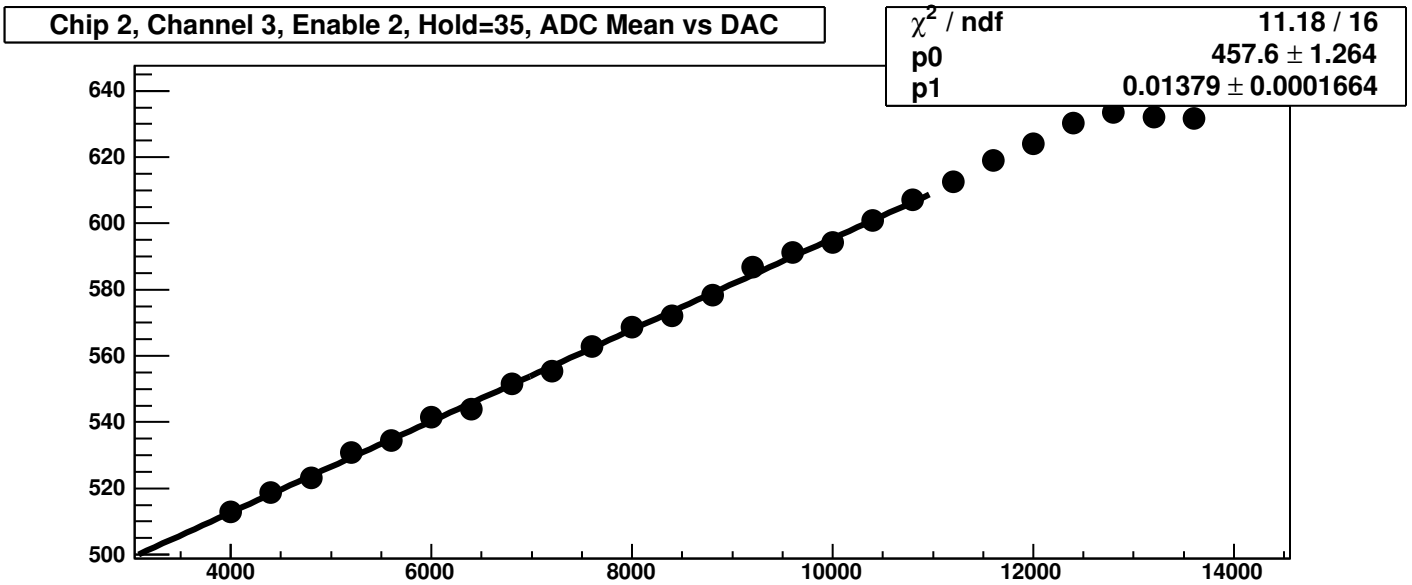

Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

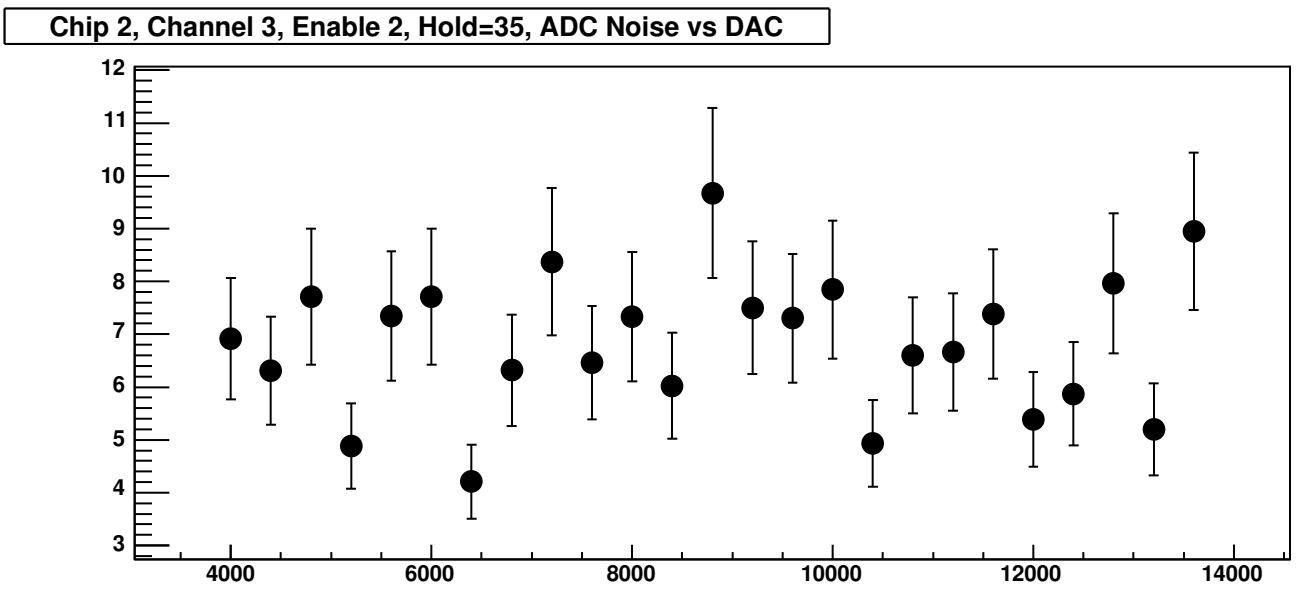

Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

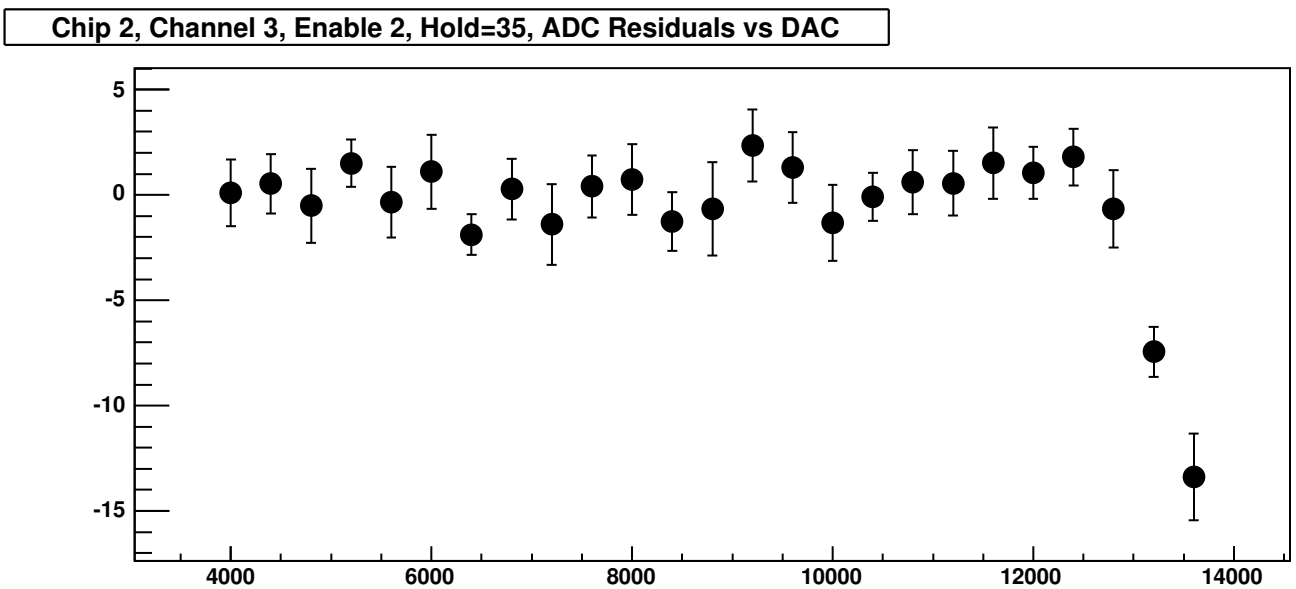

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 594.5 / 16
p0 613.8 ± 2.733
p1 2.547 ± 0.0004568
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

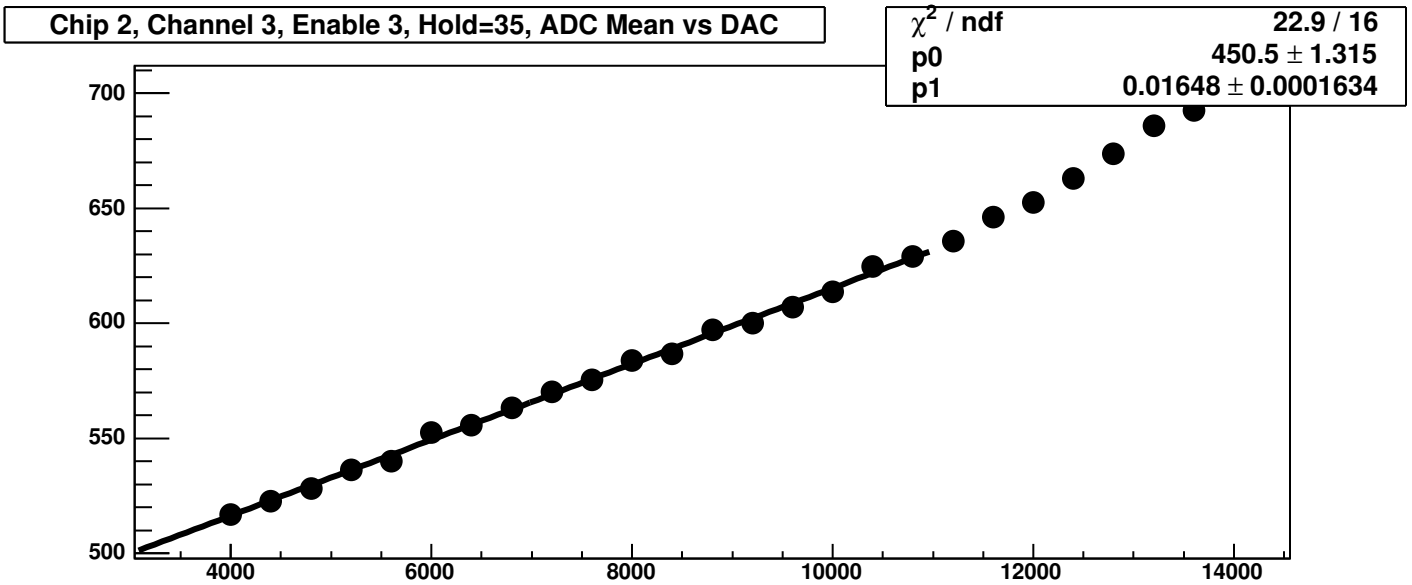

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

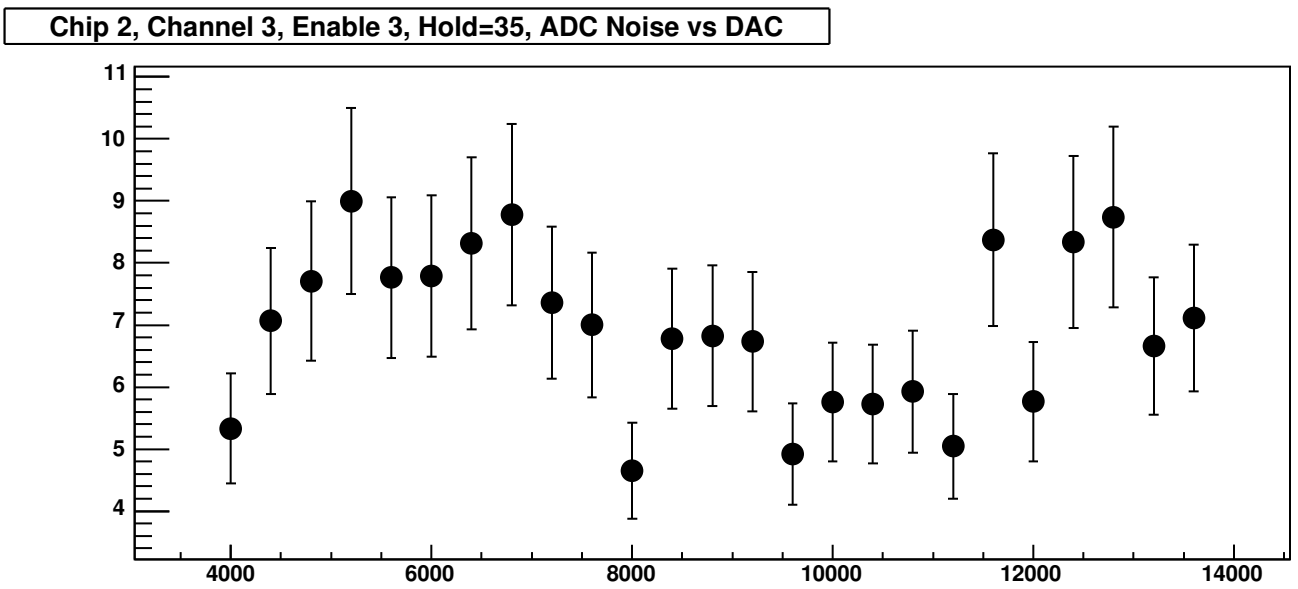

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

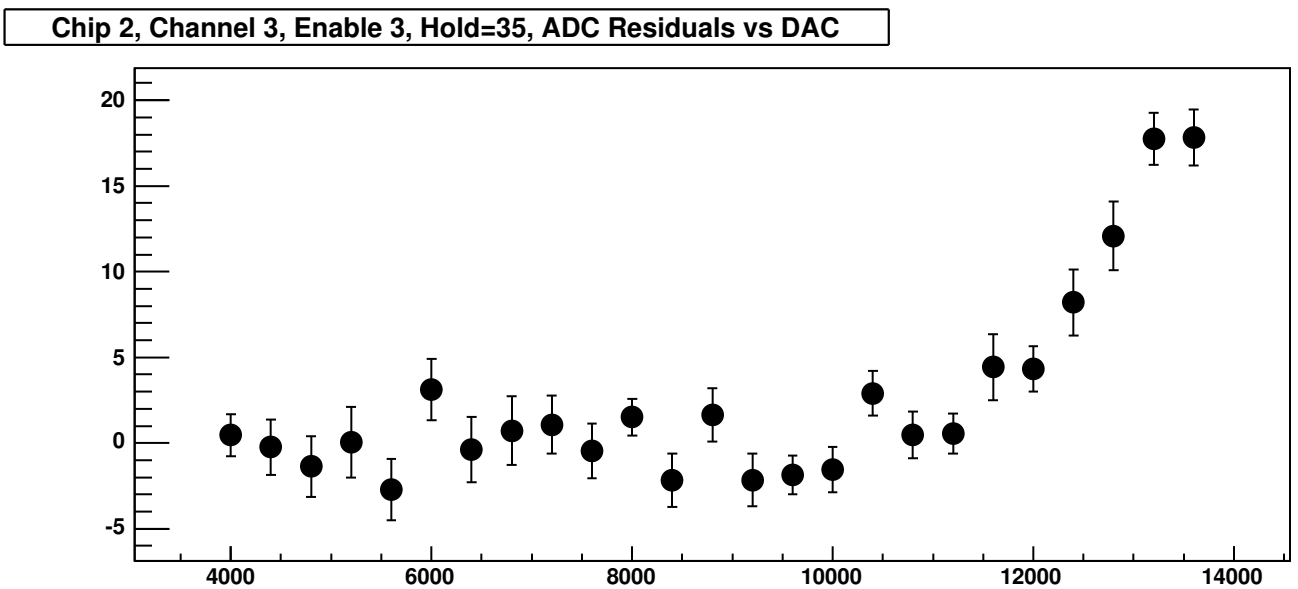

Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 11.18 / 16
p0 457.6 ± 1.264
p1 0.01379 ± 0.0001664
640
620
600
580
560
540
520
500
4000
6000
8000
10000
12000
14000

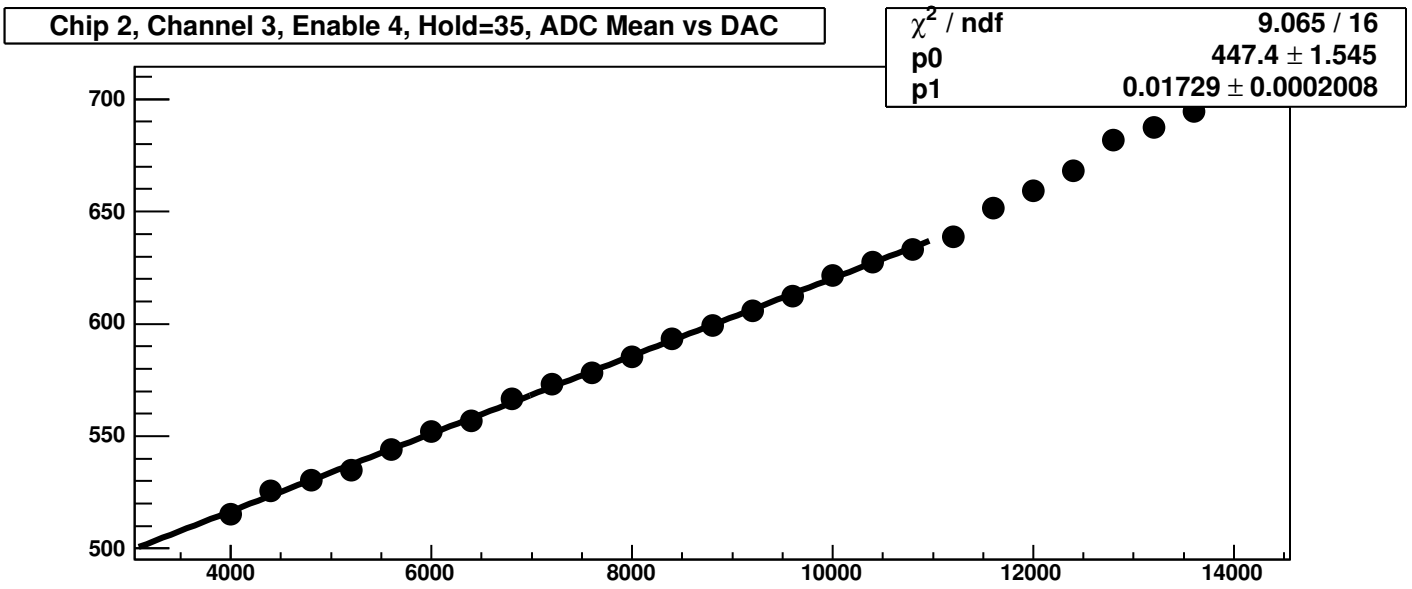

Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

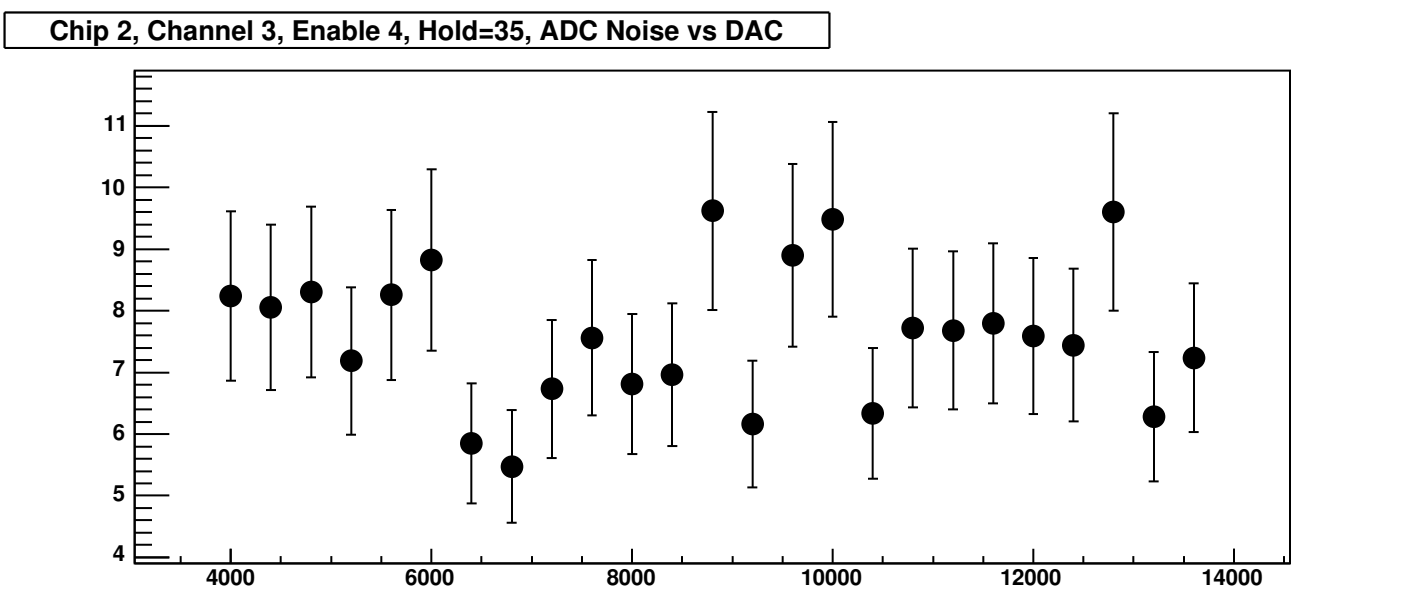

Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

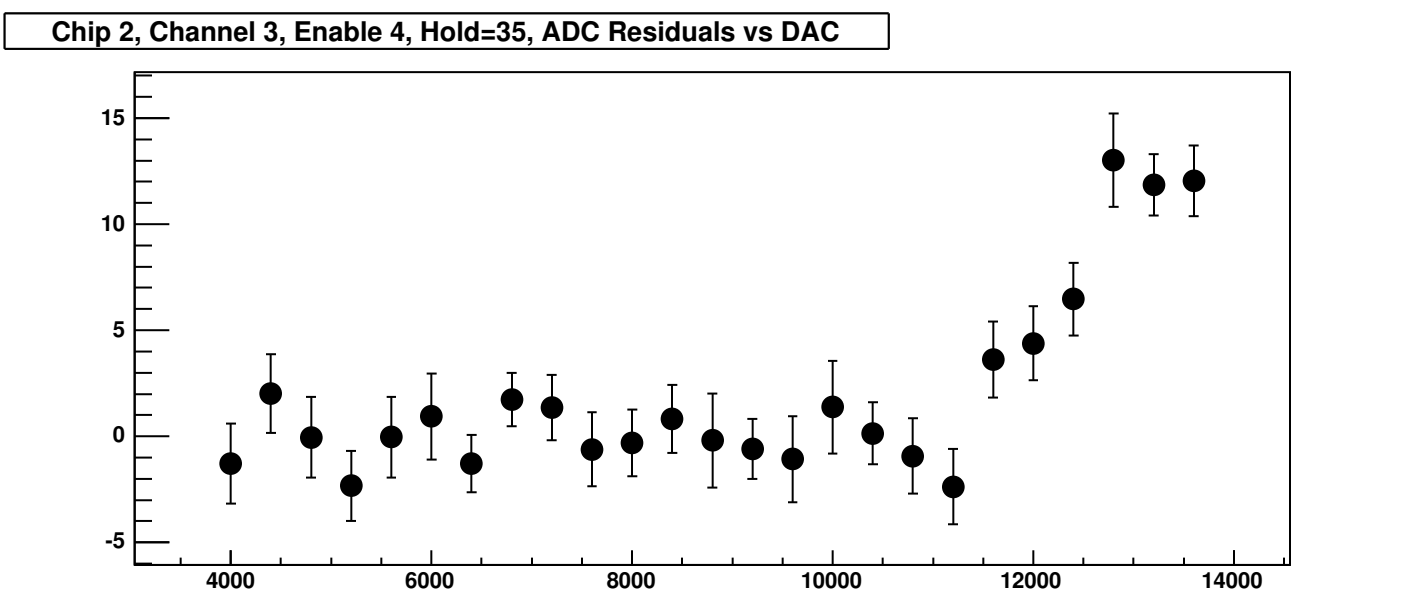

Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.9 / 16
p0 450.5 ± 1.315
p1 0.01648 ± 0.0001634
700
650
600
550
500
4000
6000
8000
10000
12000
14000

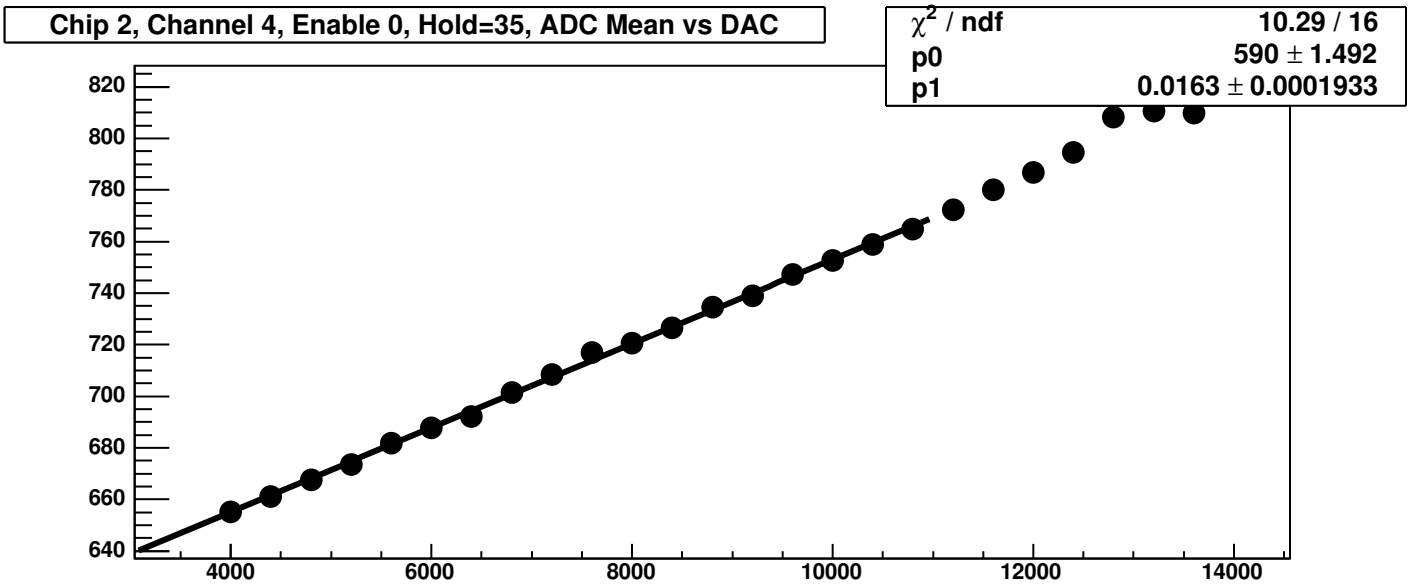

Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

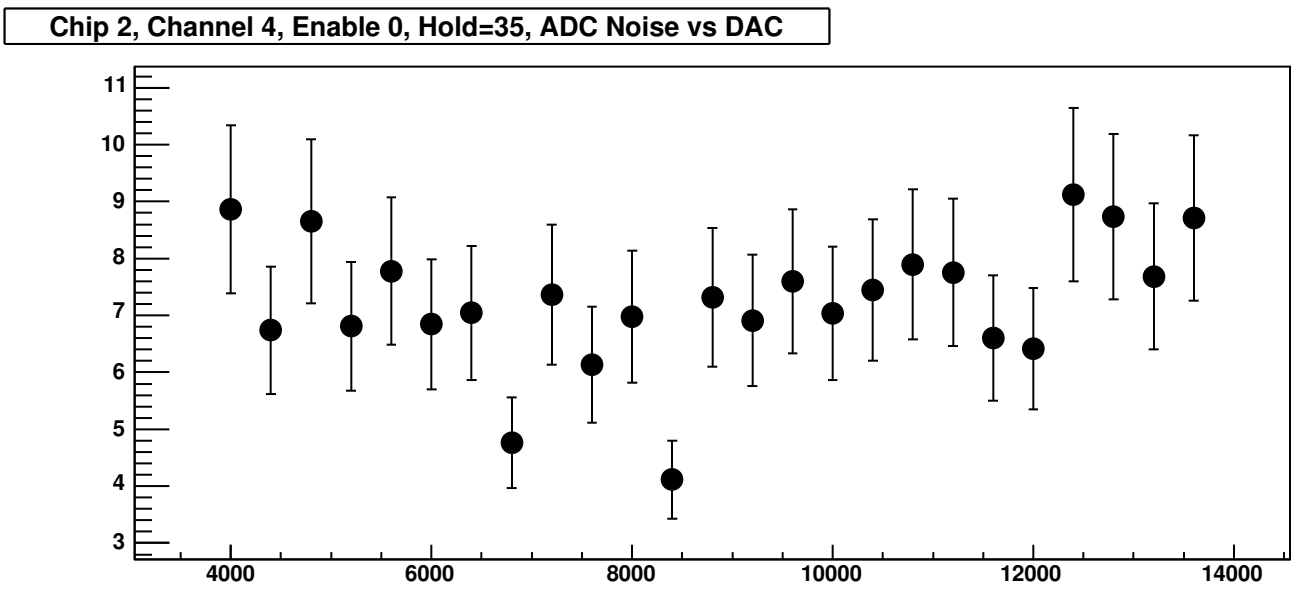

Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

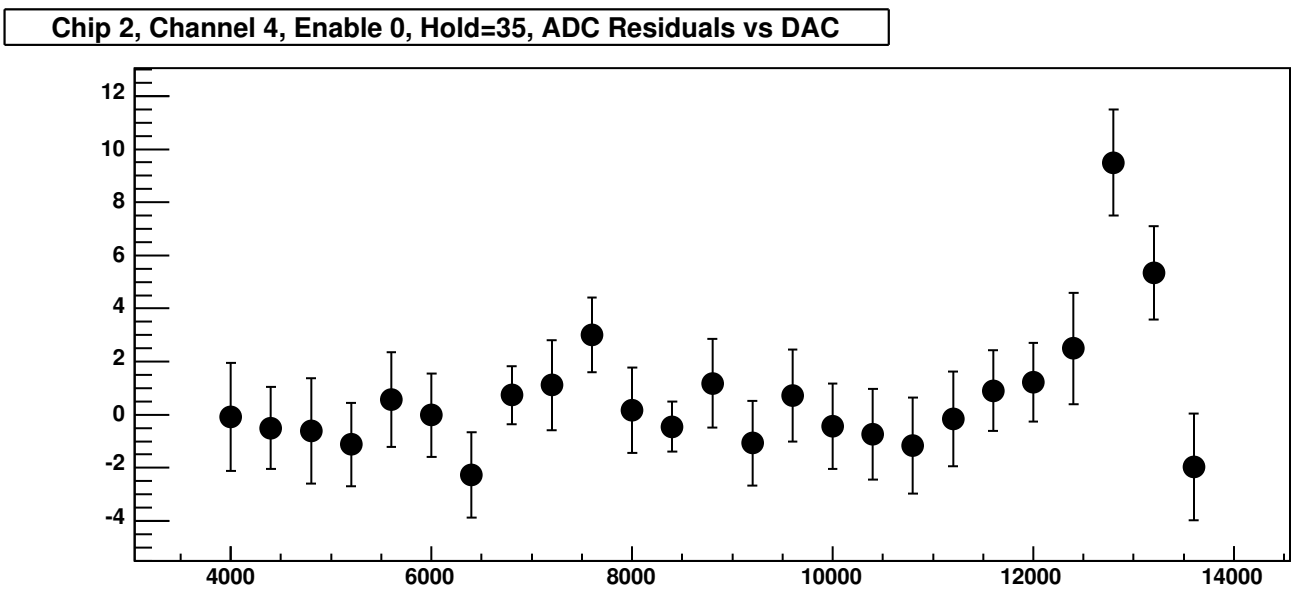

Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 9.065 / 16
p0 447.4 ± 1.545
p1 0.01729 ± 0.0002008
700
650
600
550
500
4000
6000
8000
10000
12000
14000


Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

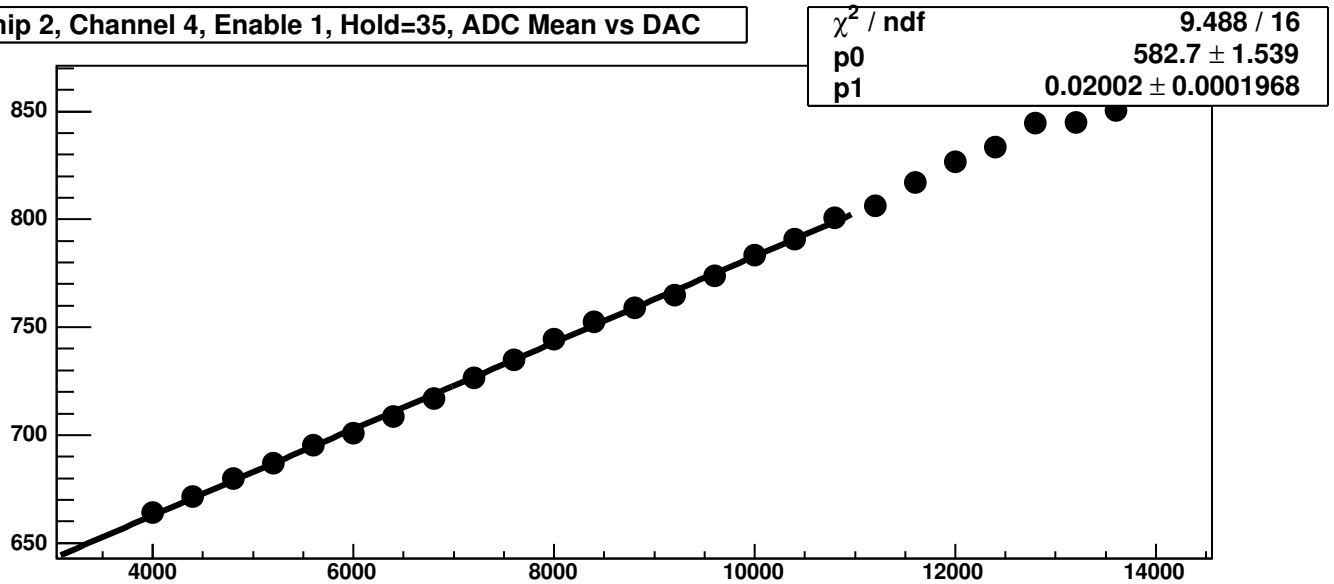

Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 10.29 / 16
p0 590 ± 1.492
p1 0.0163 ± 0.0001933
820
800
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000

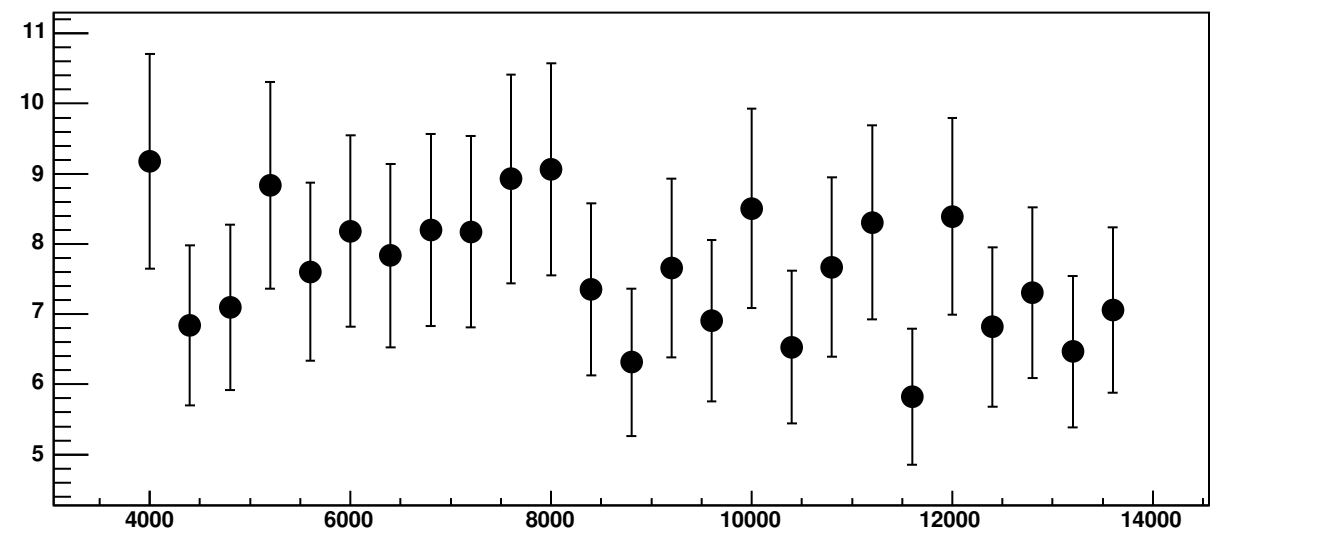

Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

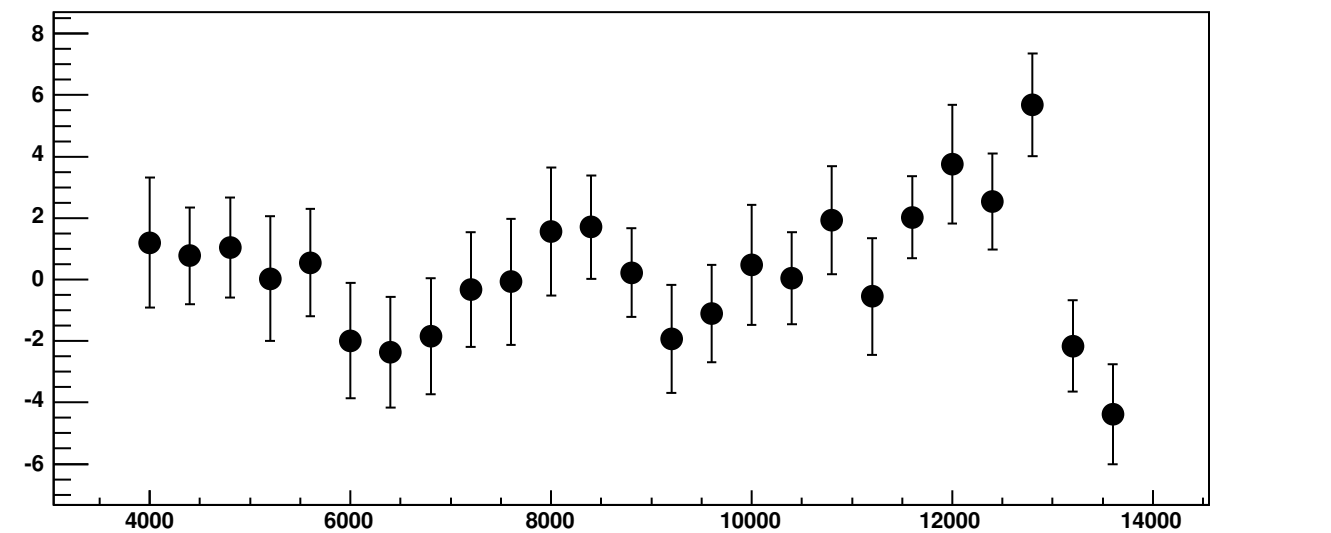
Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



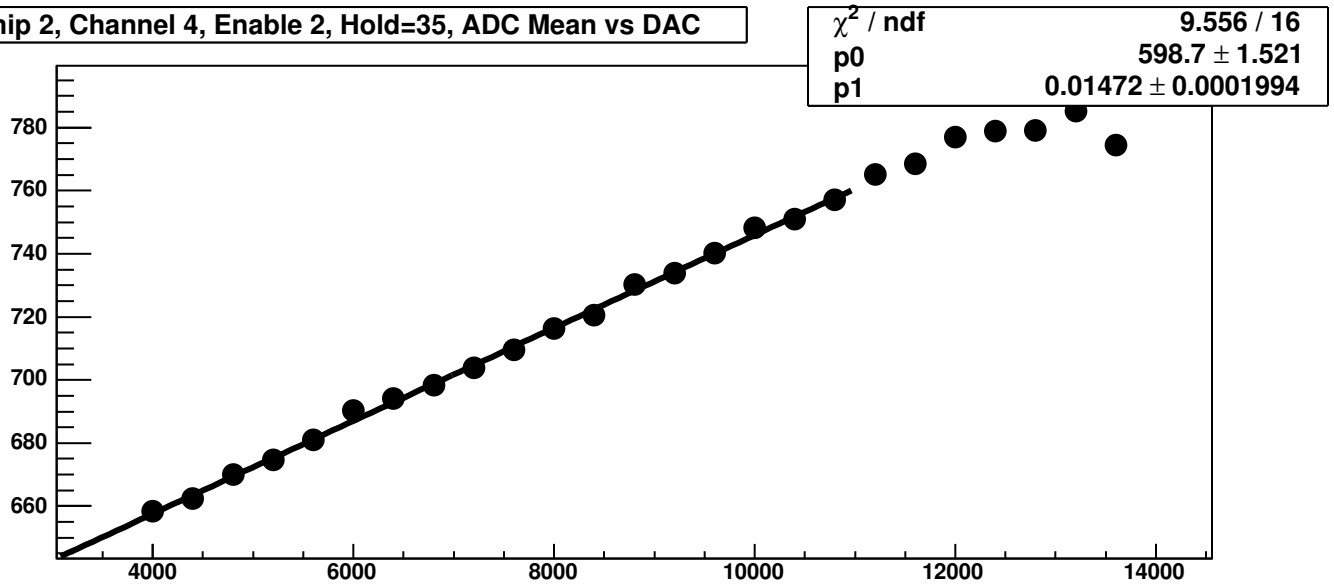
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



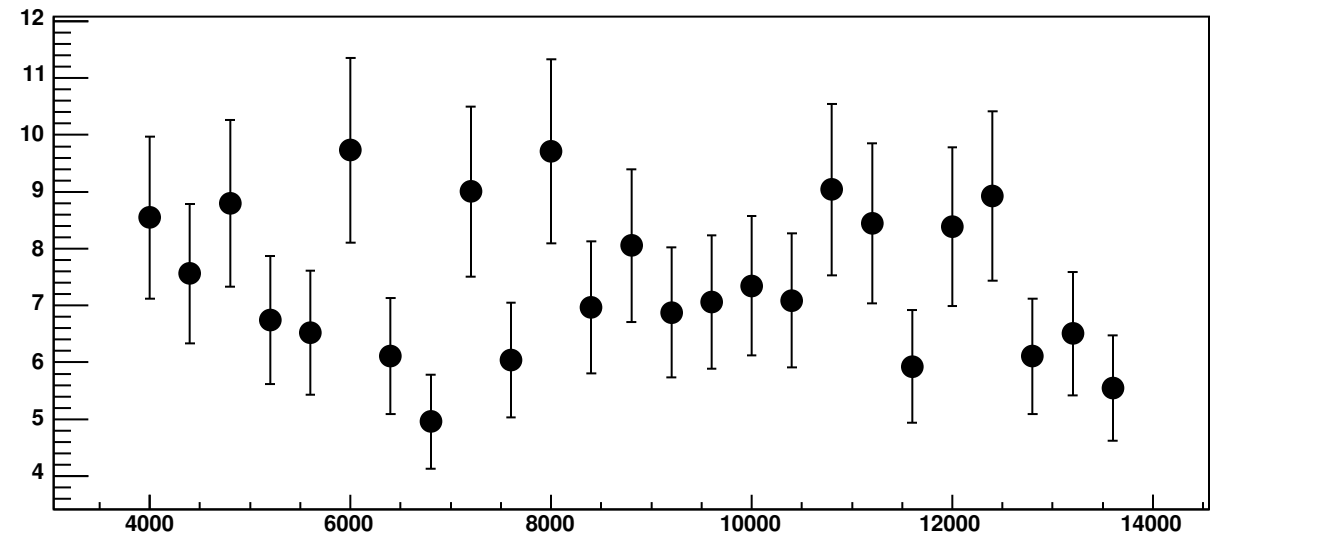
Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



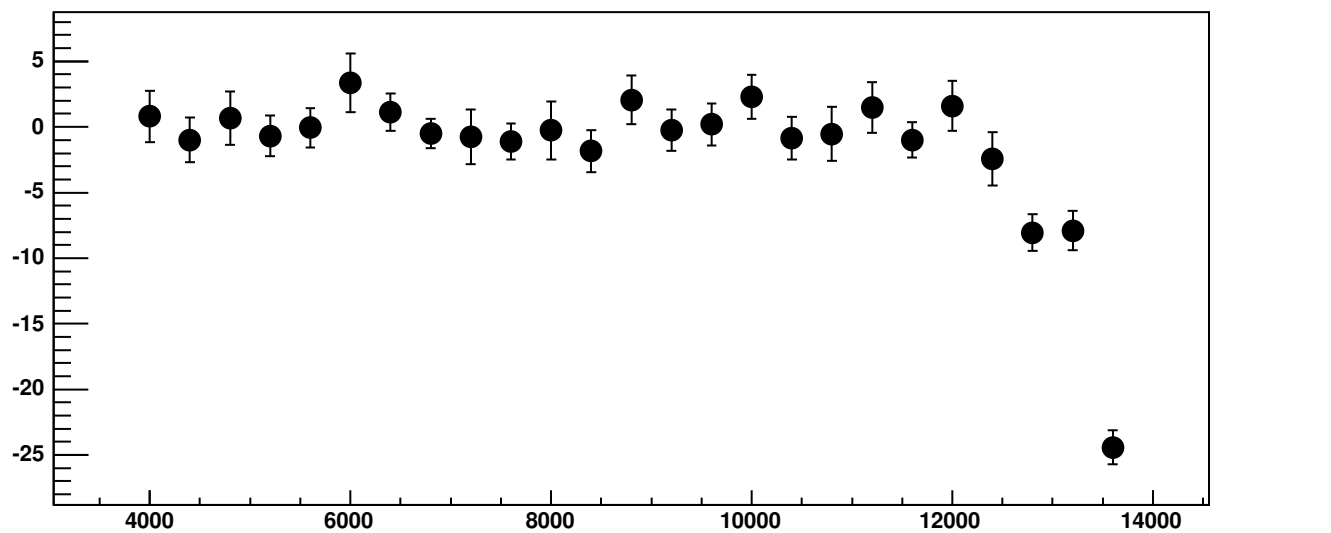
Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

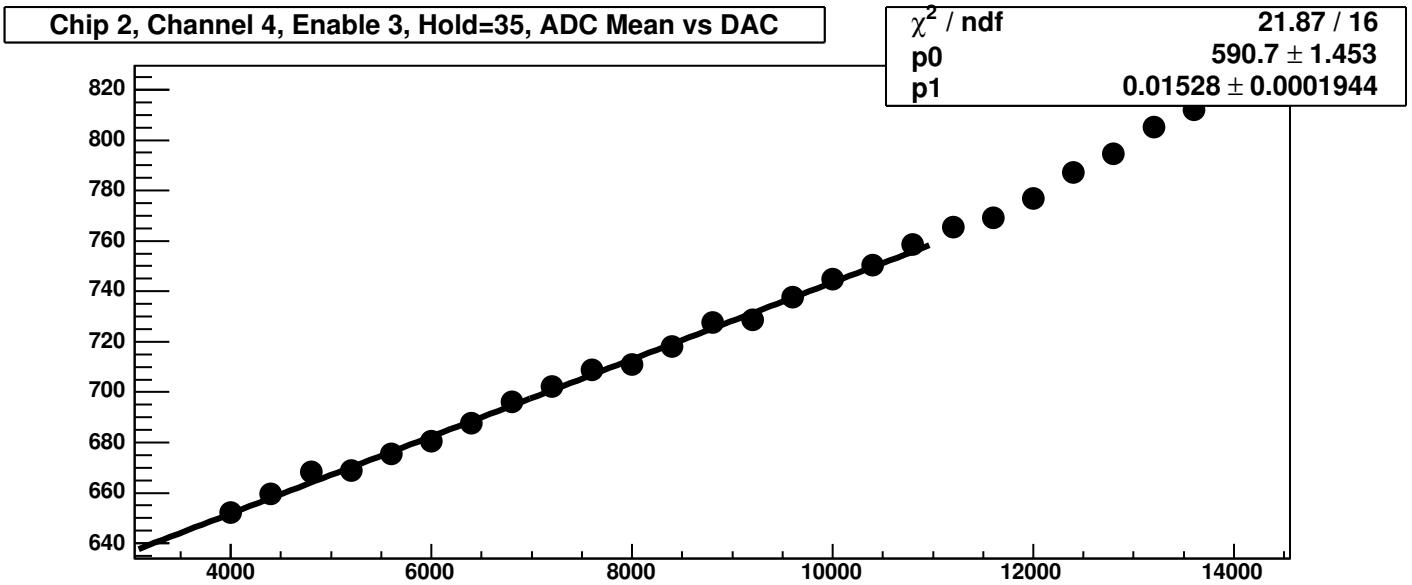


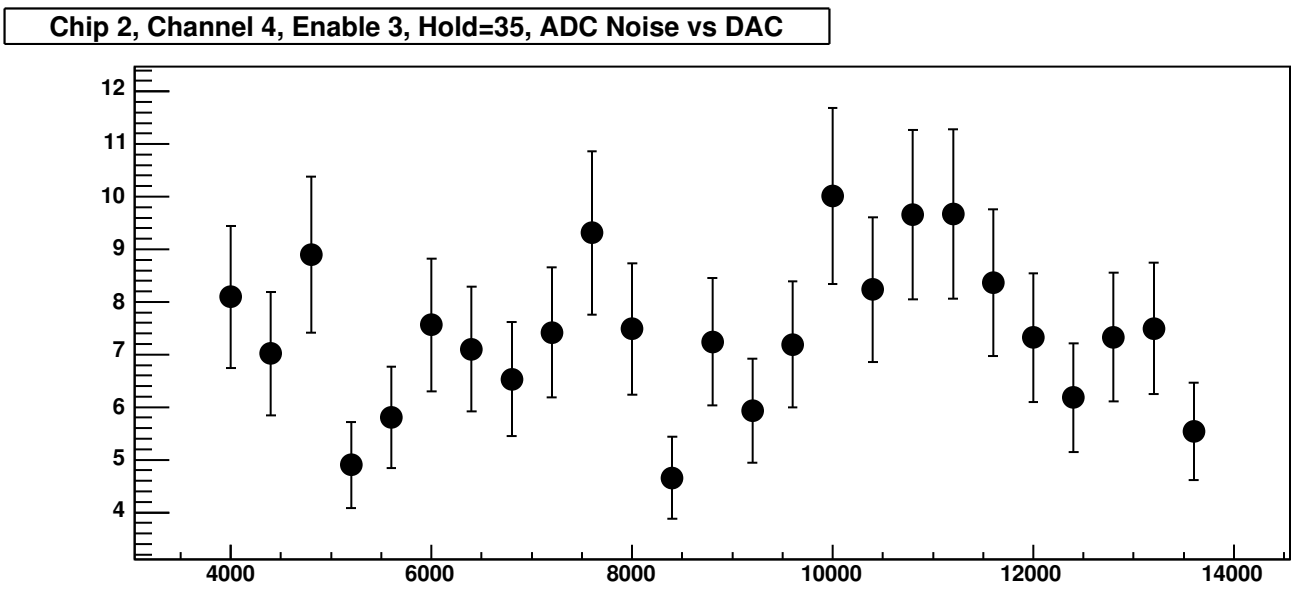
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

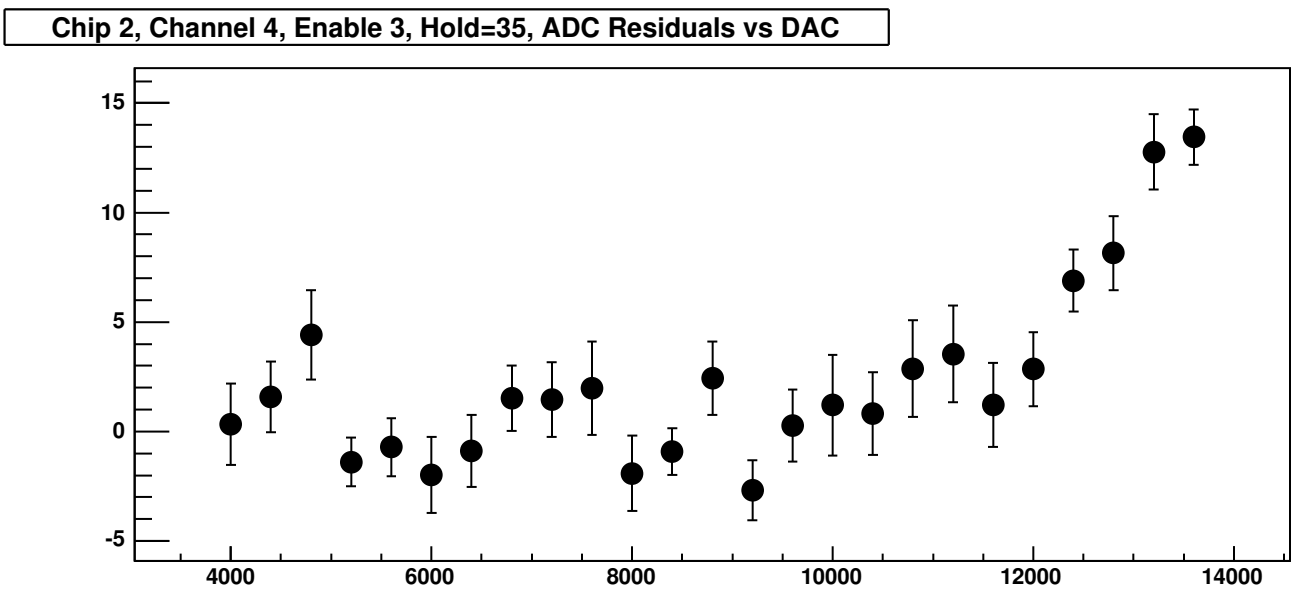


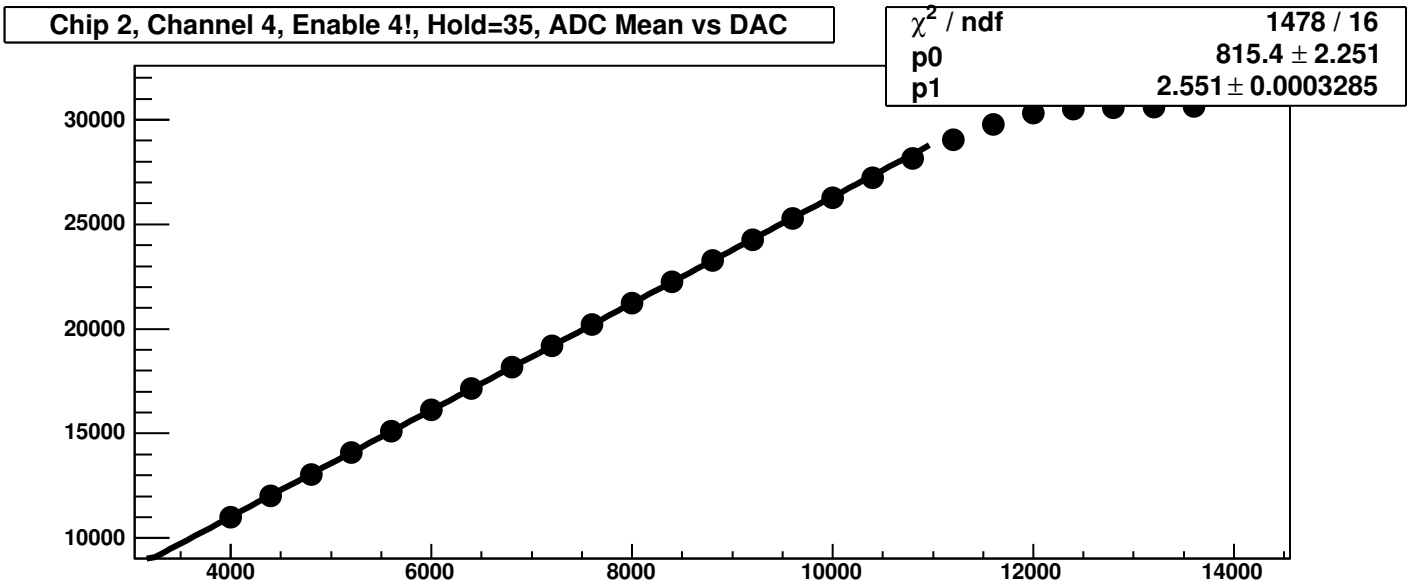
Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

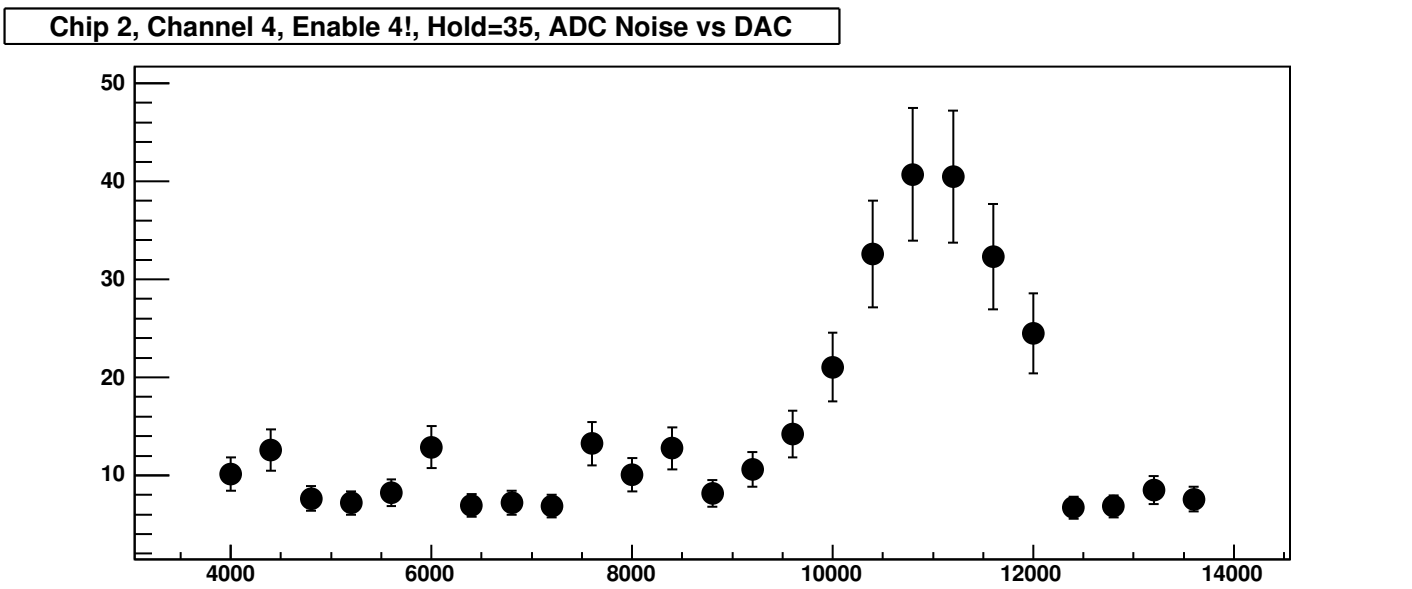


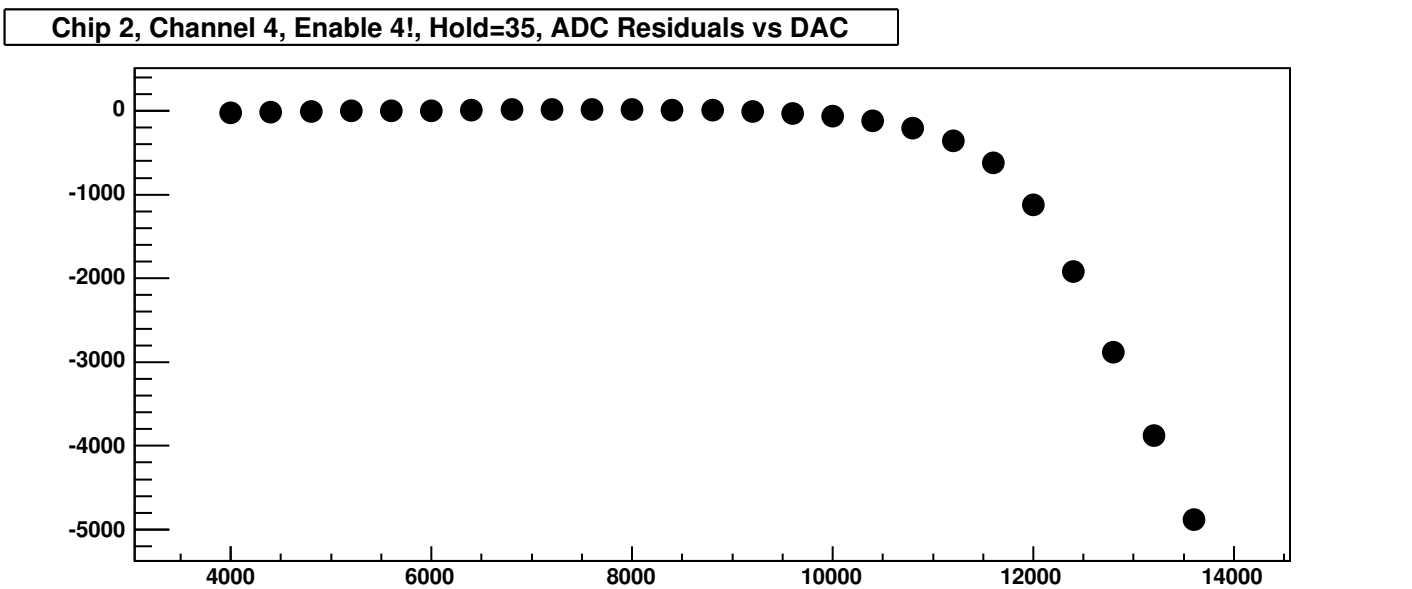

Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.87 / 16
p0 590.7 ± 1.453
p1 0.01528 ± 0.0001944
820
800
780
760
740
720
700
680
660
640
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

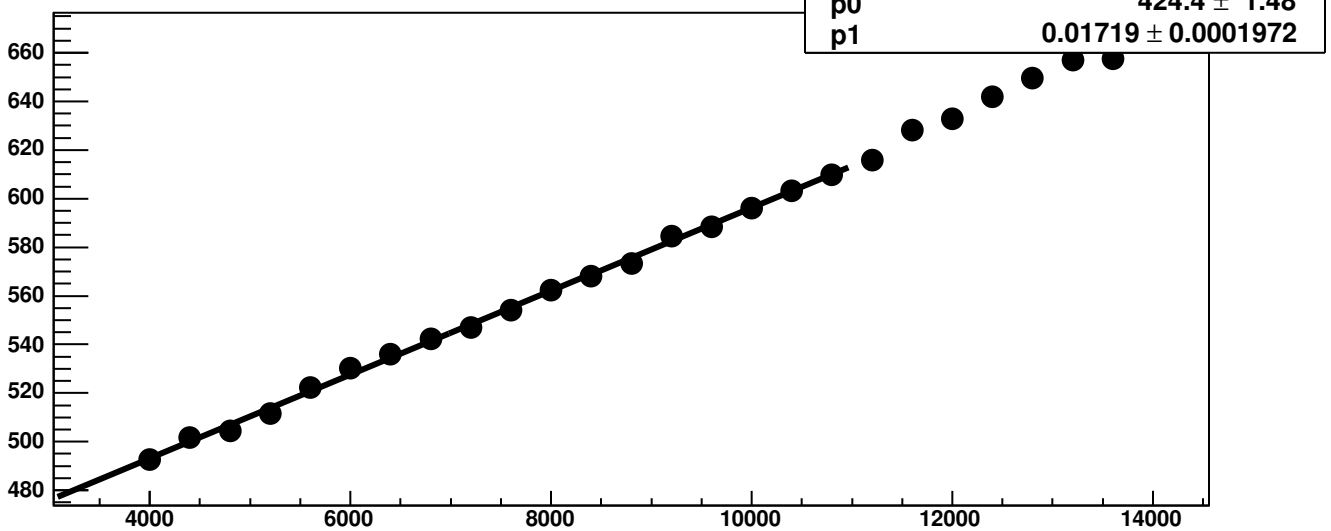

Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1478 / 16
p0 815.4 ± 2.251
p1 2.551 ± 0.0003285
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

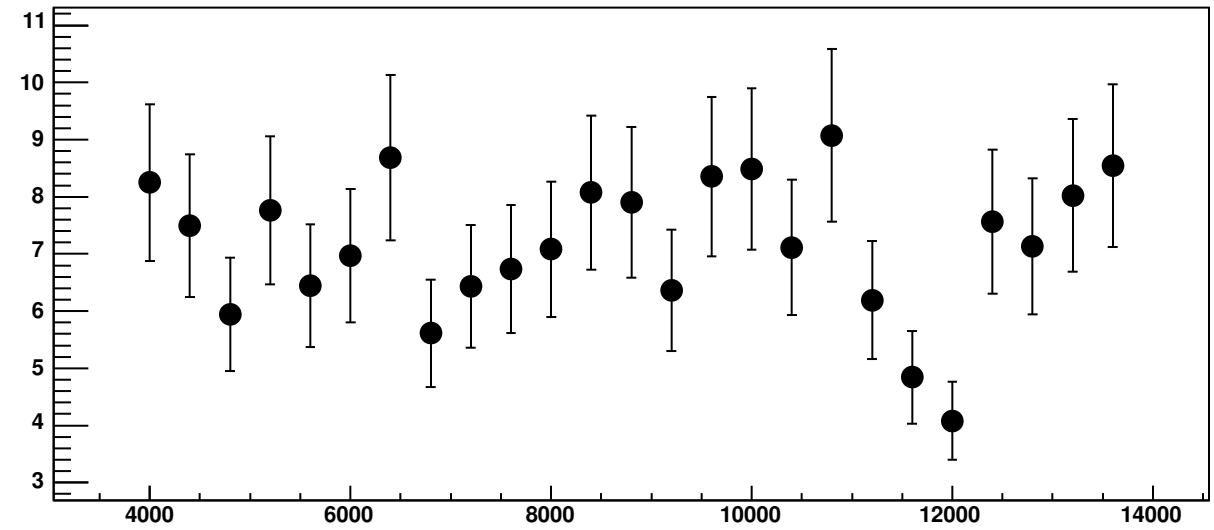

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

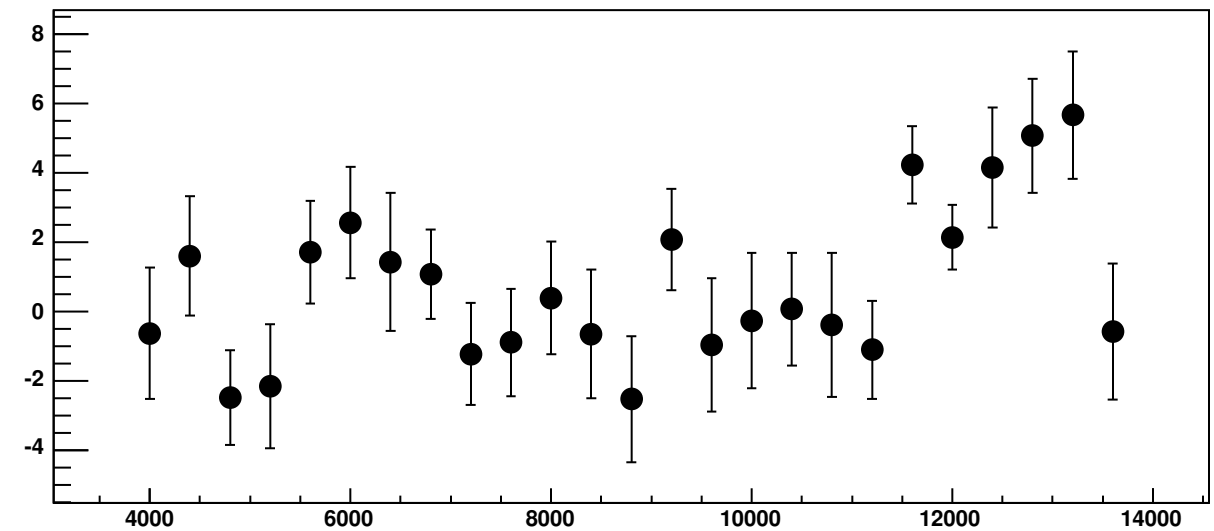
Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

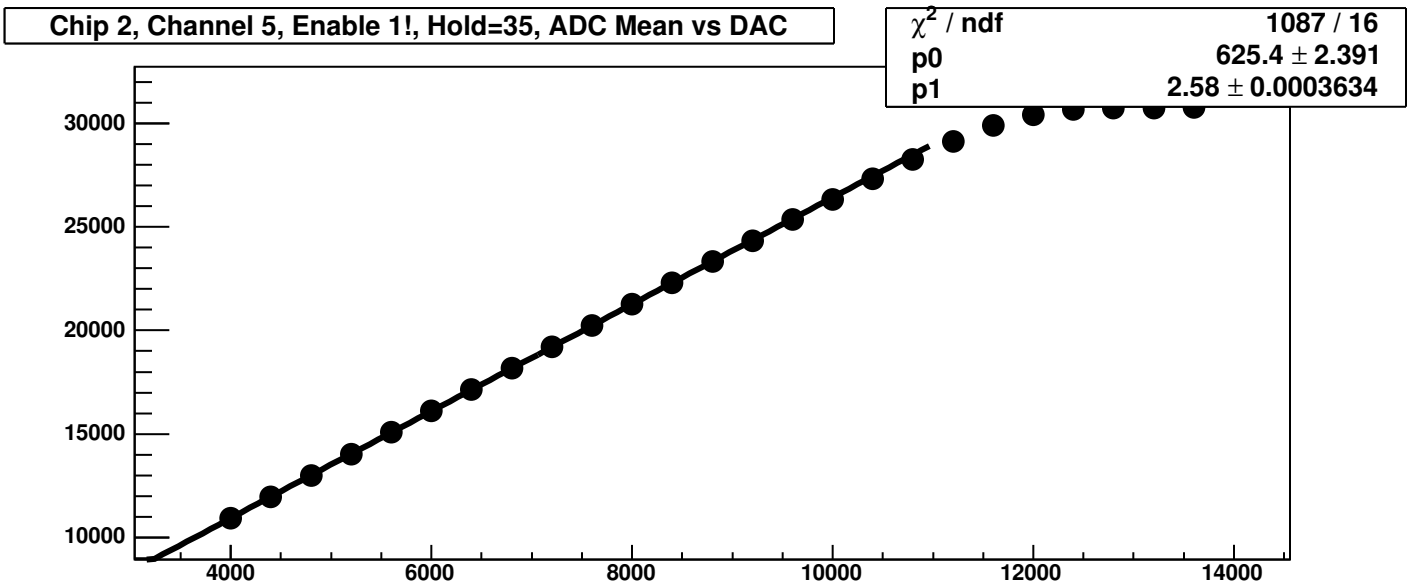


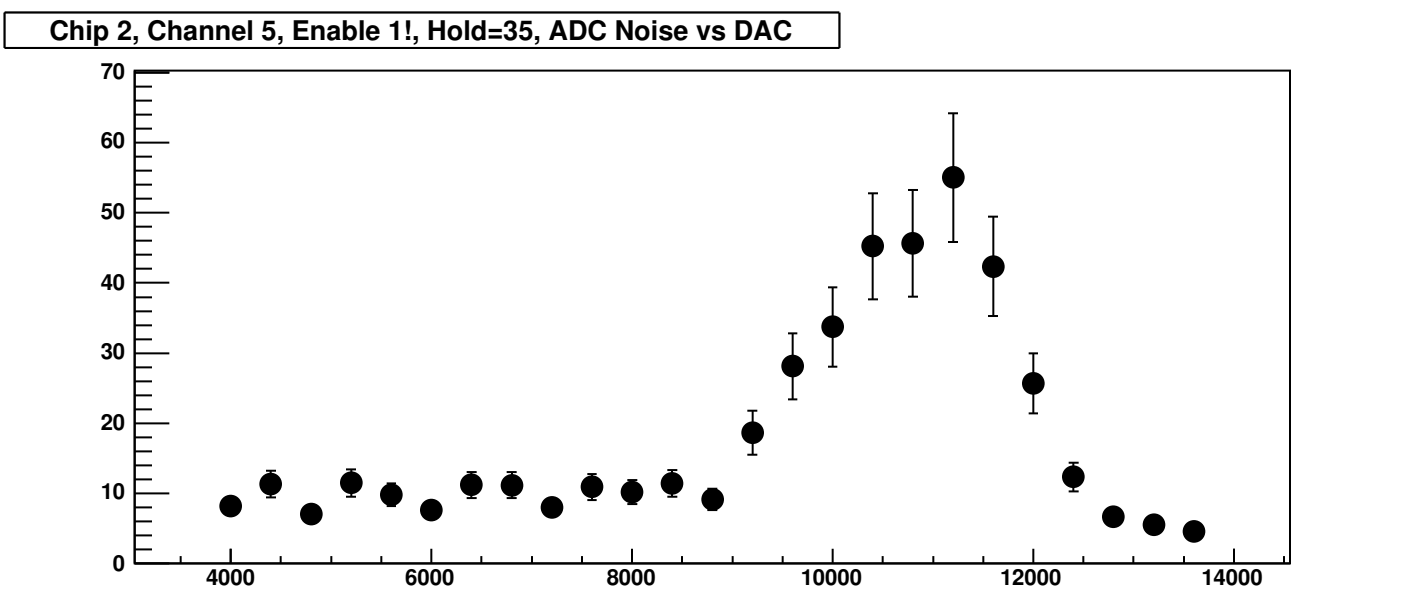
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

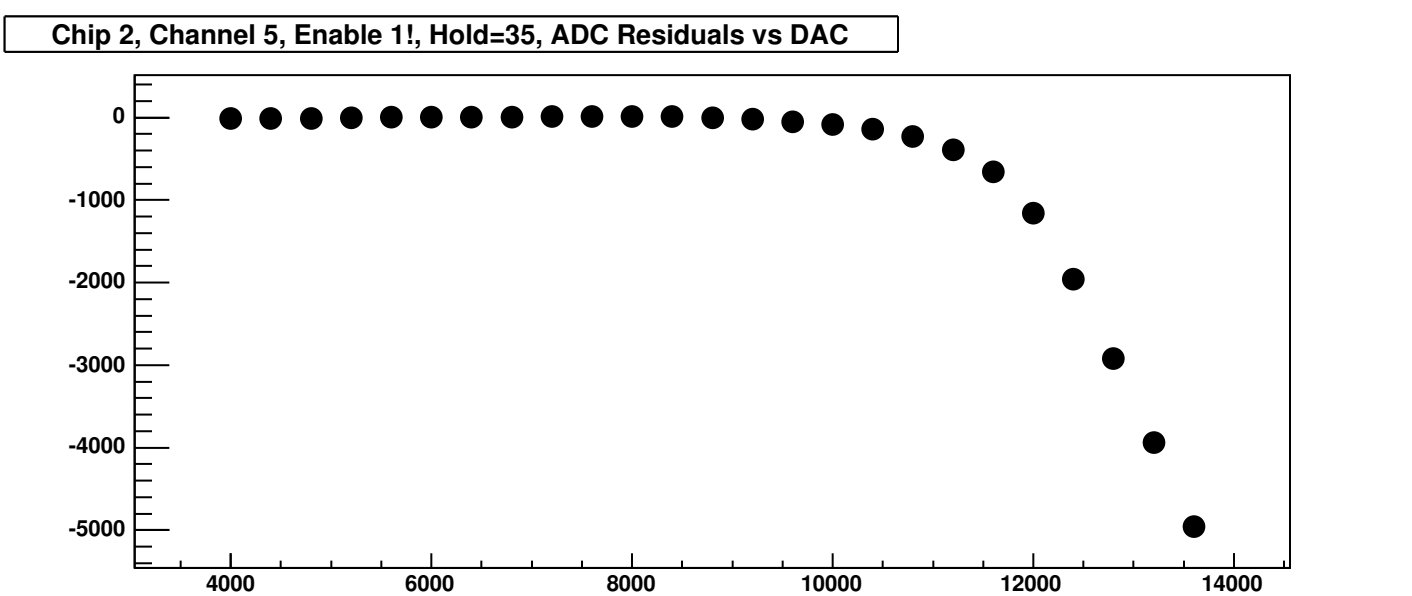


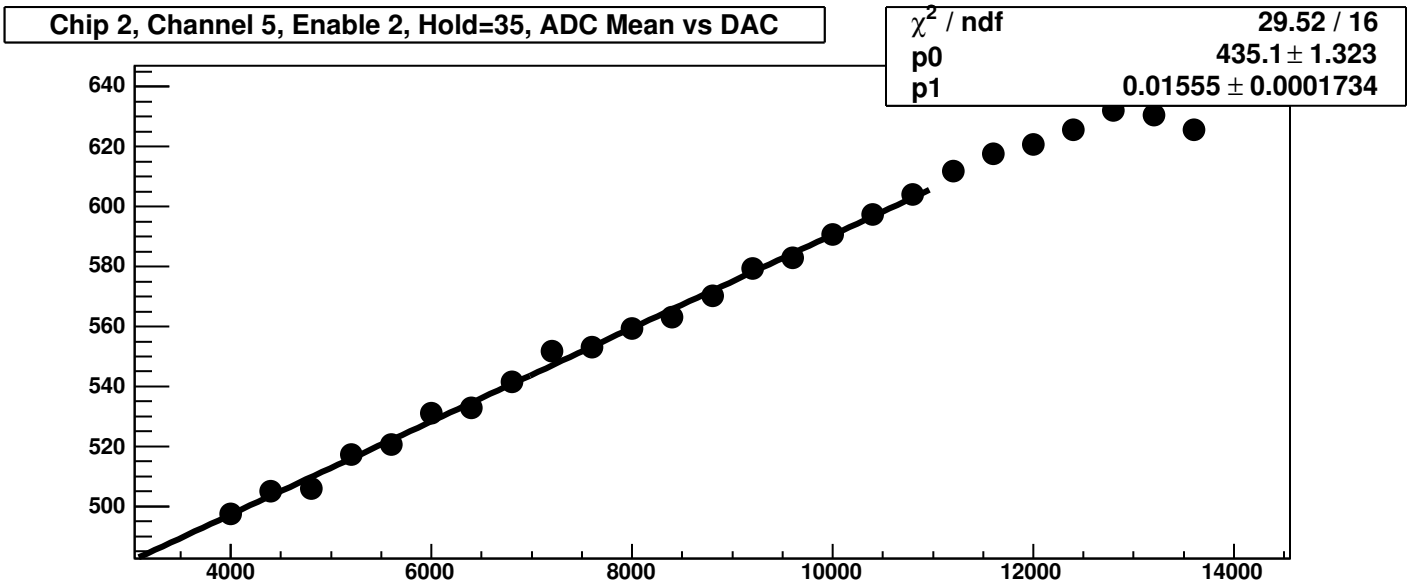
Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

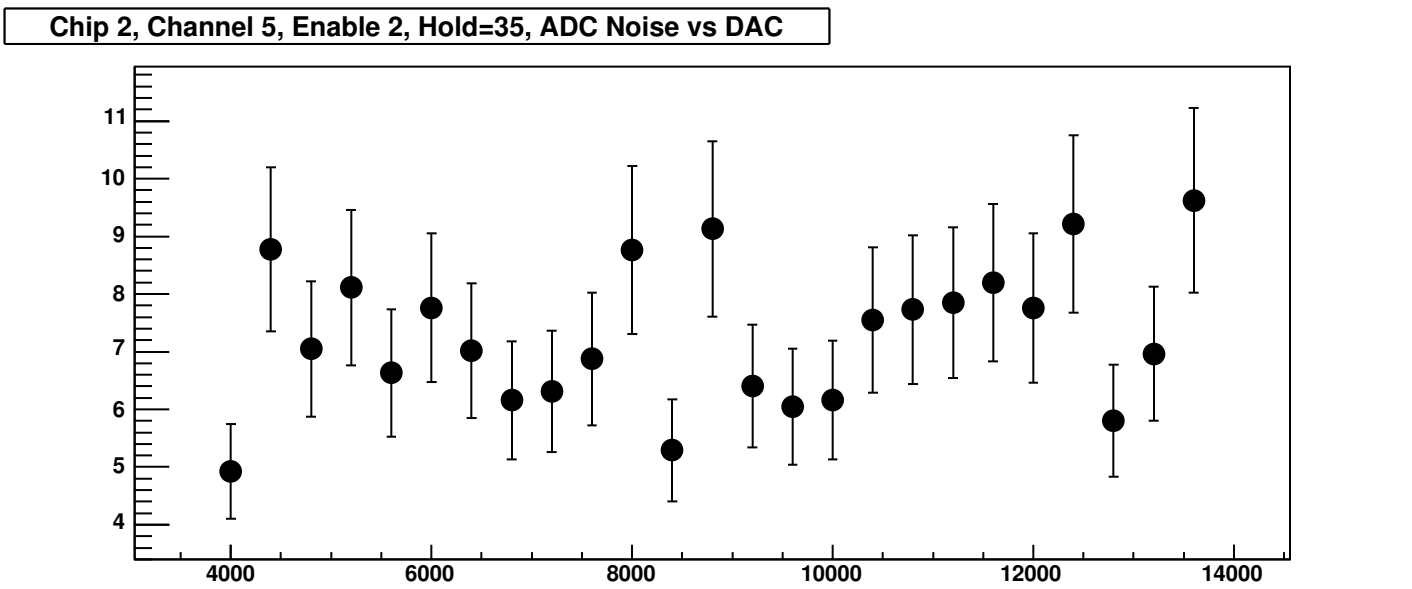


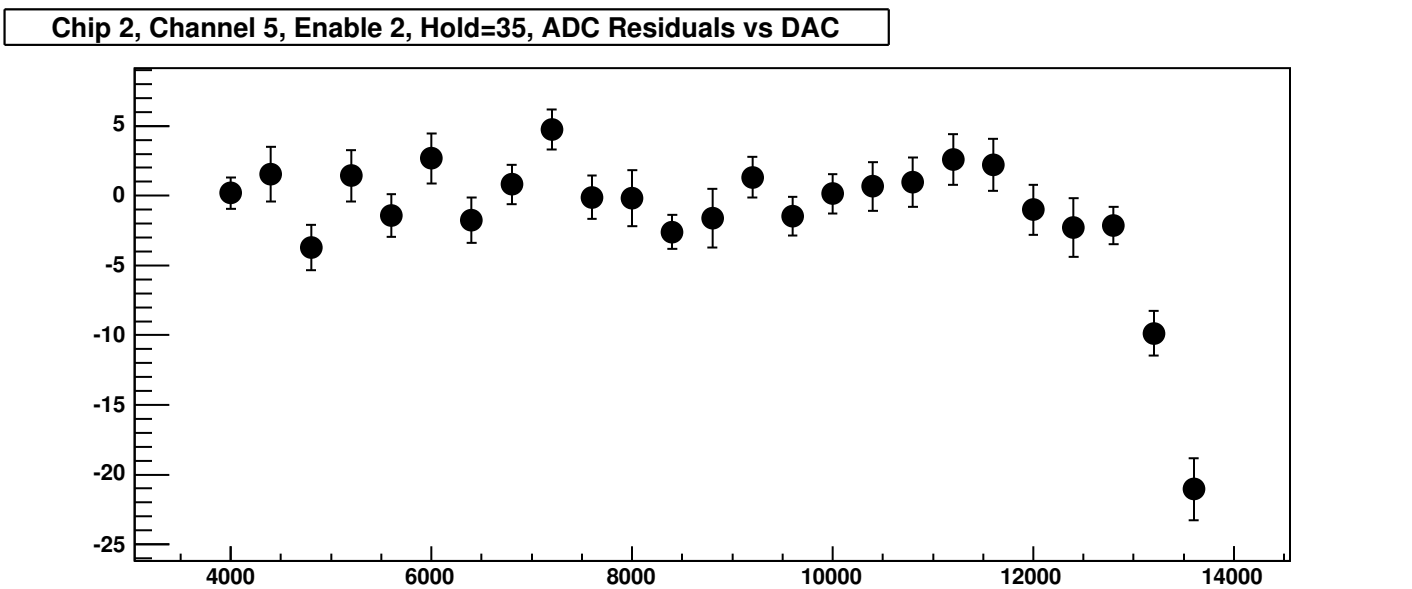

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1087 / 16
p0 625.4 ± 2.391
p1 2.58 ± 0.0003634
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

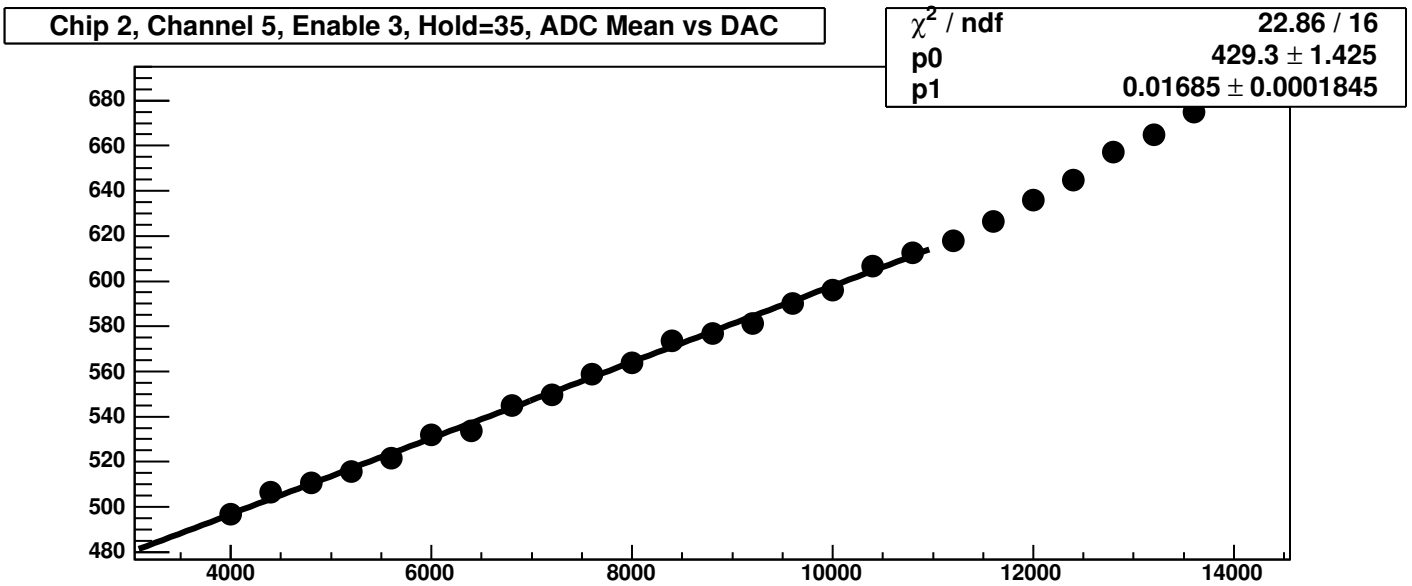

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

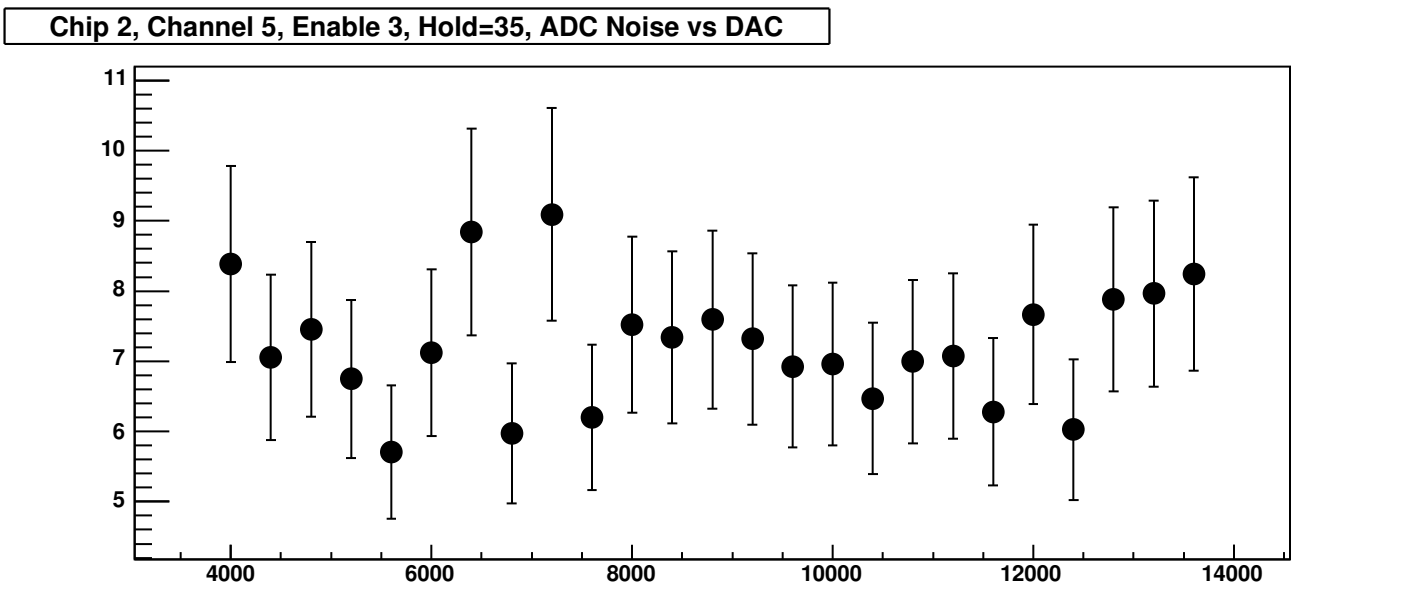

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

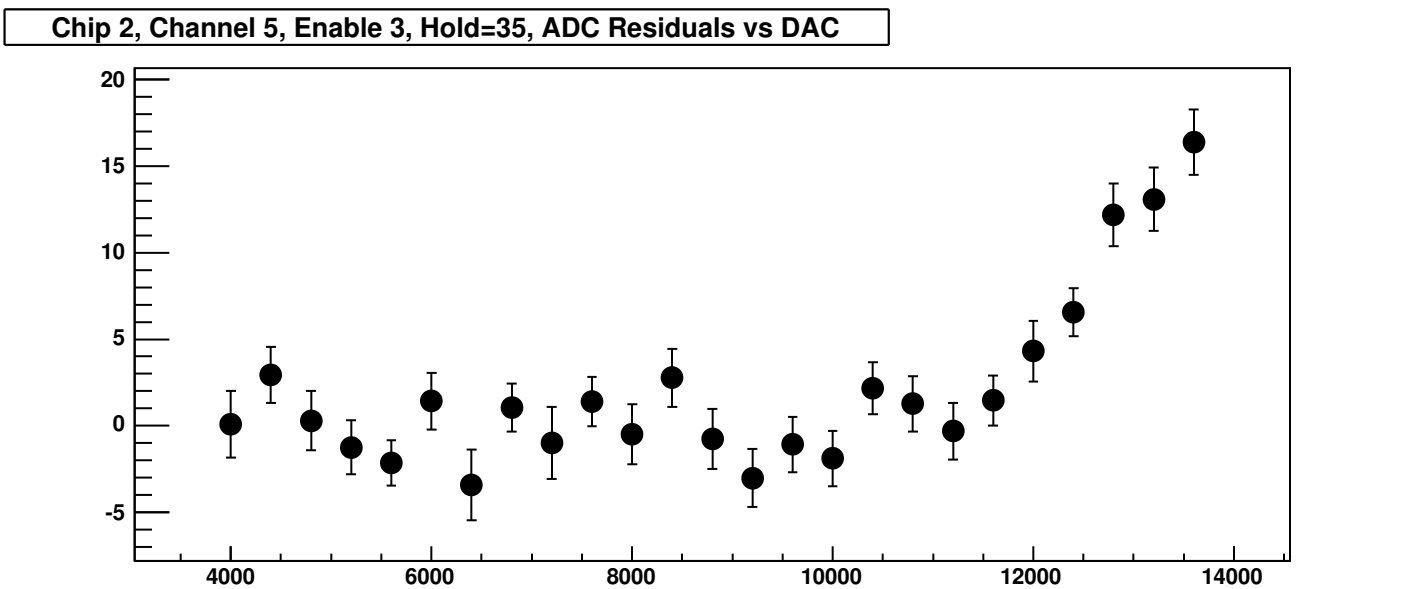

Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.52 / 16
p0 435.1 ± 1.323
p1 0.01555 ± 0.0001734
640
620
600
580
560
540
520
500
4000
6000
8000
10000
12000
14000

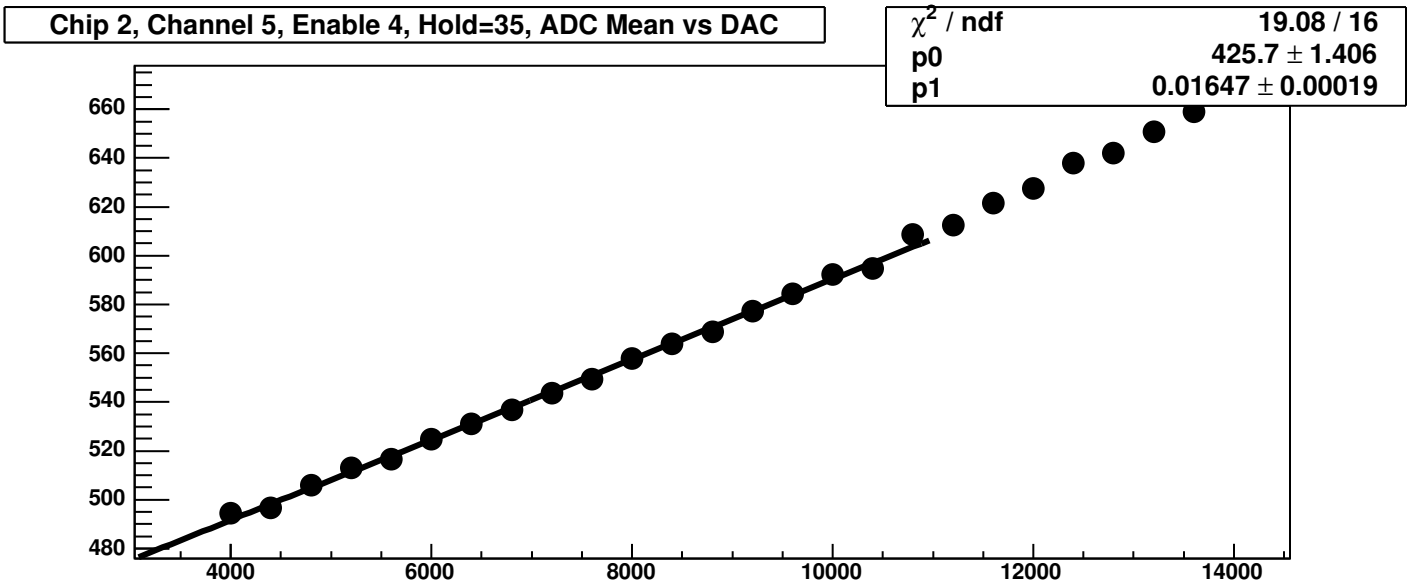

Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

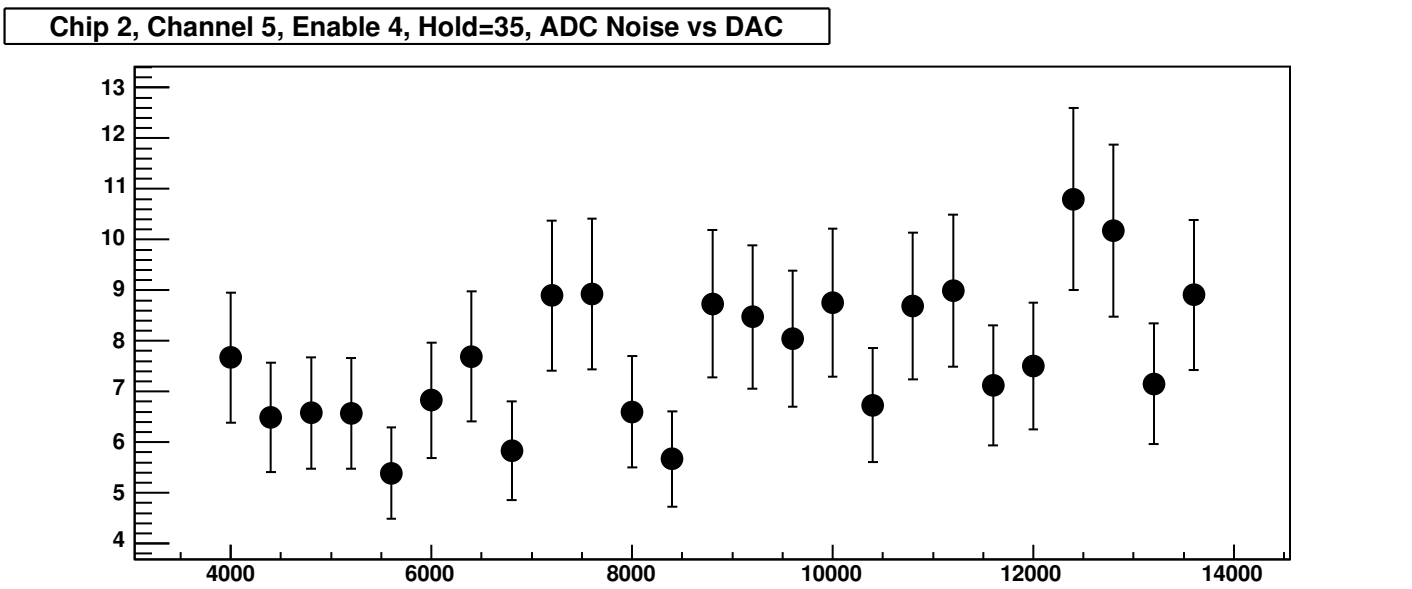

Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

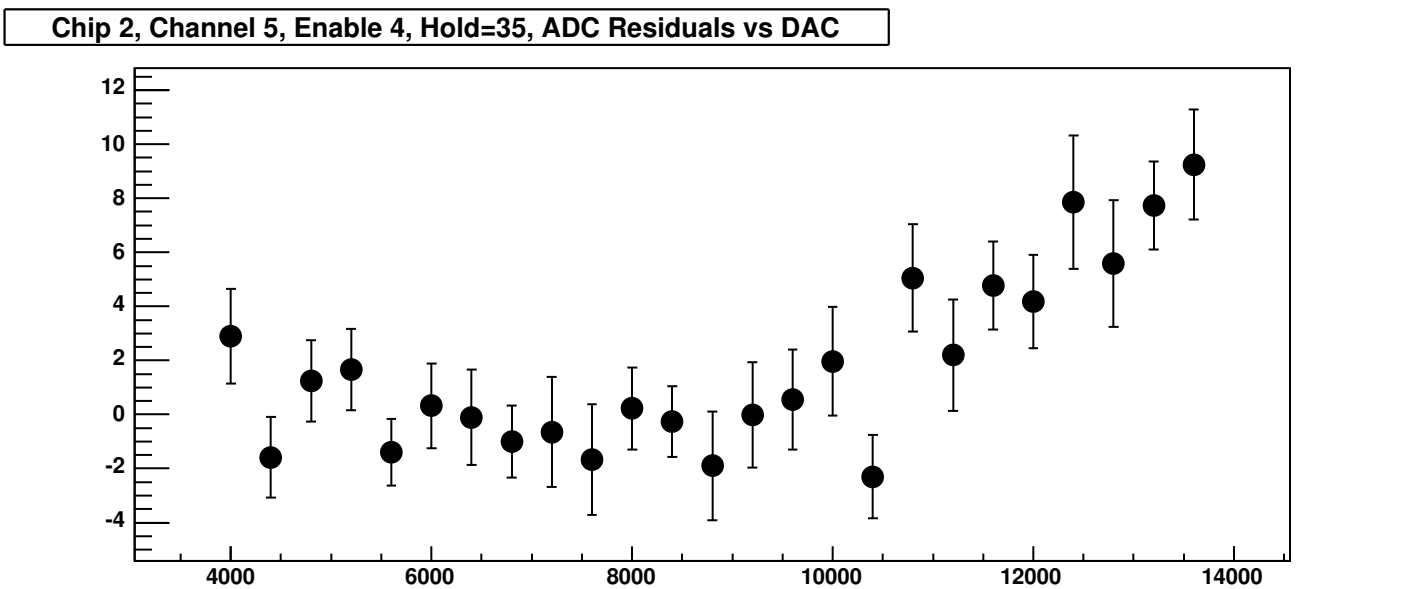

Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.86 / 16
p0 429.3 ± 1.425
p1 0.01685 ± 0.0001845
680
660
640
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

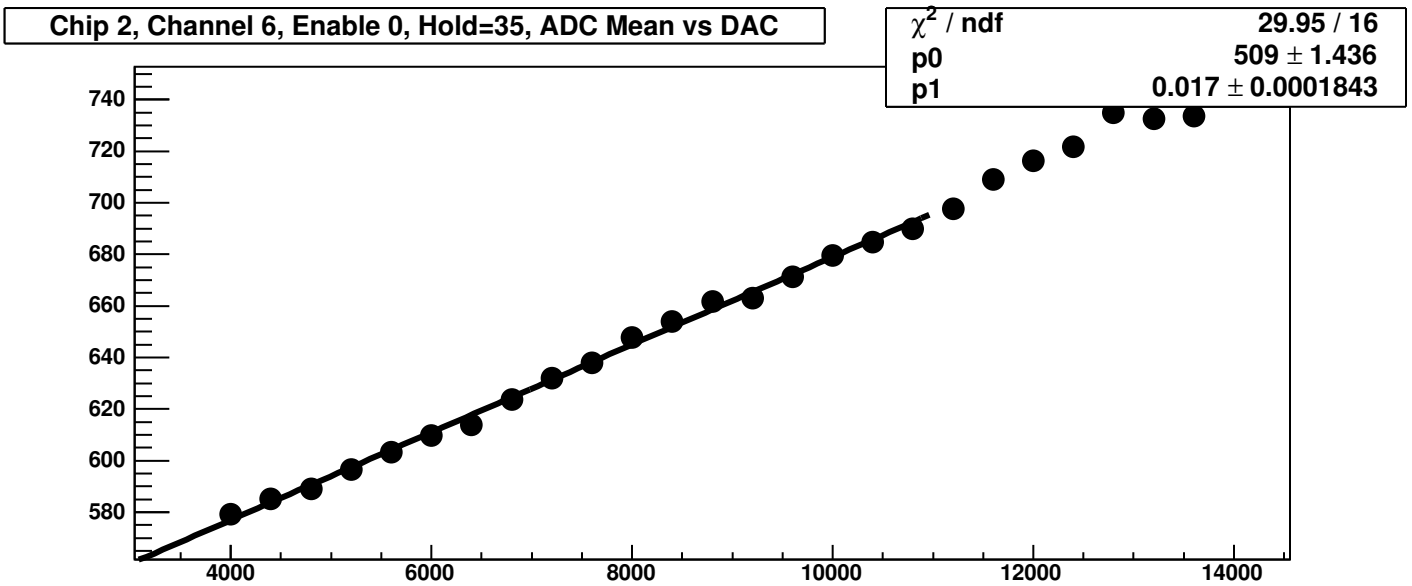

Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

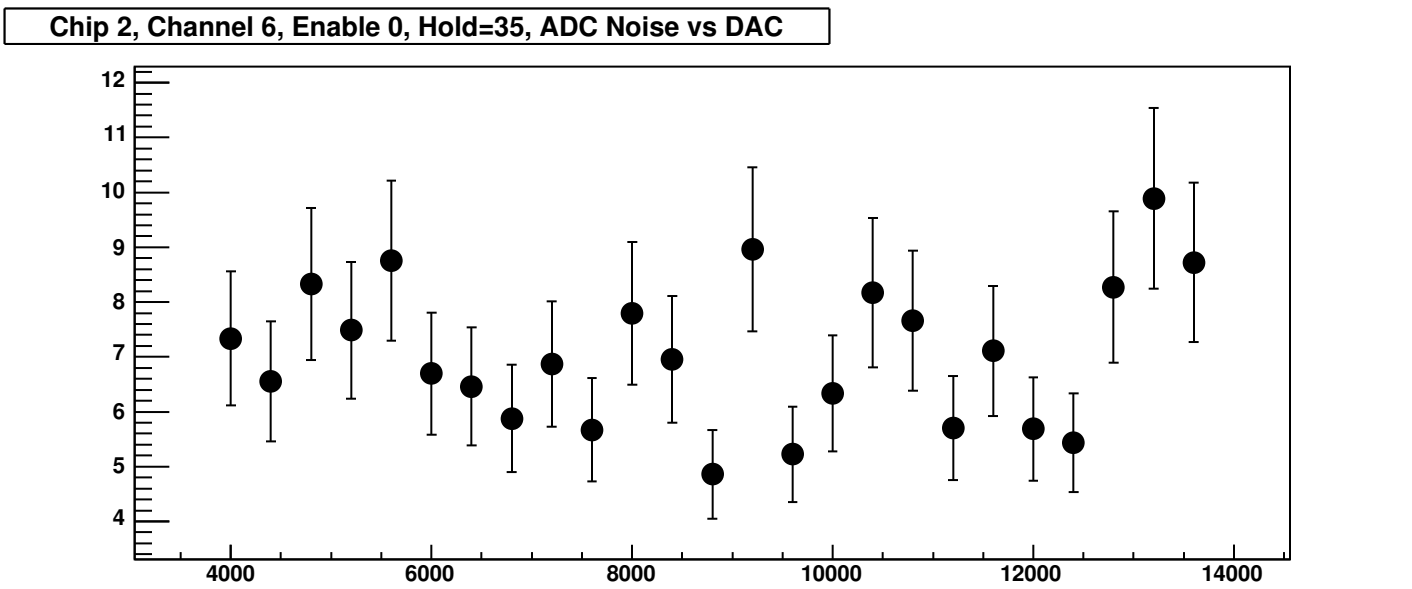

Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

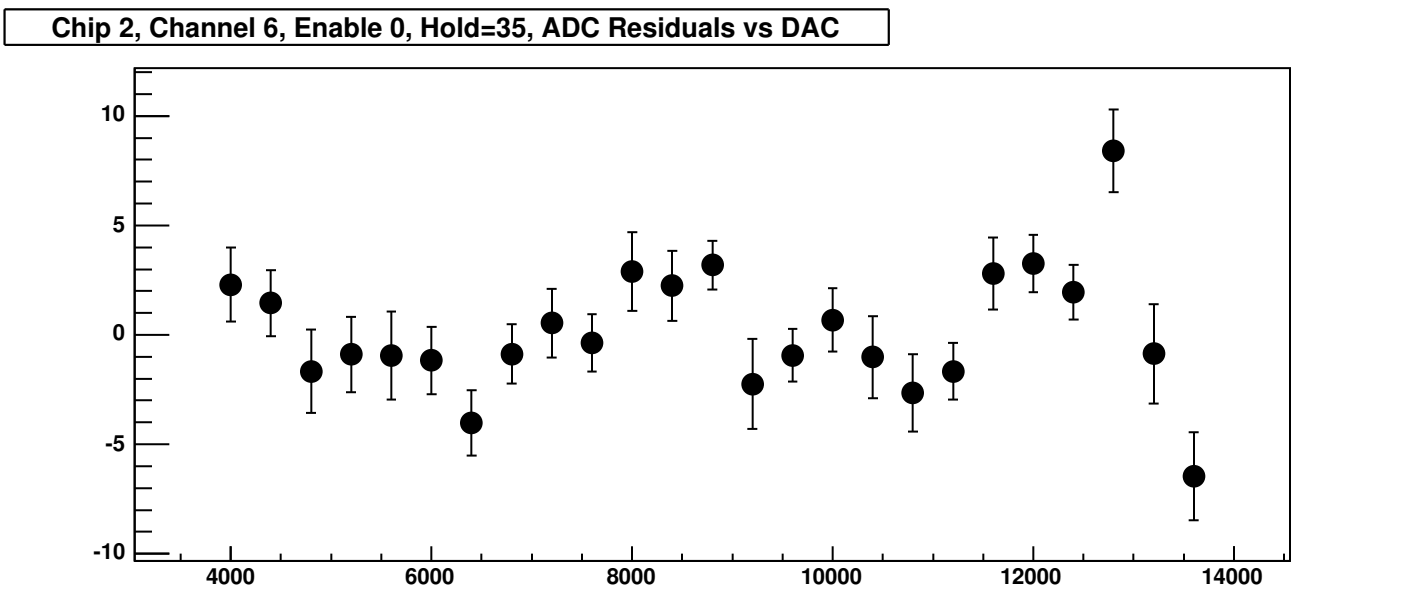

Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.08 / 16
p0 425.7 ± 1.406
p1 0.01647 ± 0.00019
660
640
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000


Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

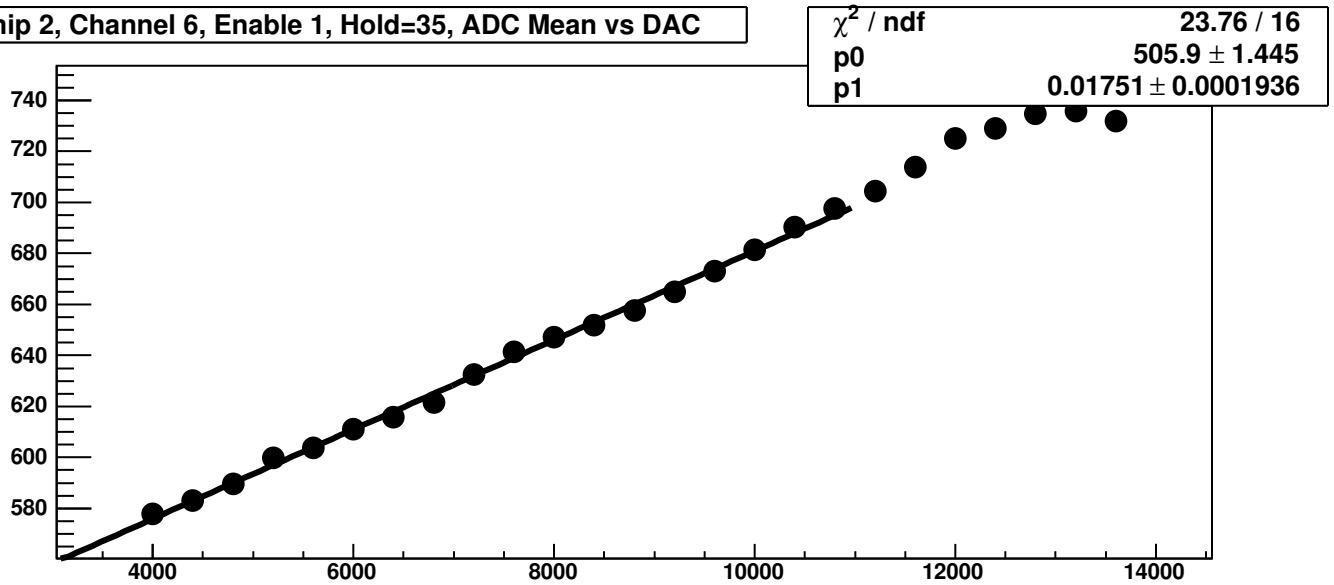

Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 29.95 / 16
p0 509 ± 1.436
p1 0.017 ± 0.0001843
740
720
700
680
660
640
620
600
580
4000
6000
8000
10000
12000
14000

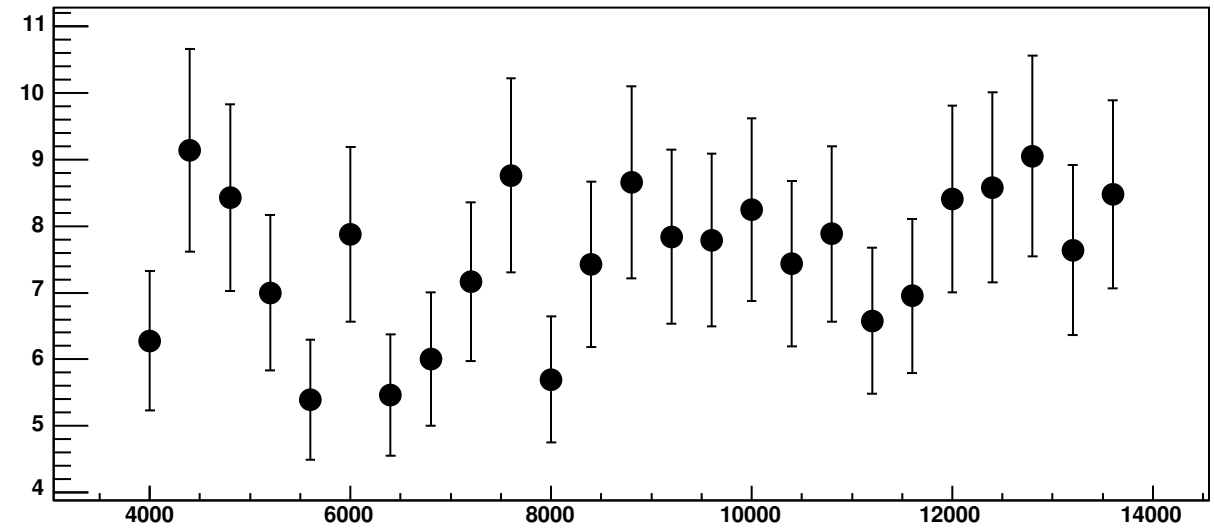

Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

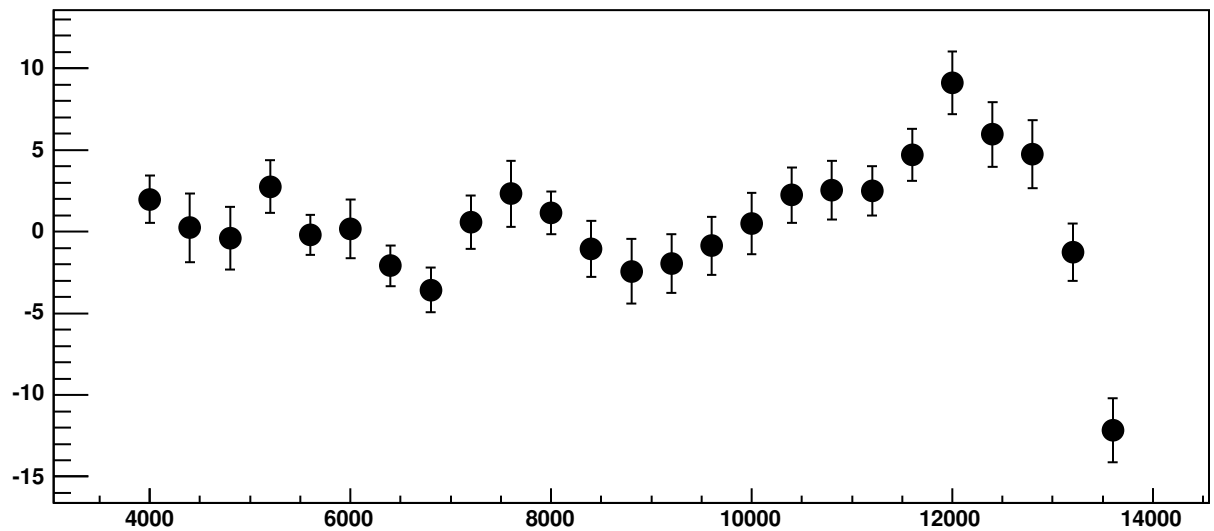
Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

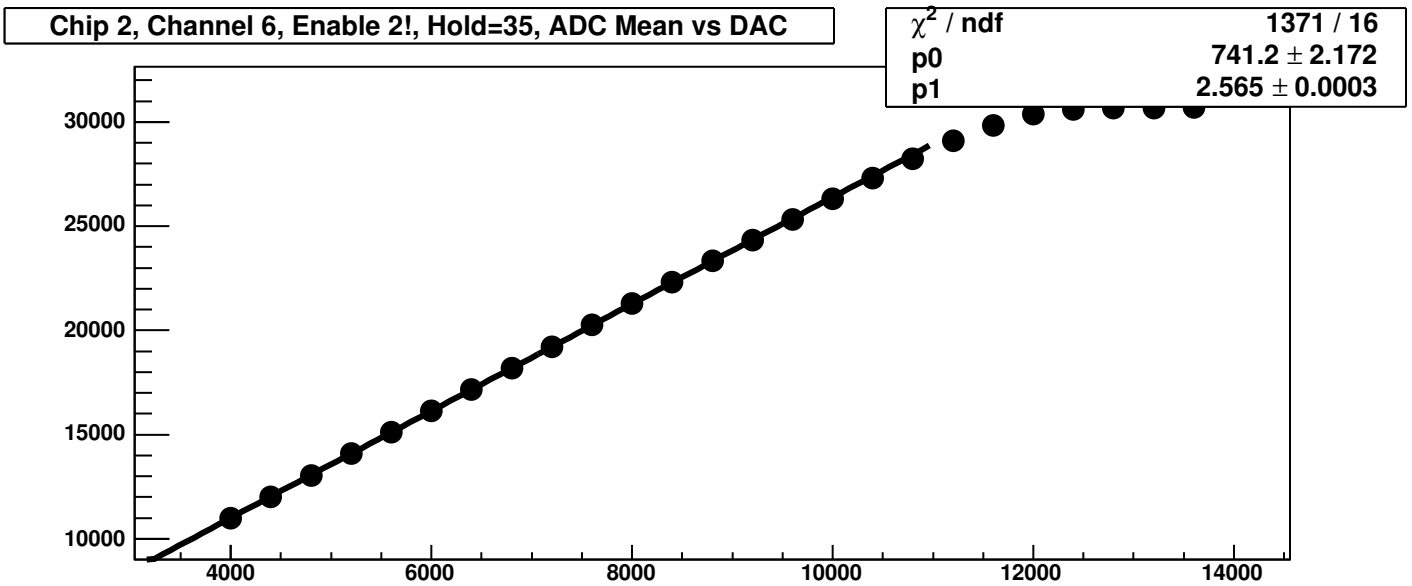


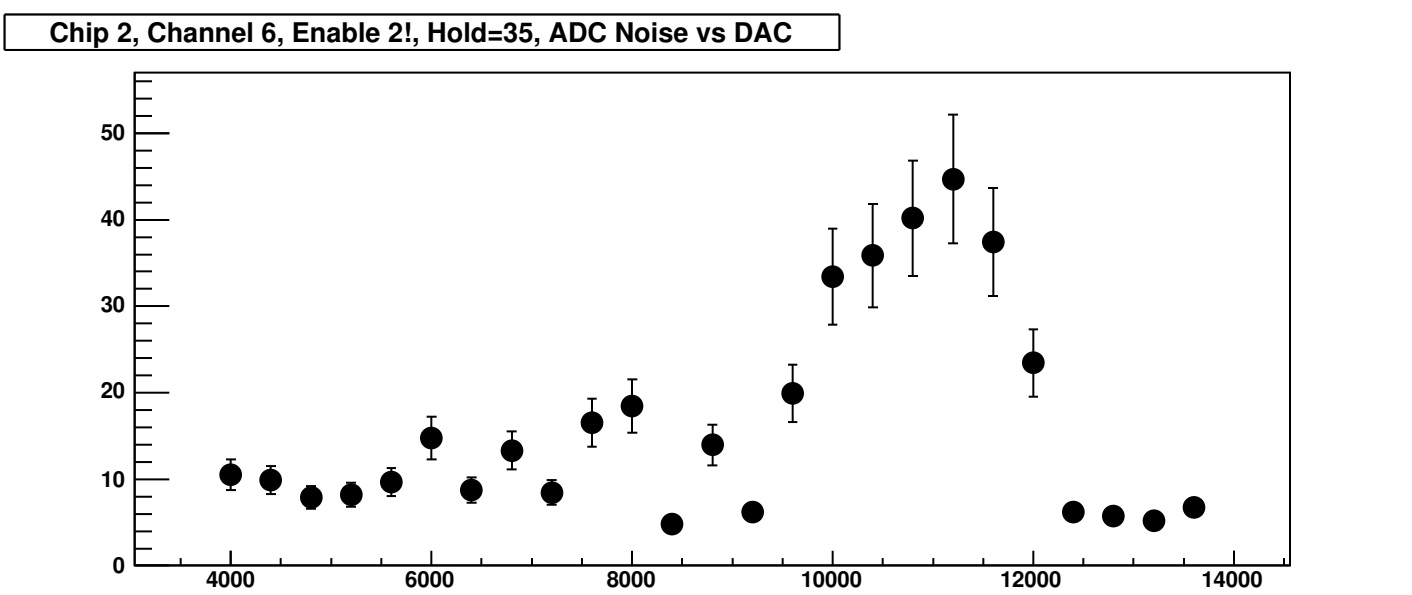
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

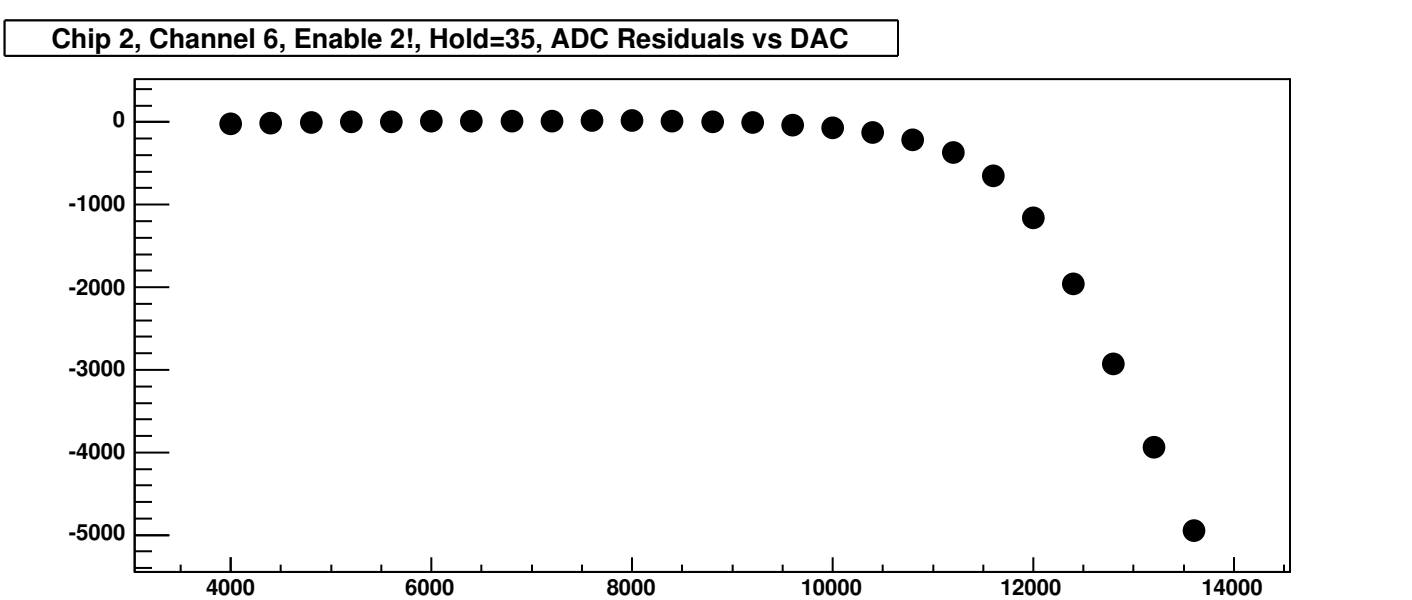


Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

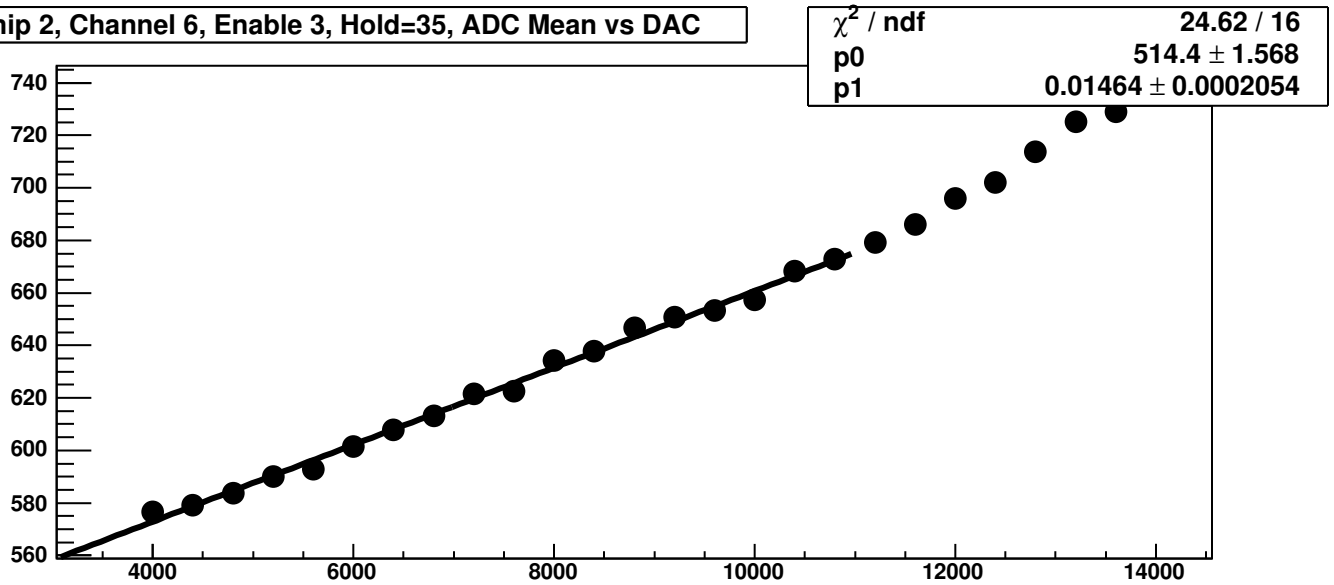



Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1371 / 16
p0 741.2 ± 2.172
p1 2.565 ± 0.0003
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

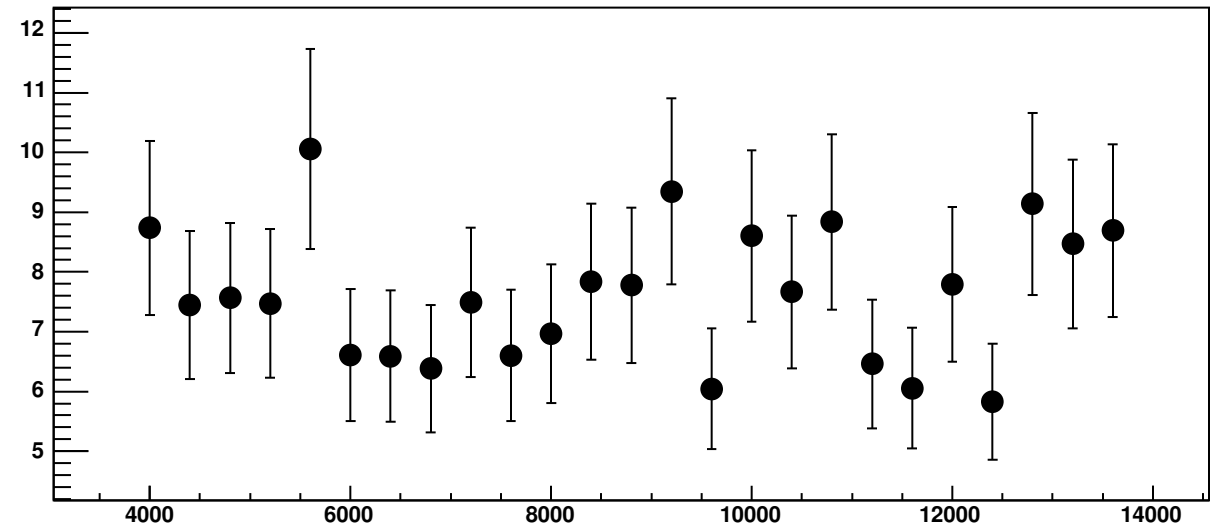

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

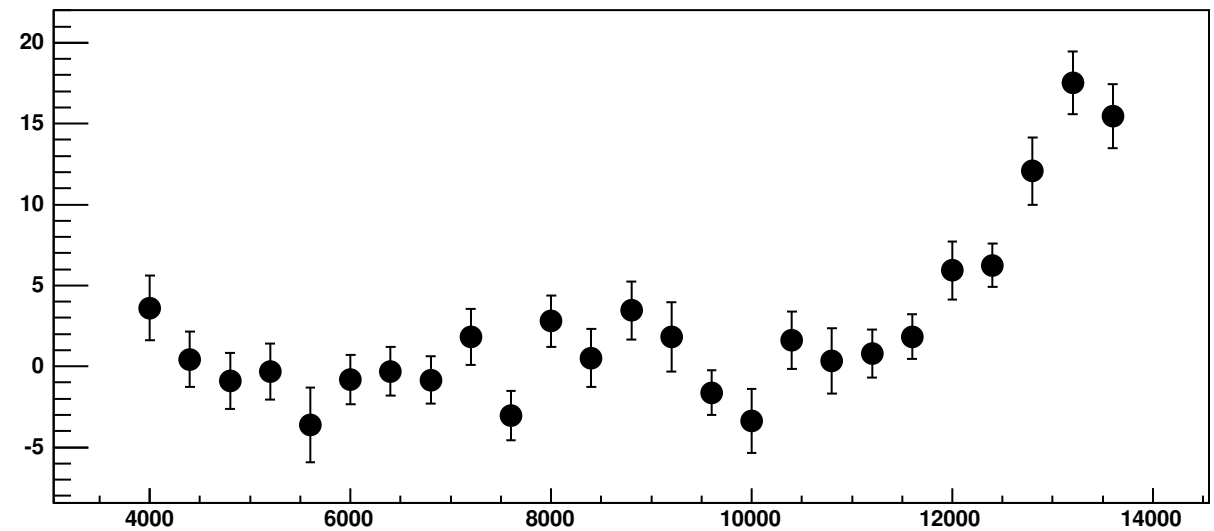
Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

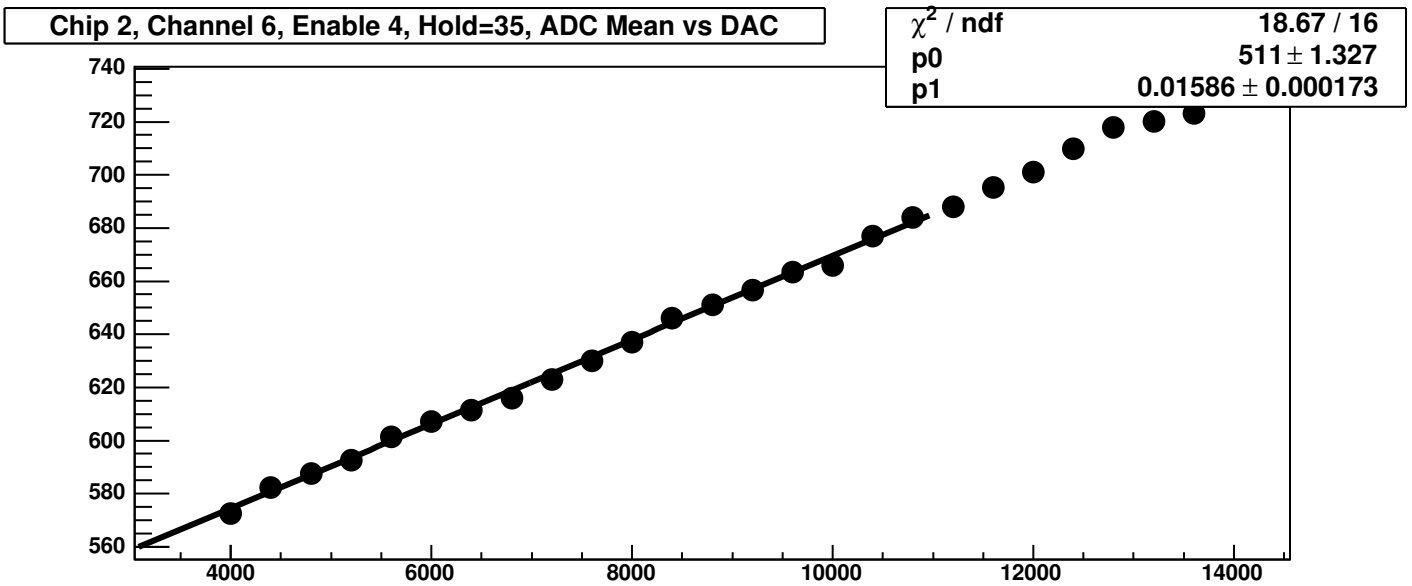


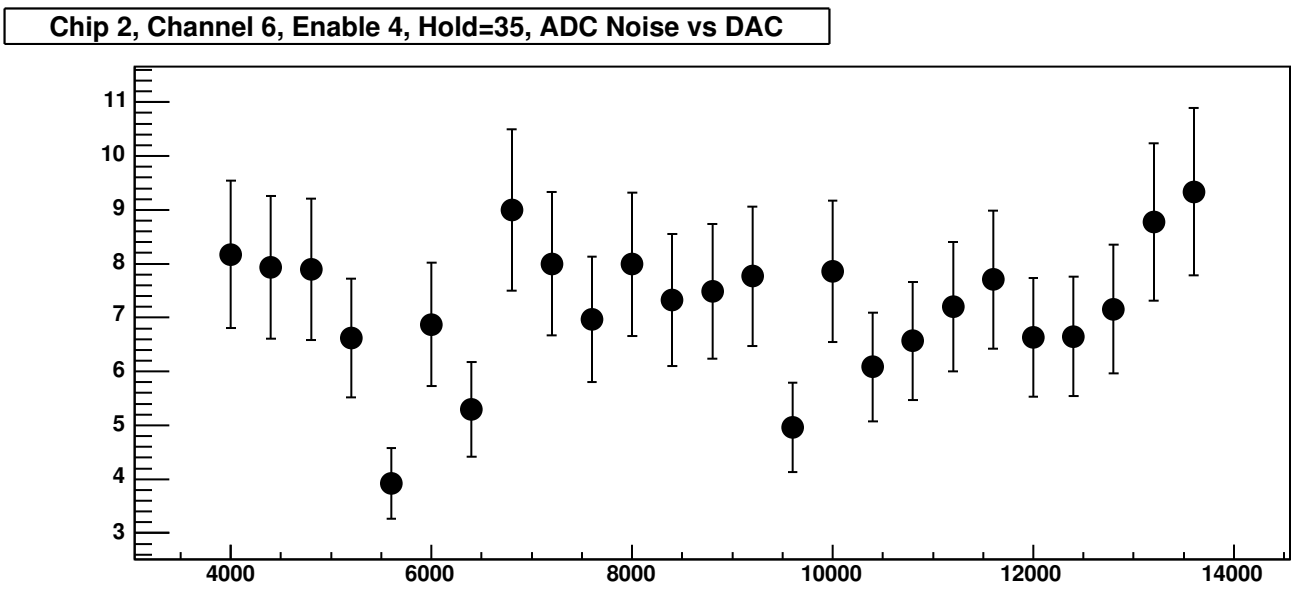
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

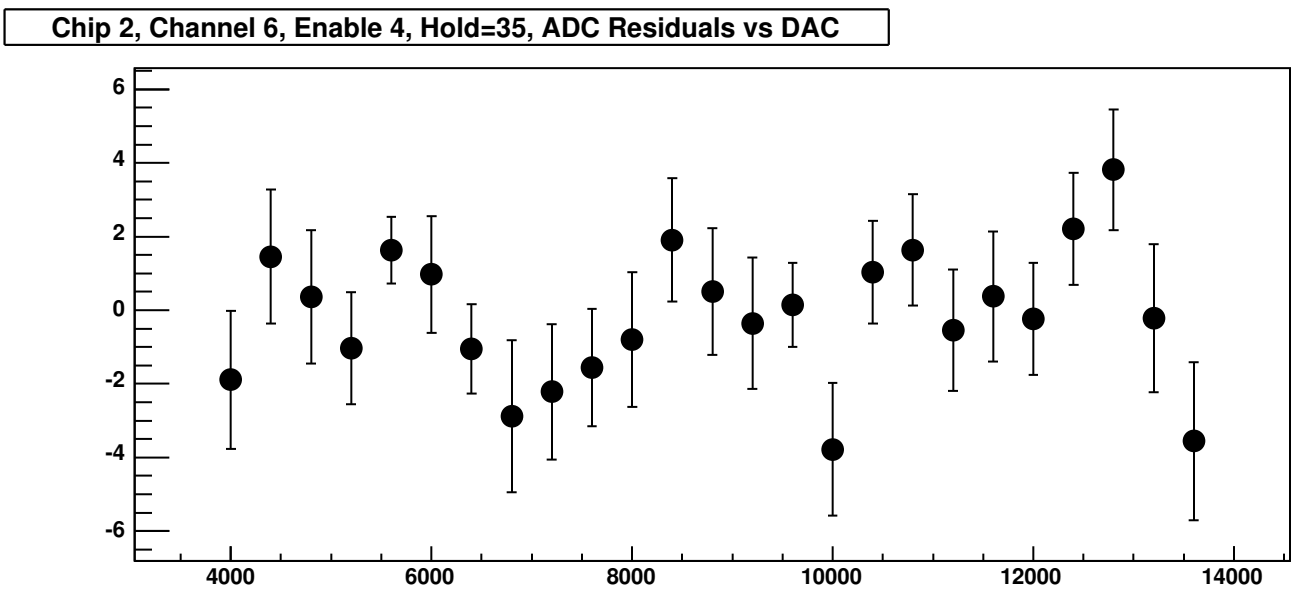


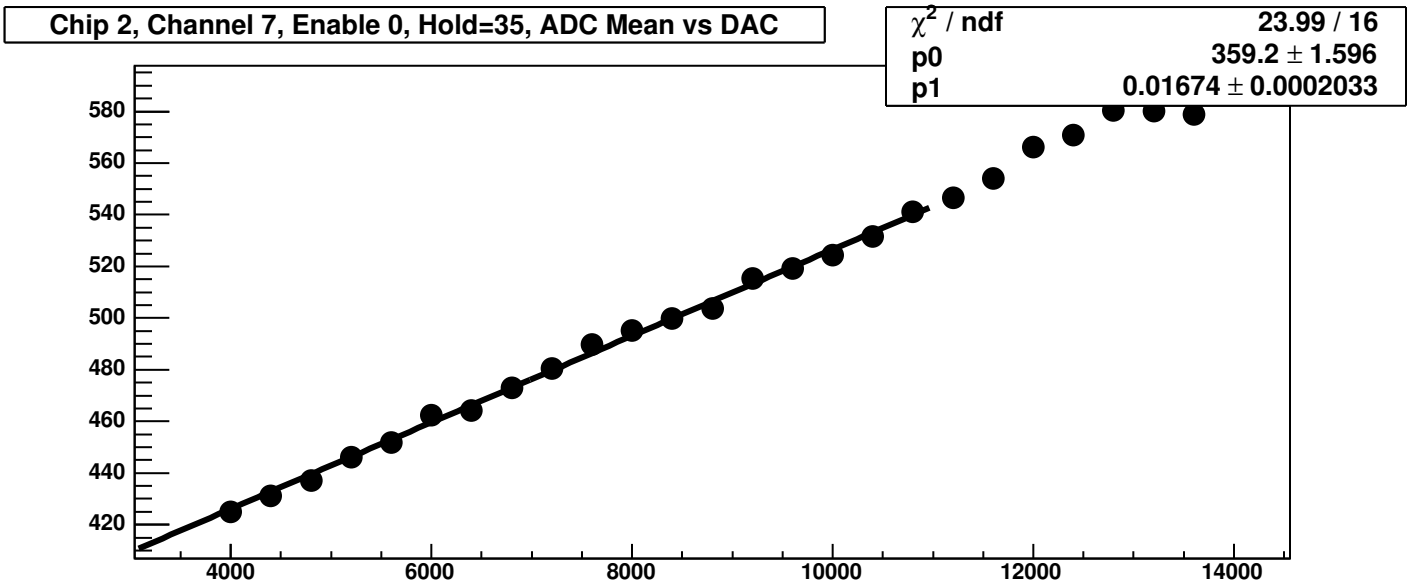
Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

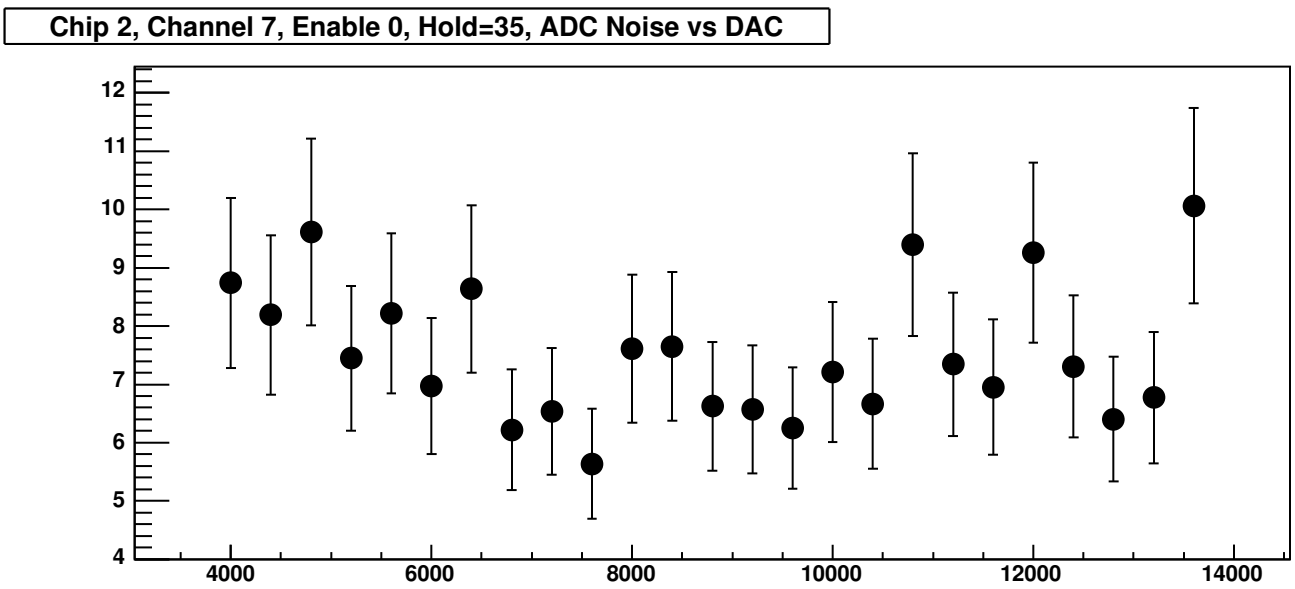


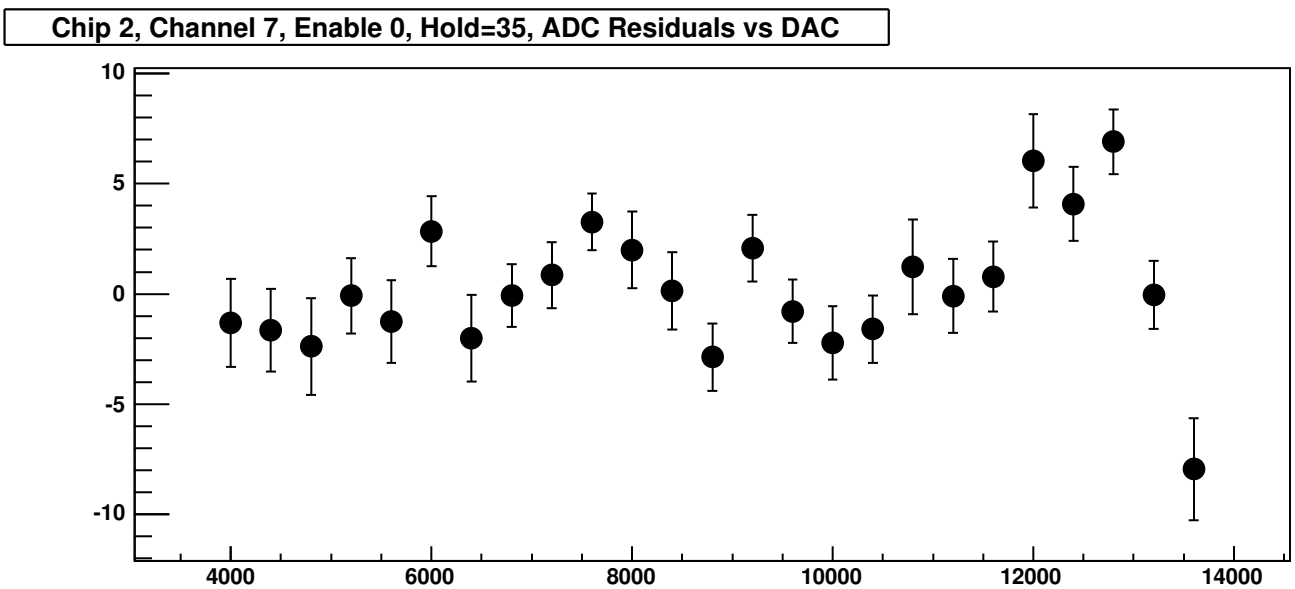

Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.67 / 16
p0 511 ± 1.327
p1 0.01586 ± 0.000173
740
720
700
680
660
640
620
600
580
560
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

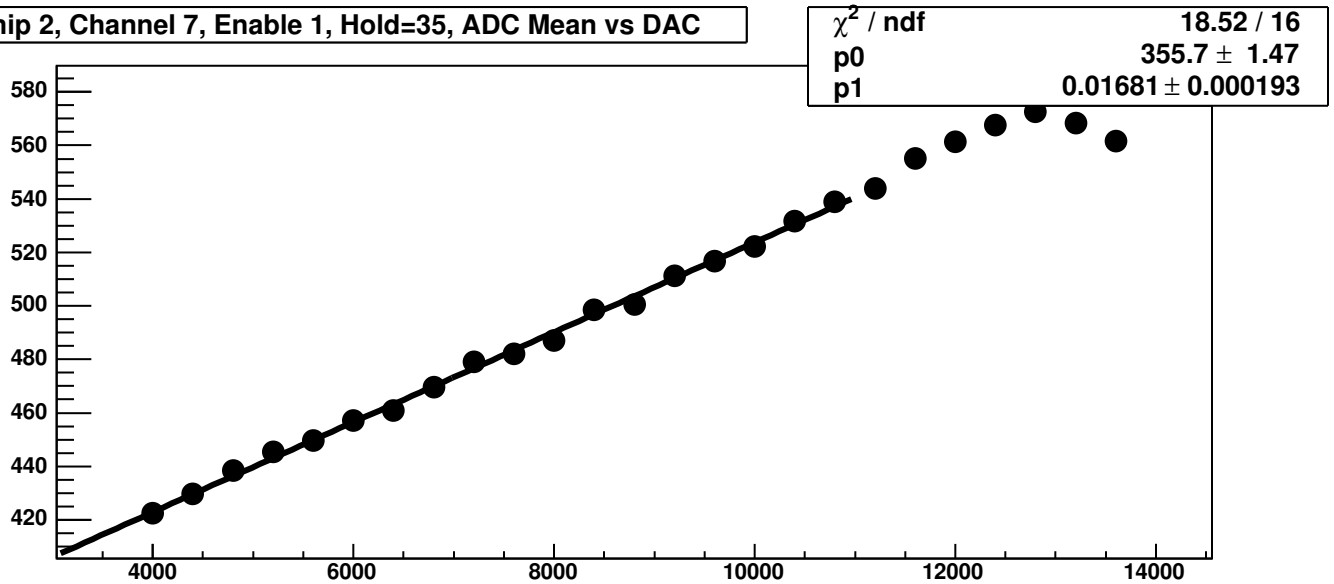

Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 23.99 / 16
p0 359.2 ± 1.596
p1 0.01674 ± 0.0002033
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

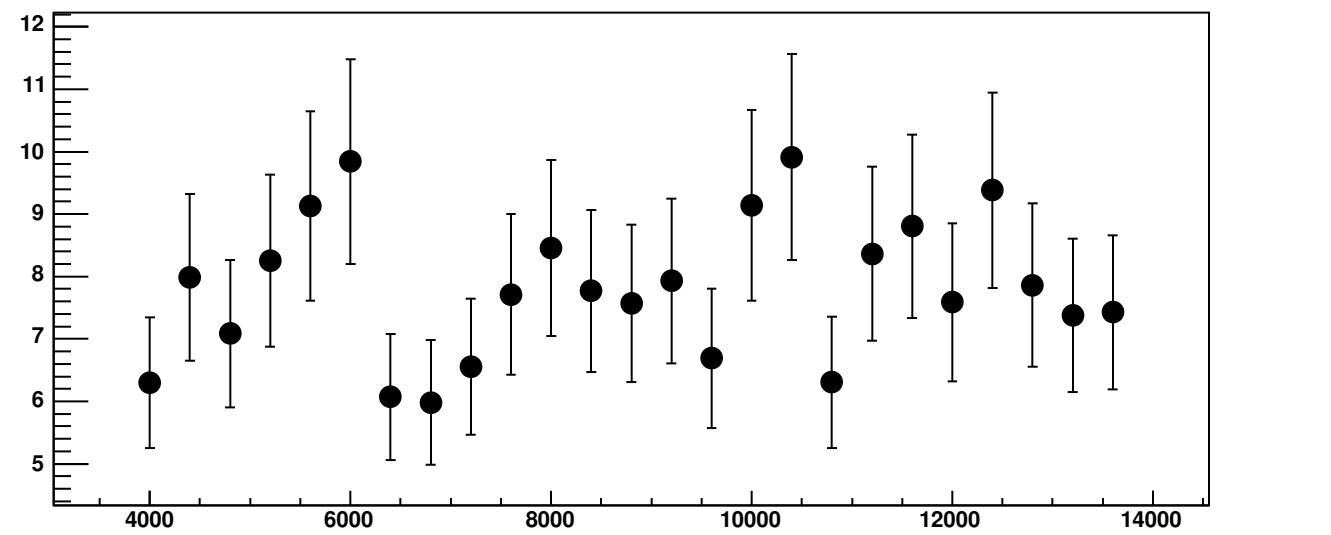

Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

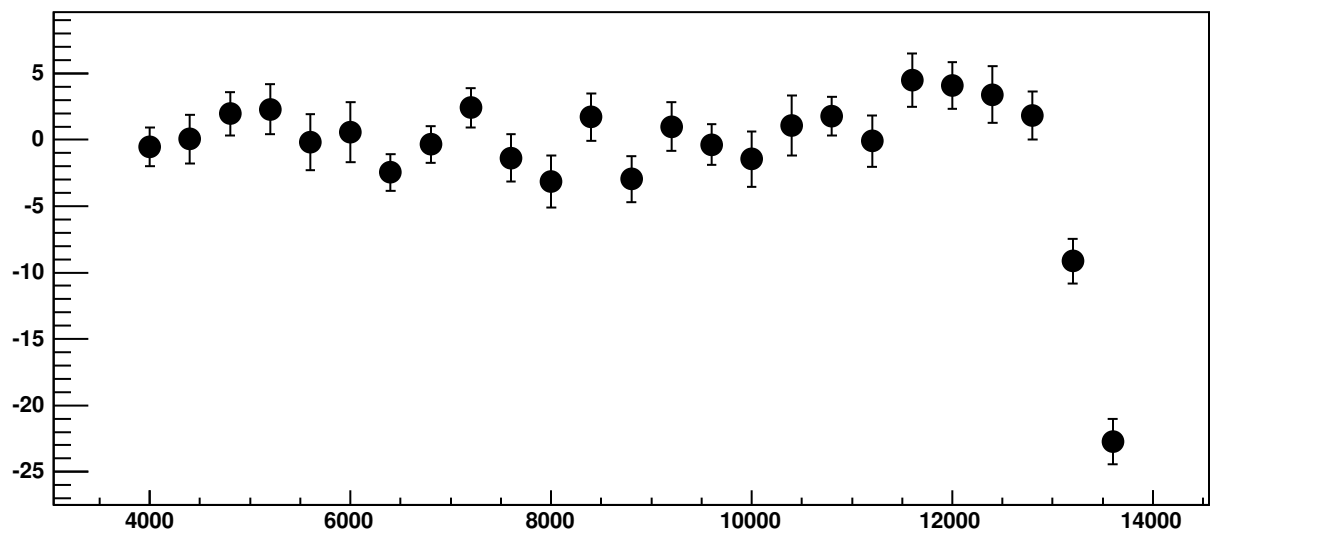
Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



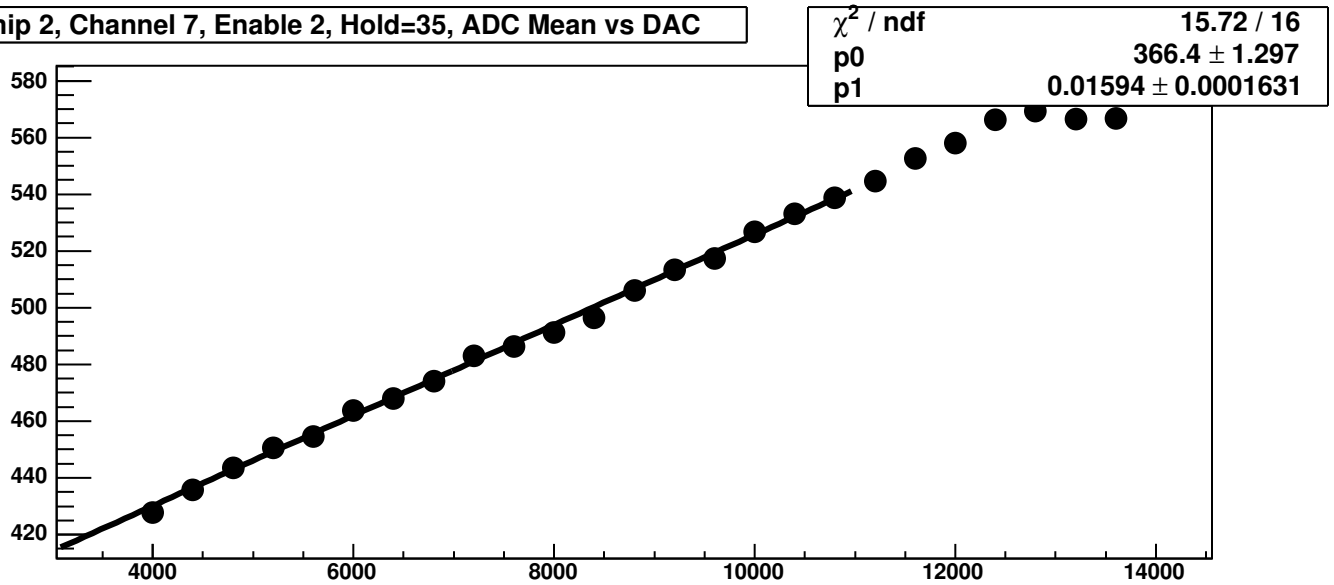
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



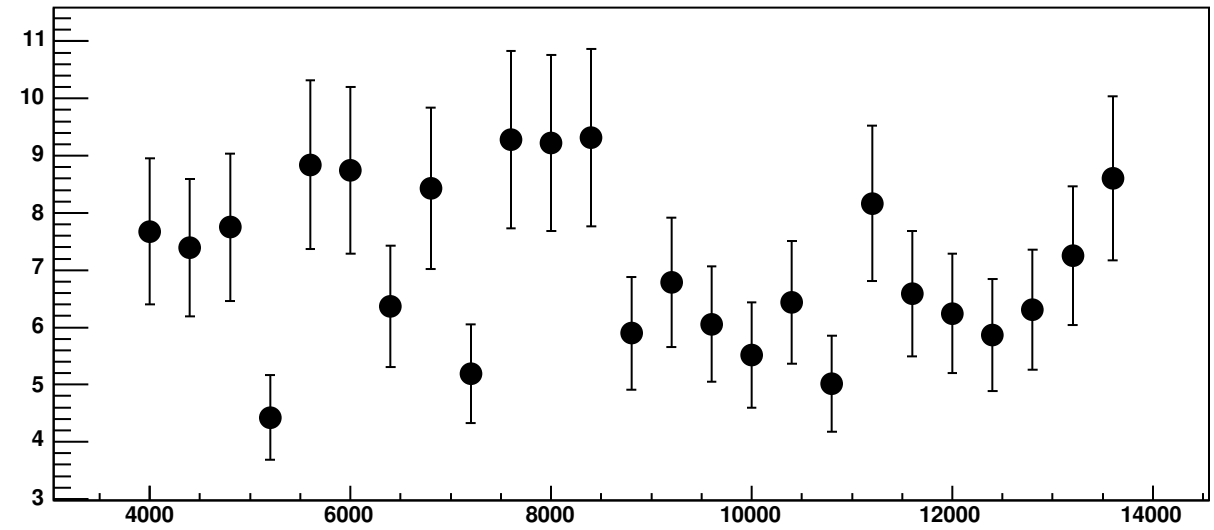
Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



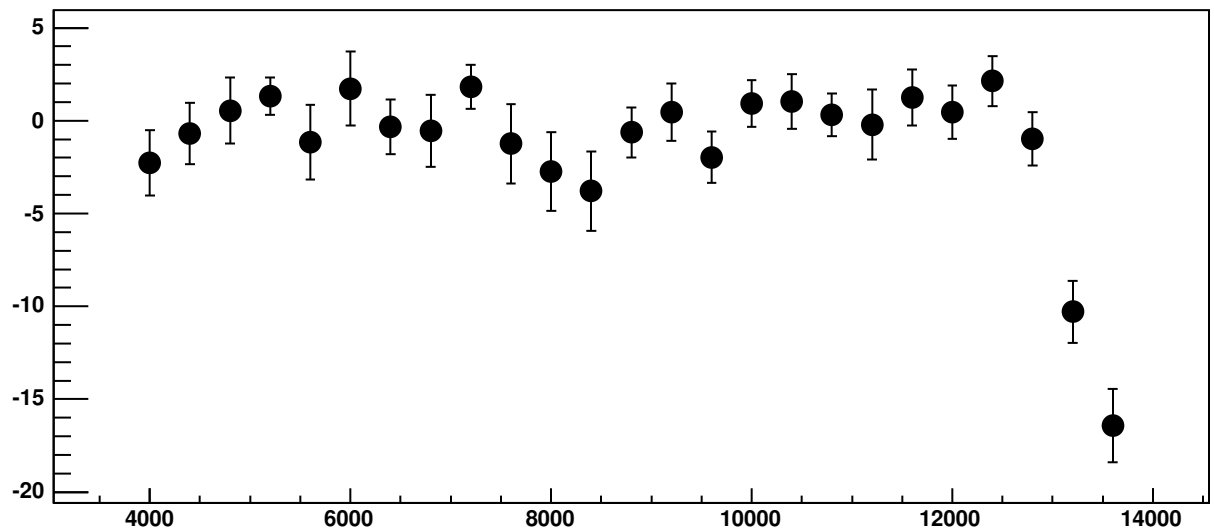
Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

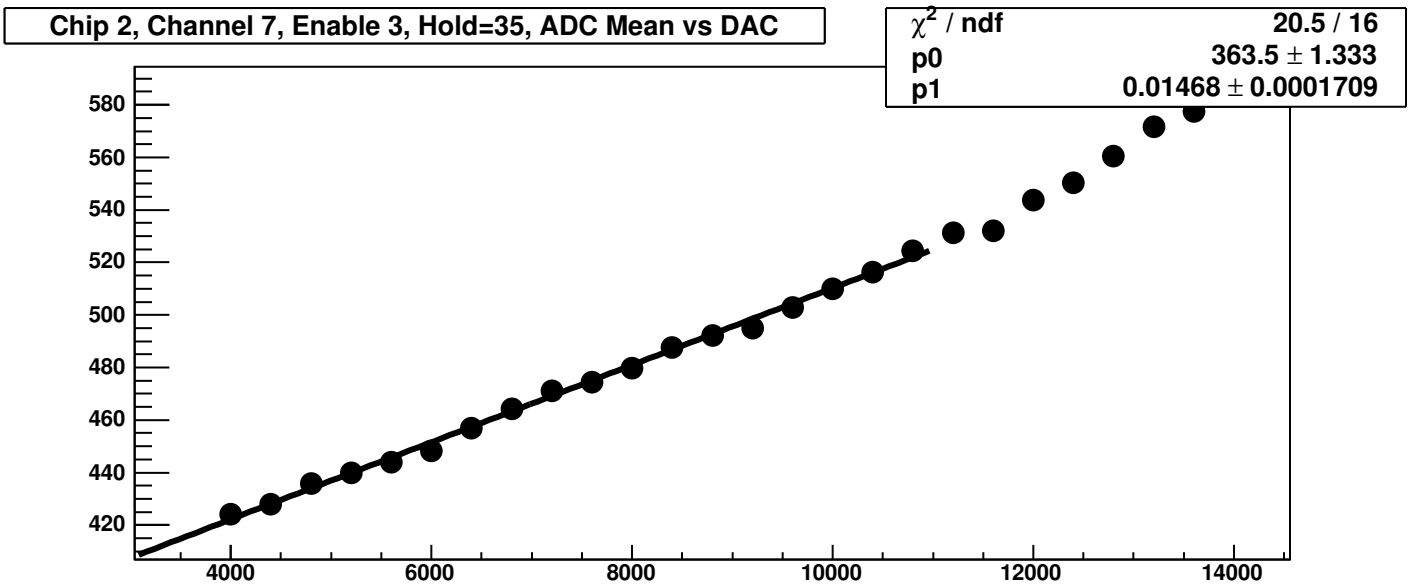


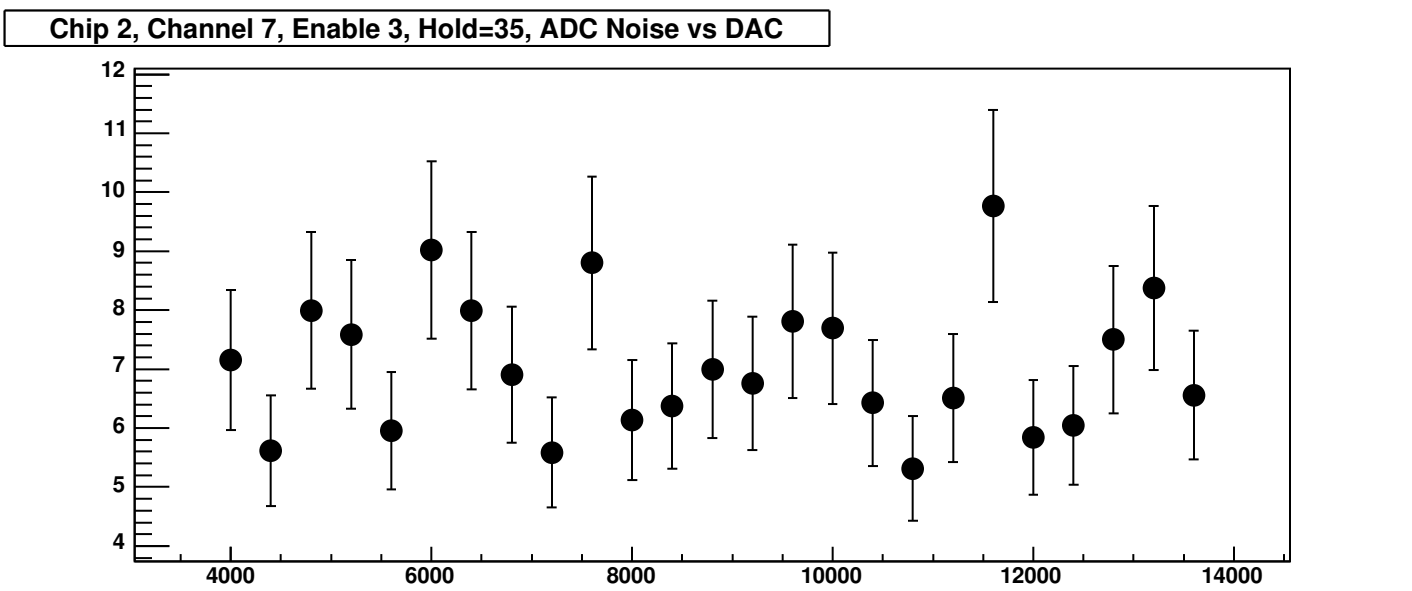
Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC

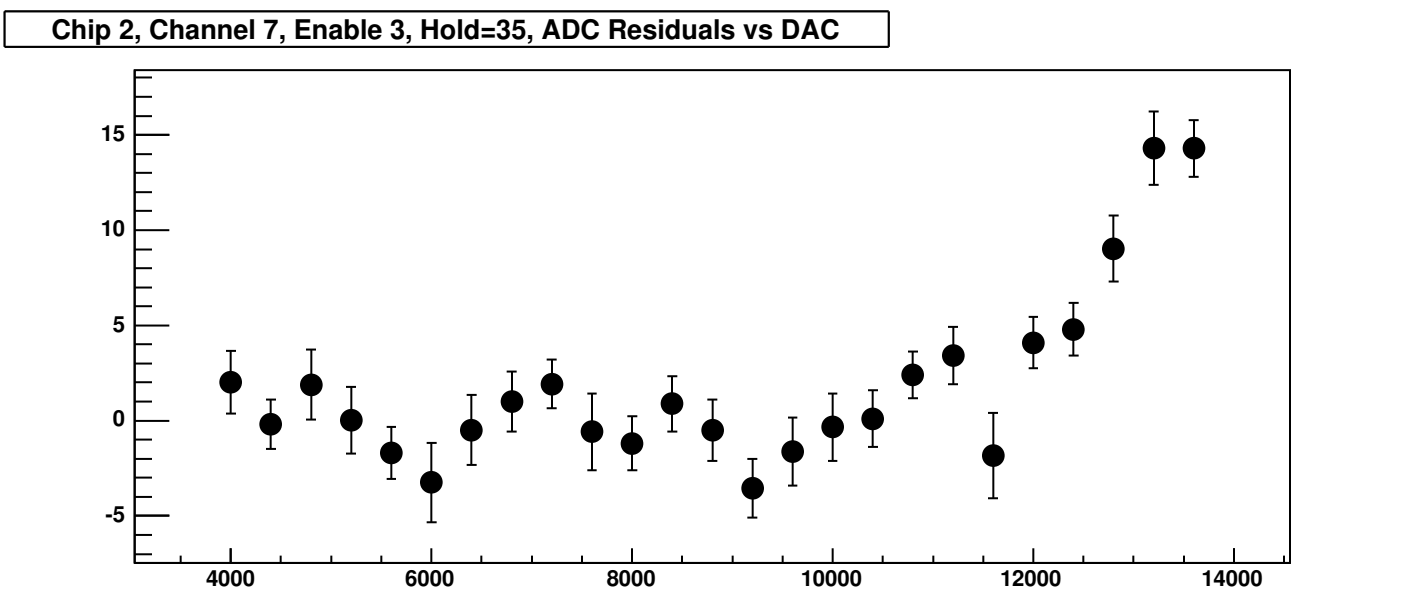


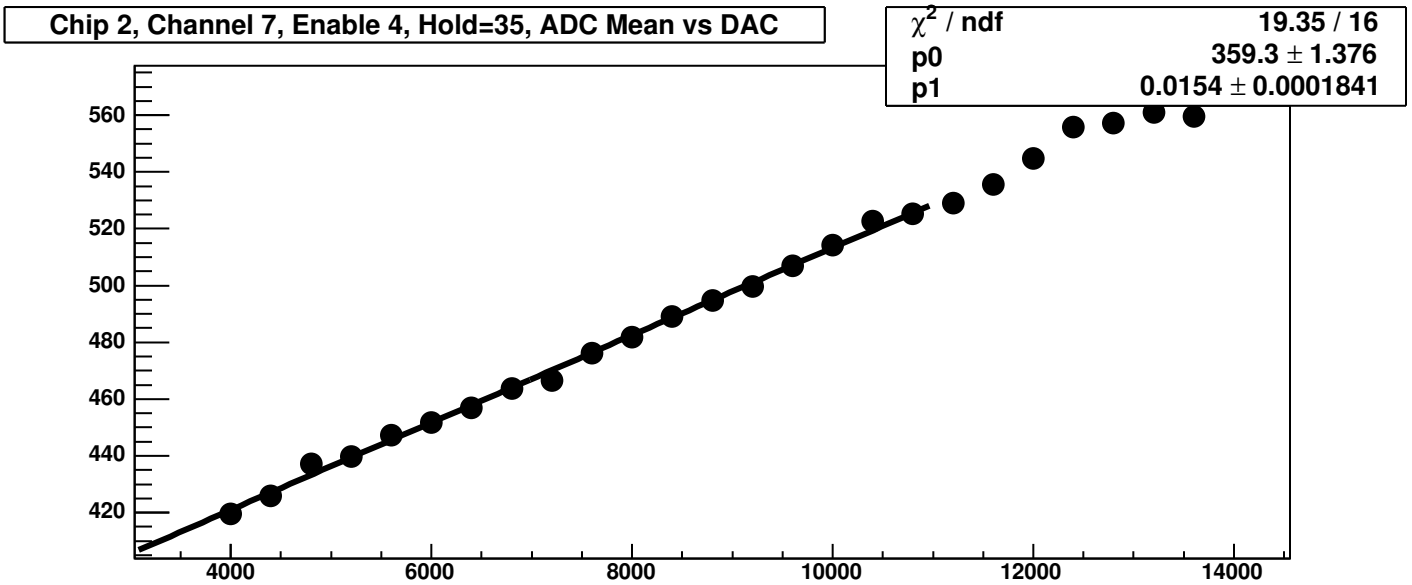
Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

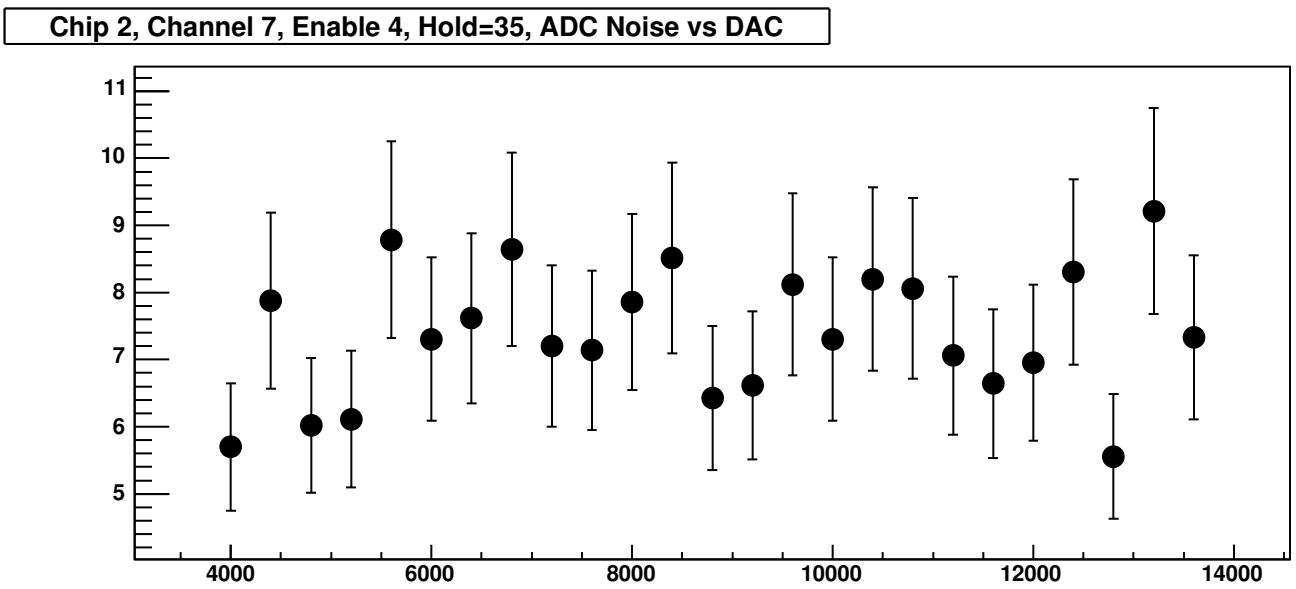


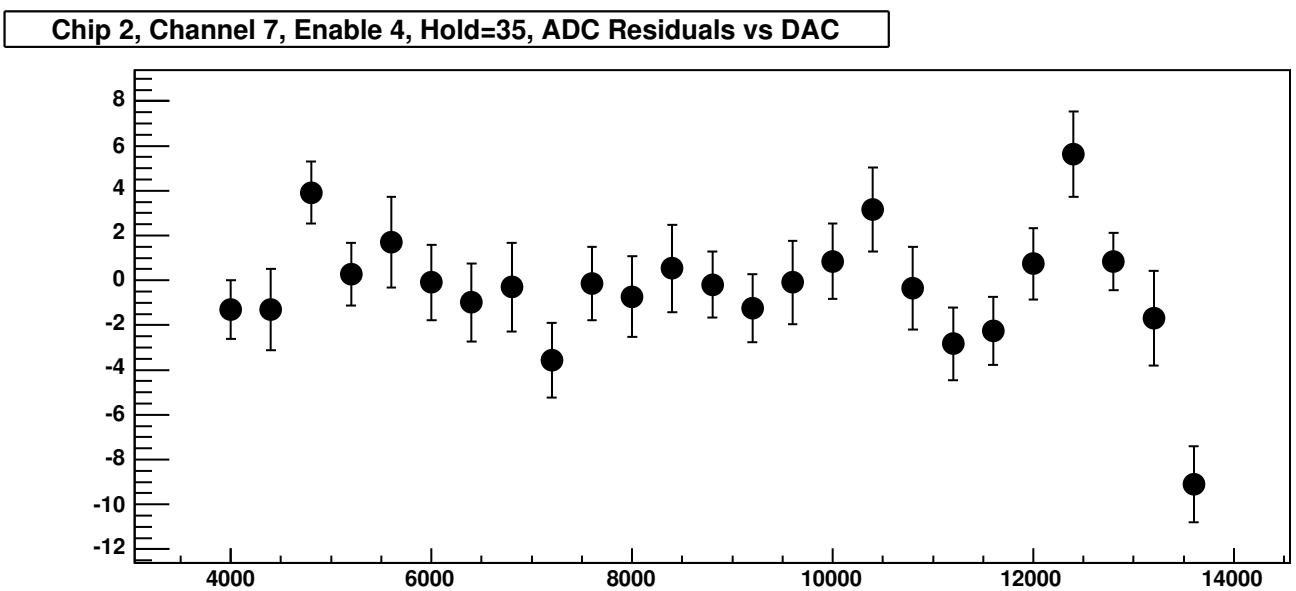

Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.5 / 16
p0 363.5 ± 1.333
p1 0.01468 ± 0.0001709
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

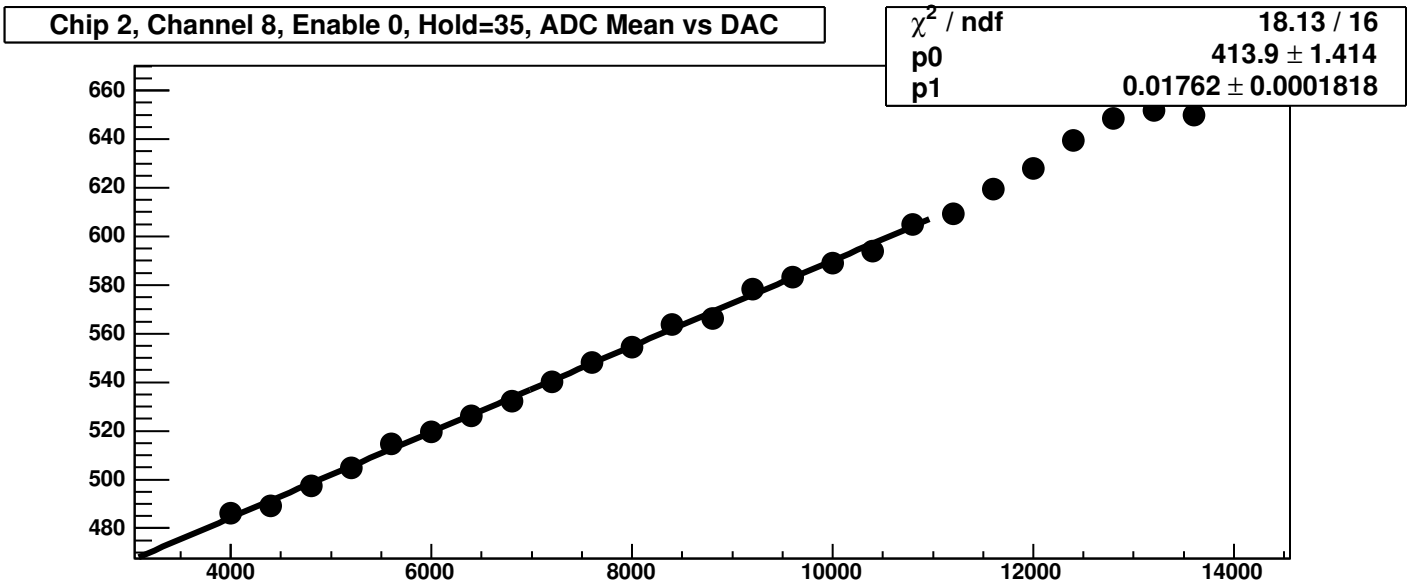

Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

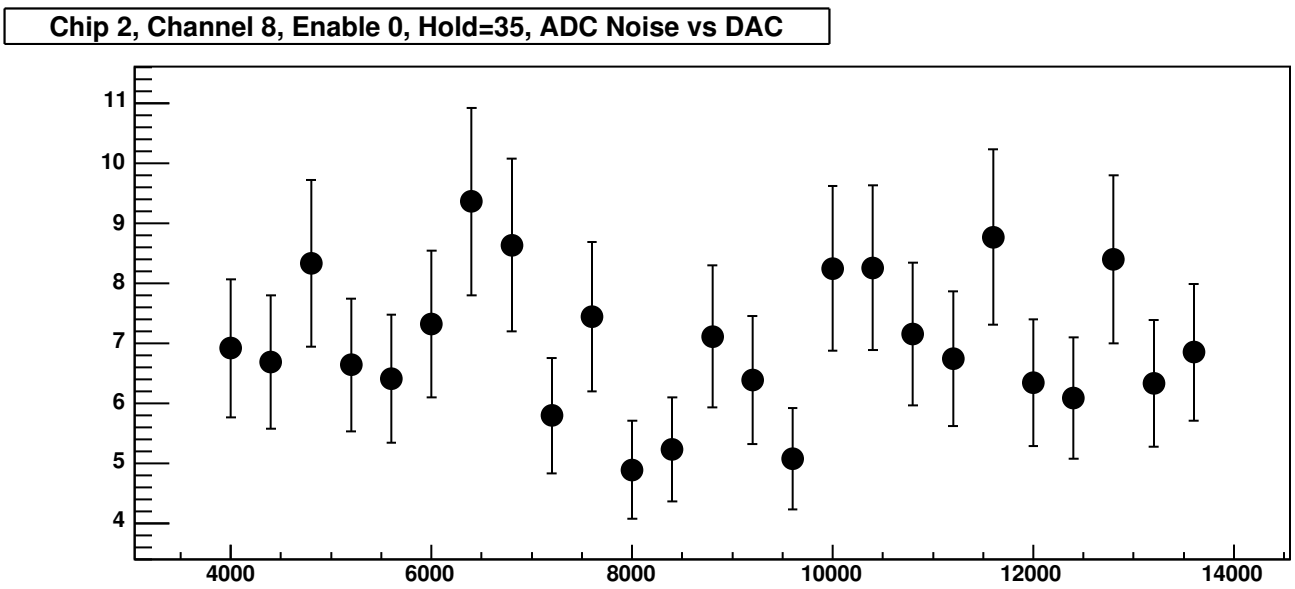

Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

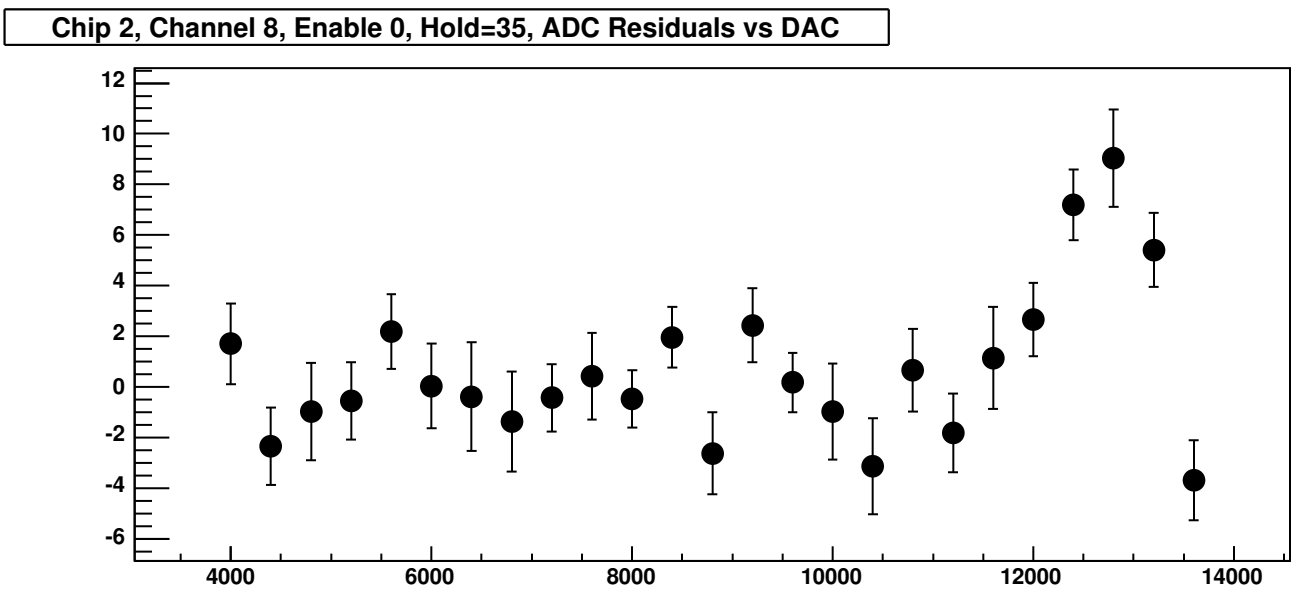

Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.35 / 16
p0 359.3 ± 1.376
p1 0.0154 ± 0.0001841
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

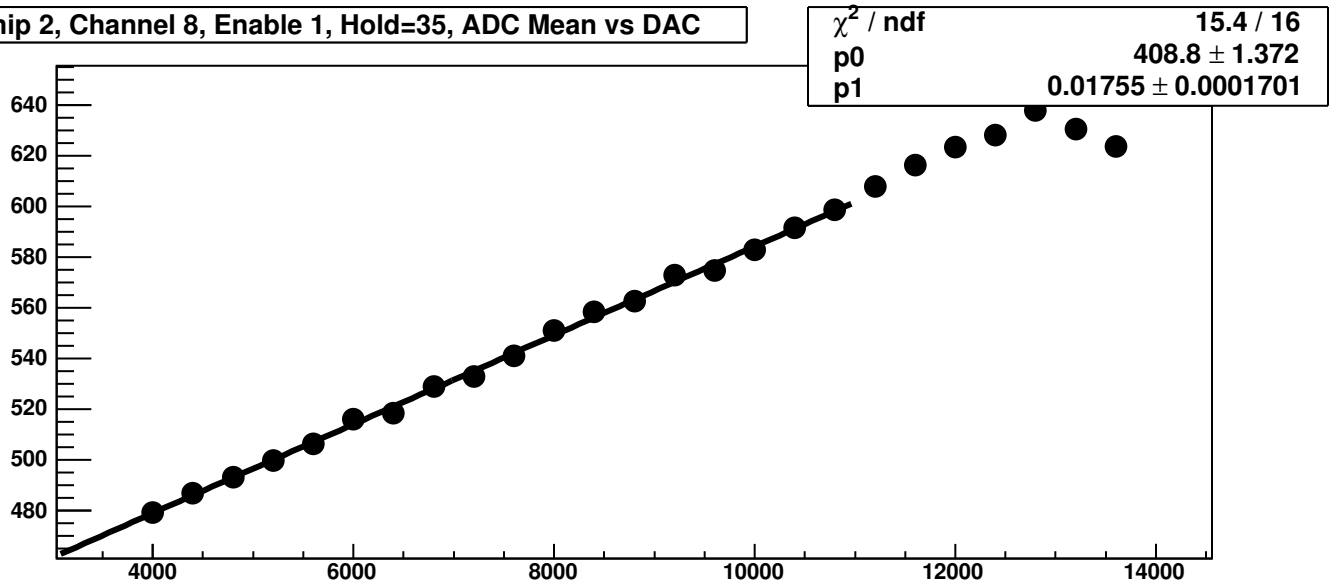

Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18.13 / 16
p0 413.9 ± 1.414
p1 0.01762 ± 0.0001818
660
640
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

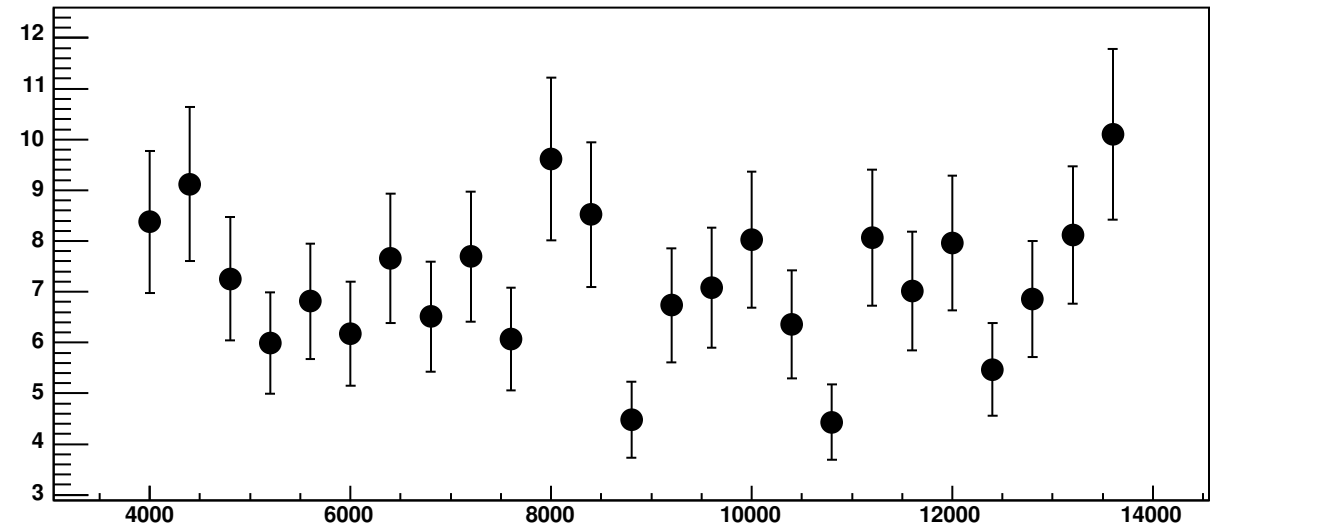

Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

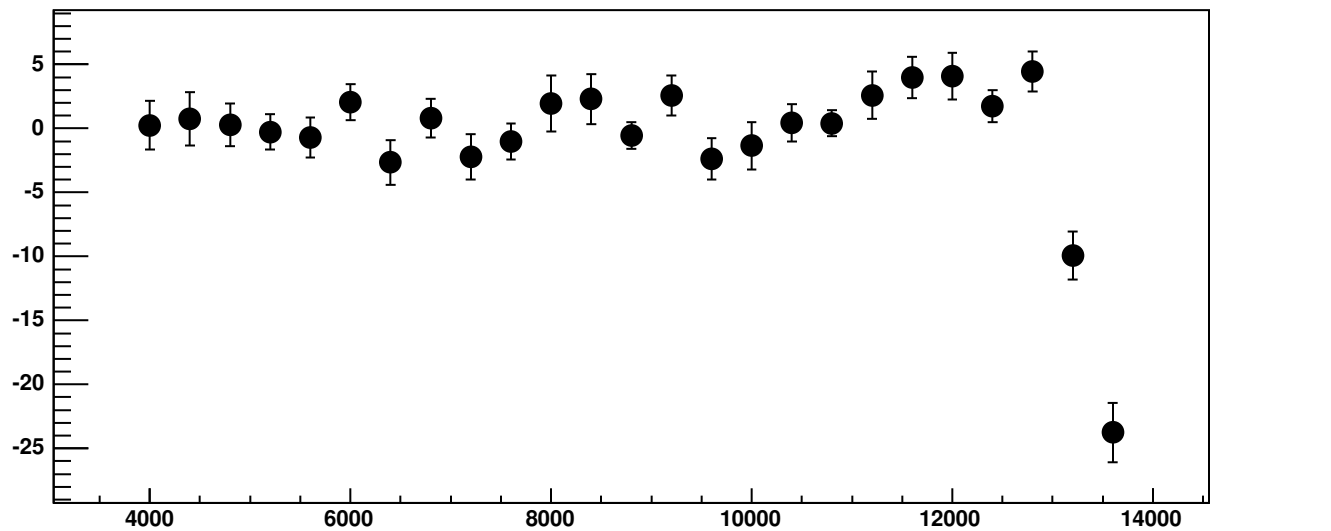
Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

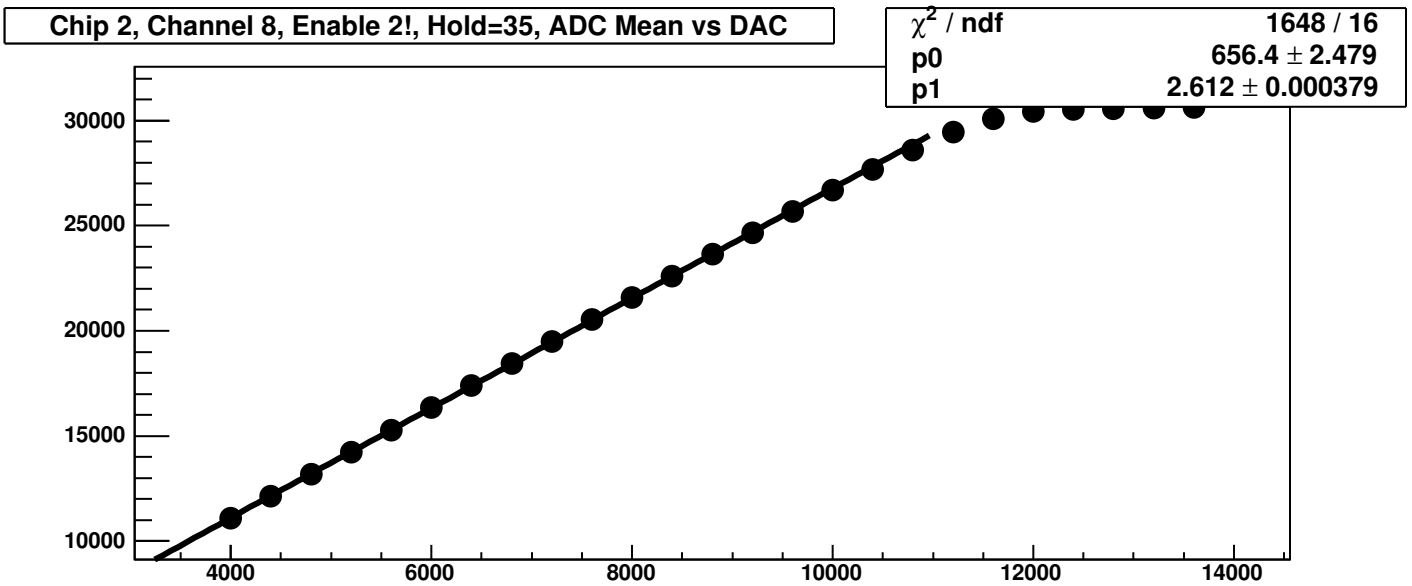


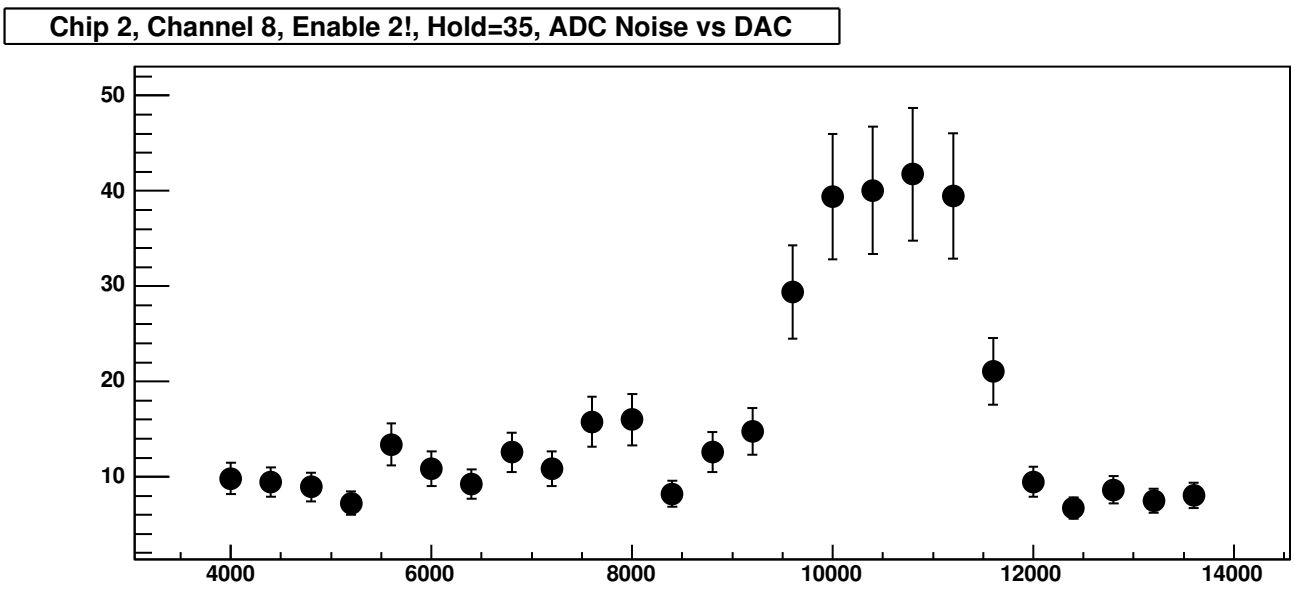
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

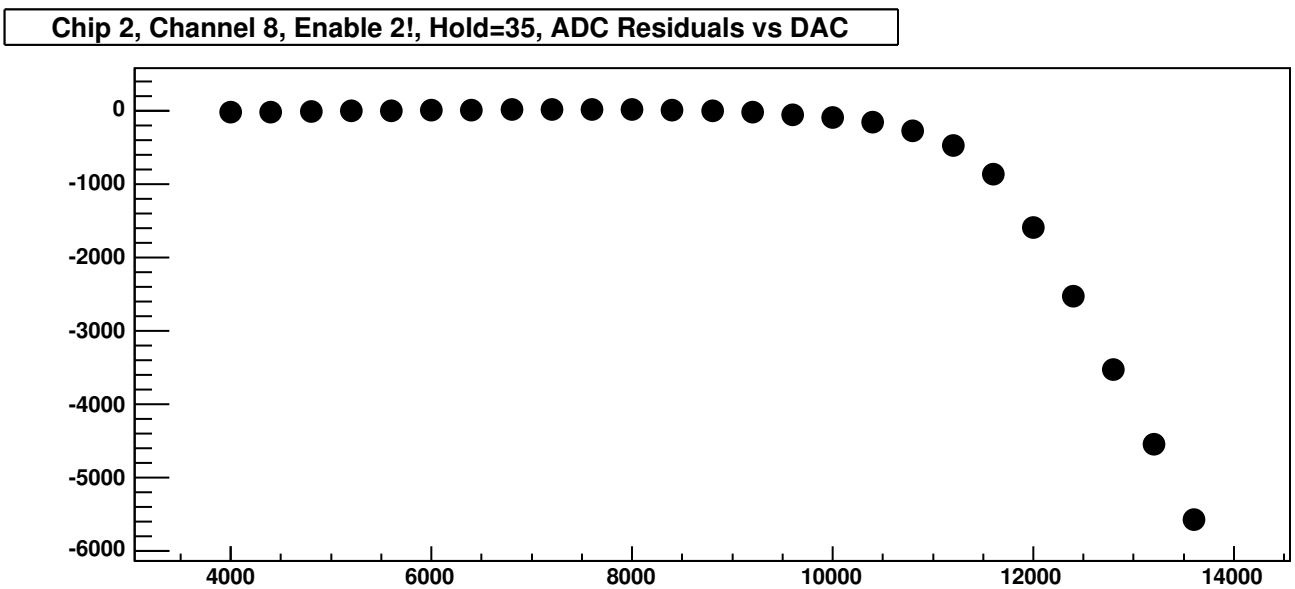


Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

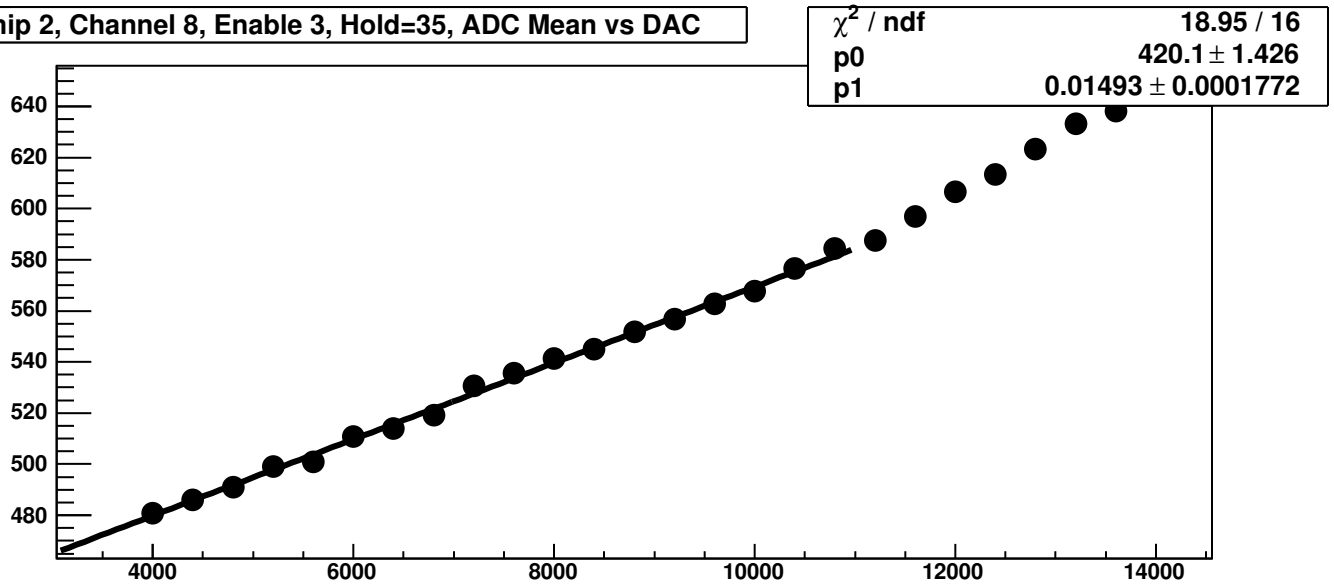



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1648 / 16
p0 656.4 ± 2.479
p1 2.612 ± 0.000379
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

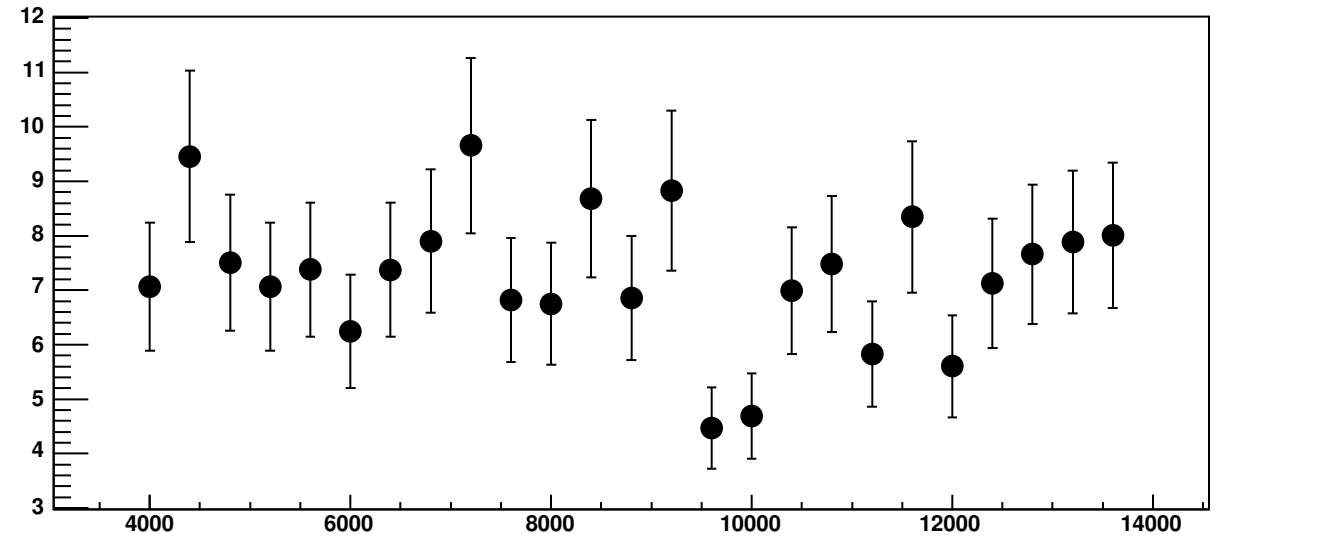

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000

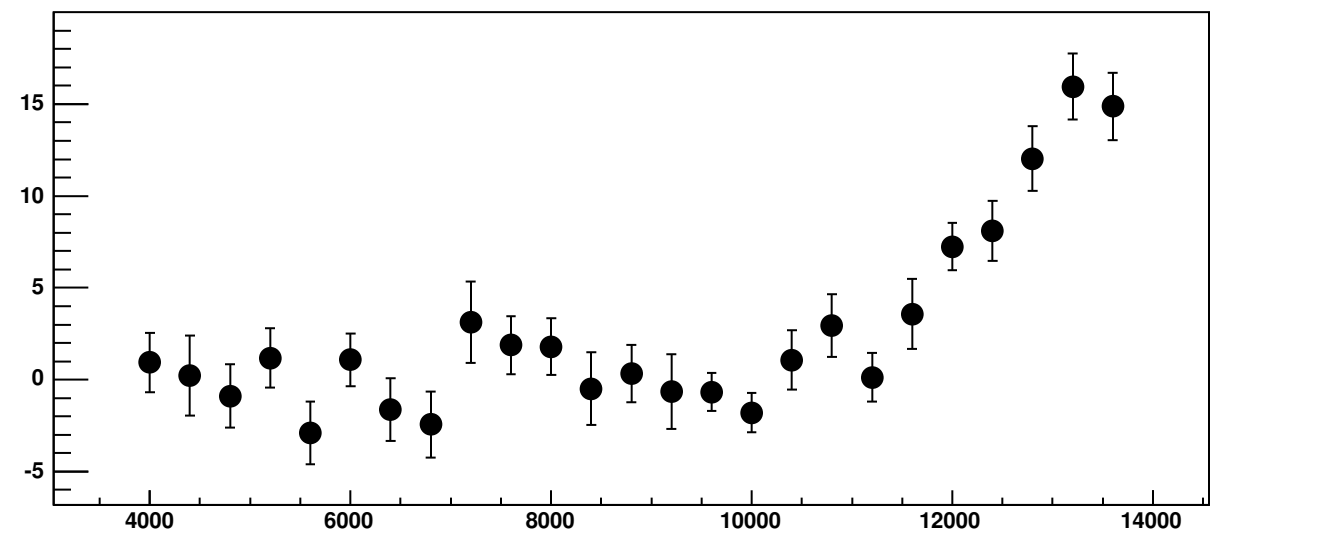
Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

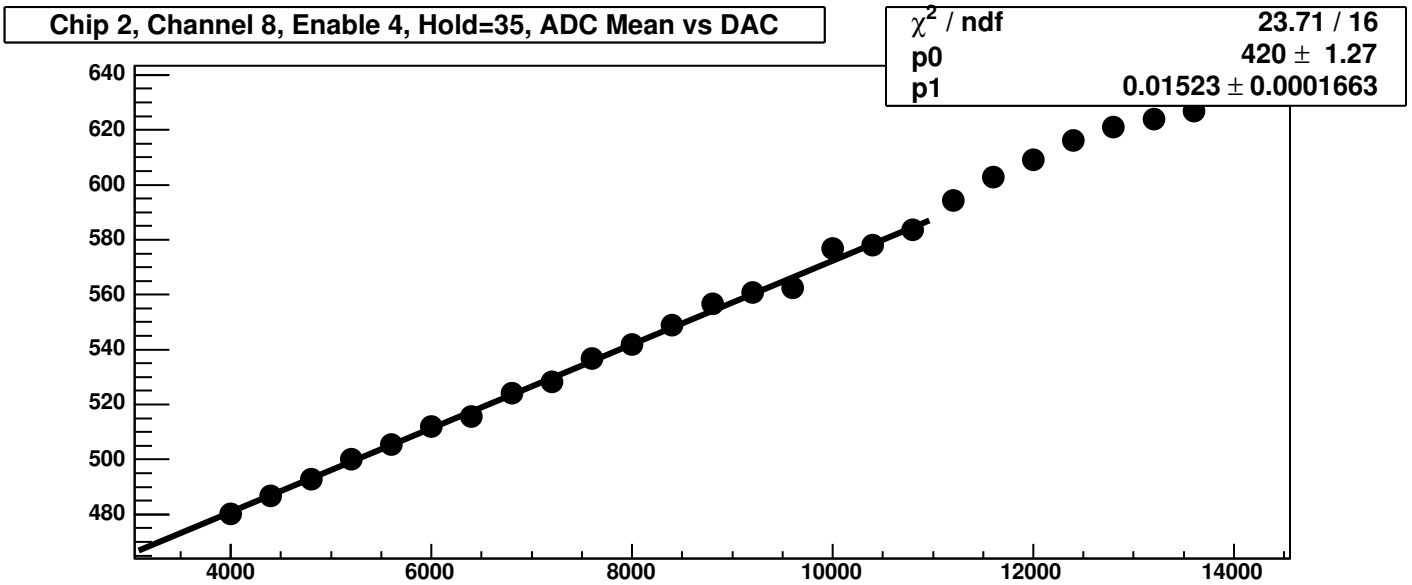


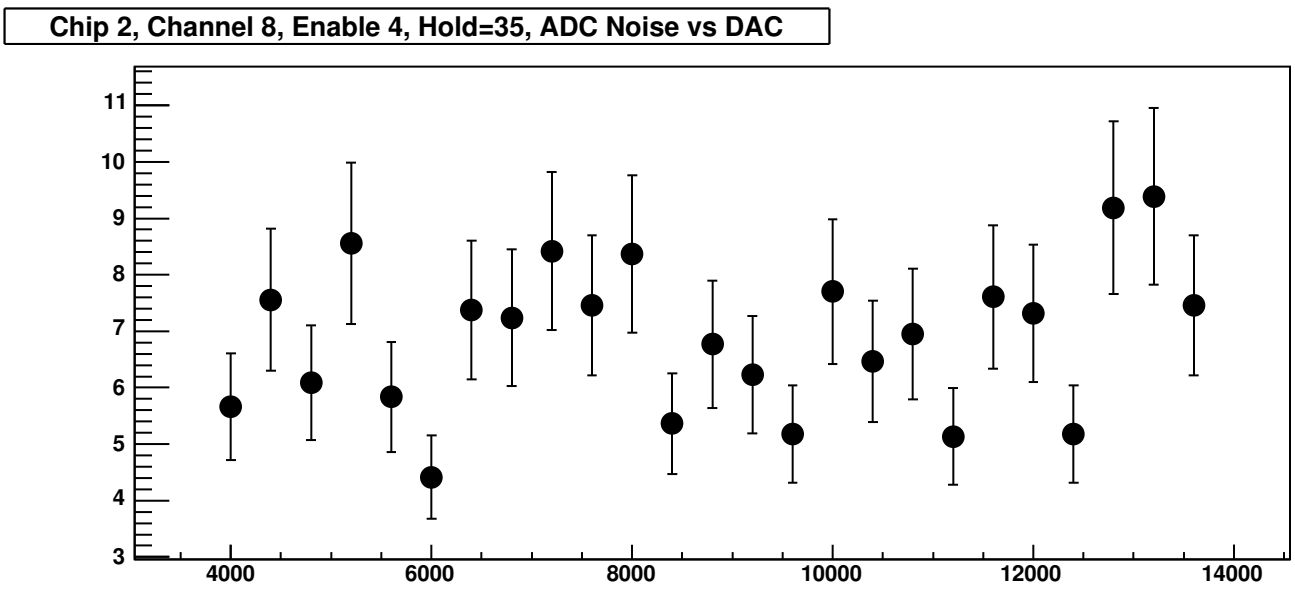
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

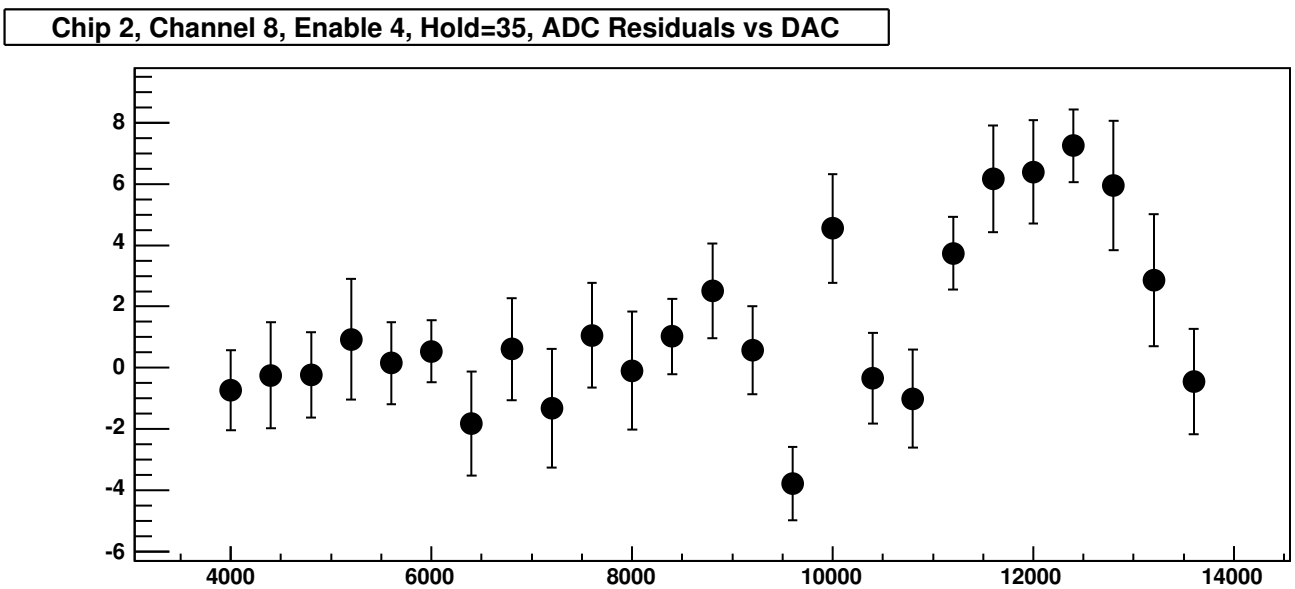


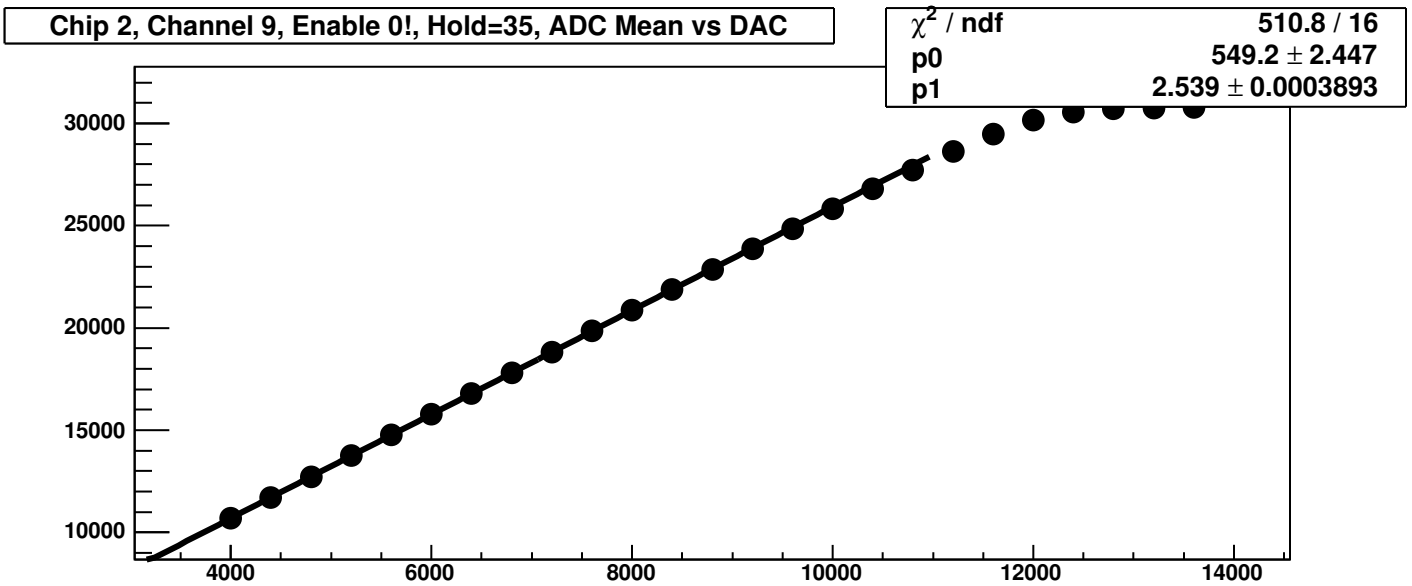
Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

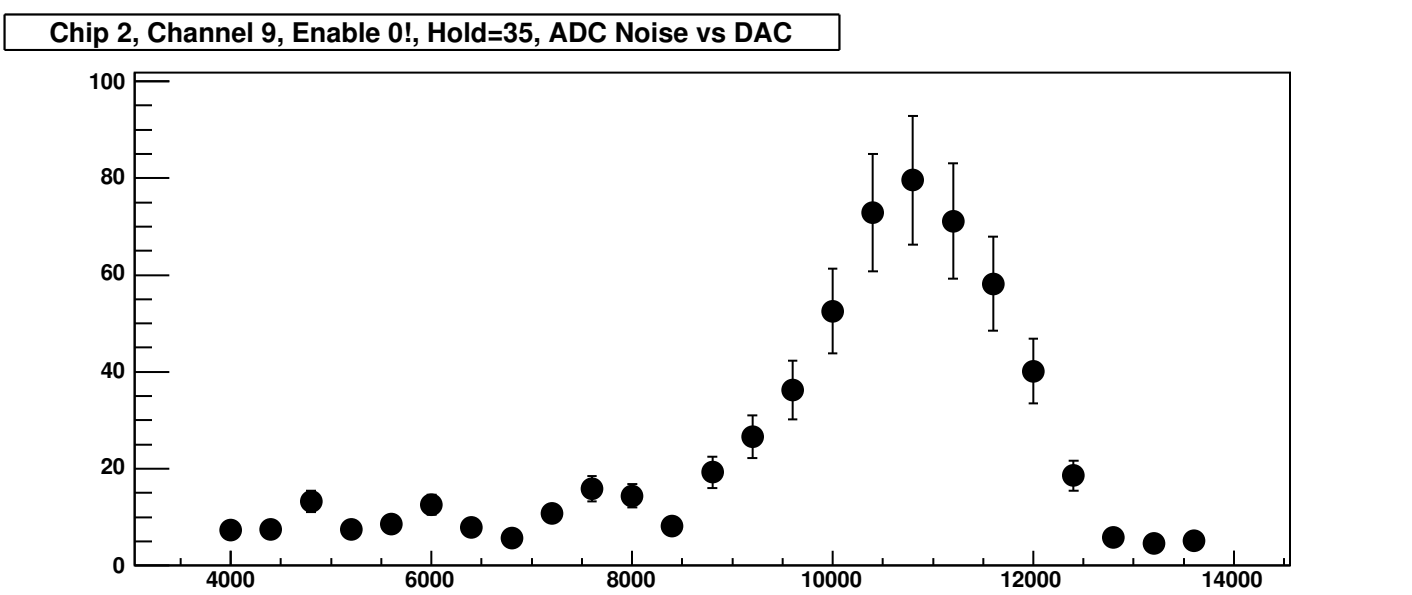


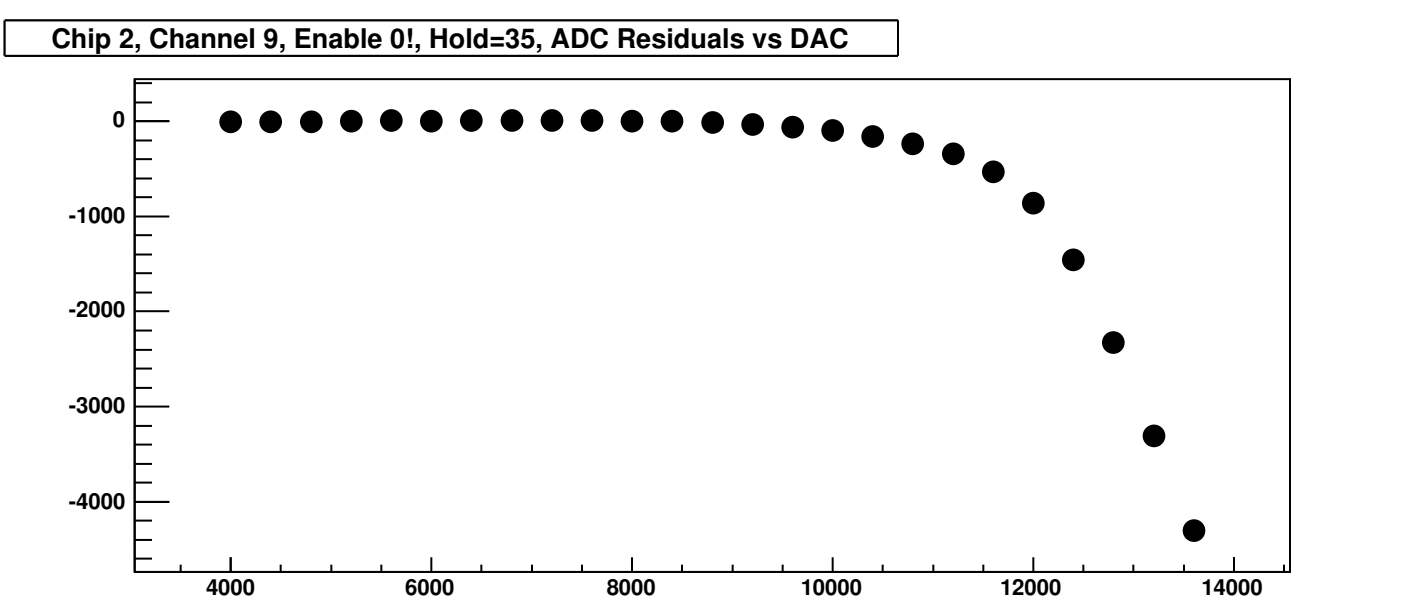

Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.71 / 16
p0 420 ± 1.27
p1 0.01523 ± 0.0001663
640
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

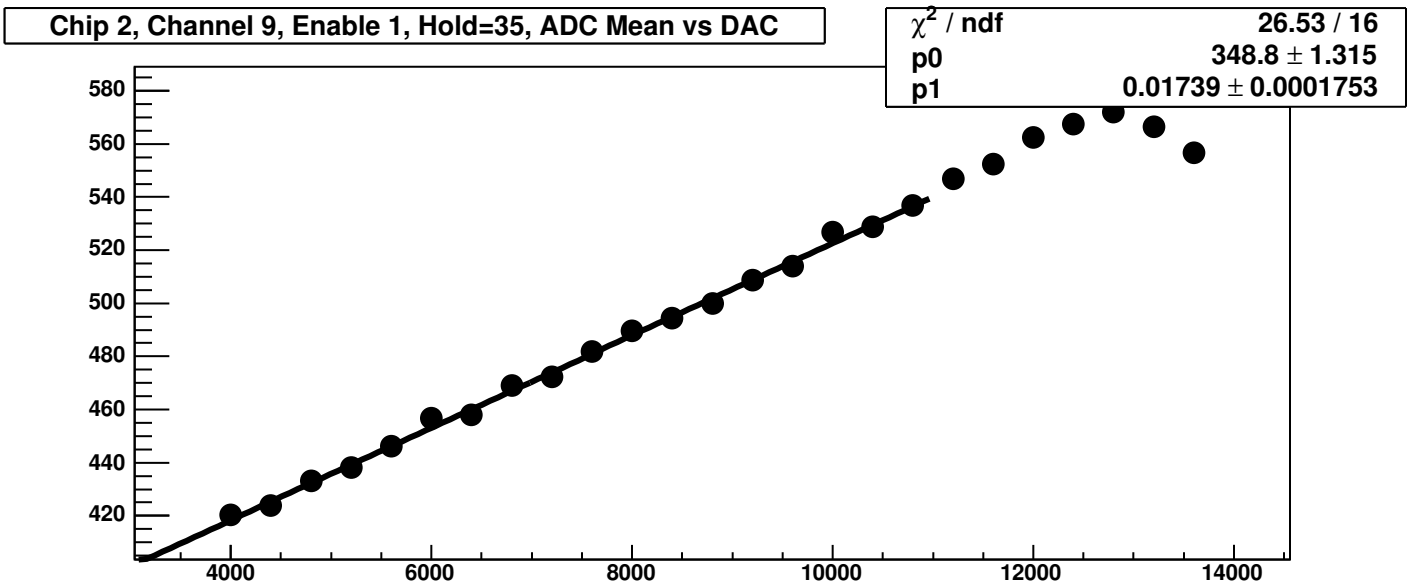

Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

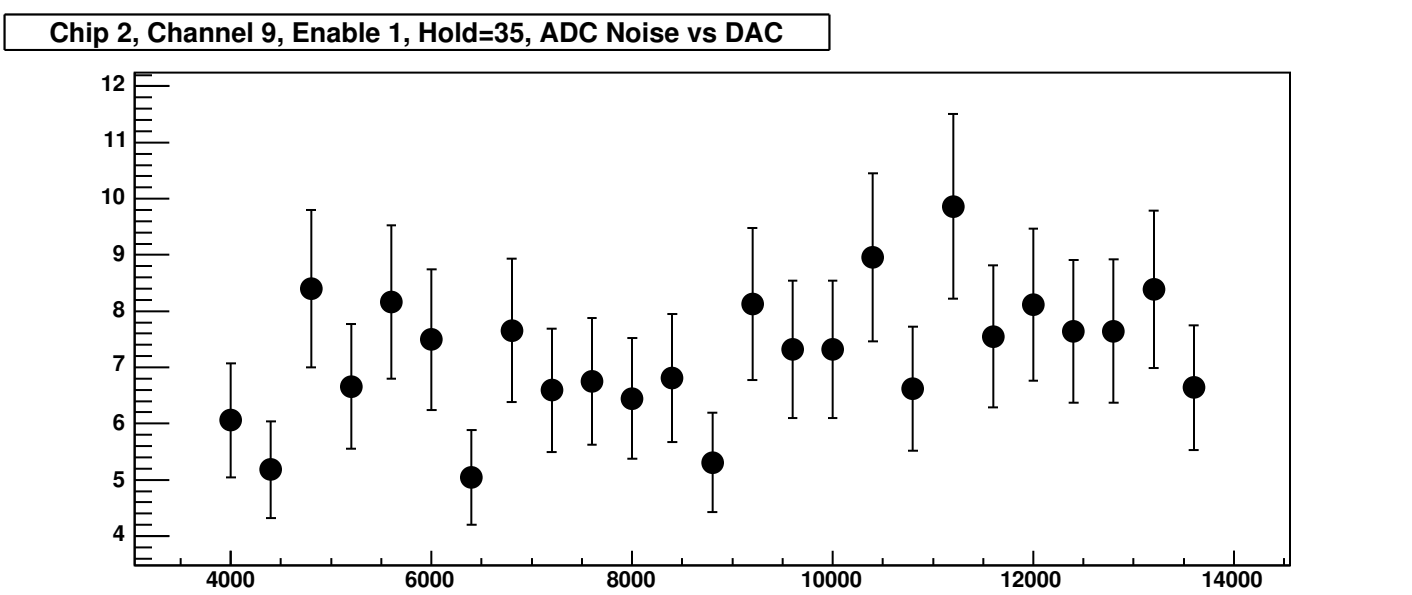

Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

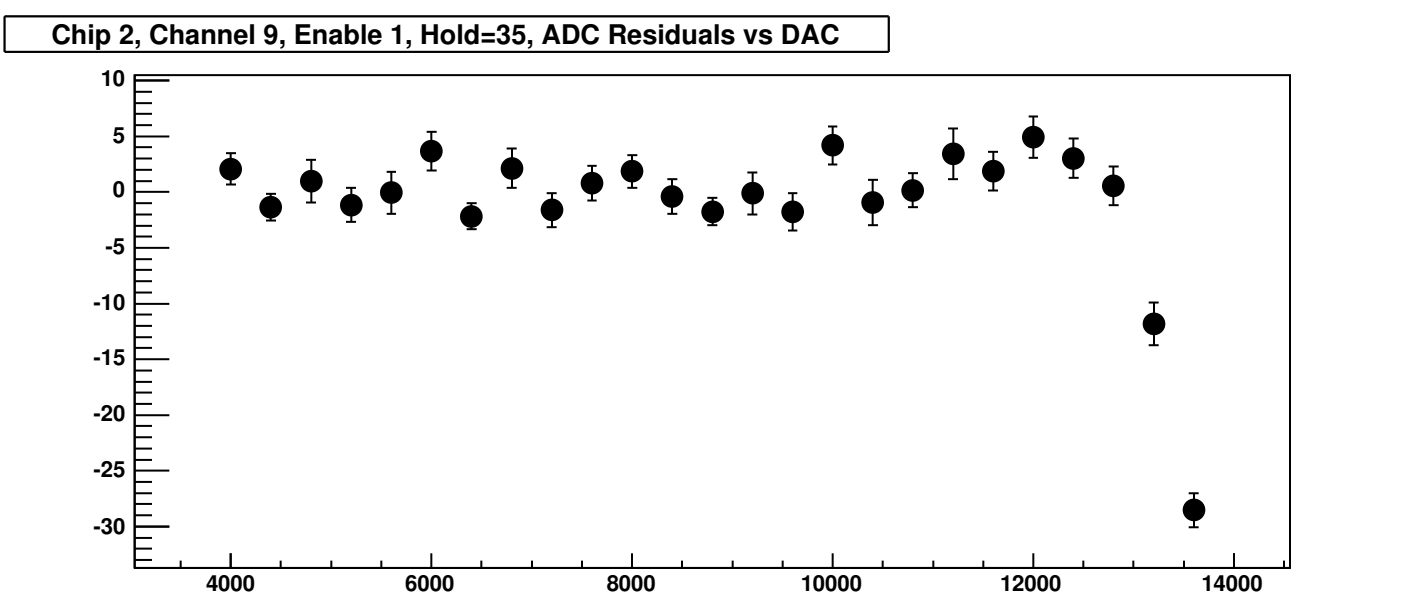

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 510.8 / 16
p0 549.2 ± 2.447
p1 2.539 ± 0.0003893
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

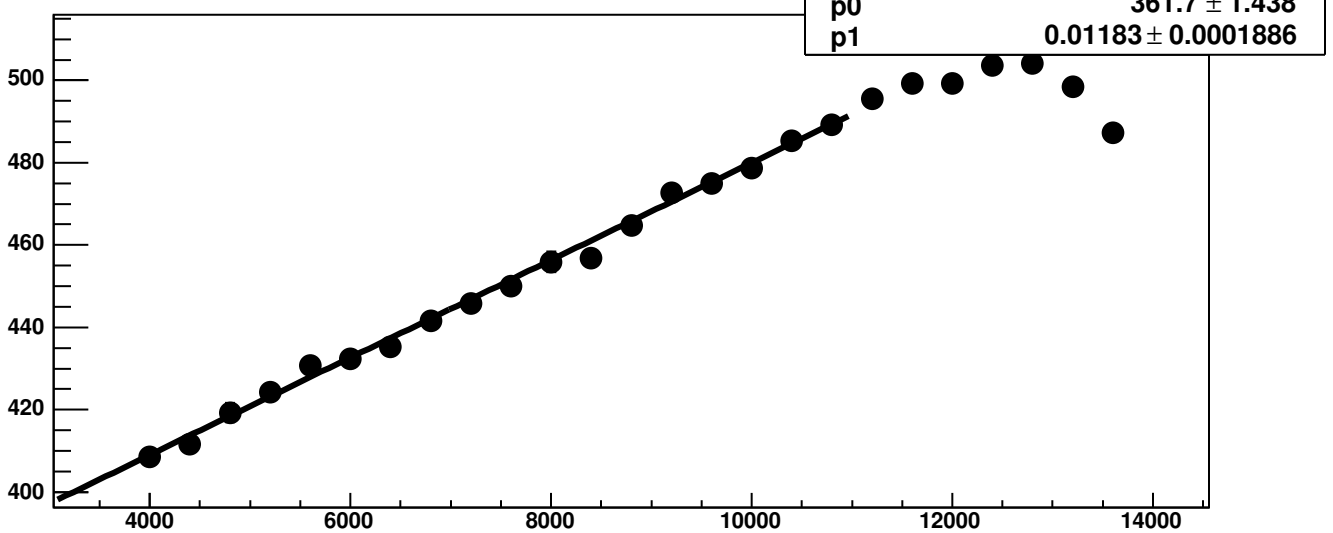

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.53 / 16
p0 348.8 ± 1.315
p1 0.01739 ± 0.0001753
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

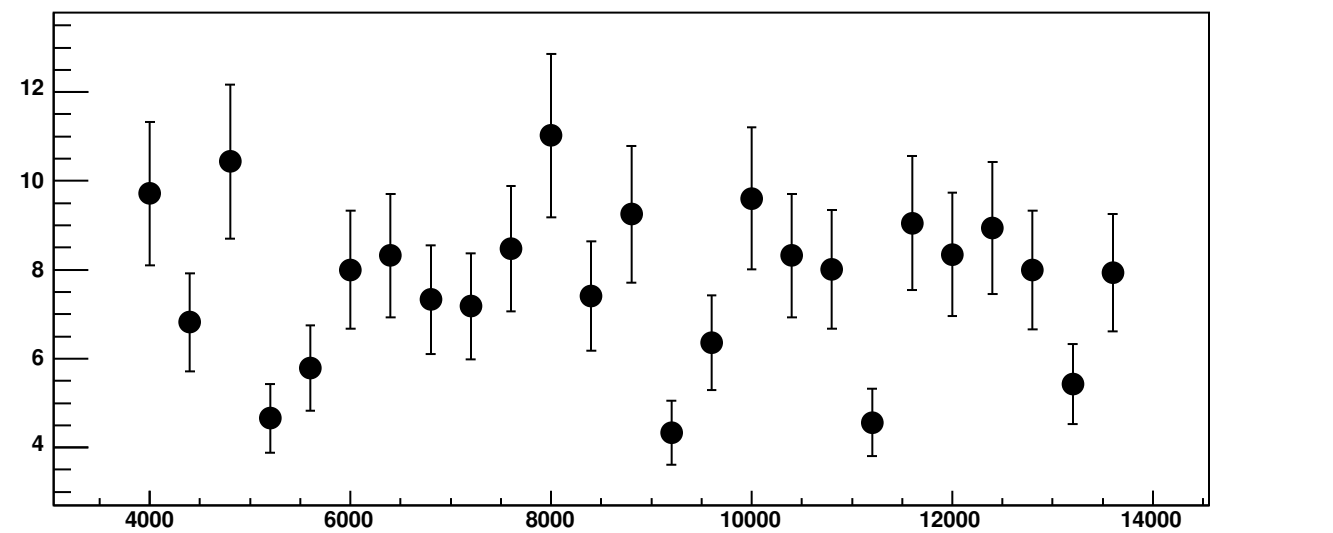

Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

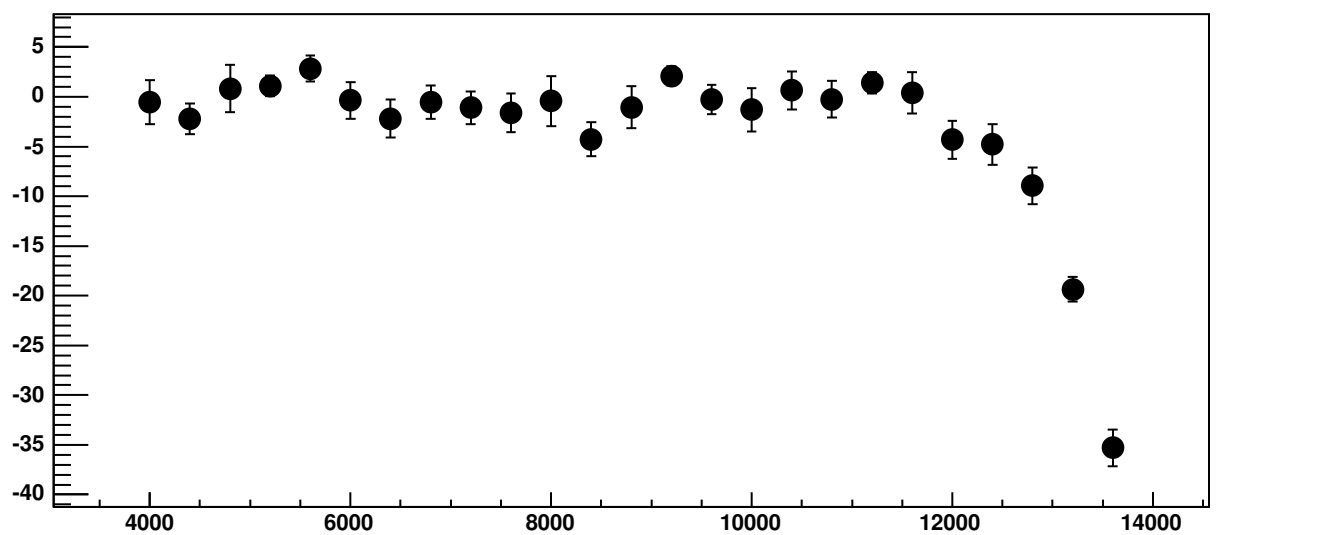
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

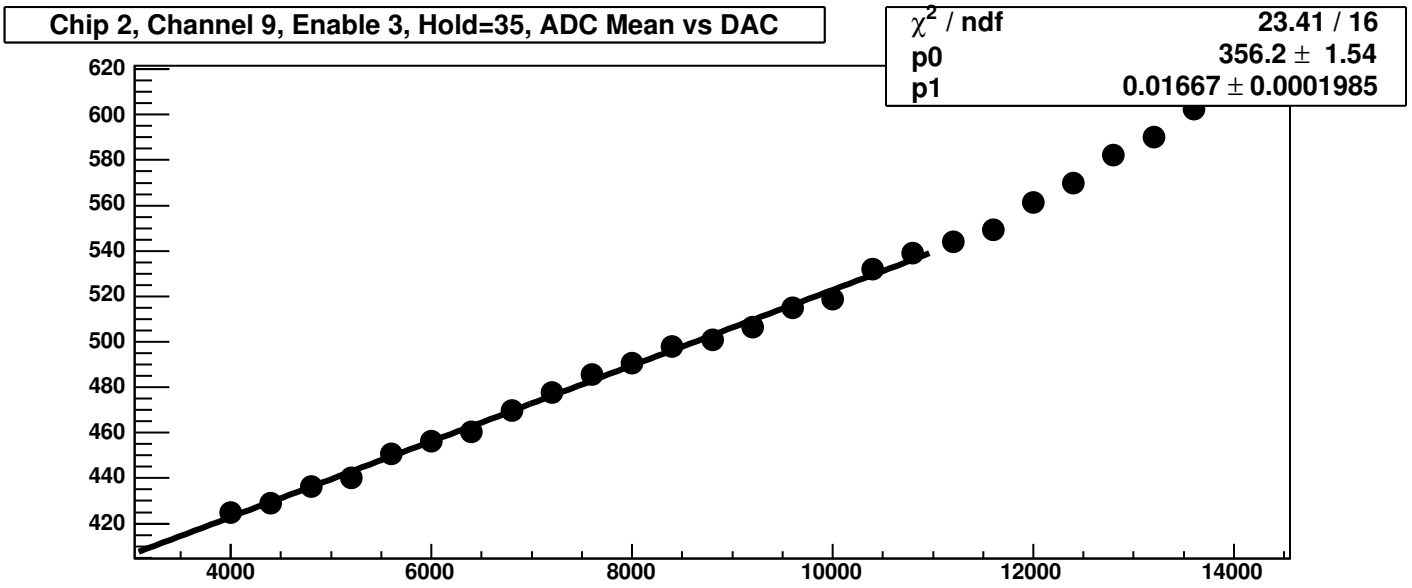


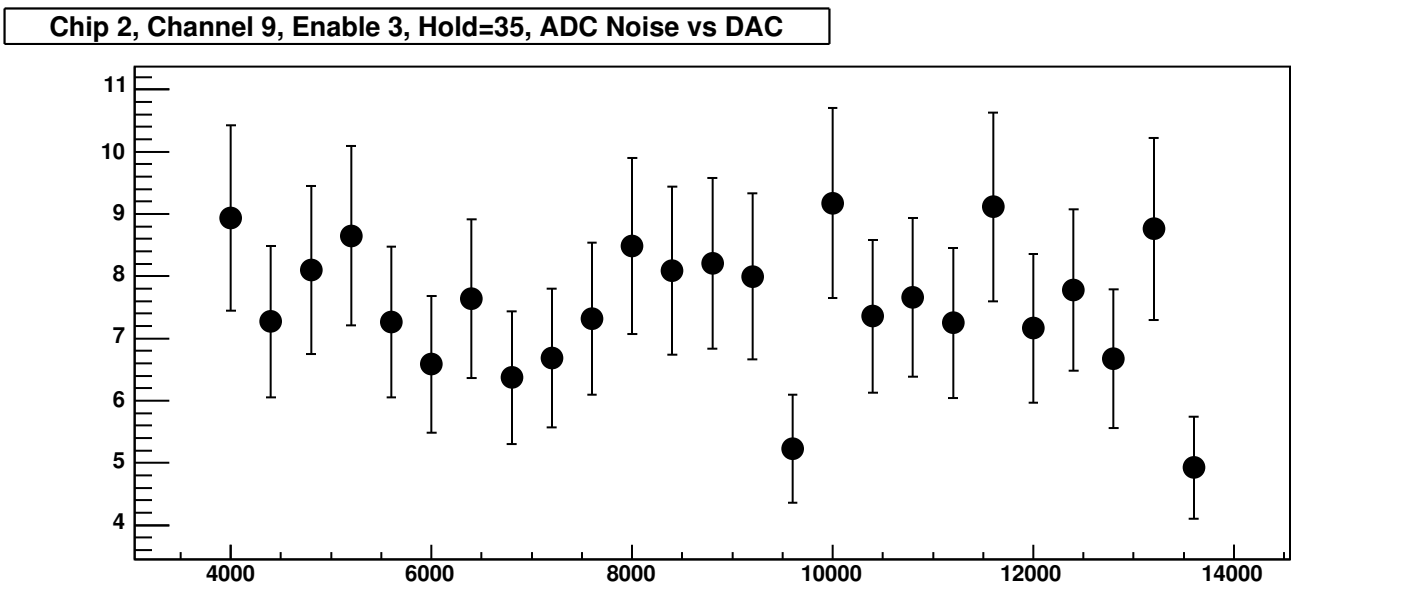
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

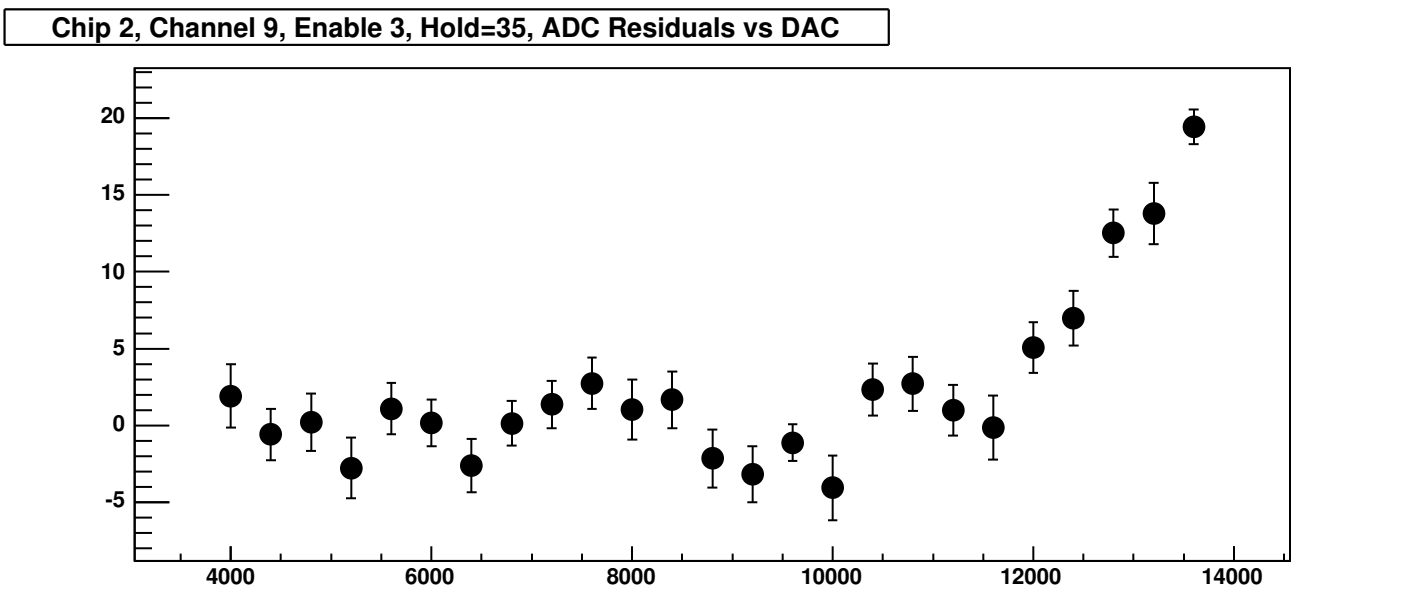


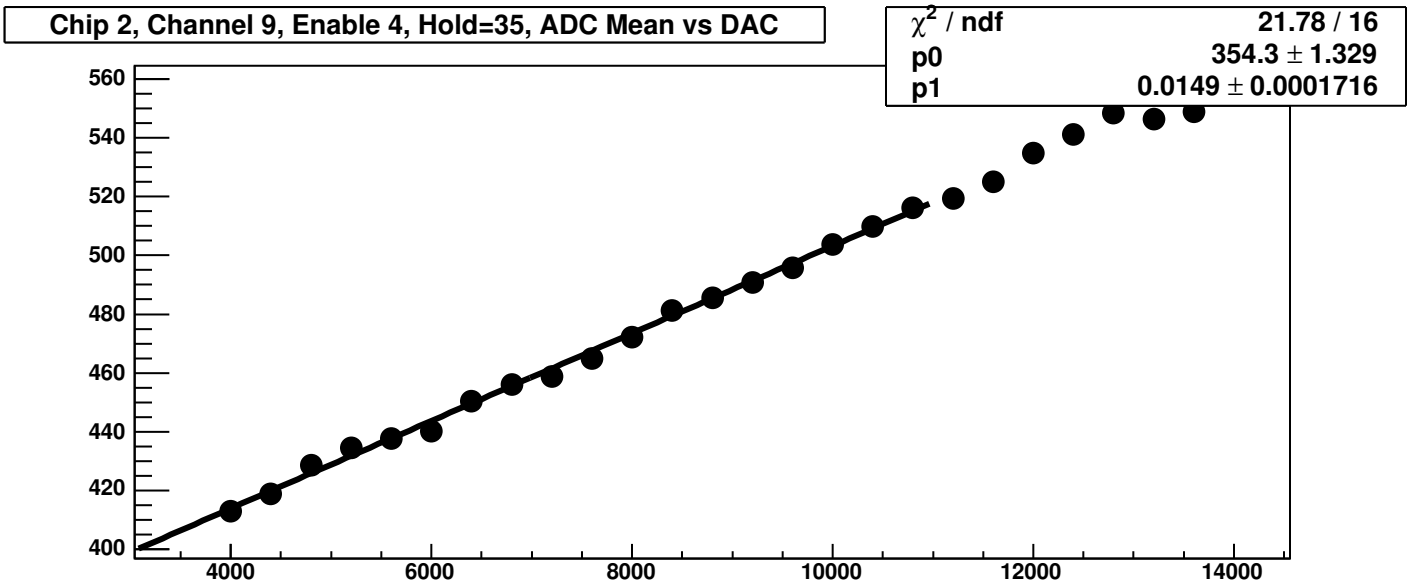
Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

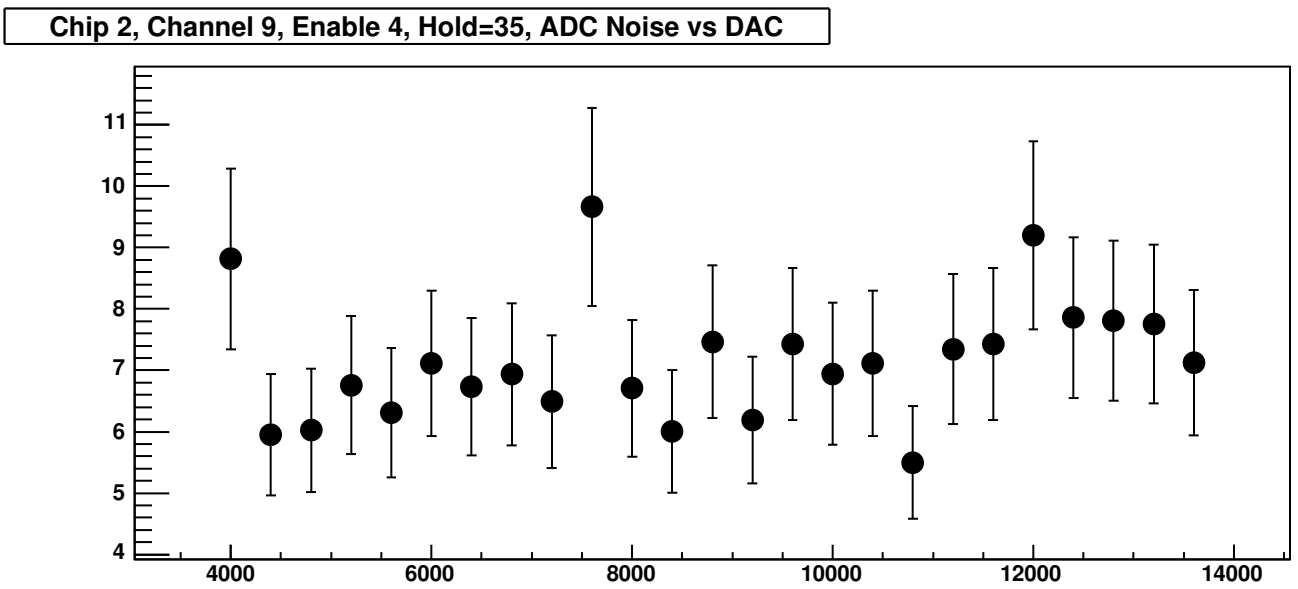


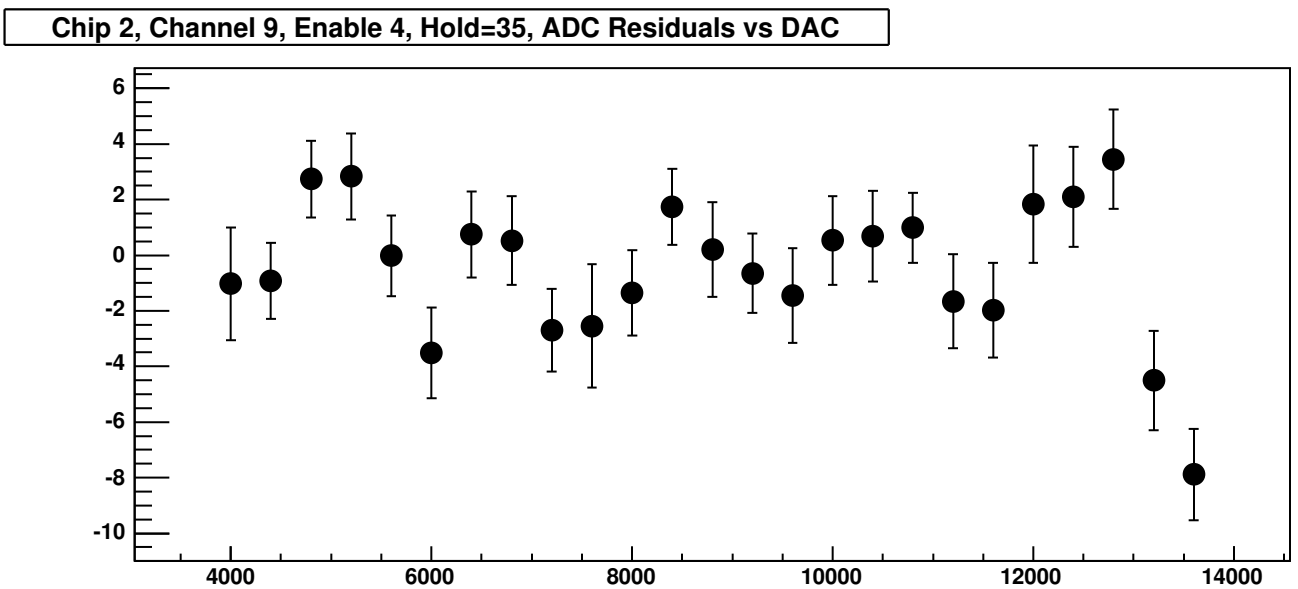

Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.41 / 16
p0 356.2 ± 1.54
p1 0.01667 ± 0.0001985
620
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

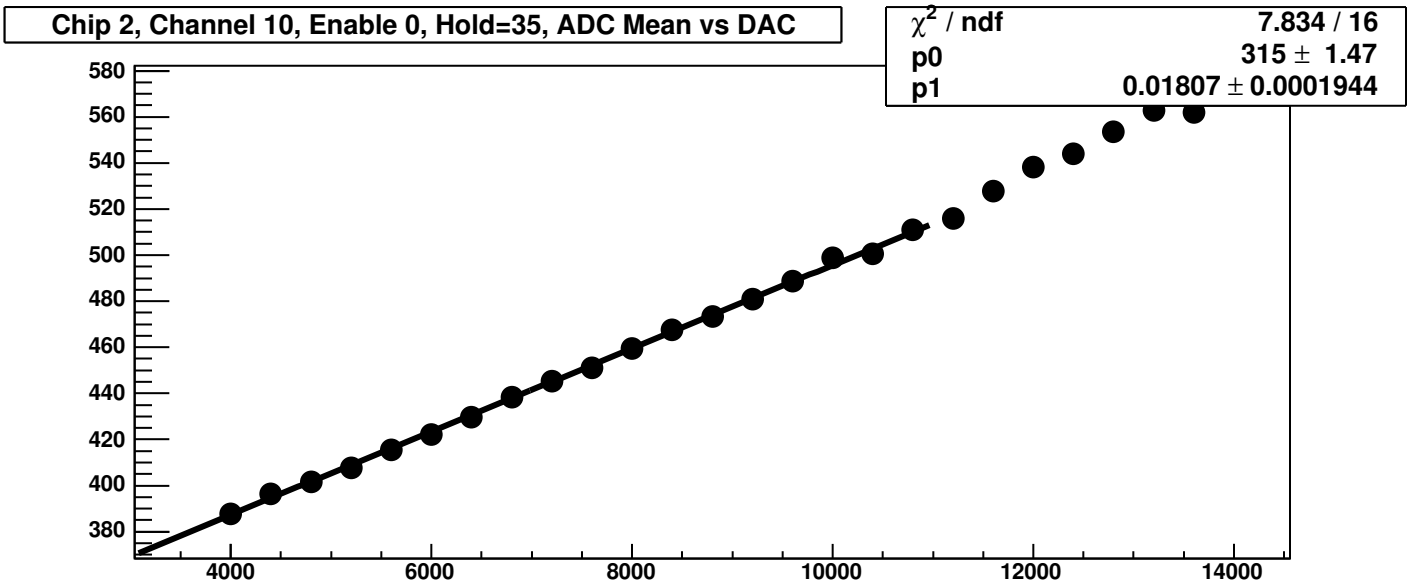

Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

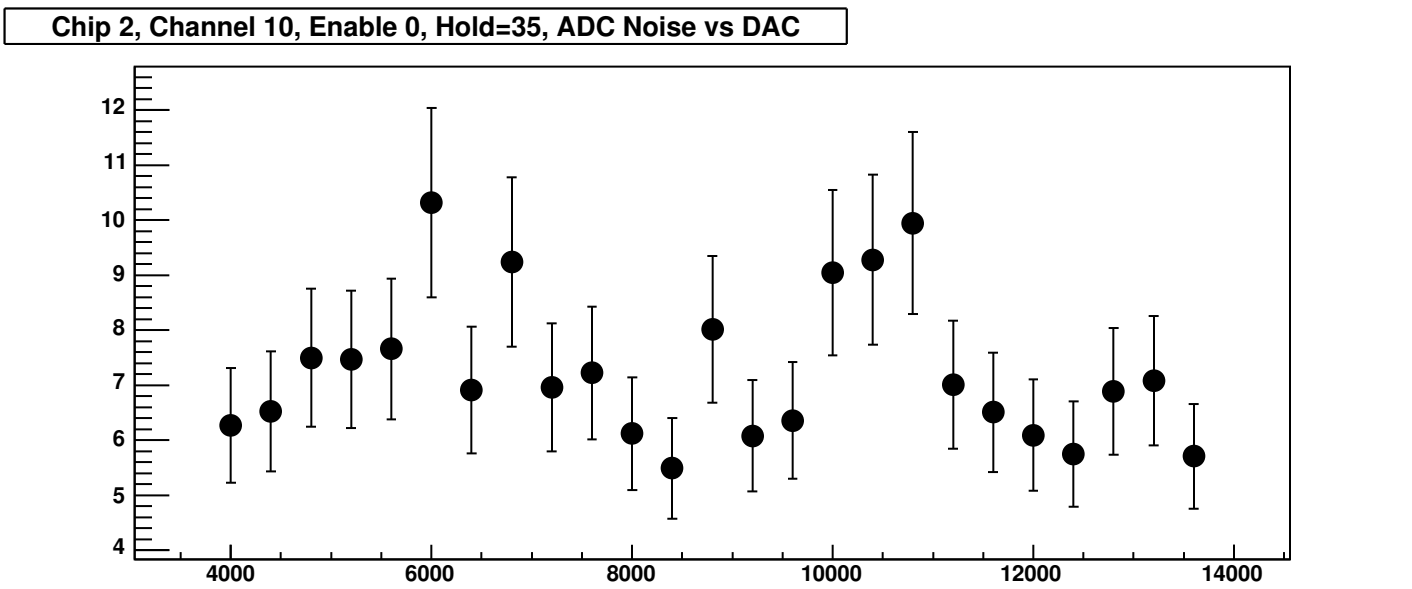

Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

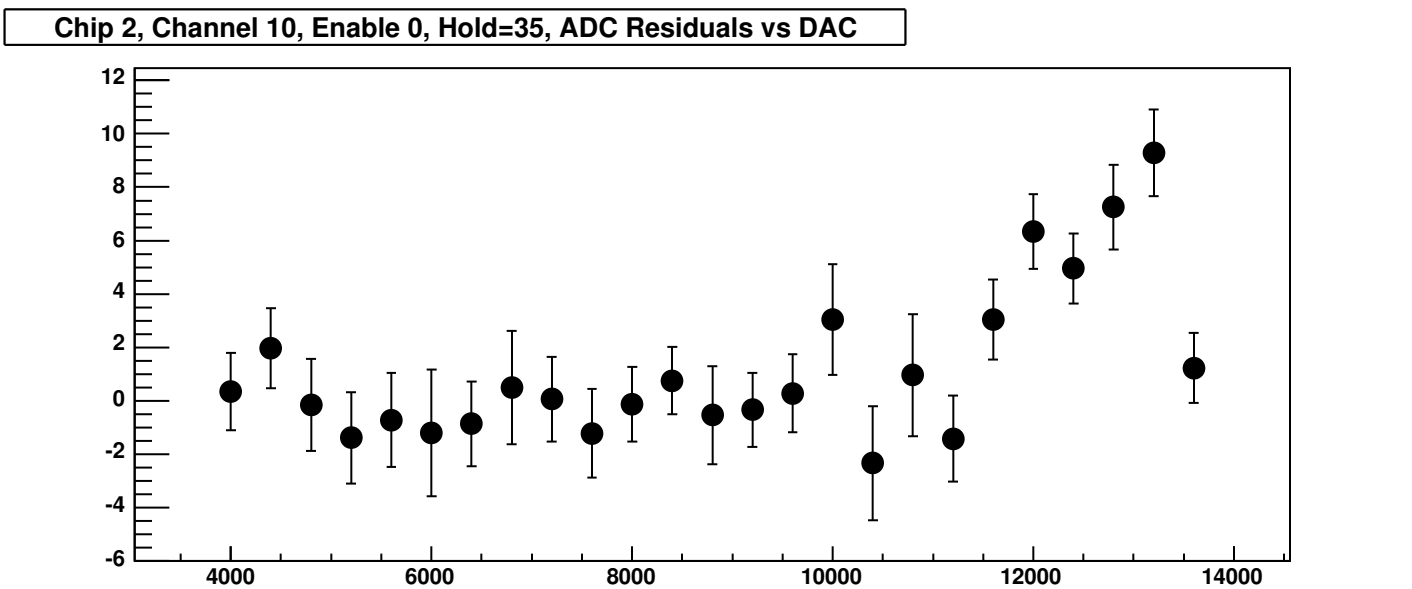

Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.78 / 16
p0 354.3 ± 1.329
p1 0.0149 ± 0.0001716
560
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

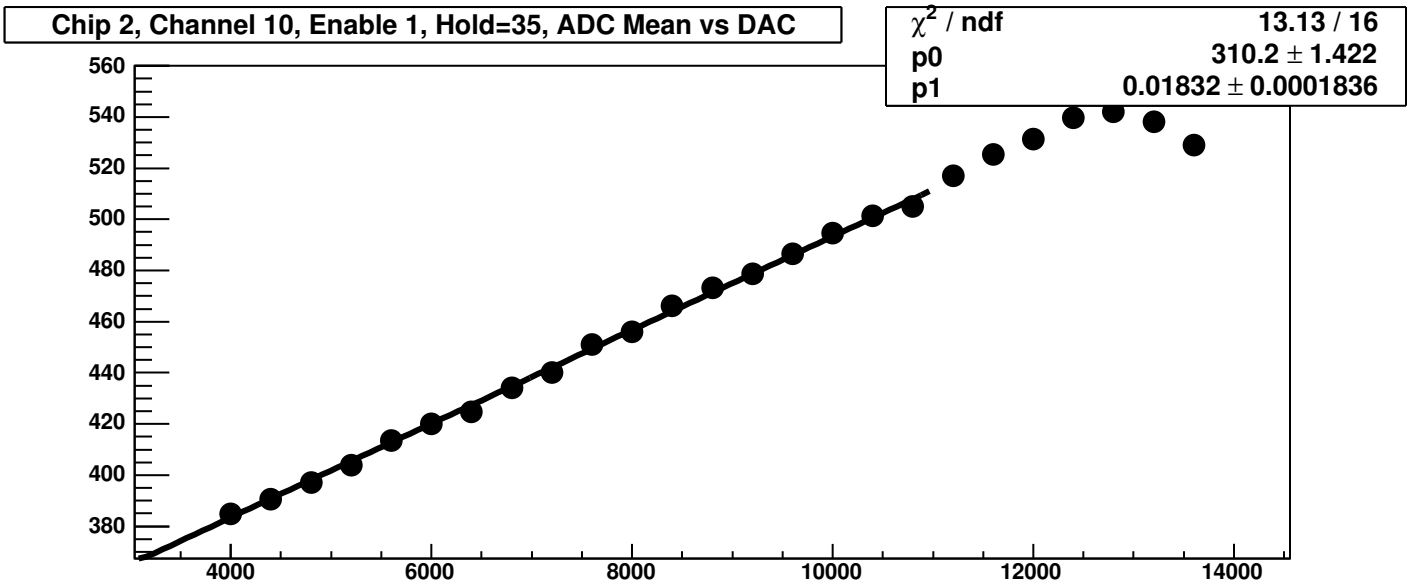

Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

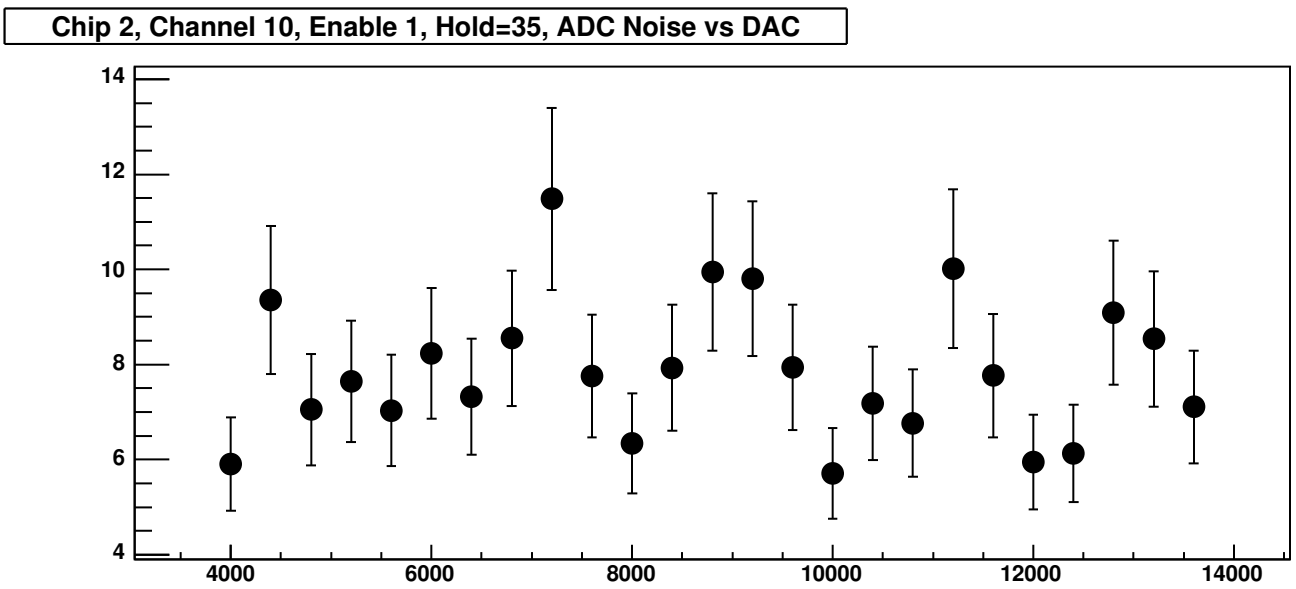

Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

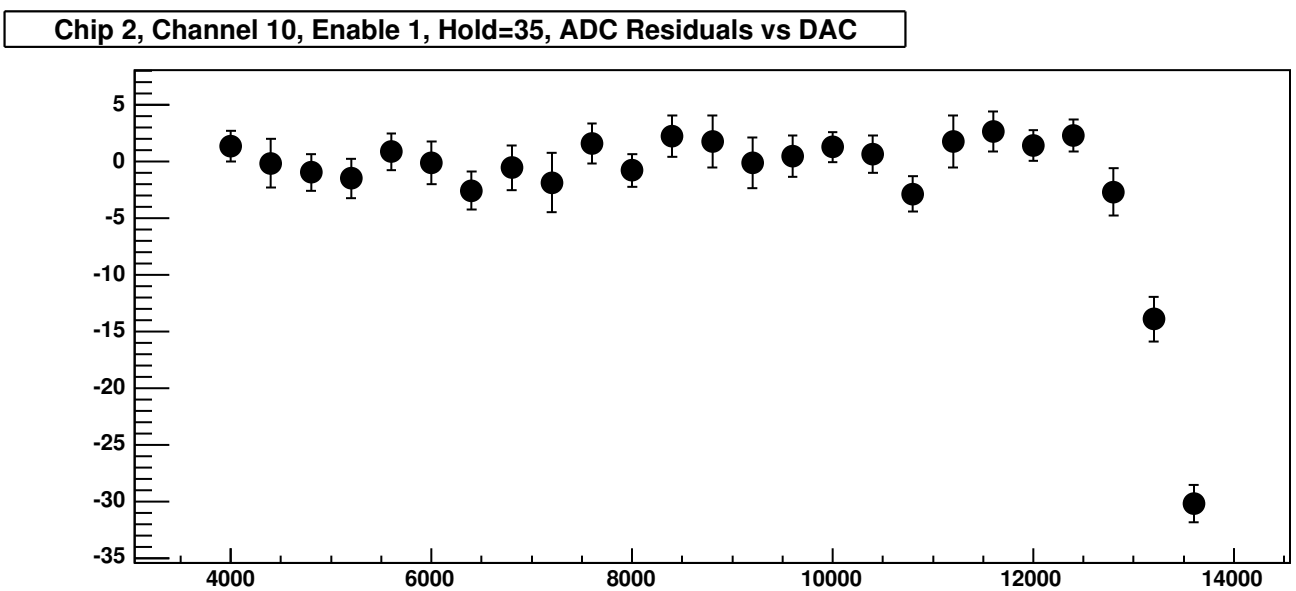

Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.834 / 16
p0 315 ± 1.47
p1 0.01807 ± 0.0001944
580
560
540
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

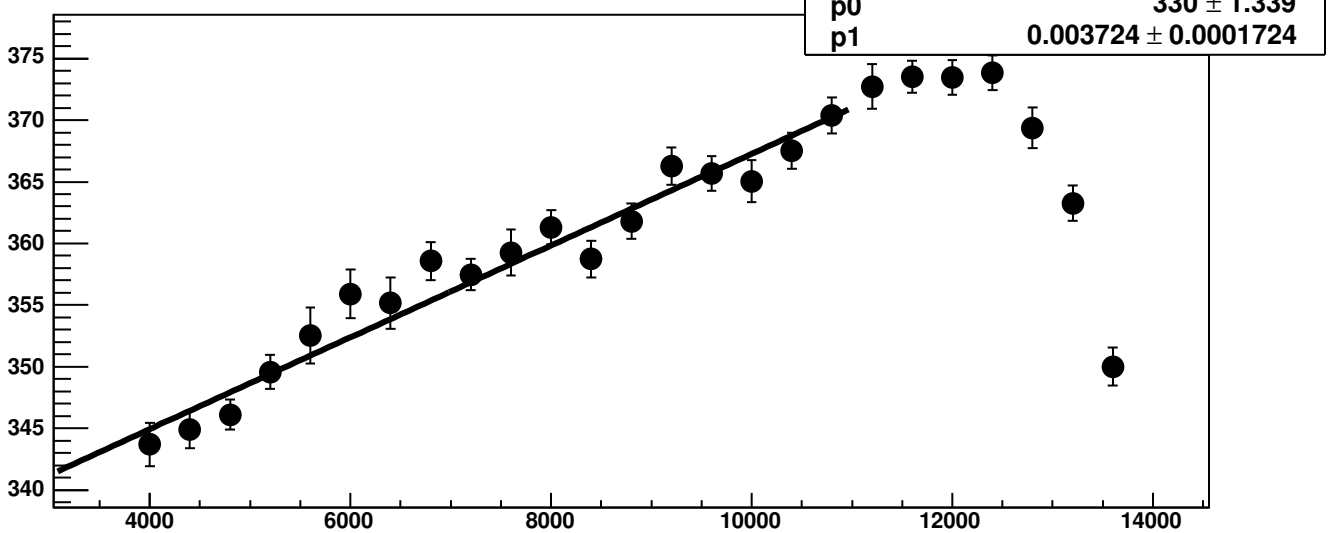

Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 13.13 / 16
p0 310.2 ± 1.422
p1 0.01832 ± 0.0001836
560
540
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000

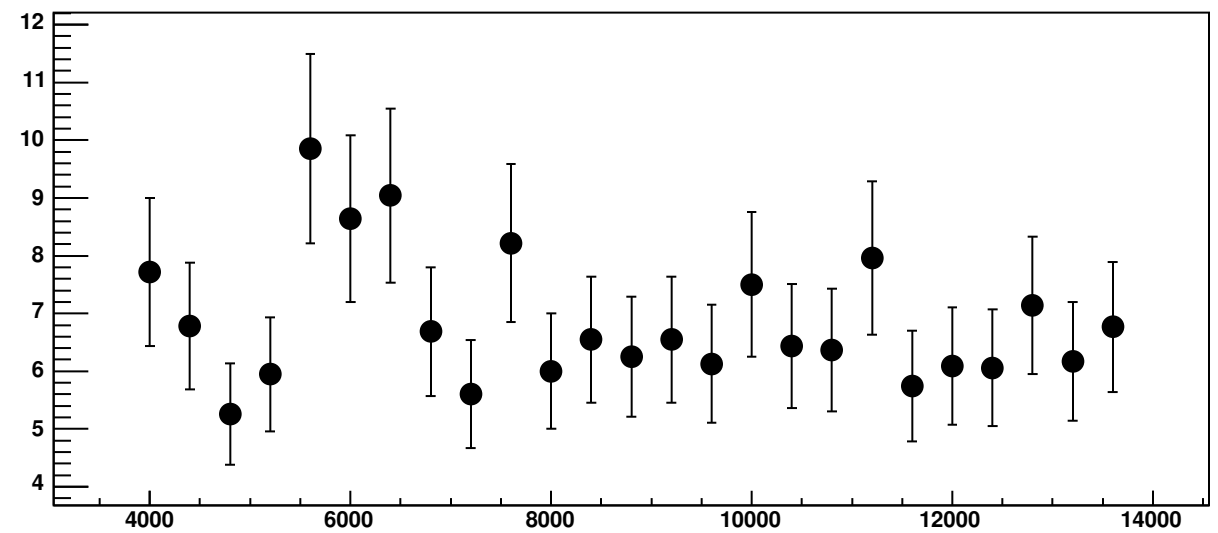

Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

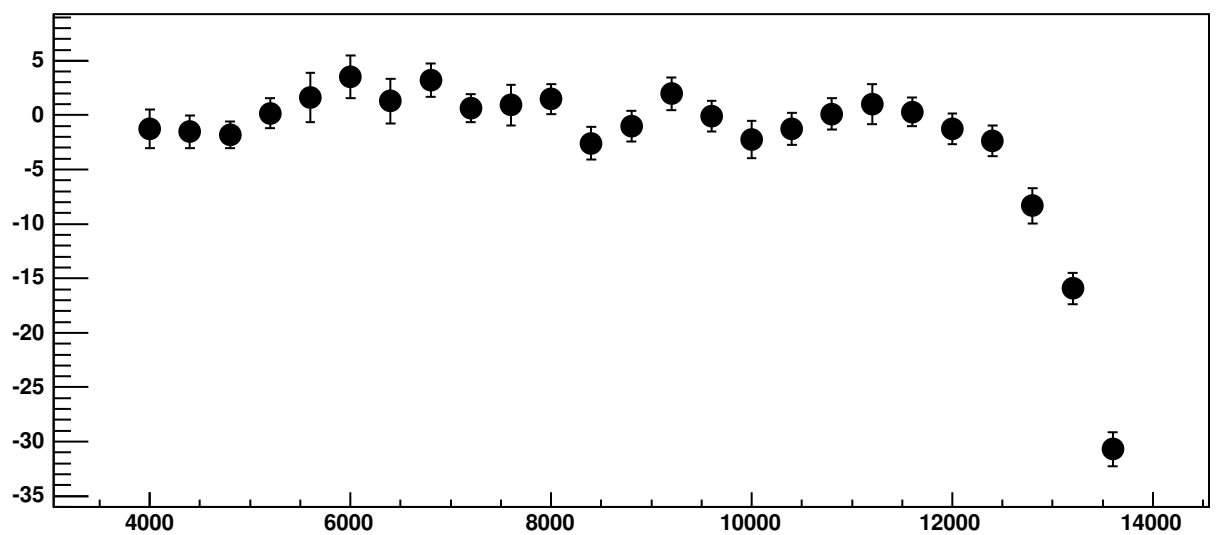
Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

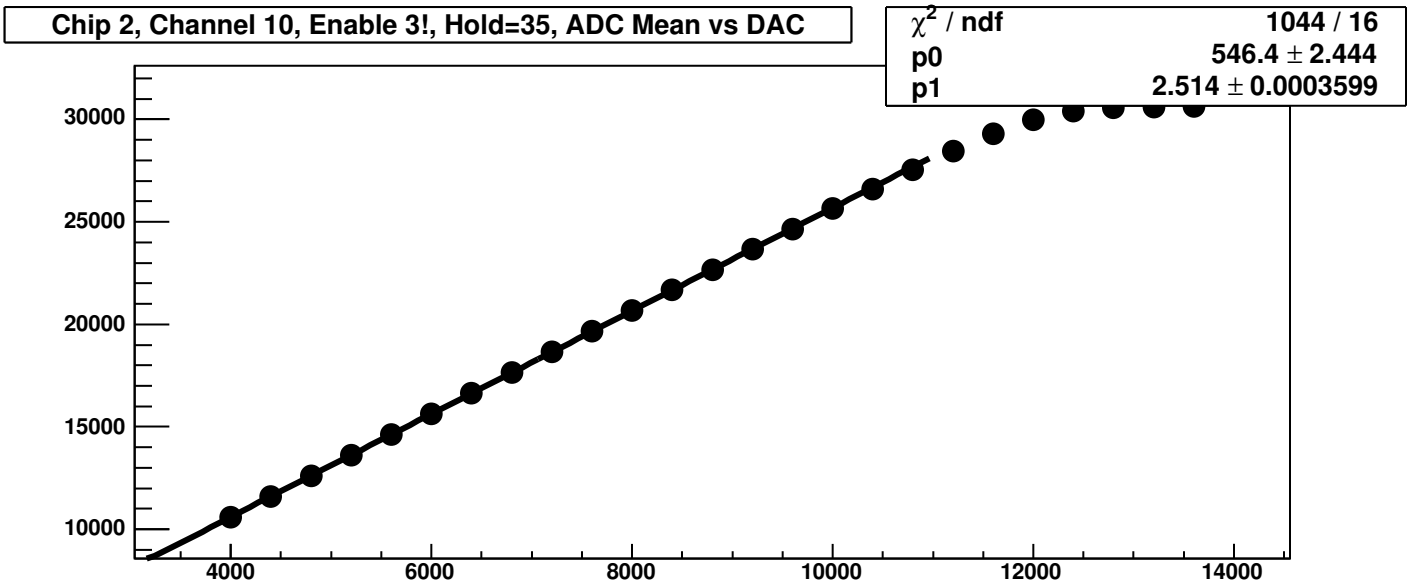


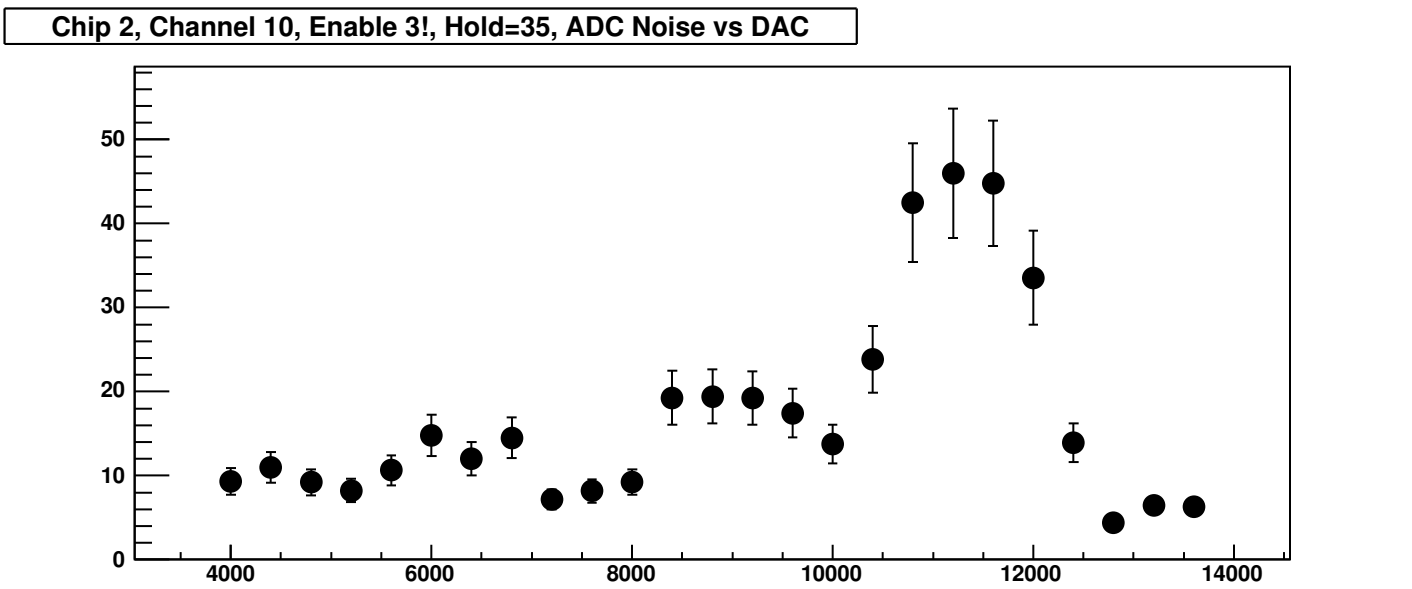
Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

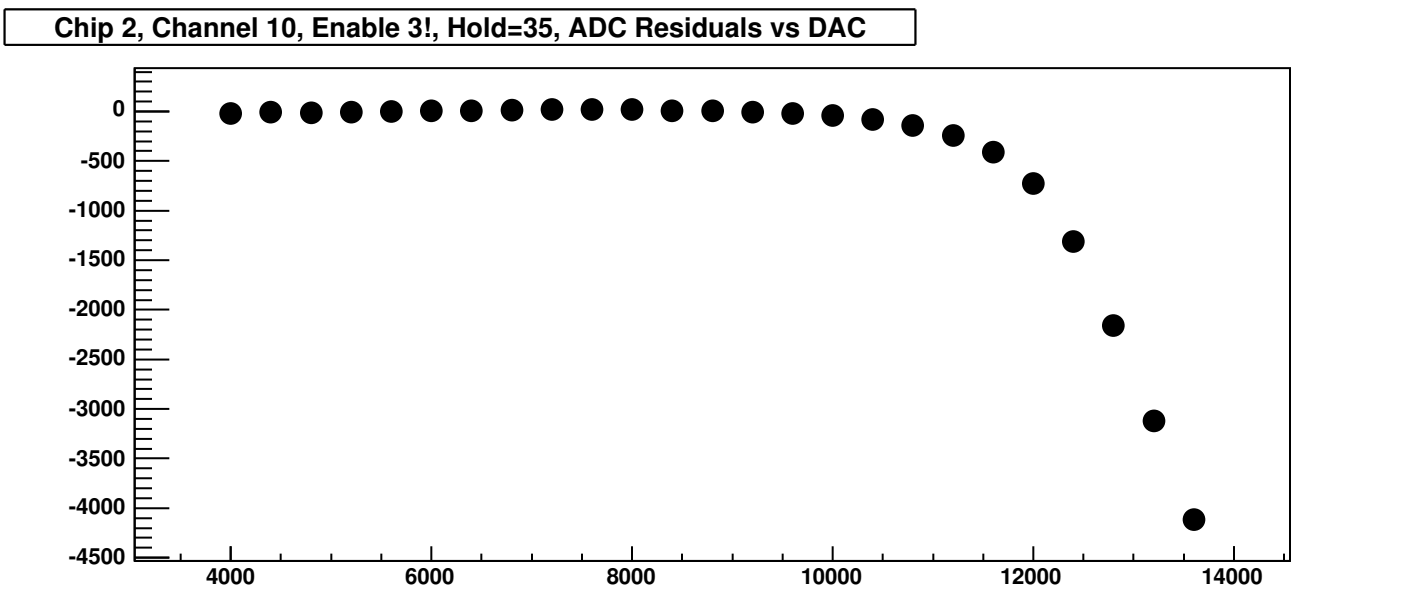


Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

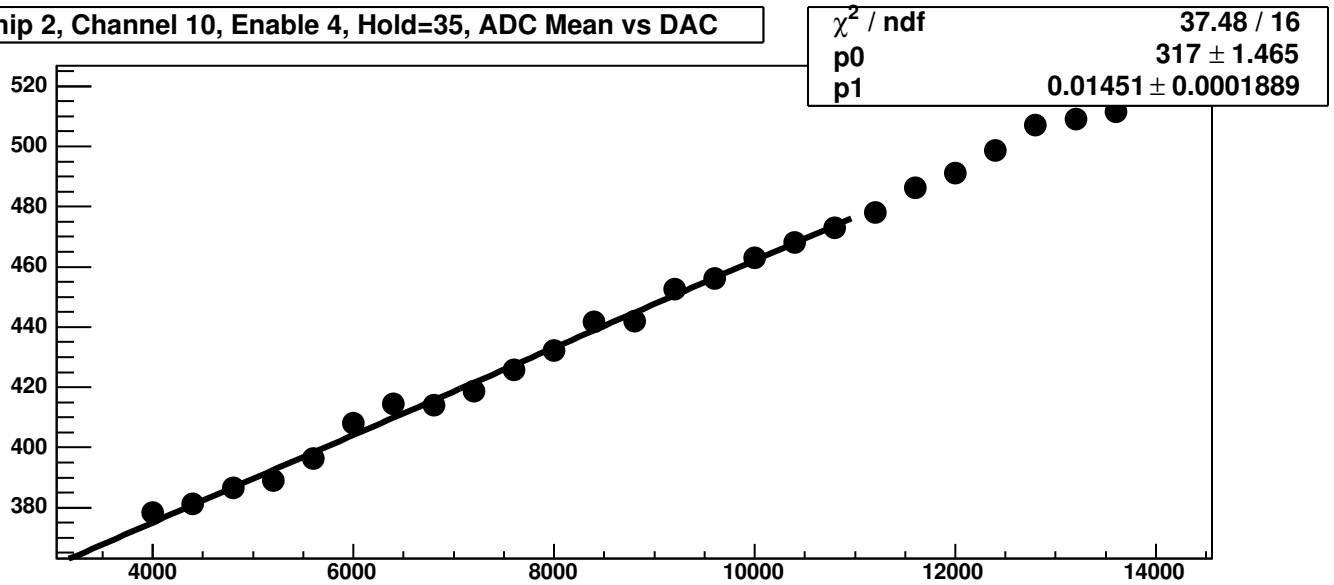



Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1044 / 16
p0 546.4 ± 2.444
p1 2.514 ± 0.0003599
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

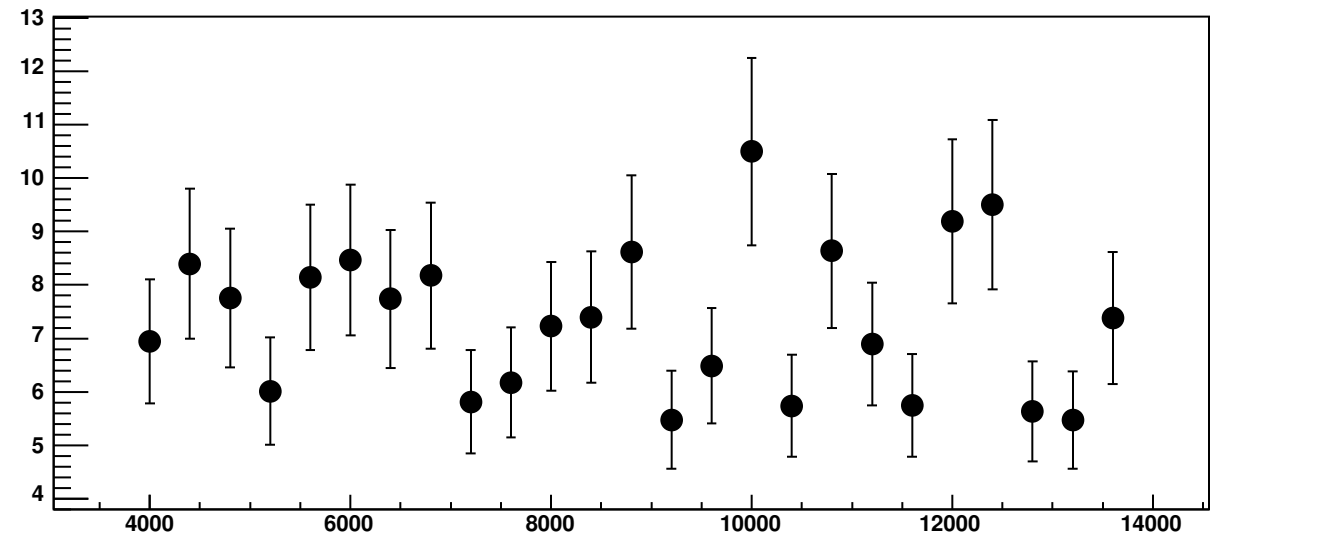

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000

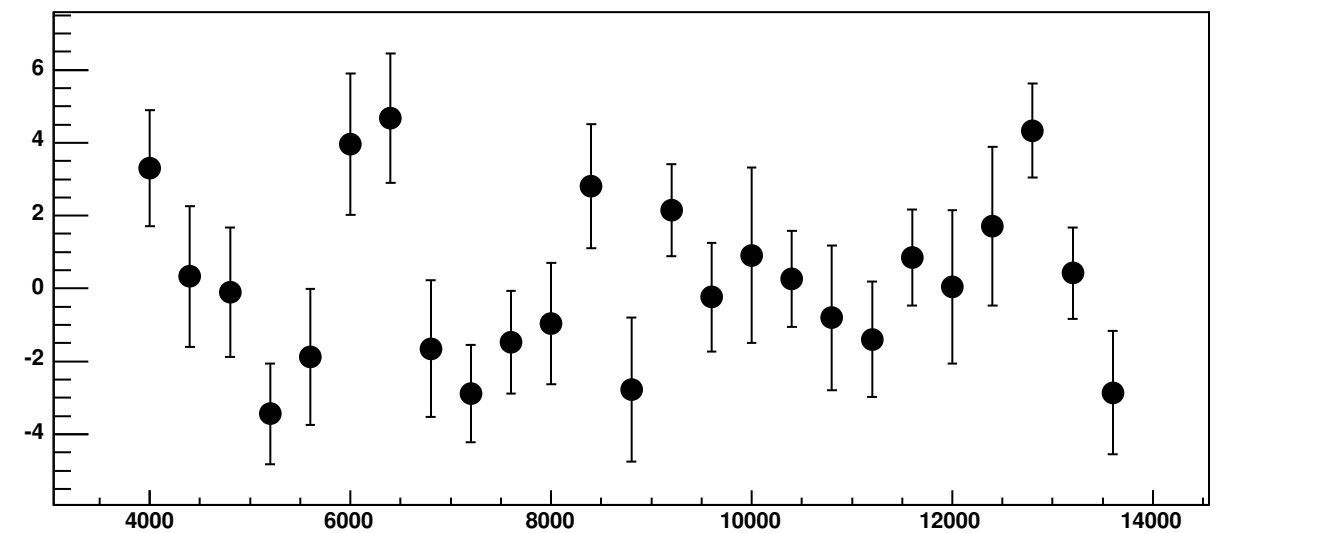
Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

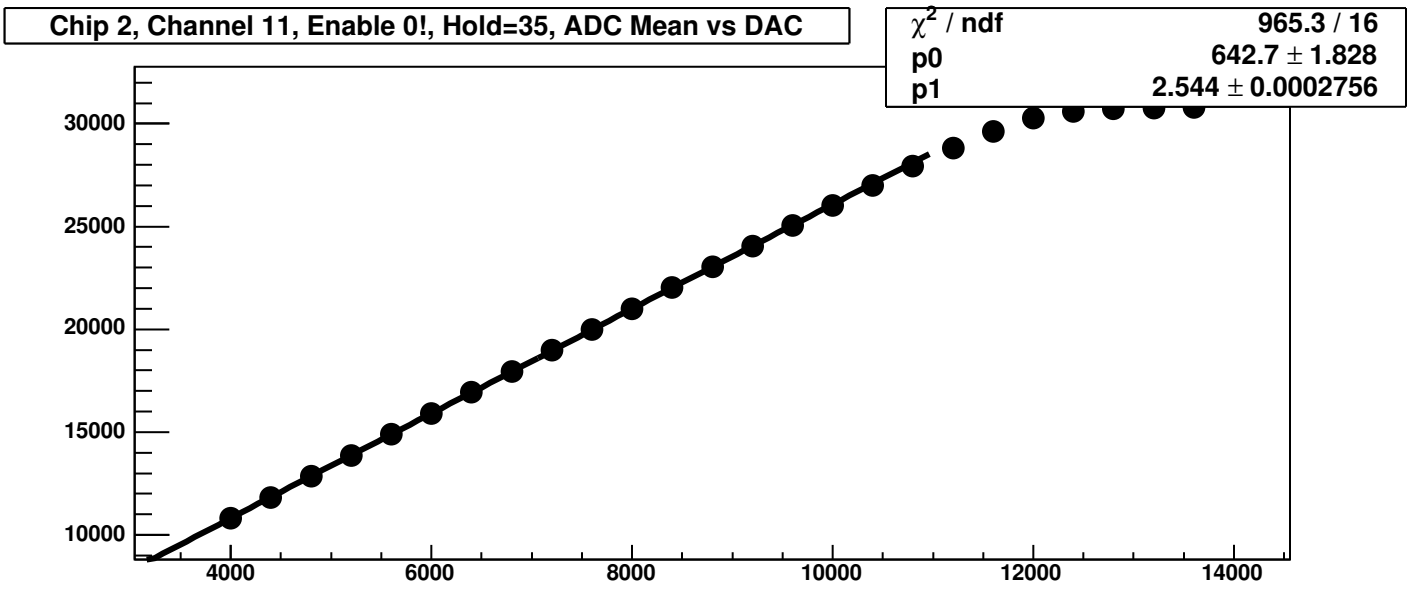


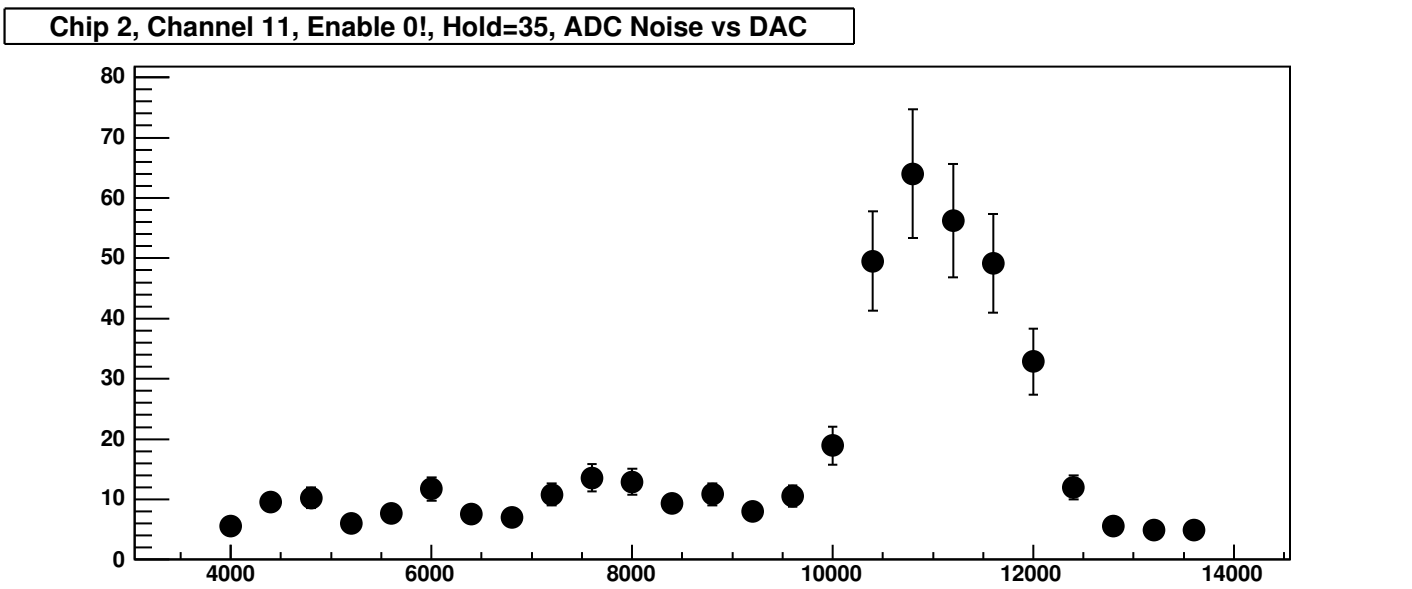
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

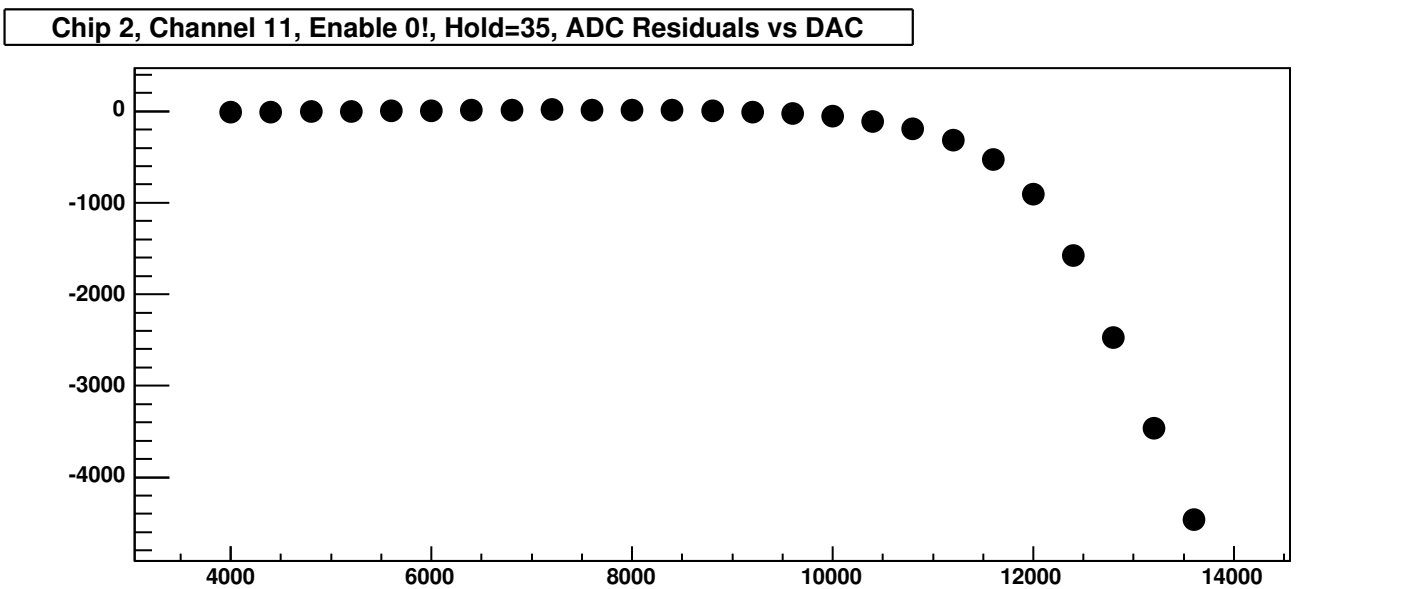


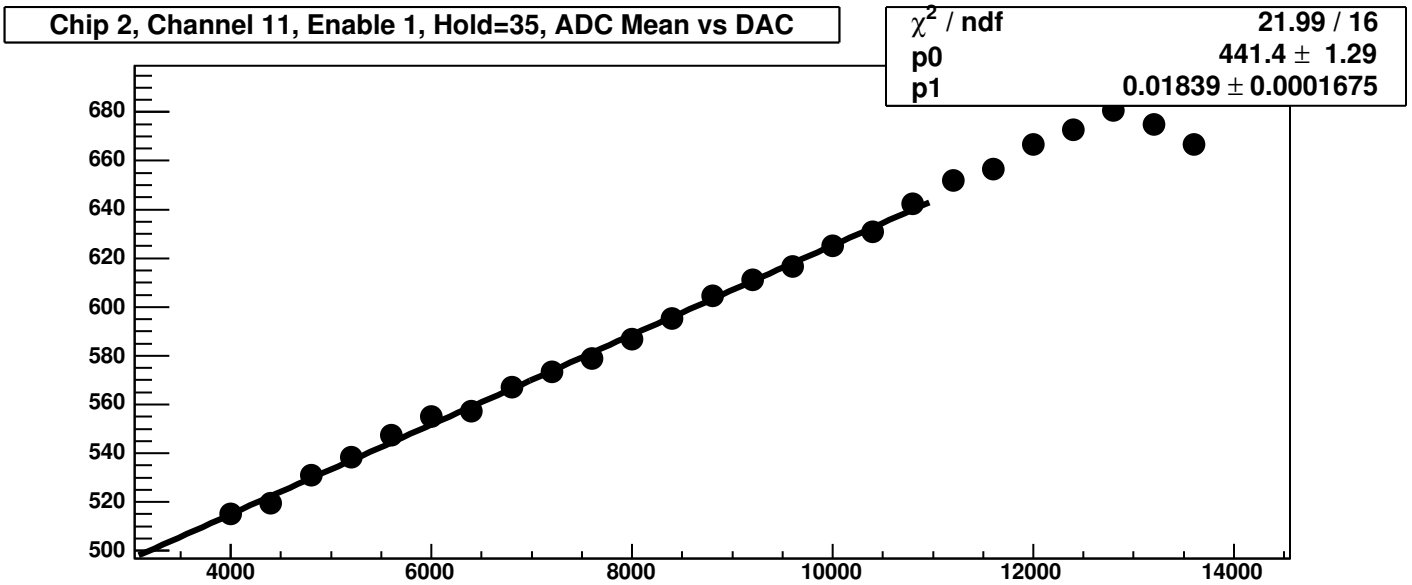
Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

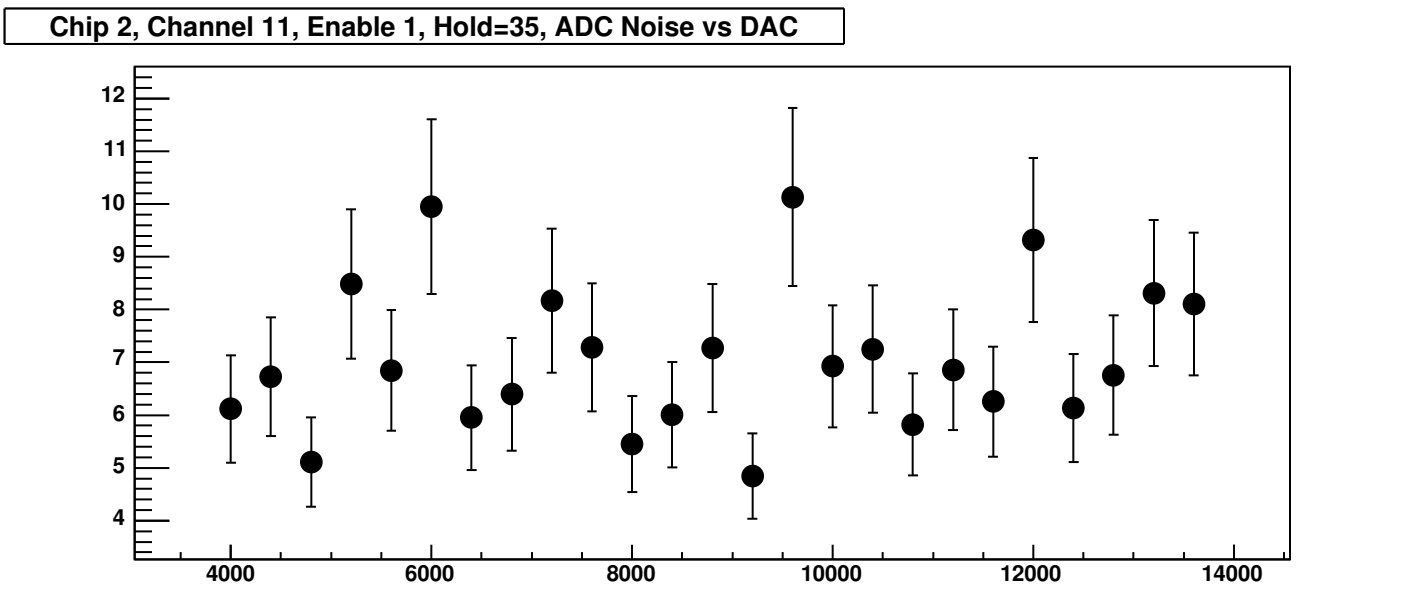


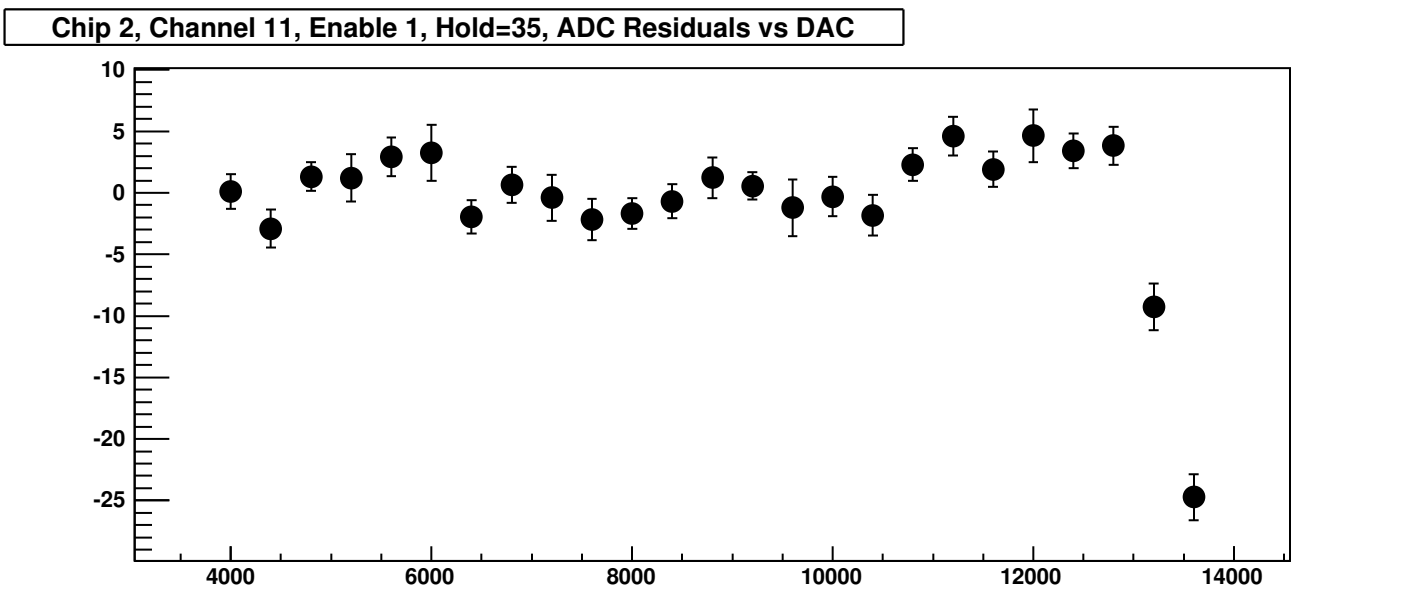

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 965.3 / 16
p0 642.7 ± 1.828
p1 2.544 ± 0.0002756
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

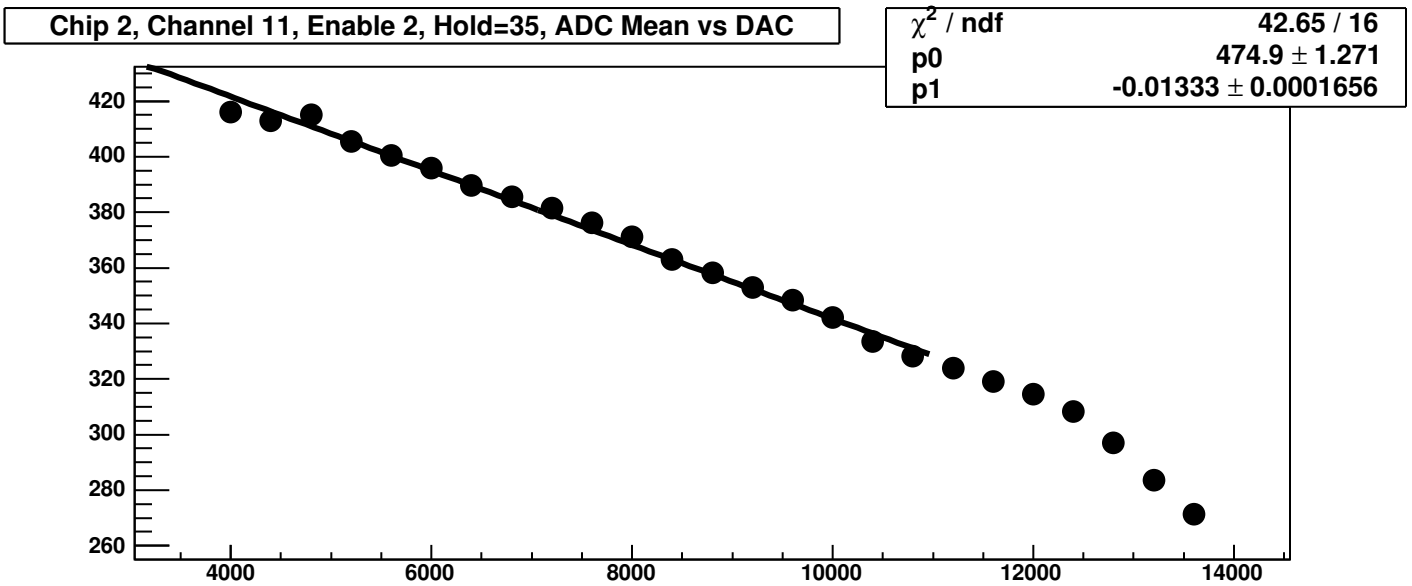

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

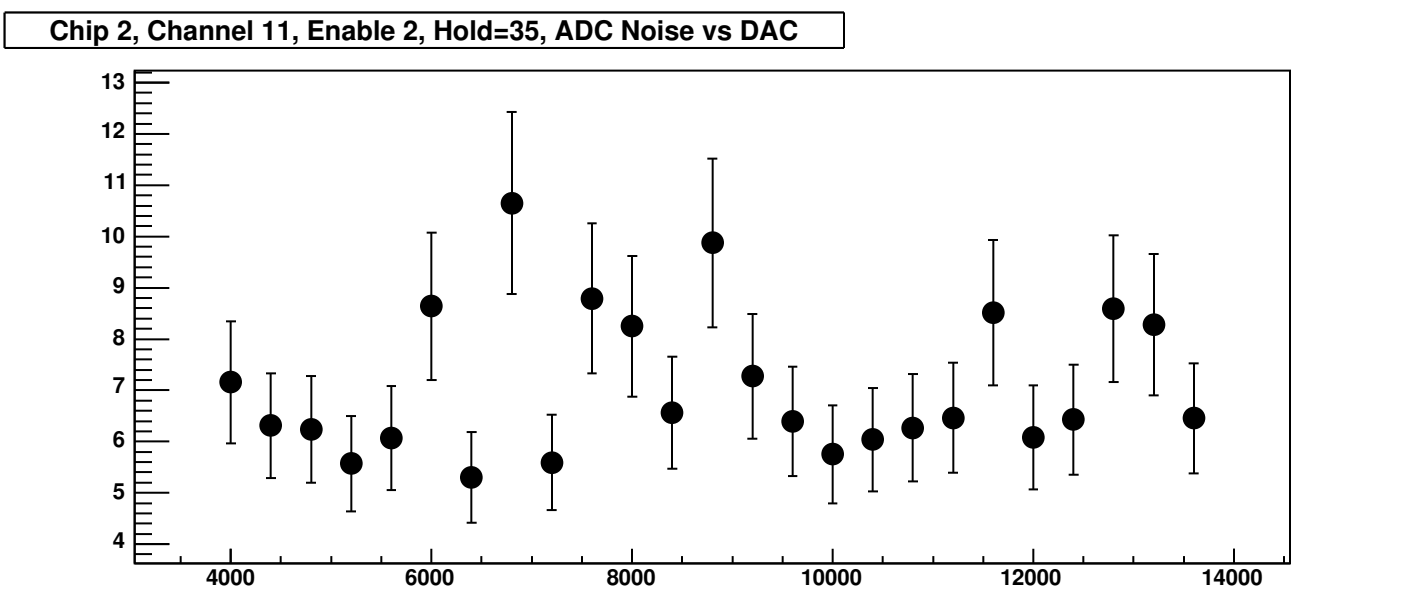

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

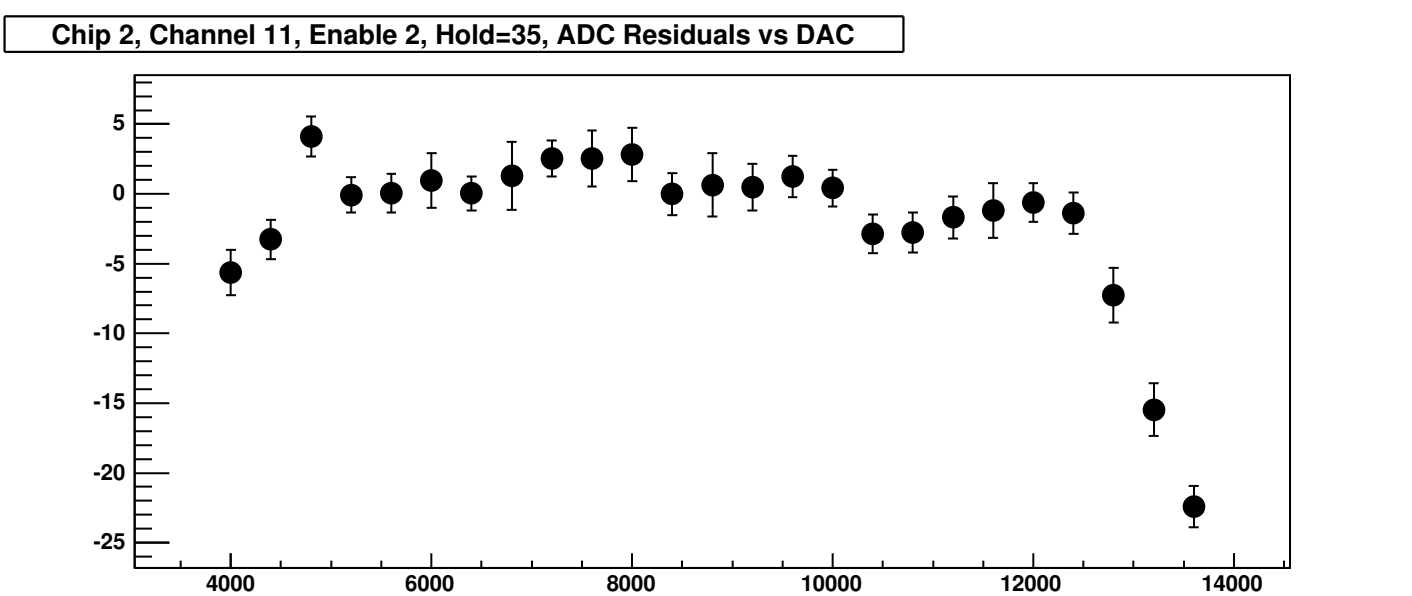

Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.99 / 16
p0 441.4 ± 1.29
p1 0.01839 ± 0.0001675
680
660
640
620
600
580
560
540
520
500
4000
6000
8000
10000
12000
14000

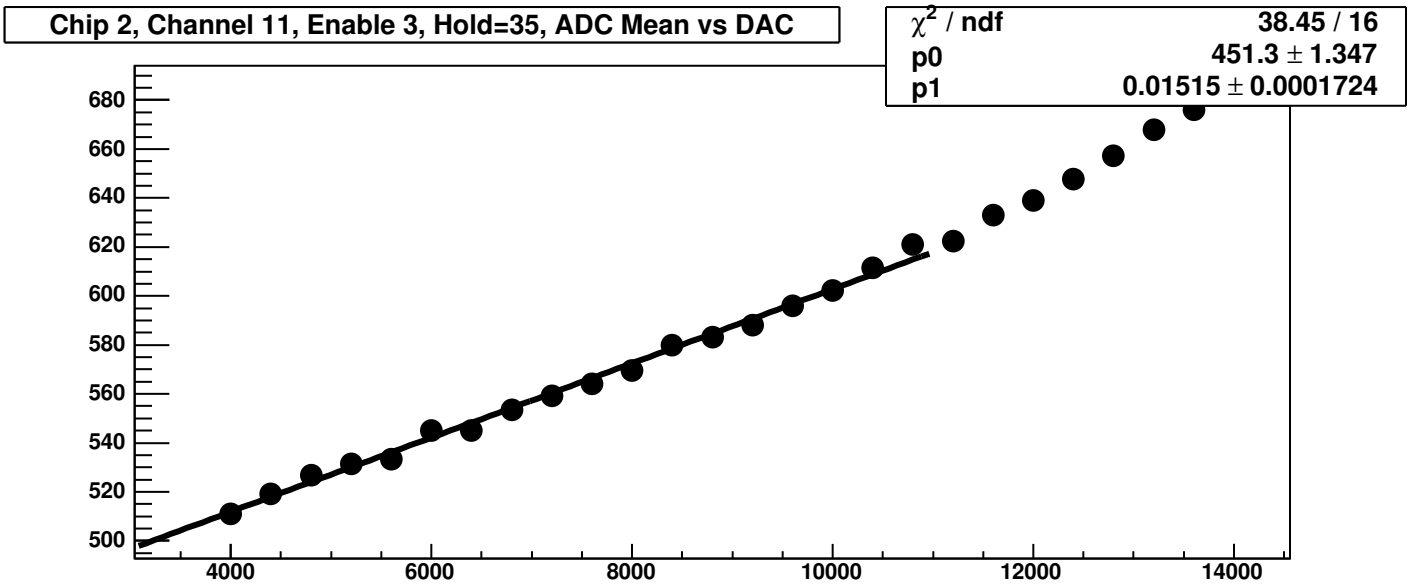

Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

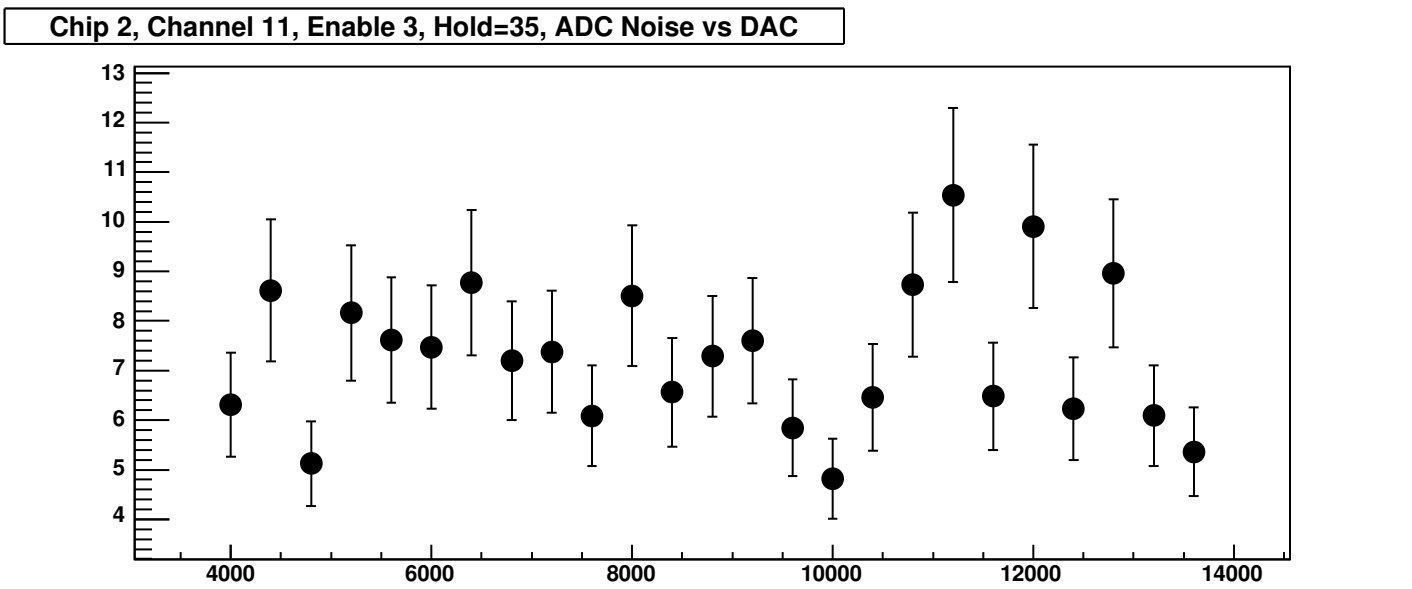

Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

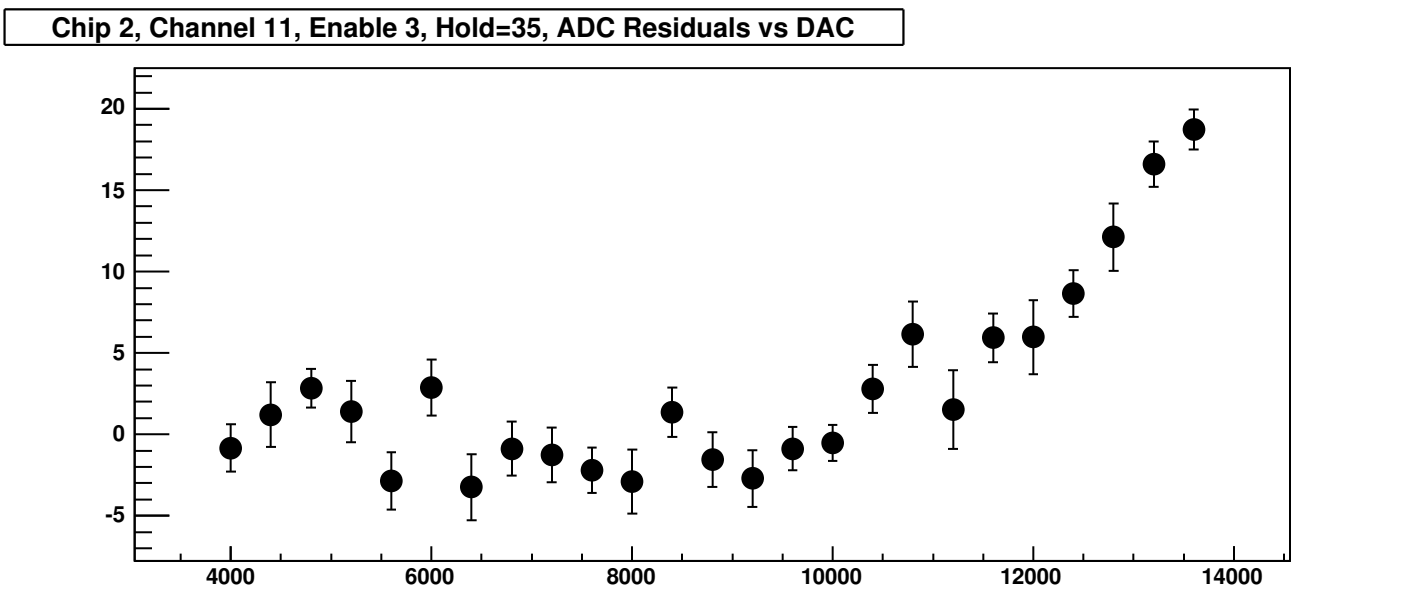

Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 42.65 / 16
p0 474.9 ± 1.271
p1 -0.01333 ± 0.0001656
420
400
380
360
340
320
300
280
260
4000
6000
8000
10000
12000
14000

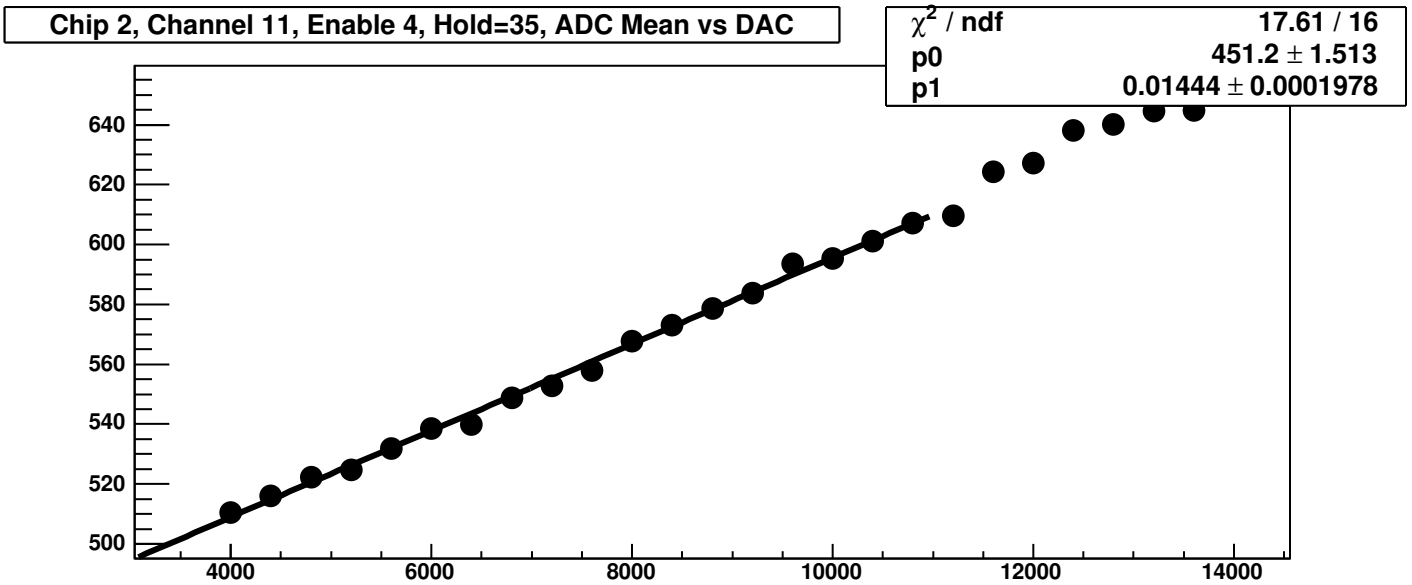

Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

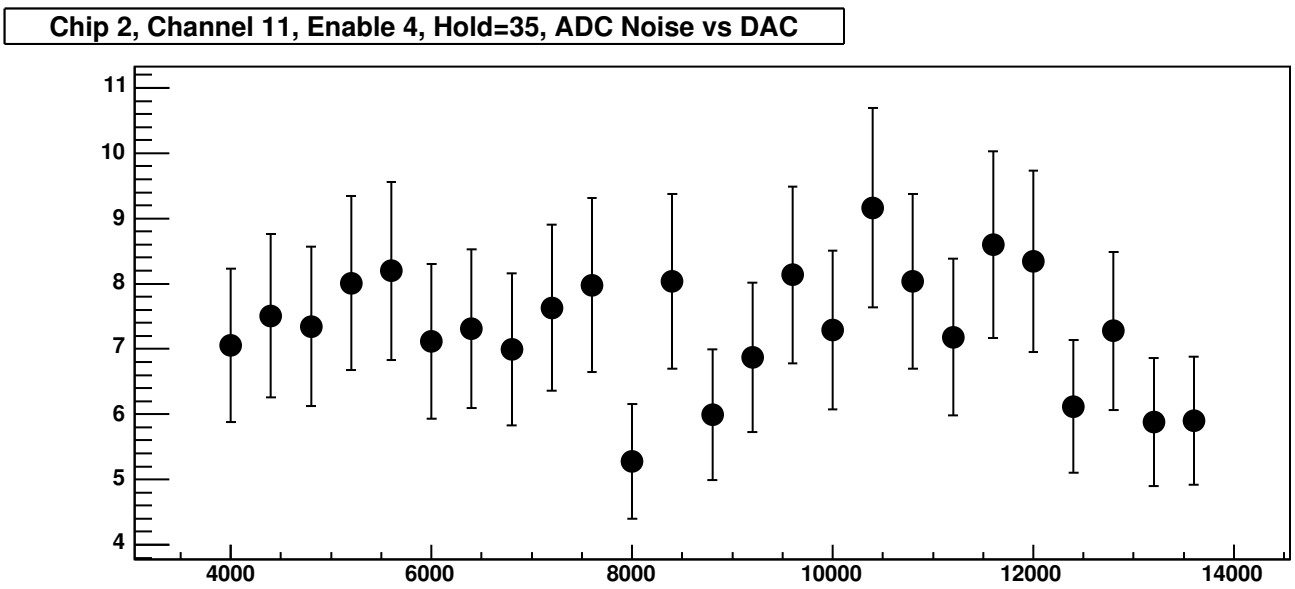

Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

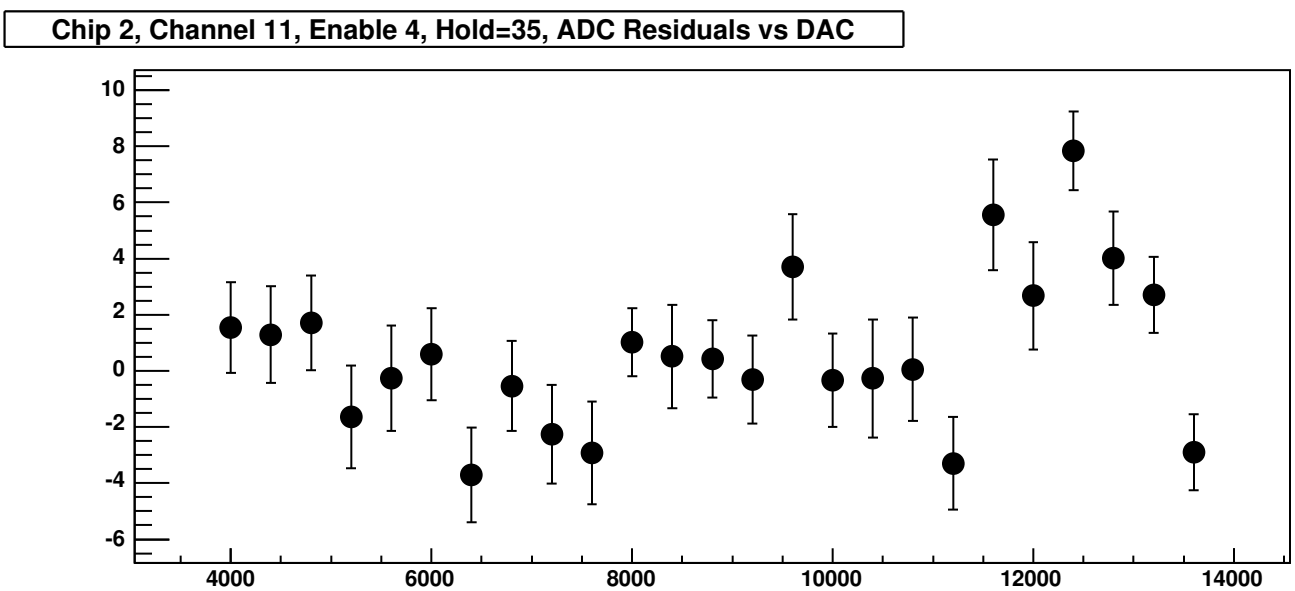

Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.45 / 16
p0 451.3 ± 1.347
p1 0.01515 ± 0.0001724
680
660
640
620
600
580
560
540
520
500
4000
6000
8000
10000
12000
14000

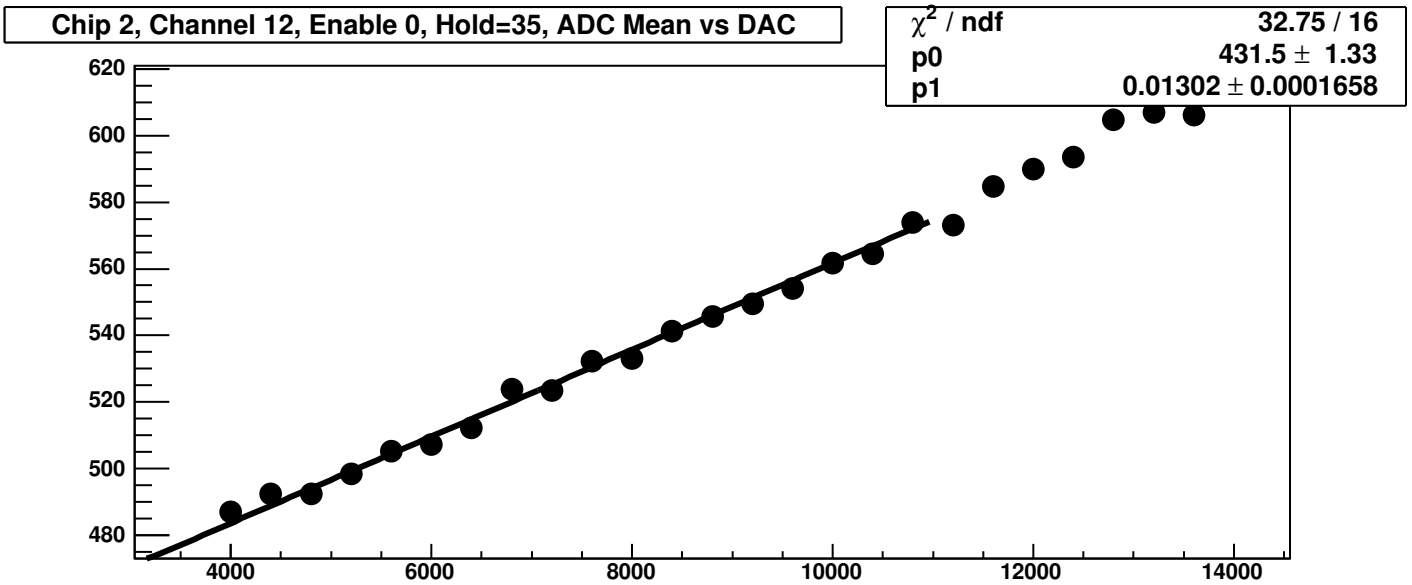

Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

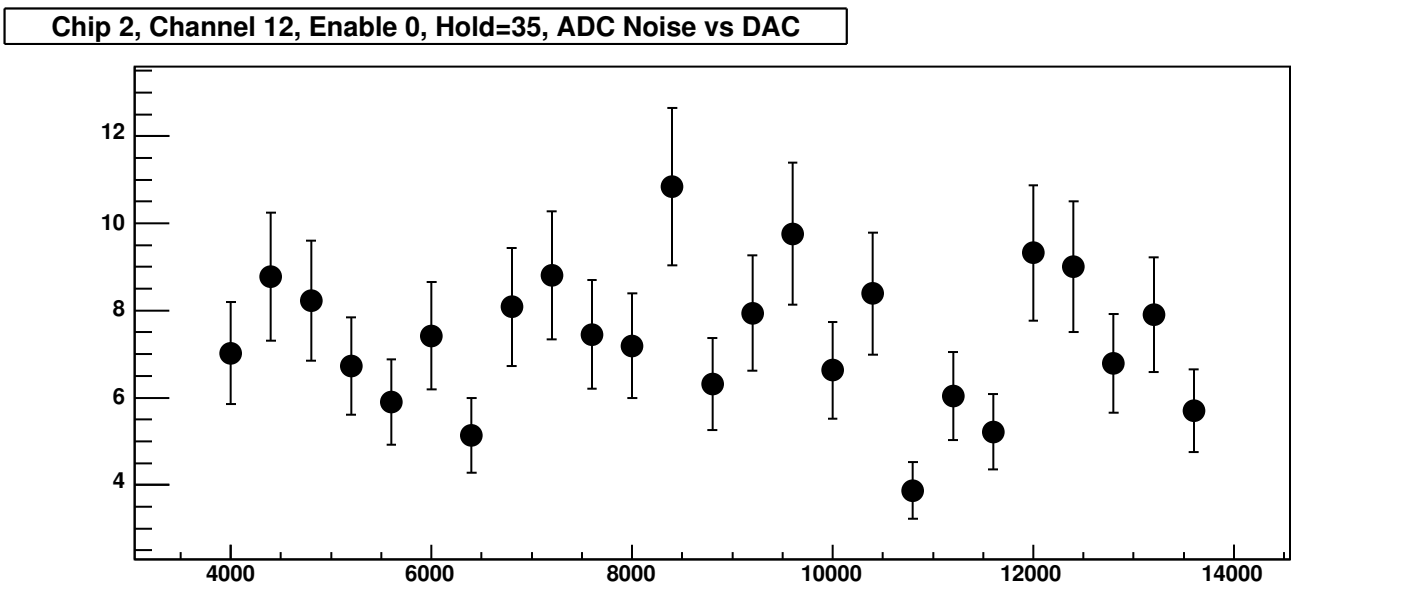

Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

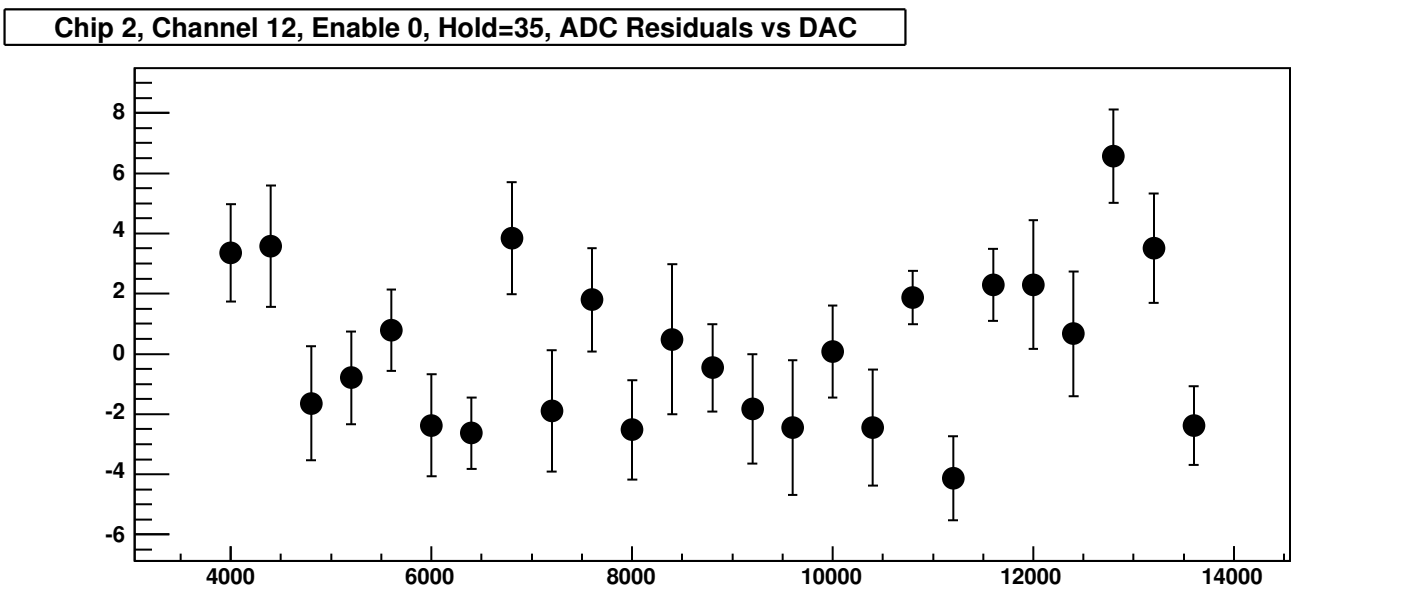

Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.61 / 16
p0 451.2 ± 1.513
p1 0.01444 ± 0.0001978
640
620
600
580
560
540
520
500
4000
6000
8000
10000
12000
14000

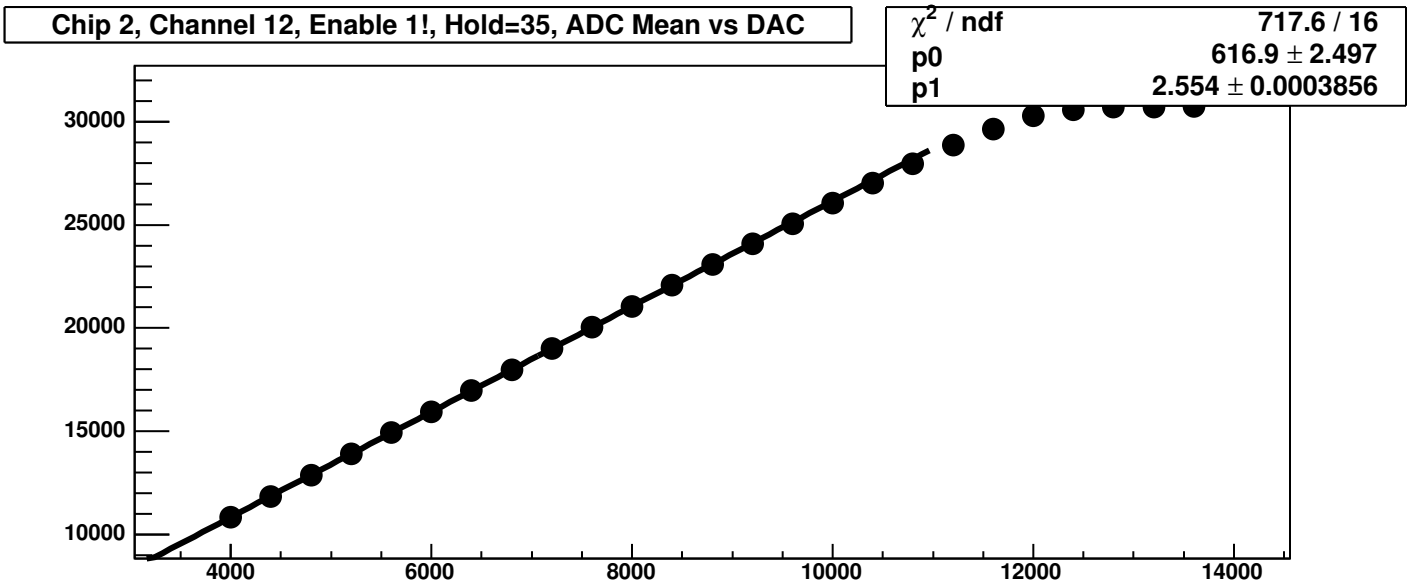

Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

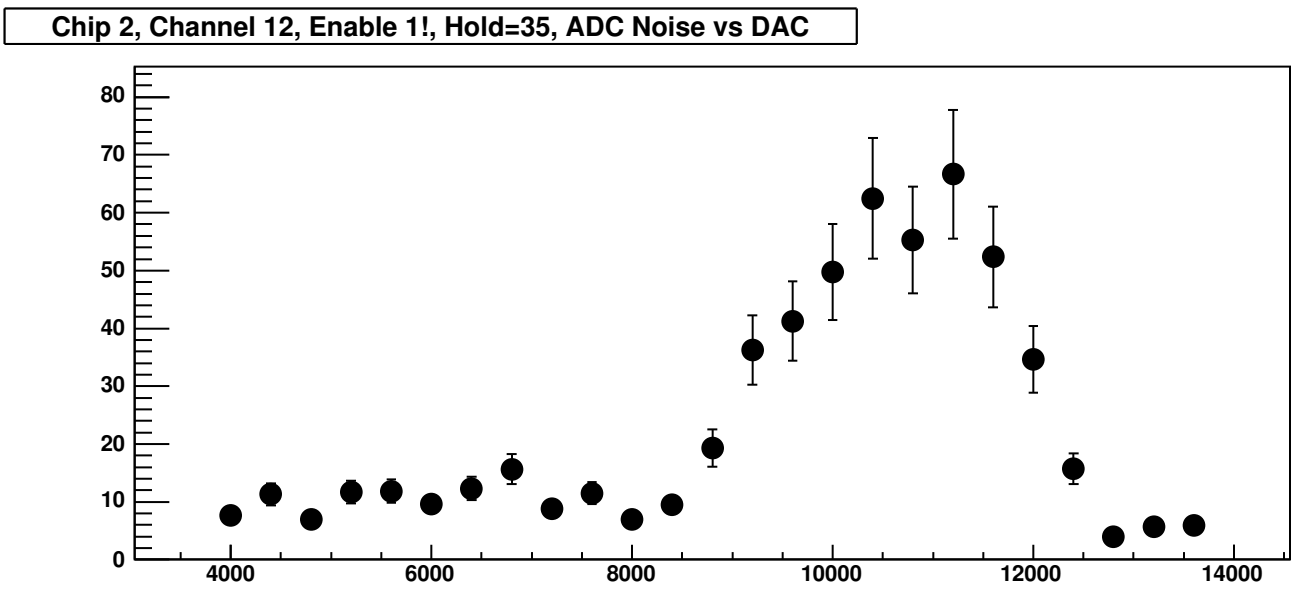

Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

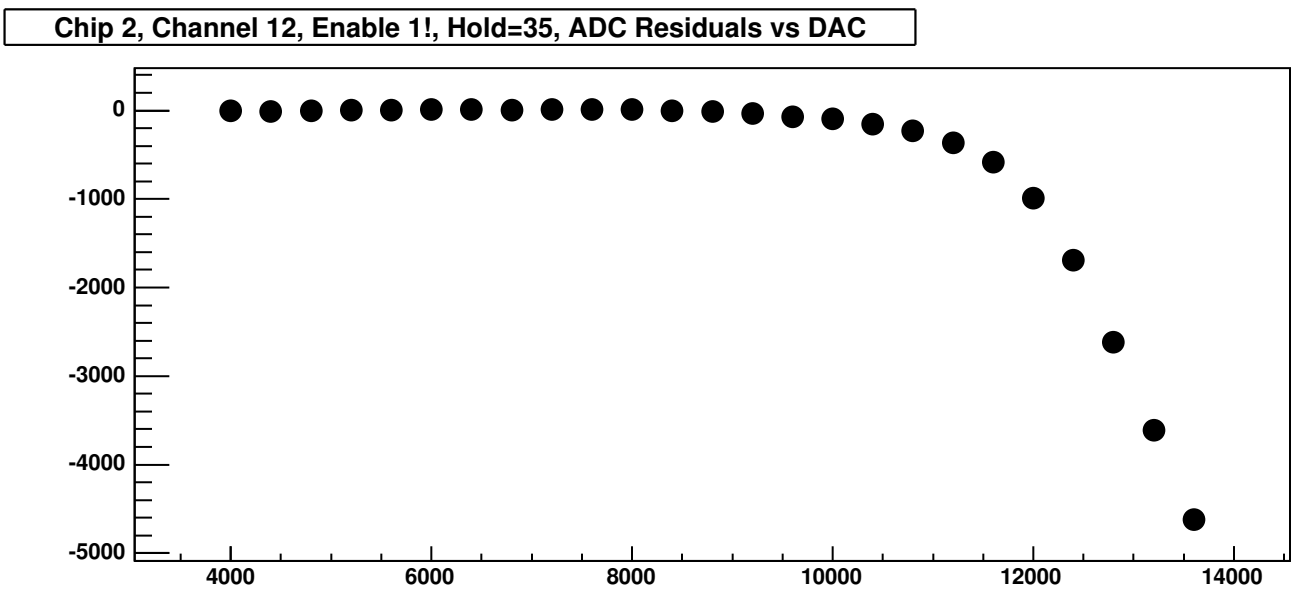

Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 32.75 / 16
p0 431.5 ± 1.33
p1 0.01302 ± 0.0001658
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

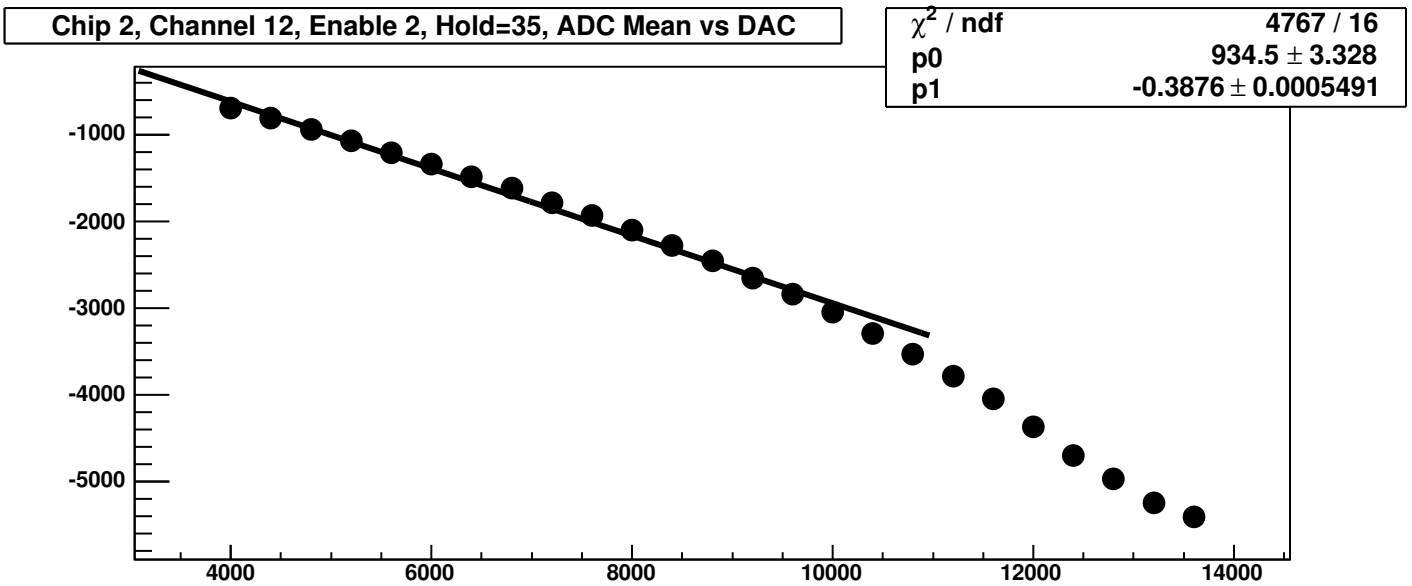

Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

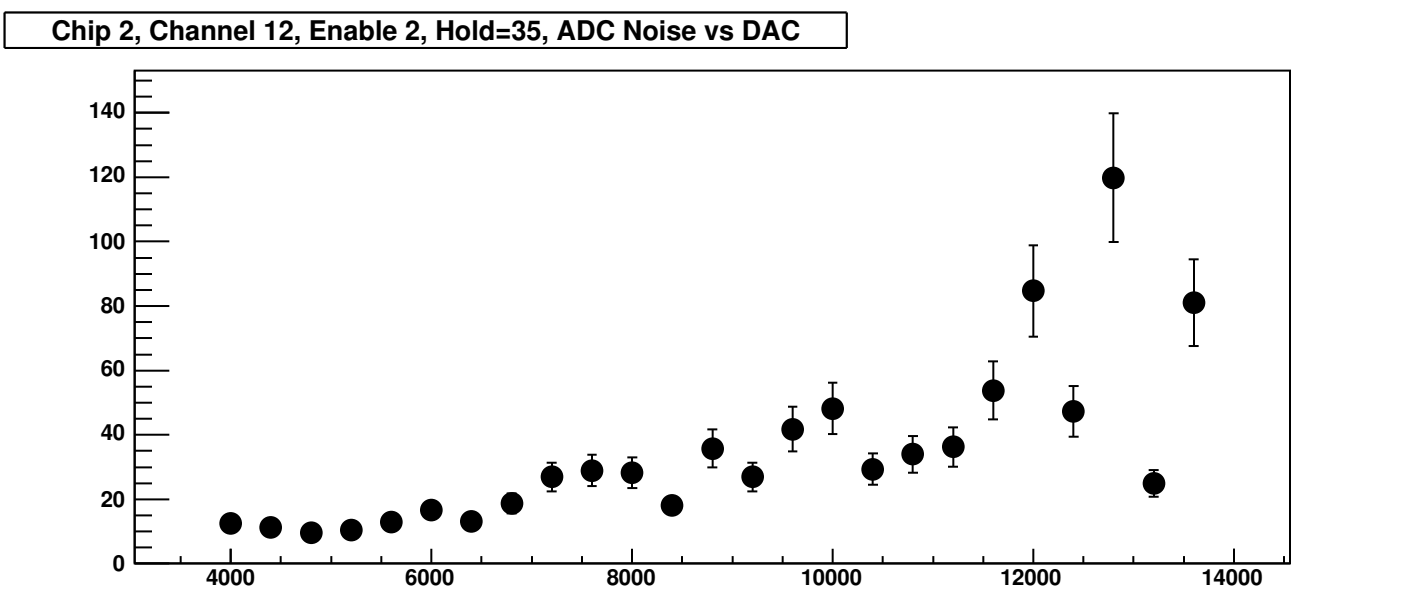

Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

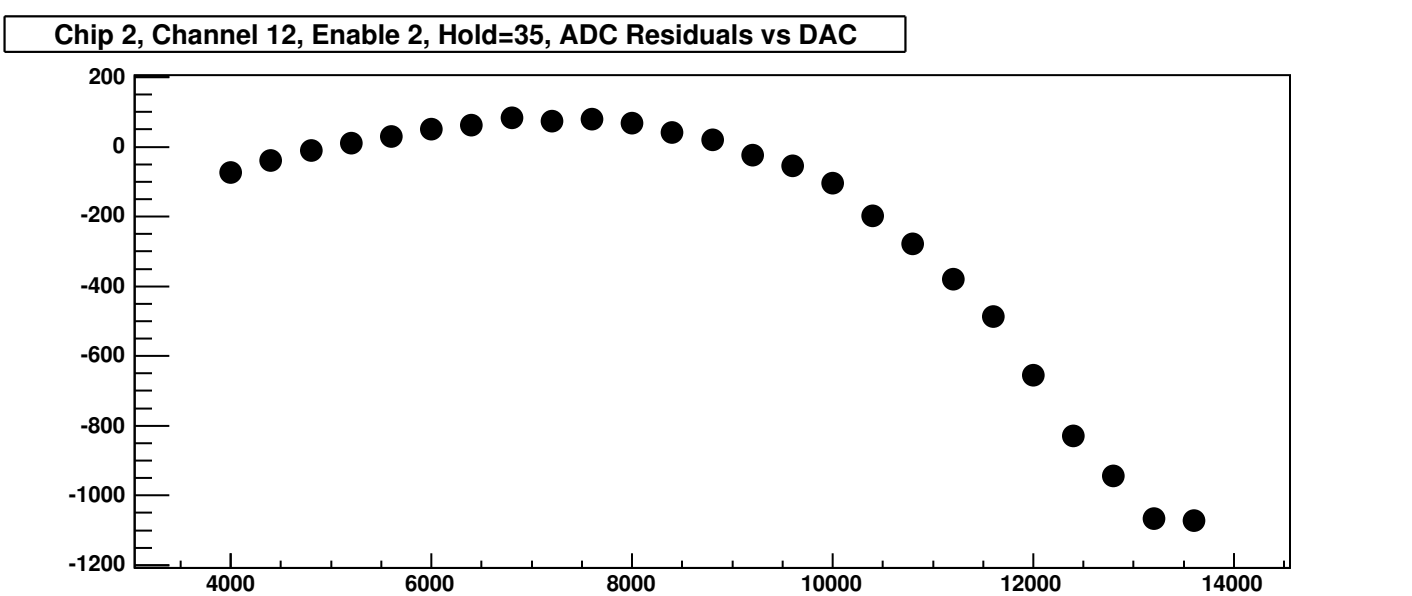

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 717.6 / 16
p0 616.9 ± 2.497
p1 2.554 ± 0.0003856
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

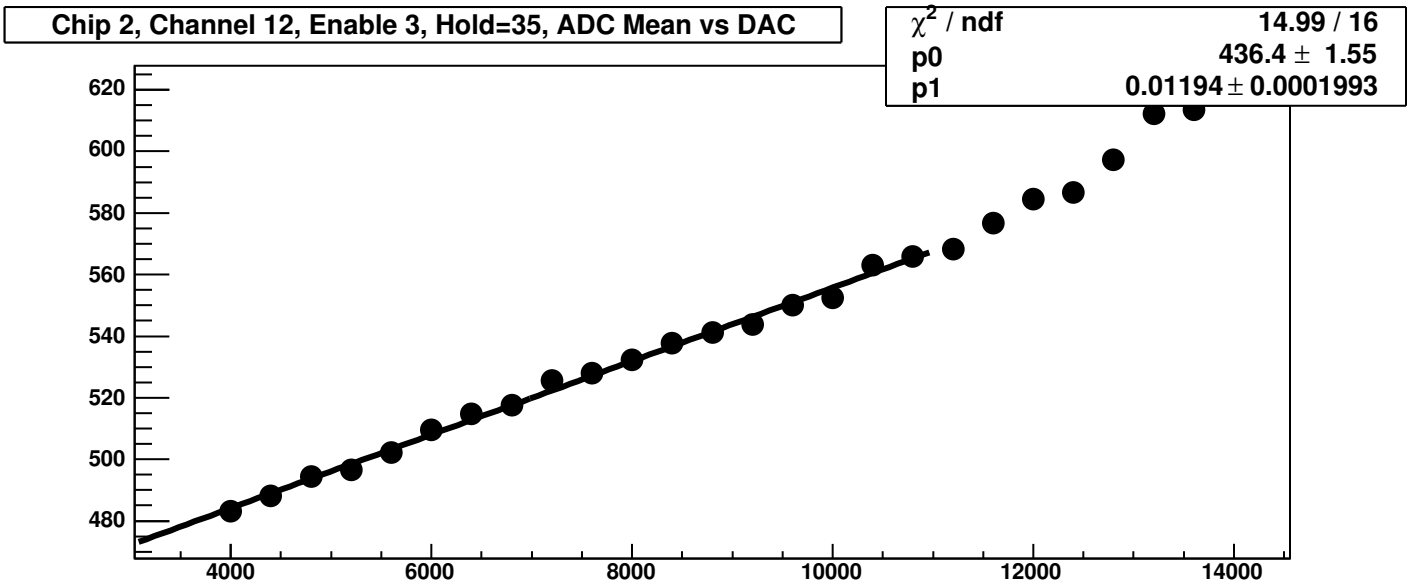

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

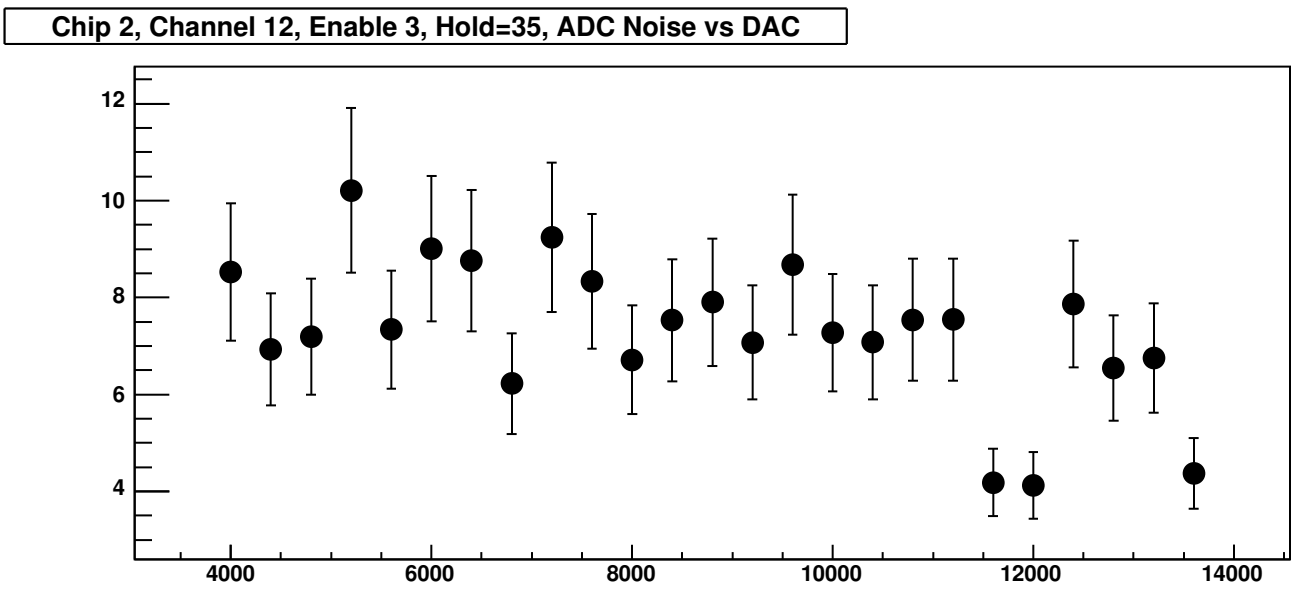

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

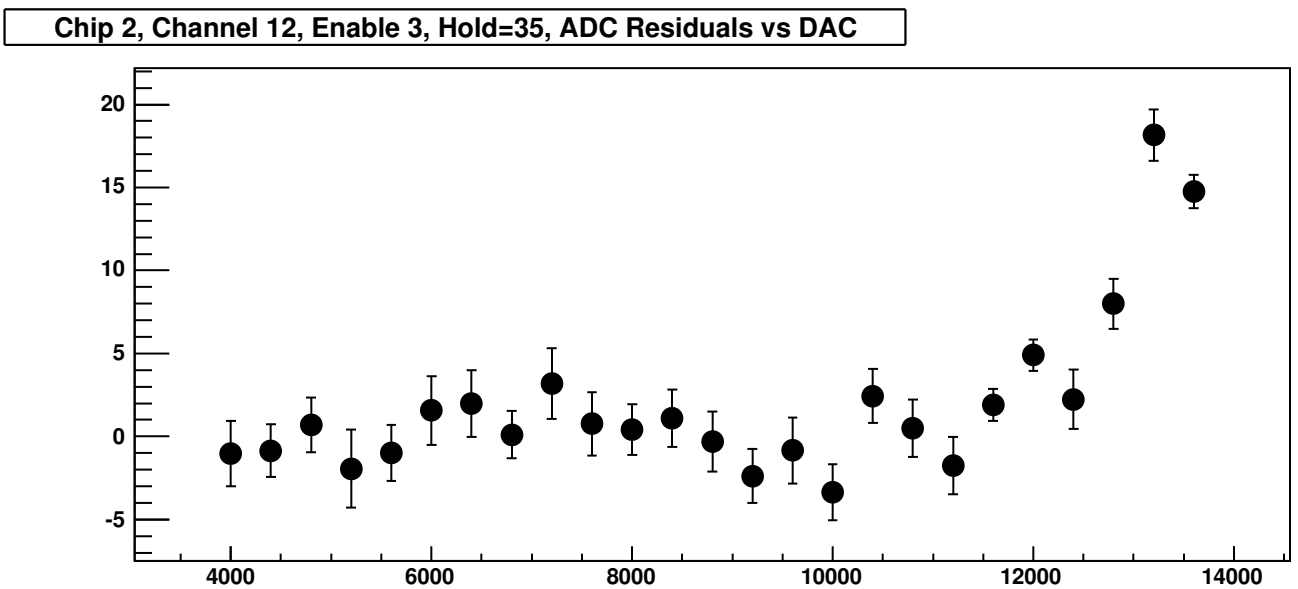

Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4767 / 16
p0 934.5 ± 3.328
p1 -0.3876 ± 0.0005491
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

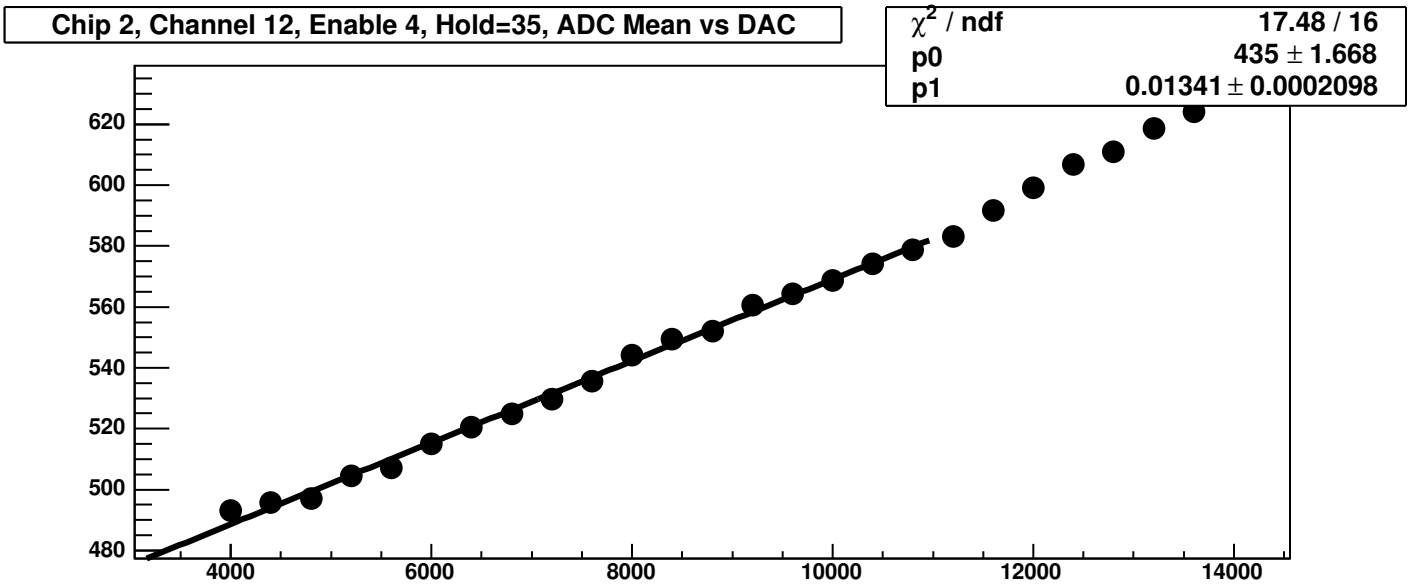

Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

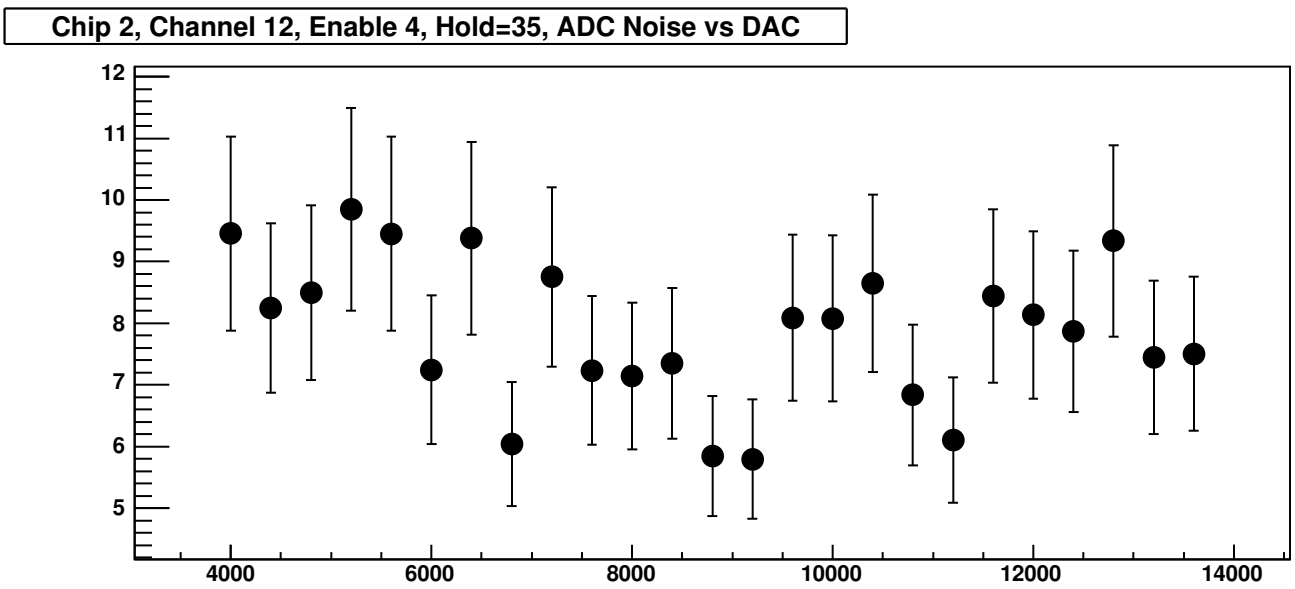

Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
4000
6000
8000
10000
12000
14000

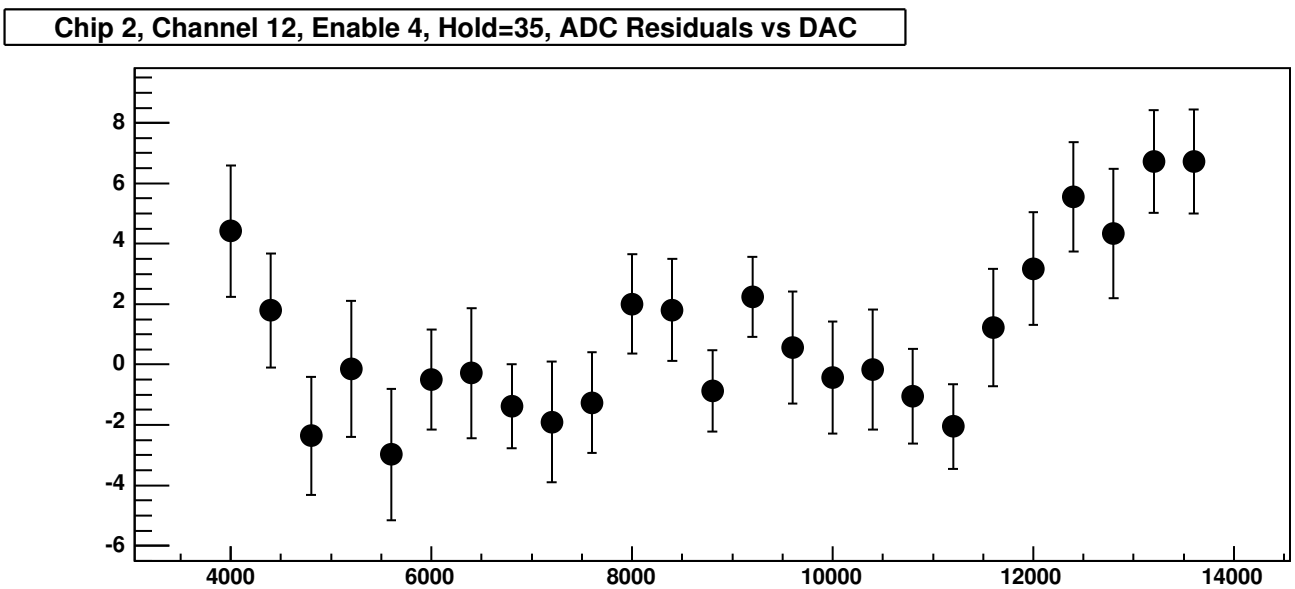

Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.99 / 16
p0 436.4 ± 1.55
p1 0.01194 ± 0.0001993
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

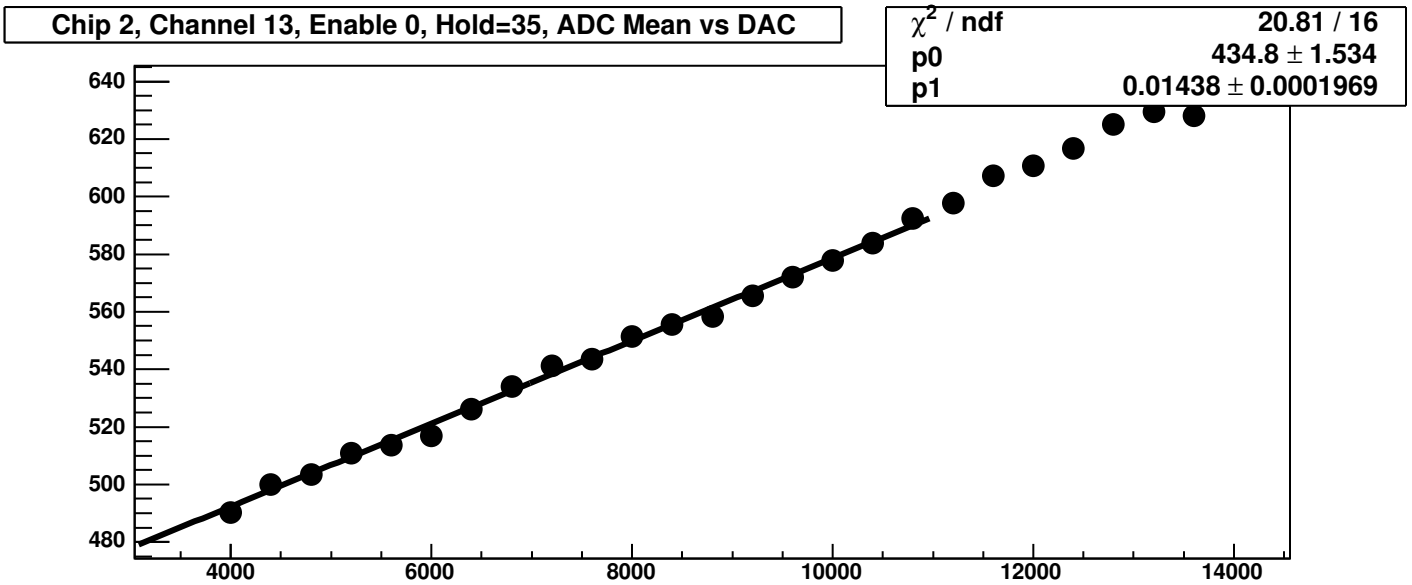

Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

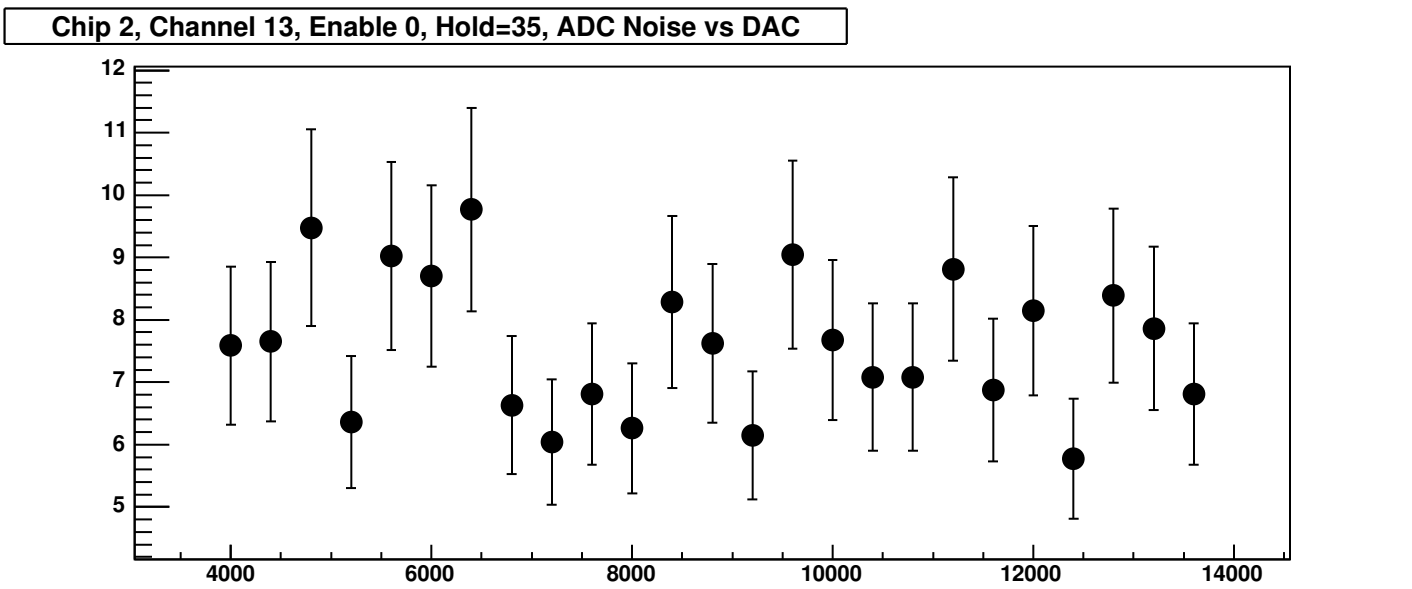

Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

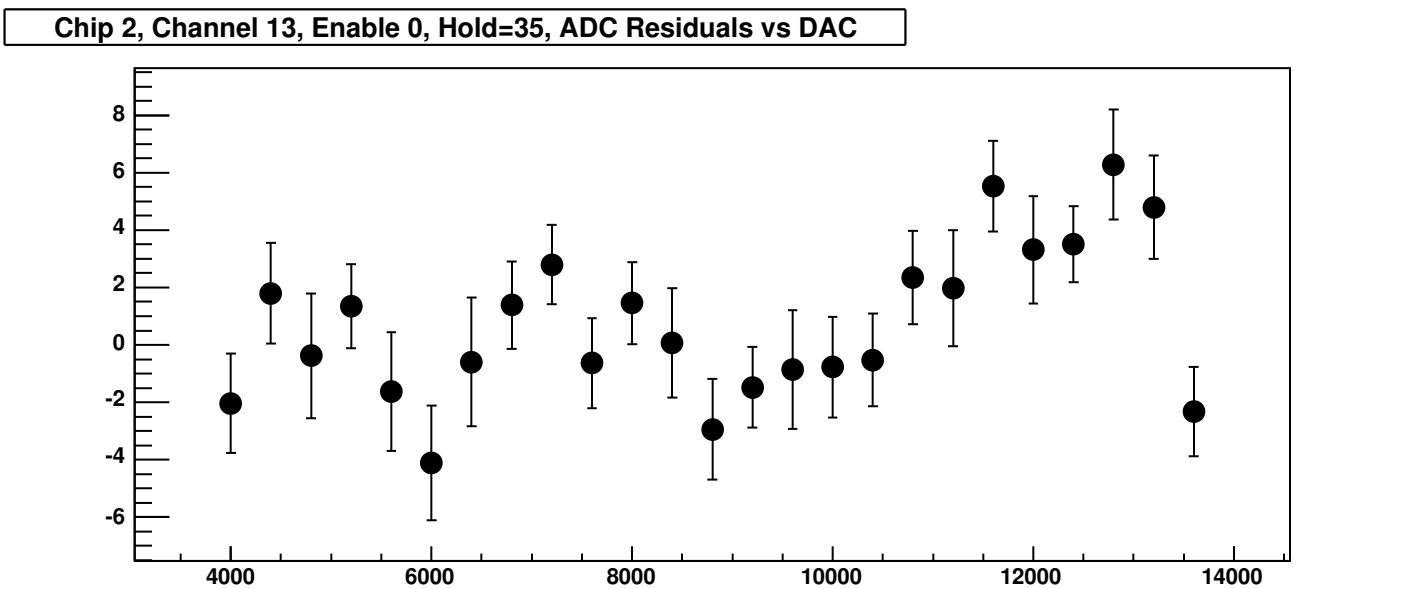

Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.48 / 16
p0 435 ± 1.668
p1 0.01341 ± 0.0002098
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

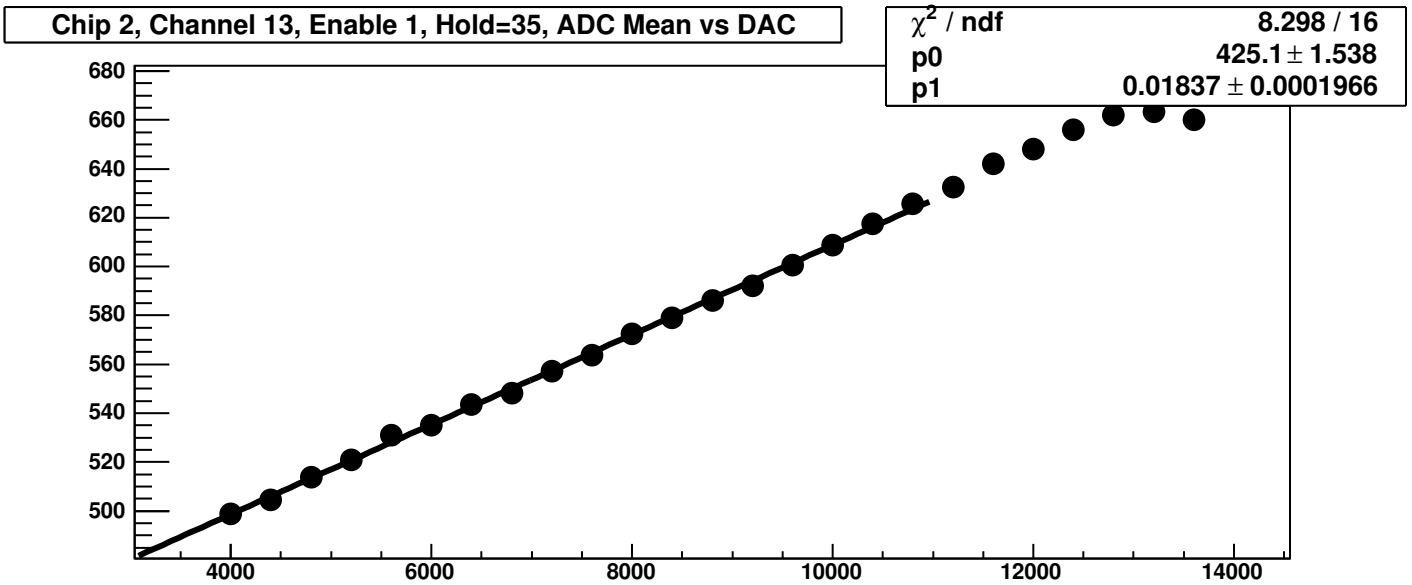

Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

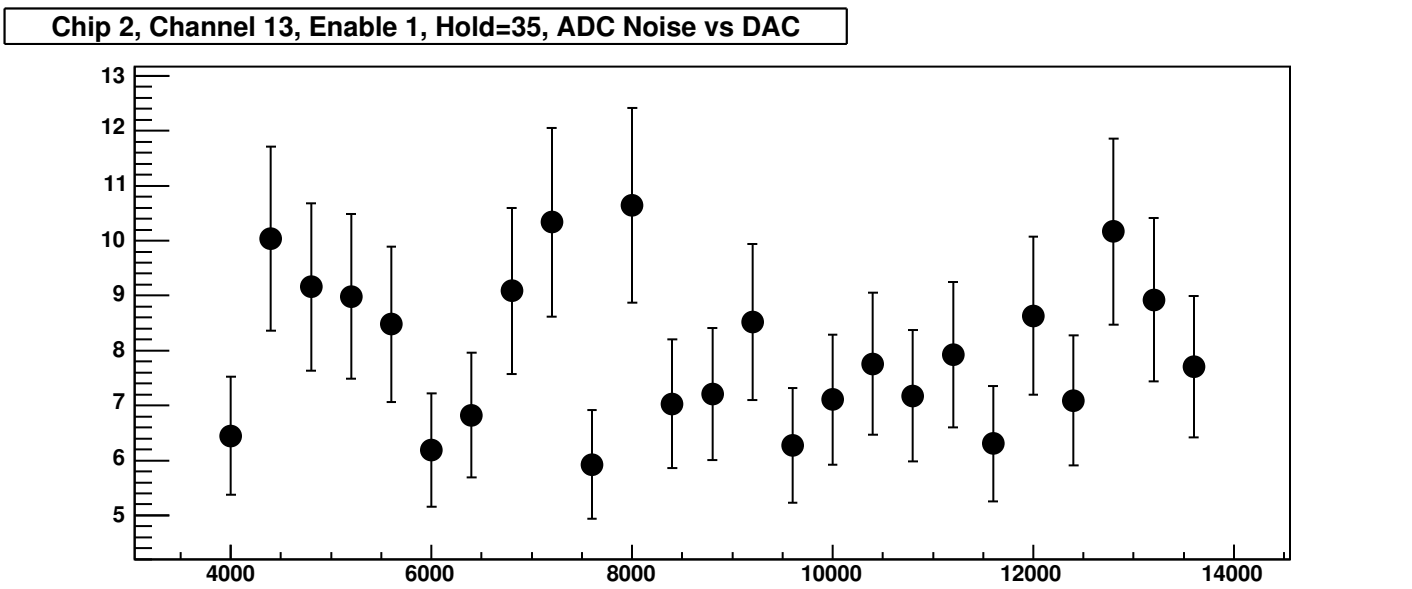

Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

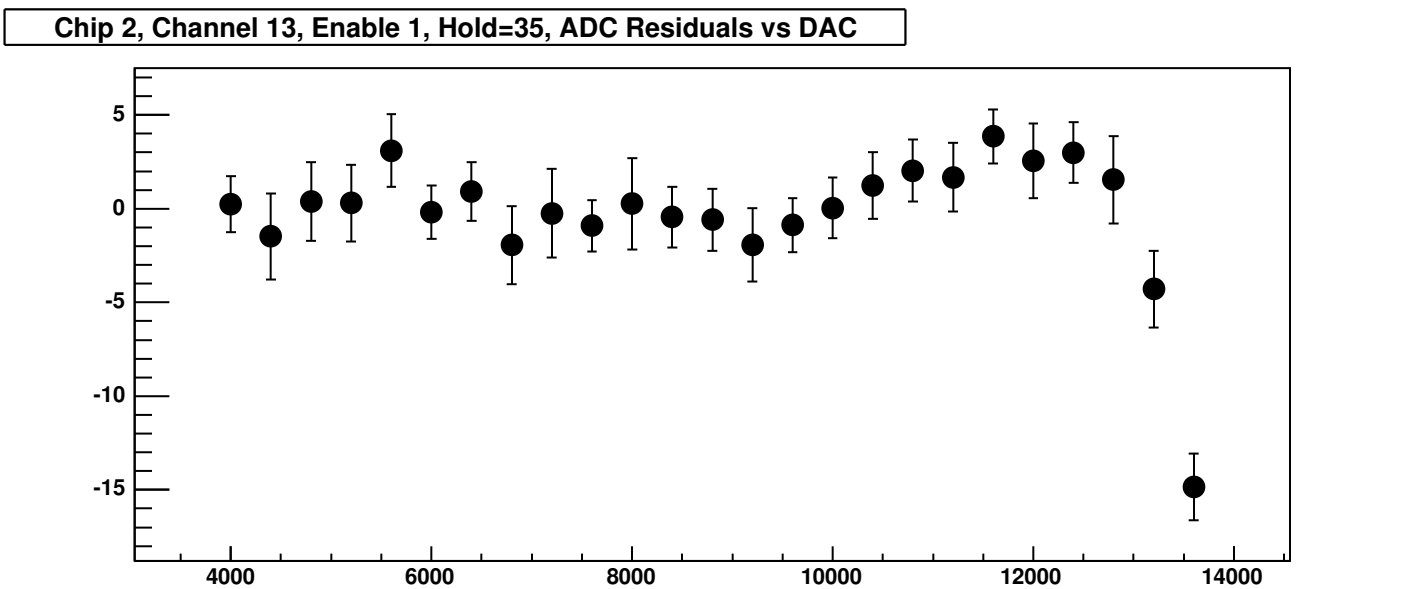

Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20.81 / 16
p0 434.8 ± 1.534
p1 0.01438 ± 0.0001969
640
620
600
580
560
540
520
500
480
4000
6000
8000
10000
12000
14000

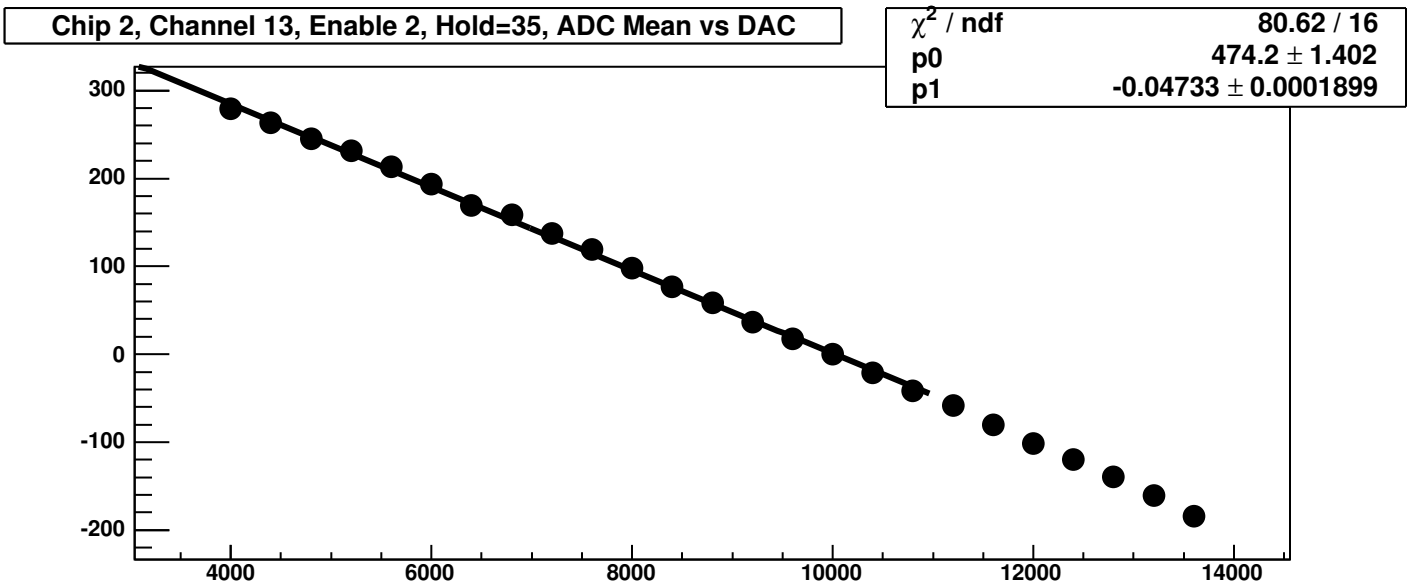

Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

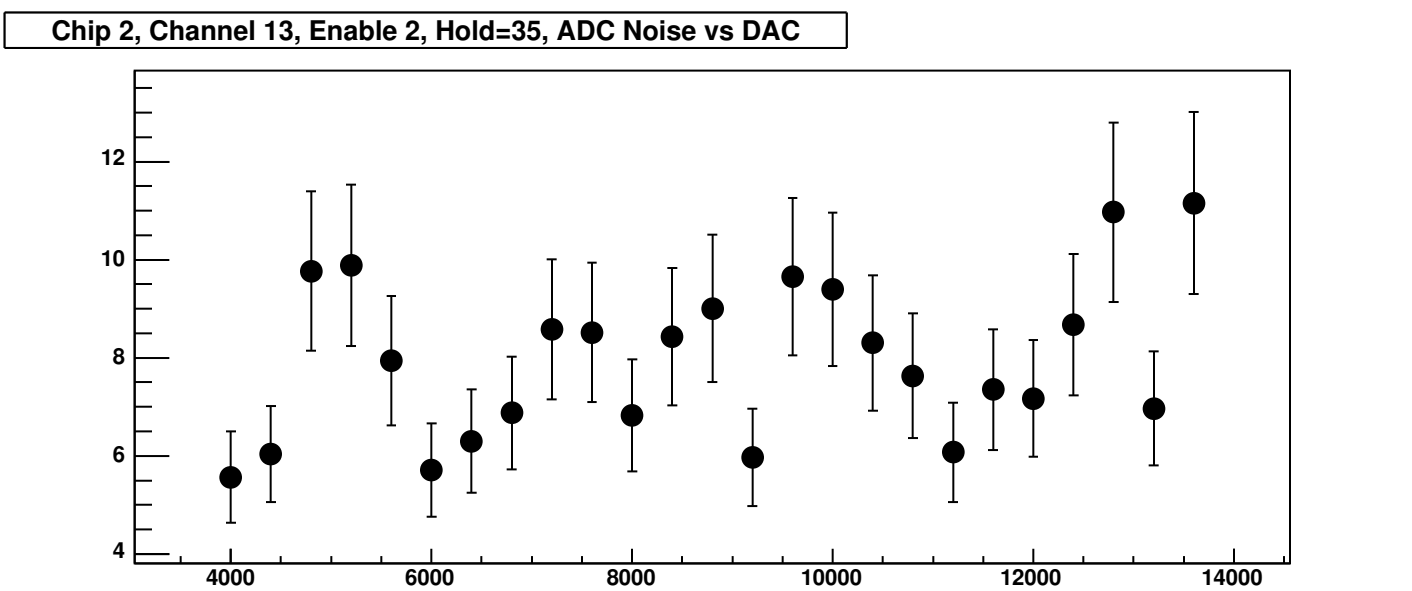

Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

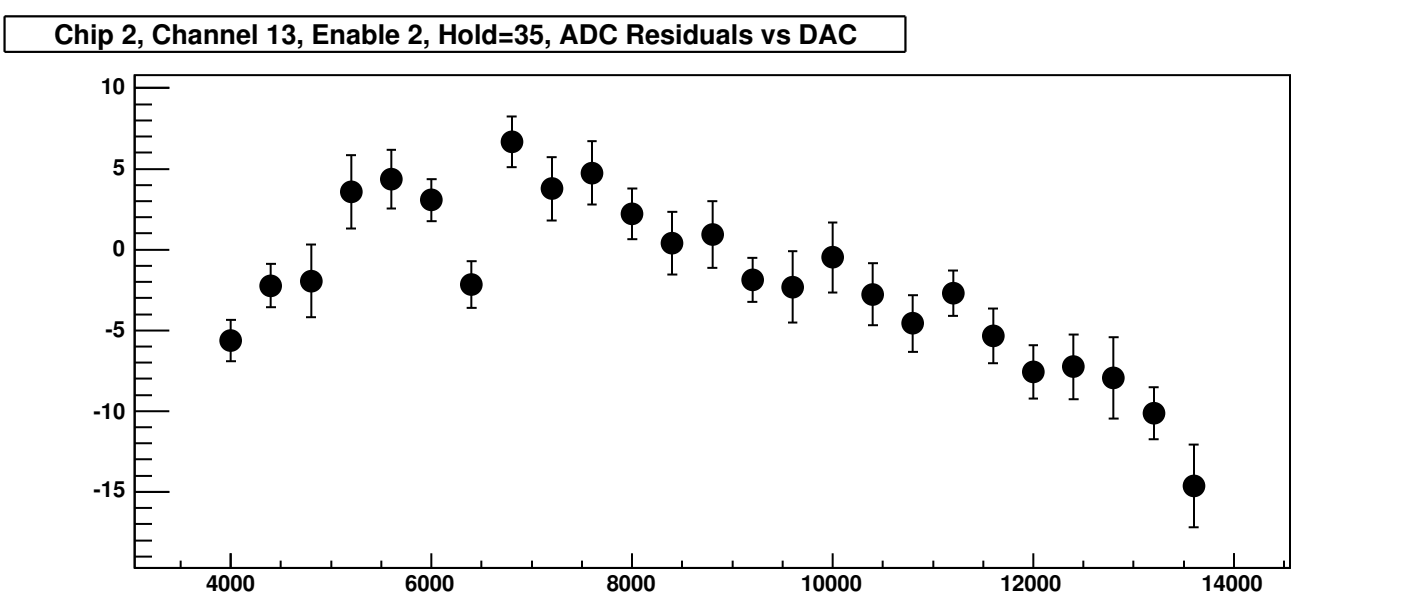

Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 8.298 / 16
p0 425.1 ± 1.538
p1 0.01837 ± 0.0001966
680
660
640
620
600
580
560
540
520
500
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

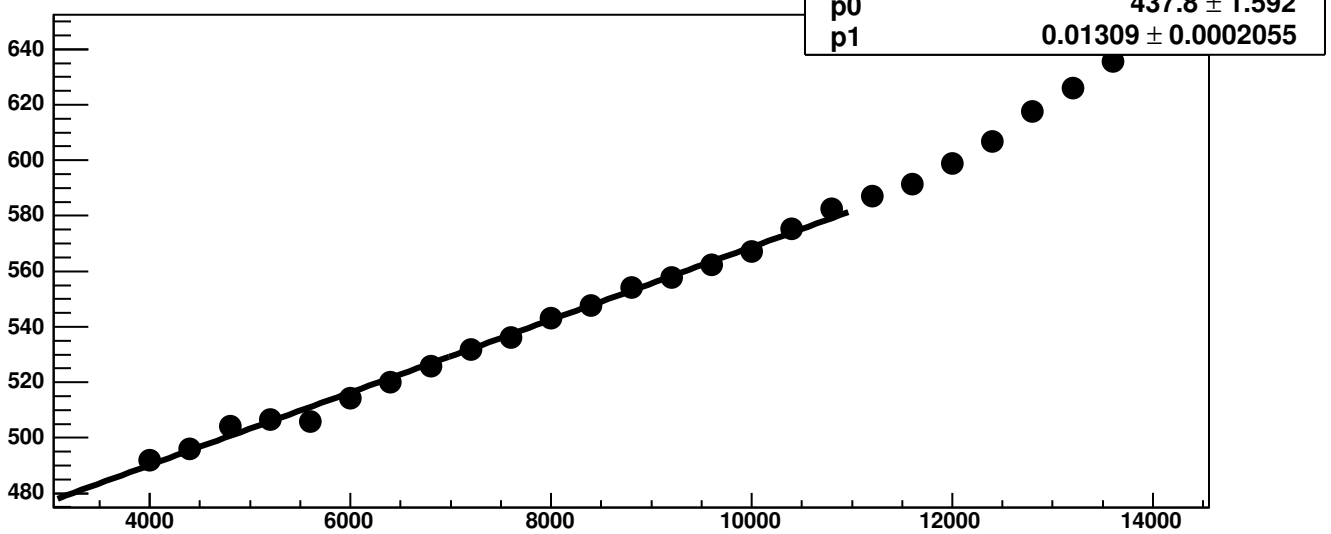

Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 80.62 / 16
p0 474.2 ± 1.402
p1 -0.04733 ± 0.0001899
300
200
100
0
-100
-200
4000
6000
8000
10000
12000
14000

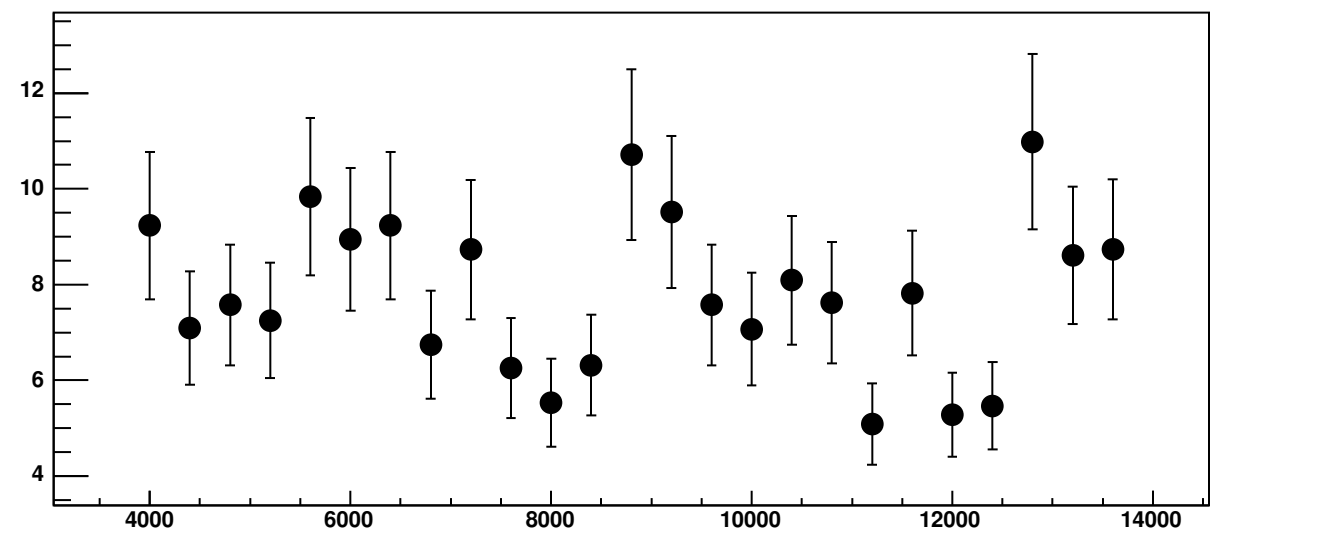

Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

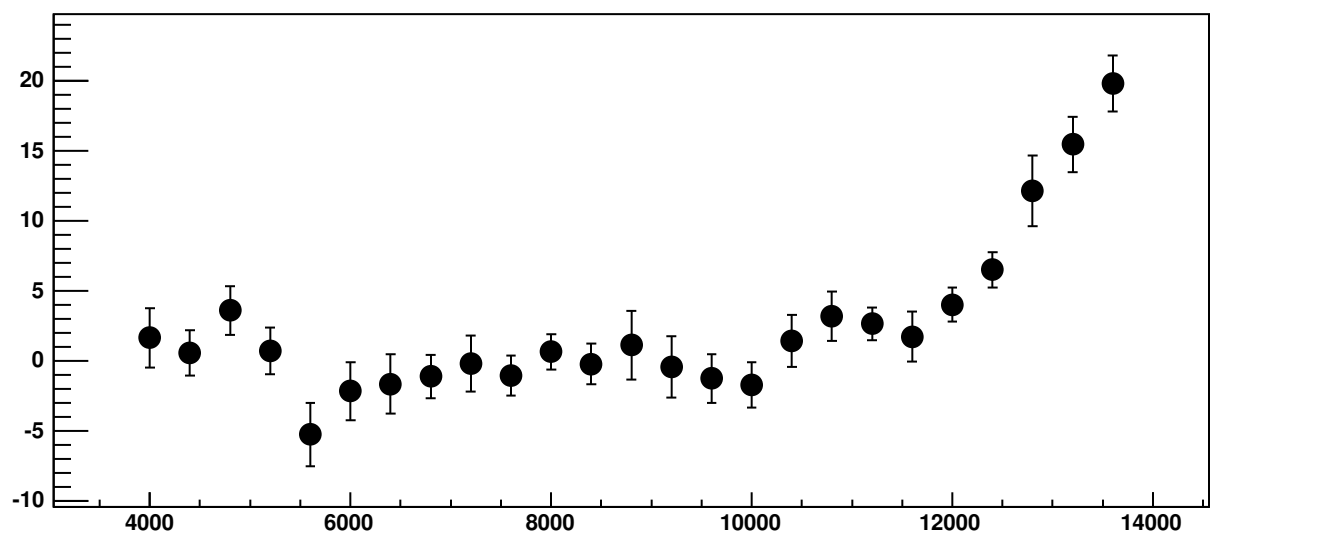
Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

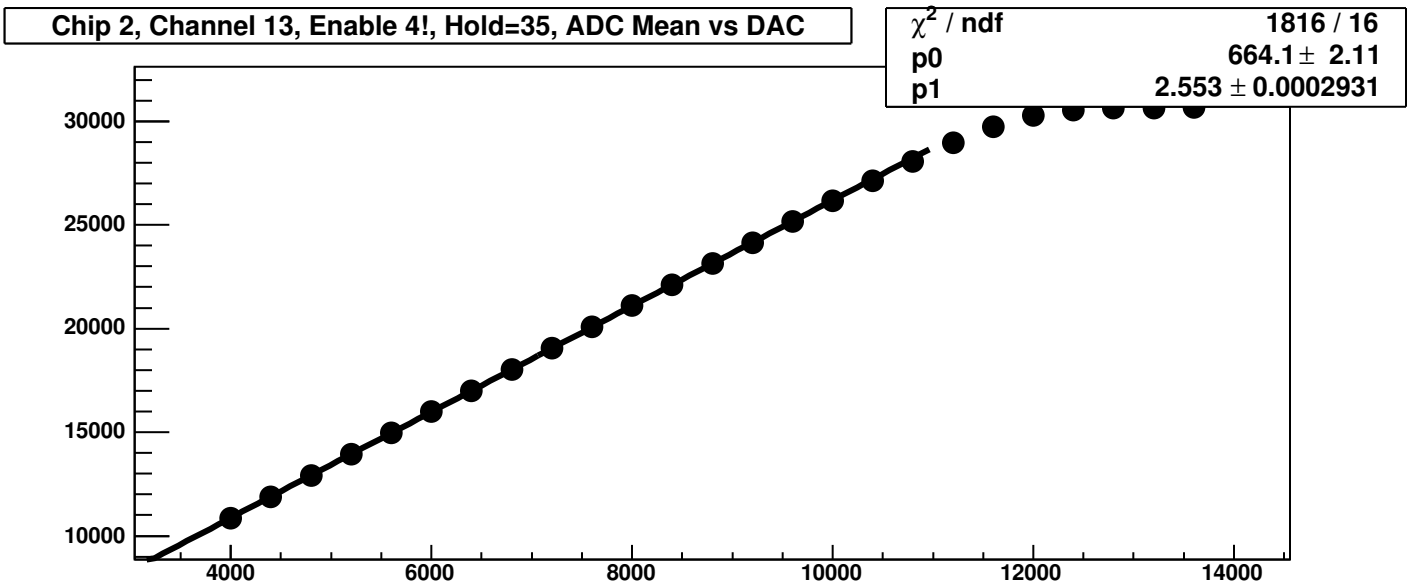


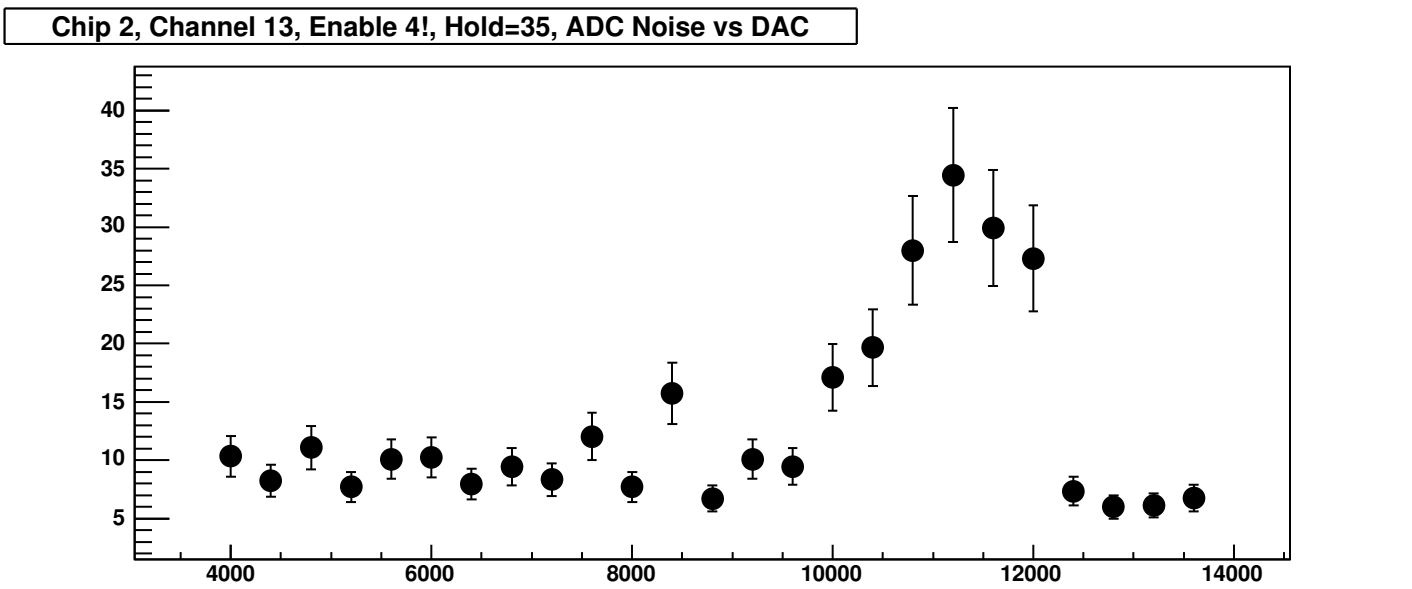
Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

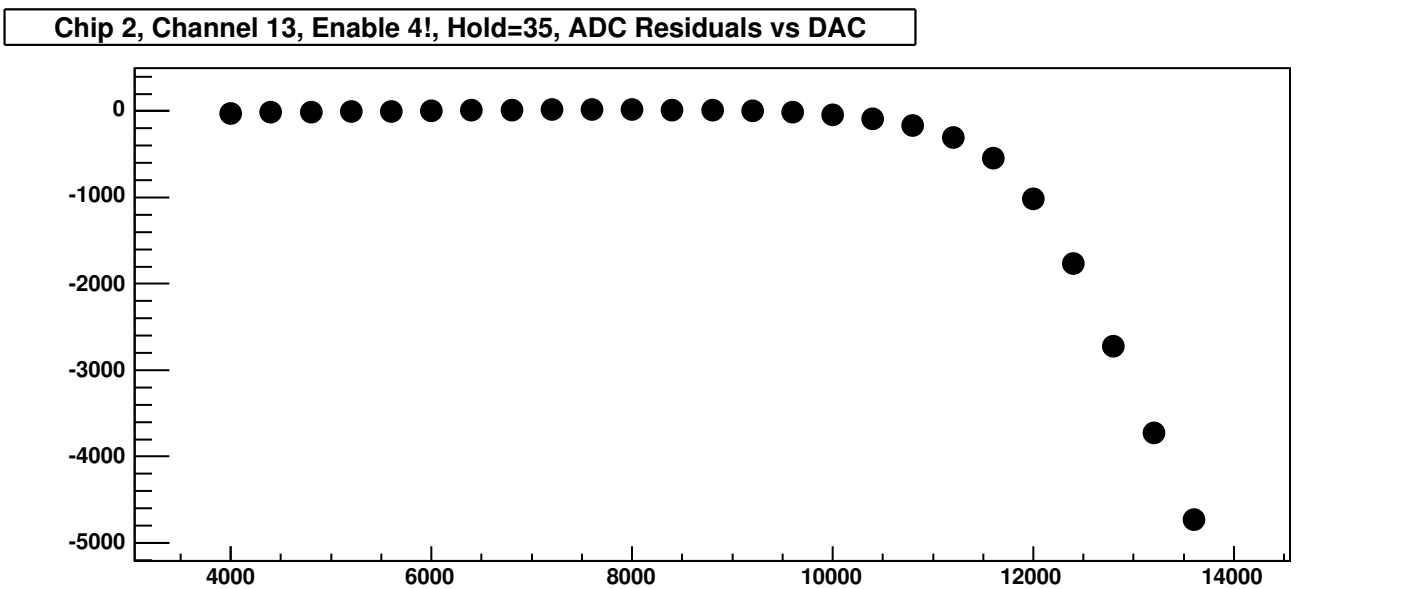


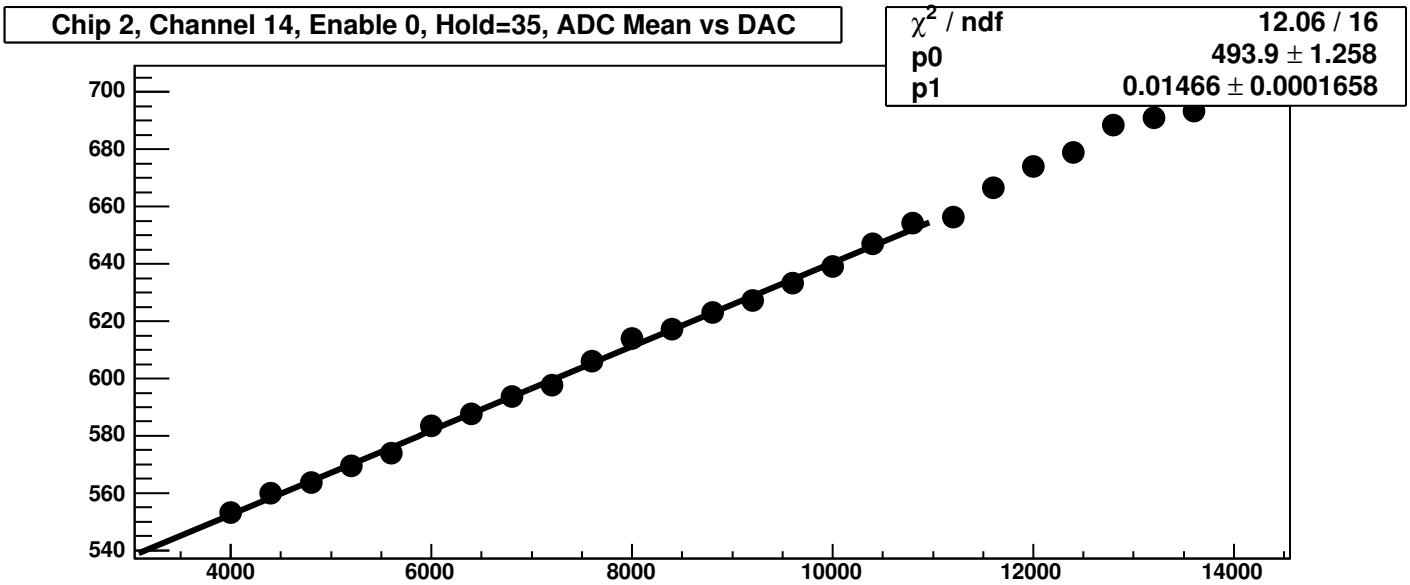
Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

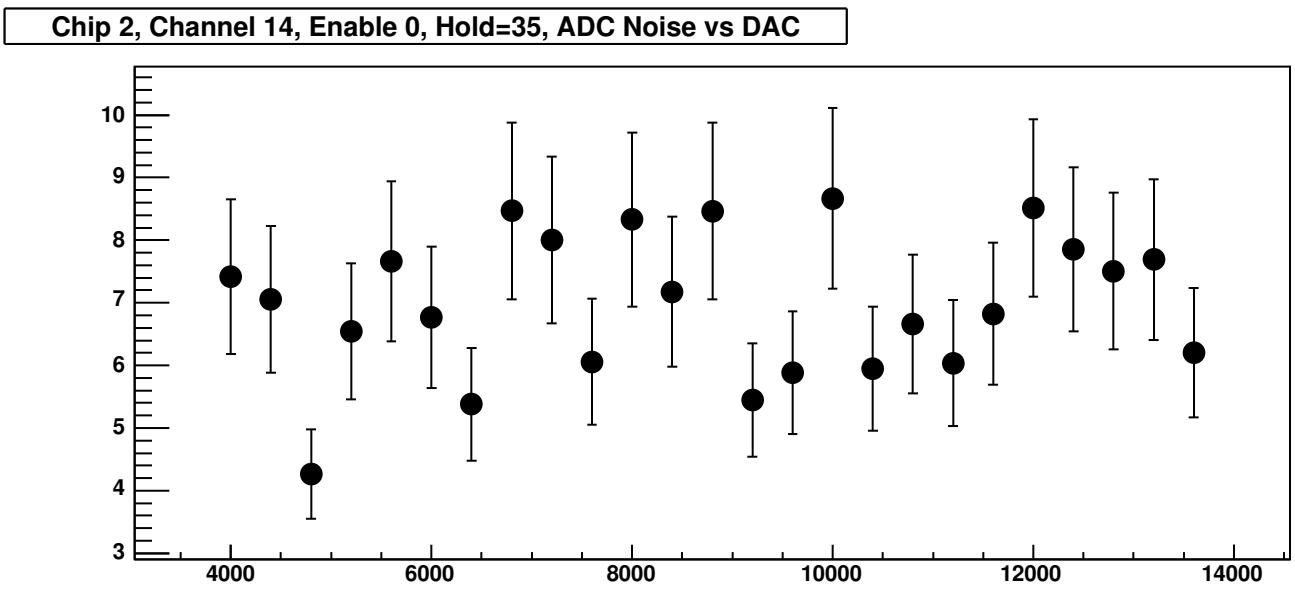


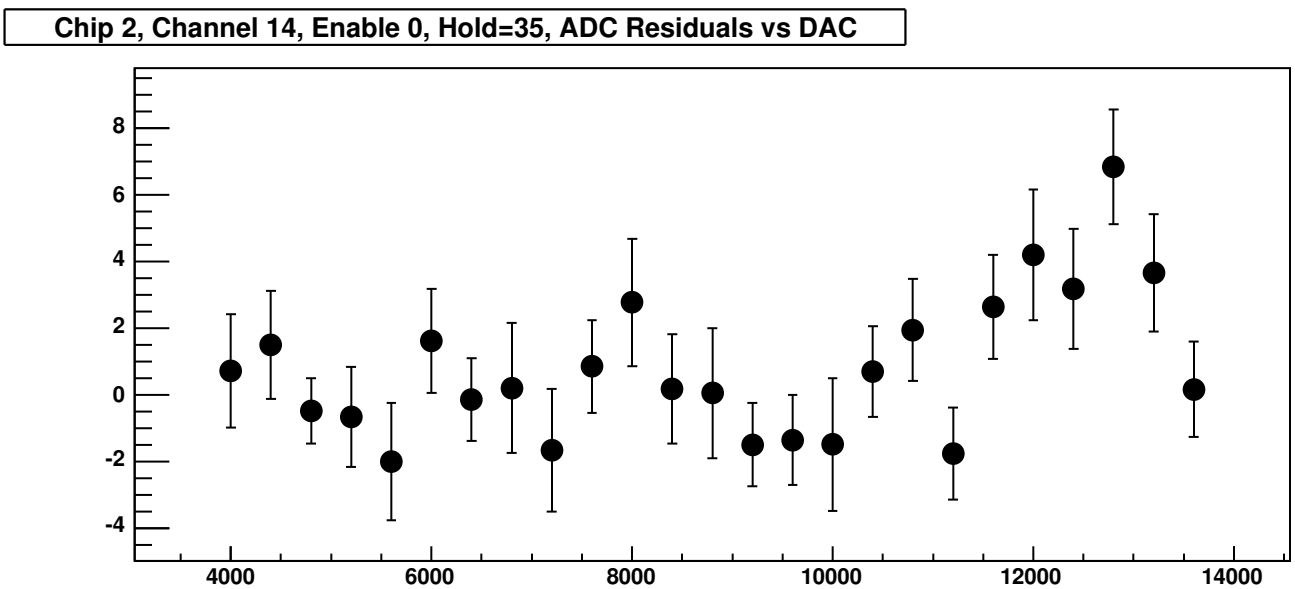

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1816 / 16
p0 664.1 ± 2.11
p1 2.553 ± 0.0002931
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

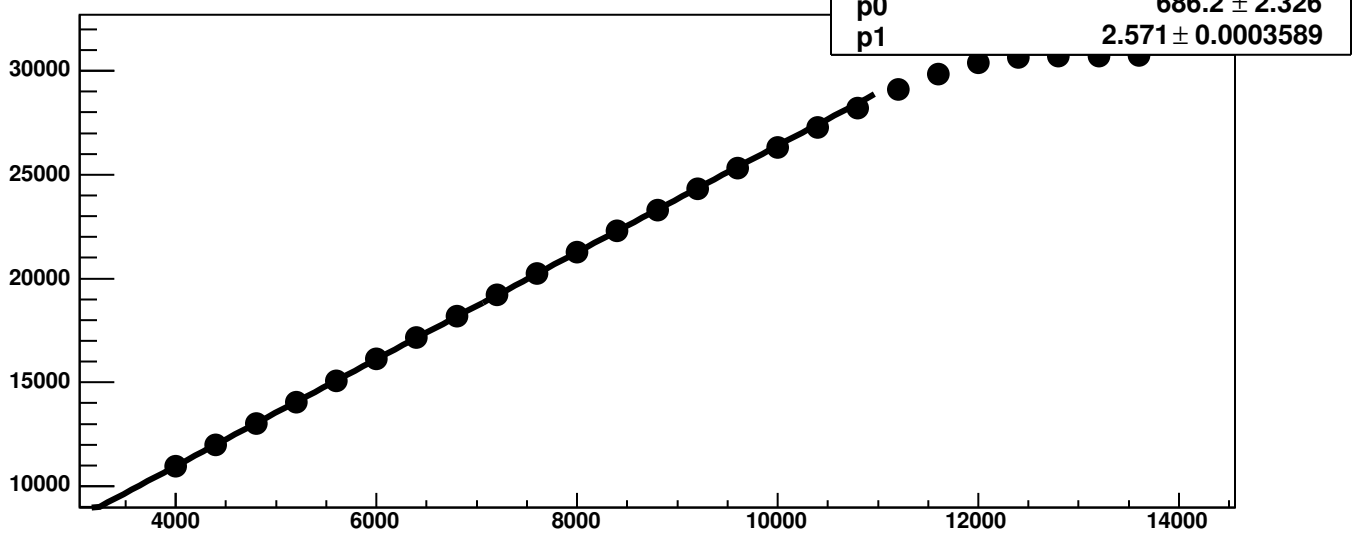

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 12.06 / 16
p0 493.9 ± 1.258
p1 0.01466 ± 0.0001658
700
680
660
640
620
600
580
560
540
4000
6000
8000
10000
12000
14000

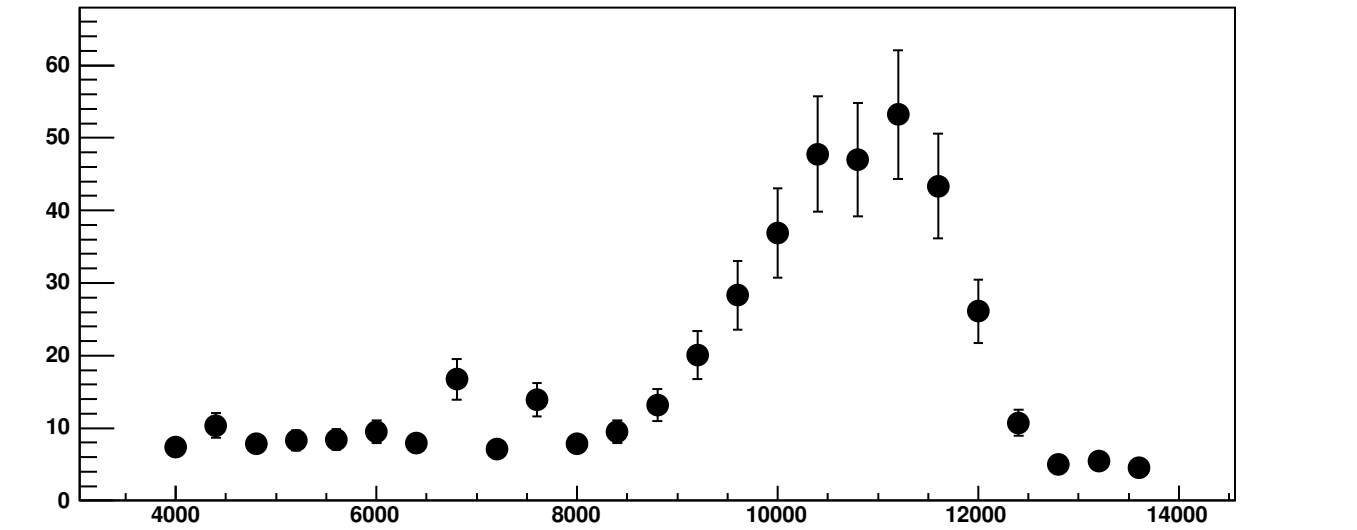

Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

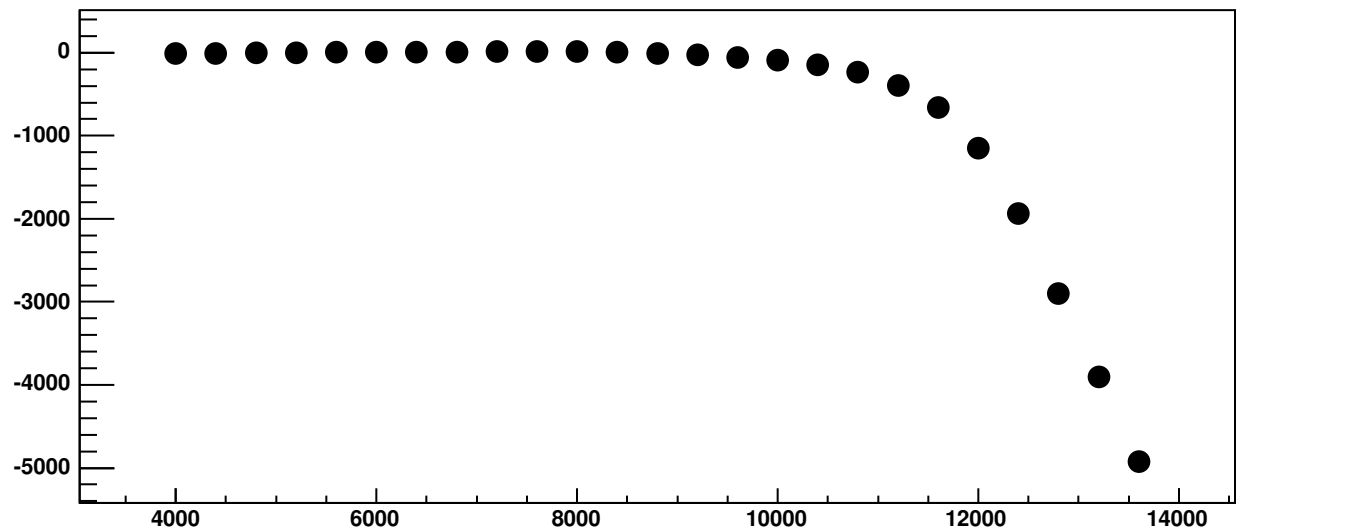
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

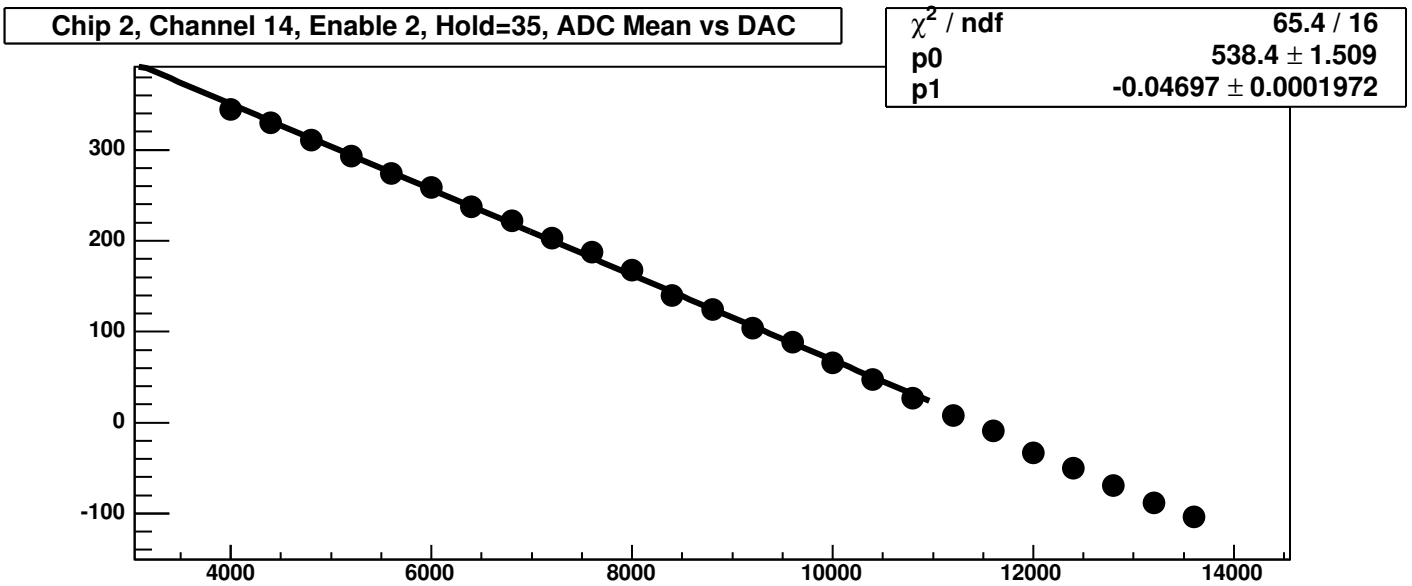


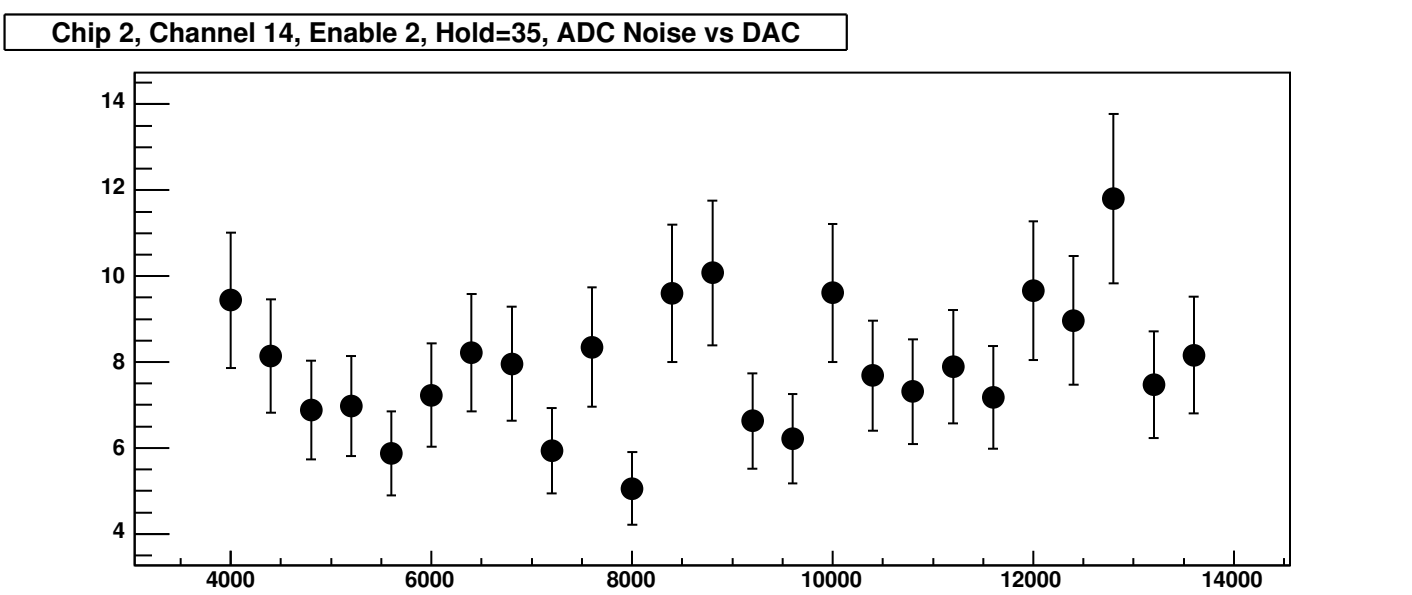
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC

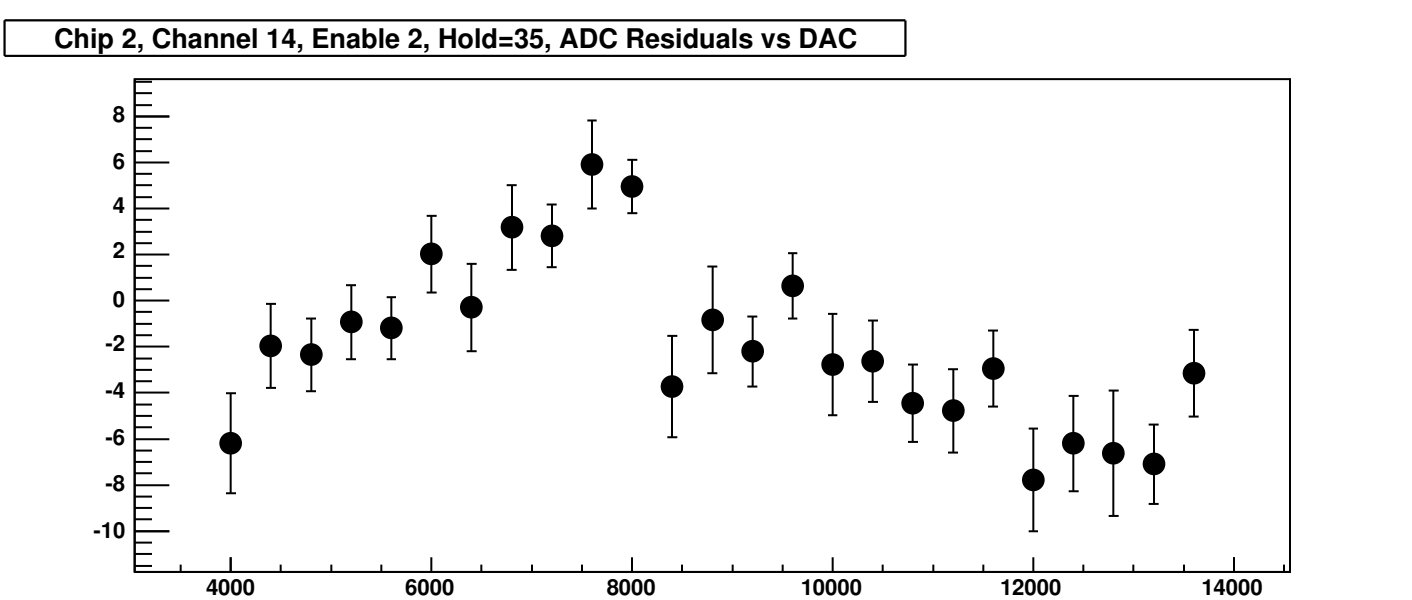


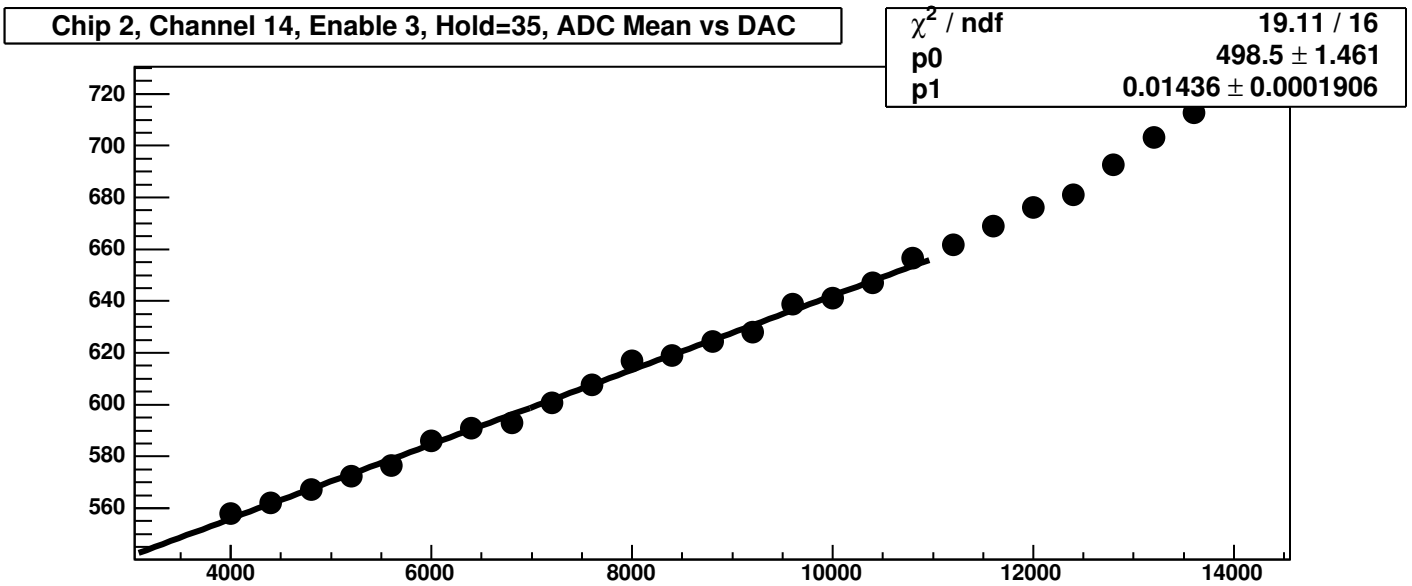
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

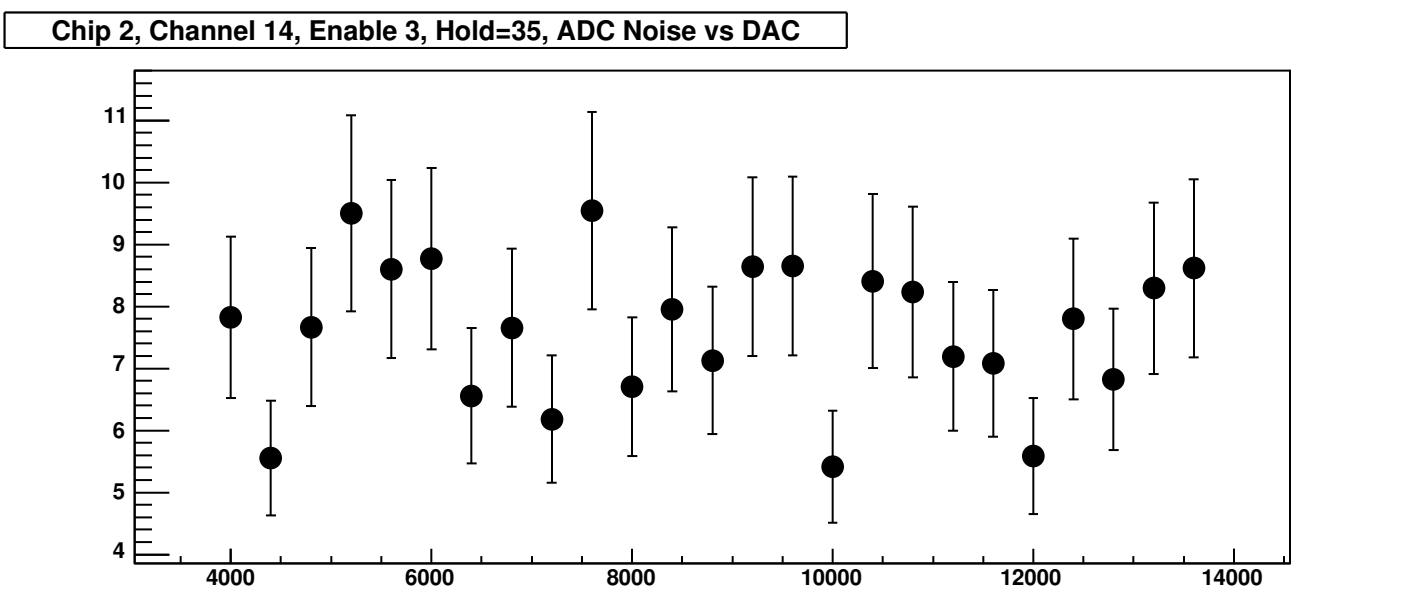


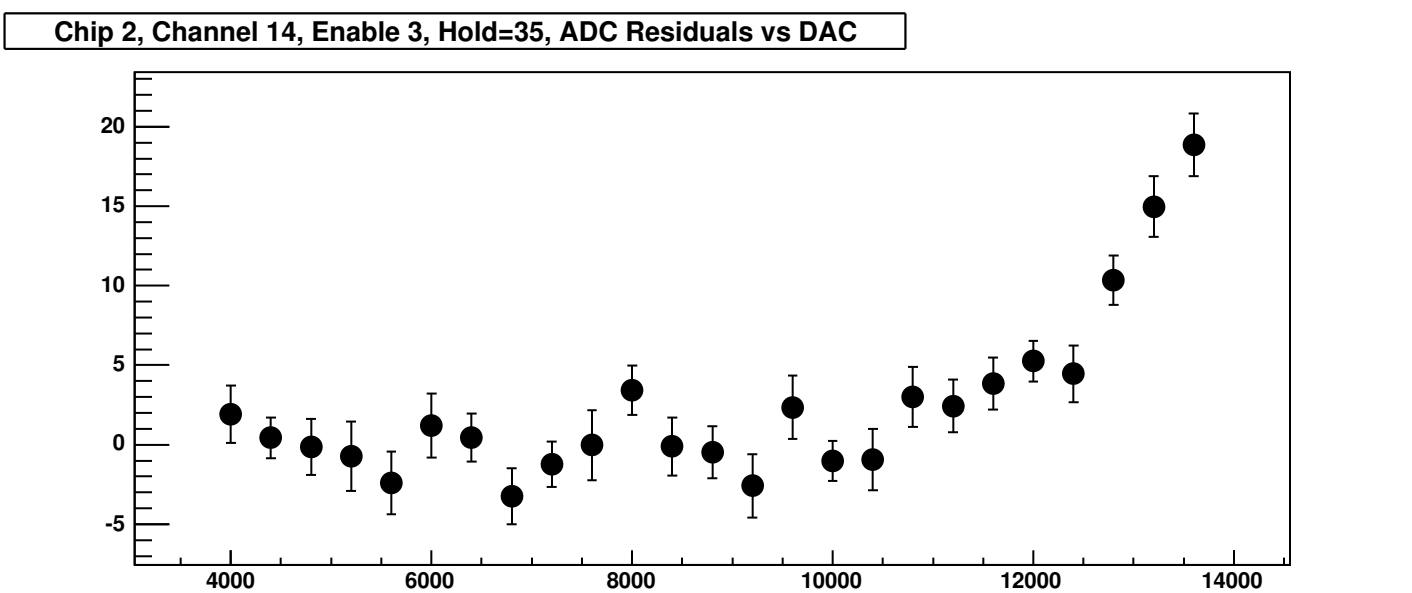

Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 65.4 / 16
p0 538.4 ± 1.509
p1 -0.04697 ± 0.0001972
300
200
100
0
-100
4000
6000
8000
10000
12000
14000

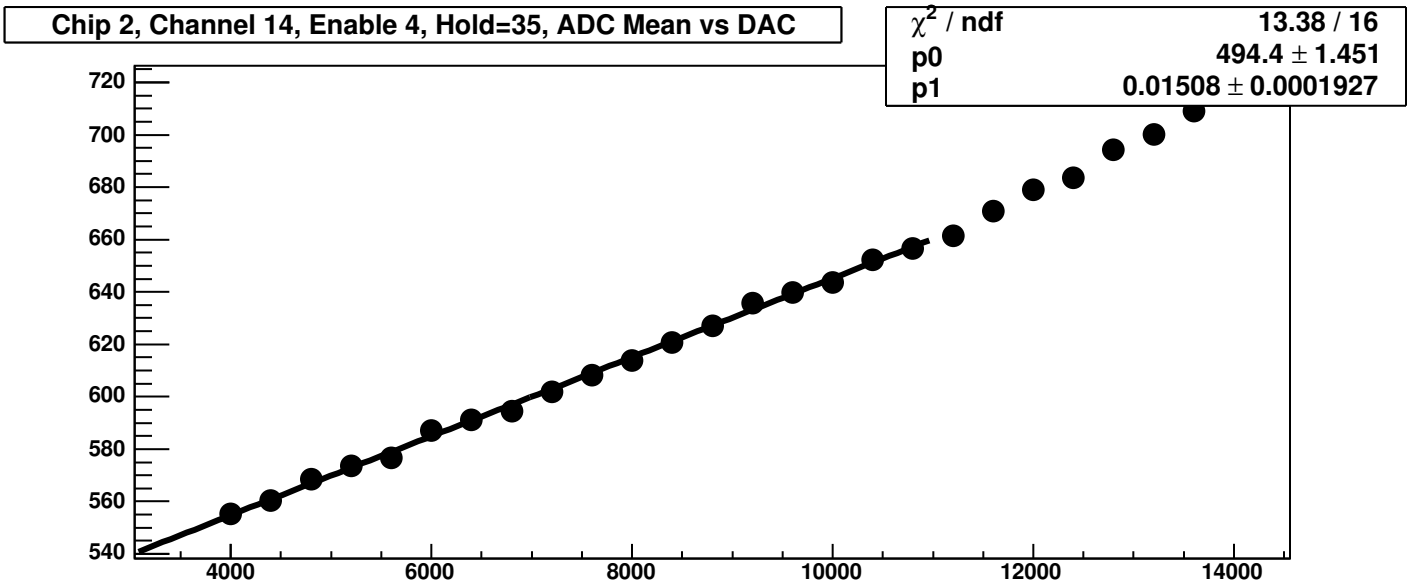

Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

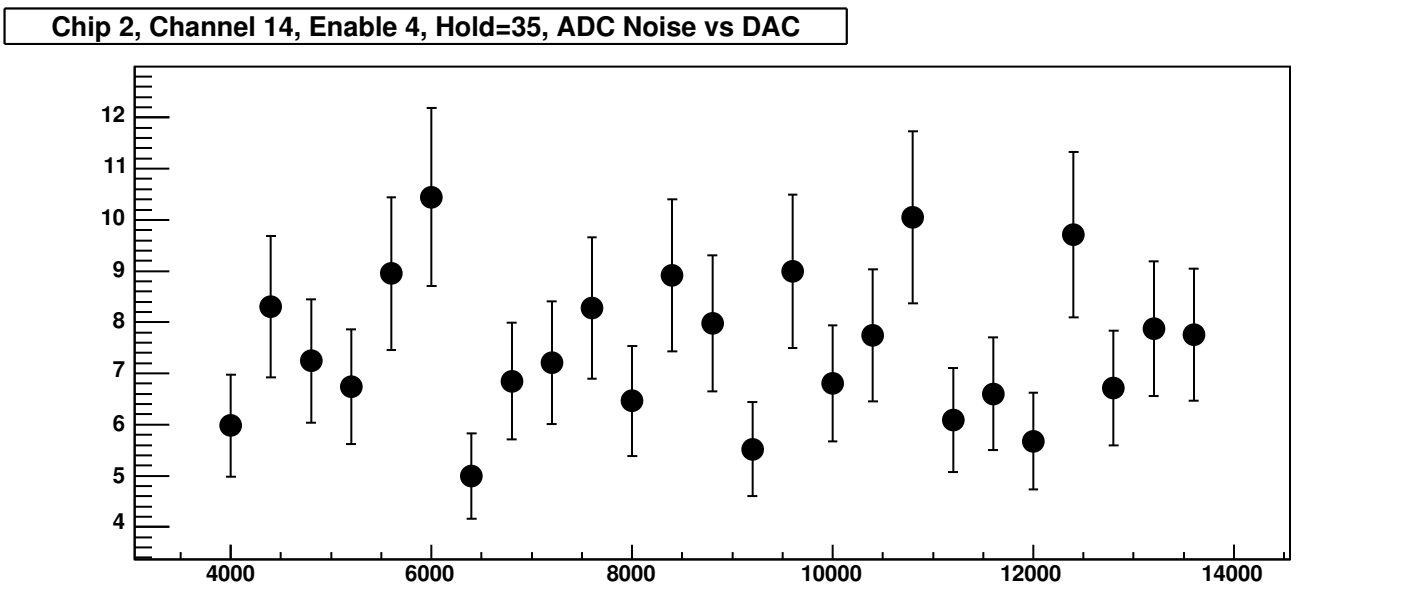

Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

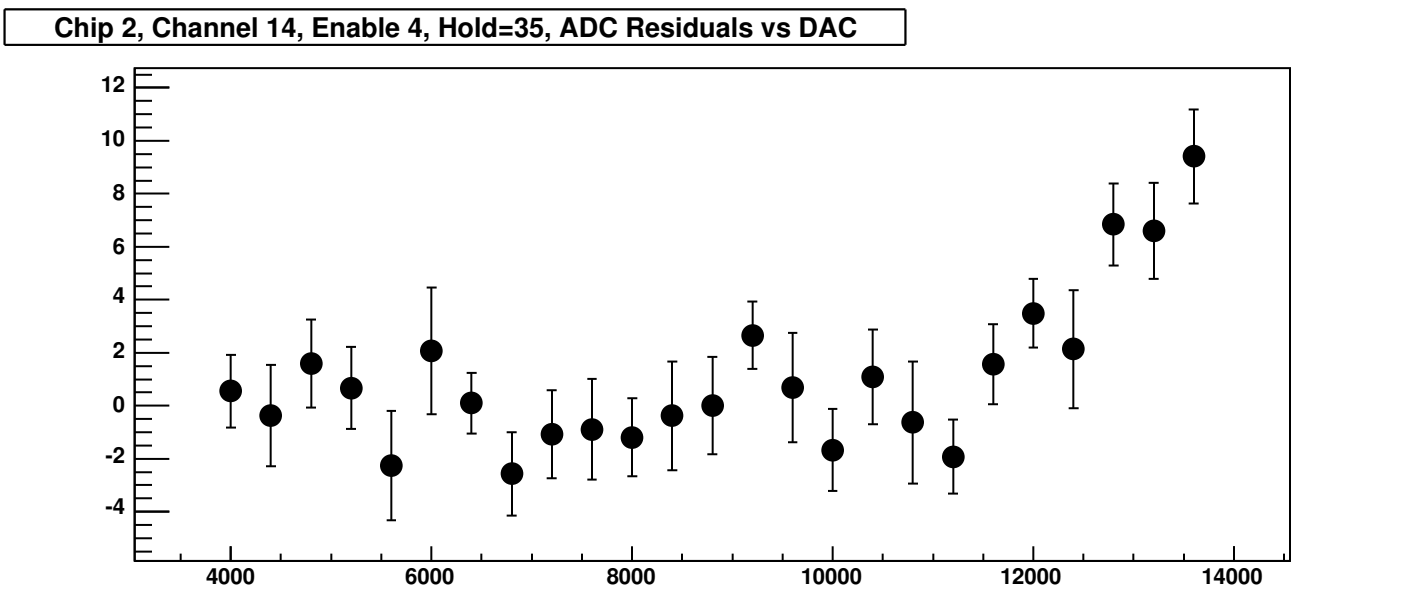

Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.11 / 16
p0 498.5 ± 1.461
p1 0.01436 ± 0.0001906
720
700
680
660
640
620
600
580
560
4000
6000
8000
10000
12000
14000

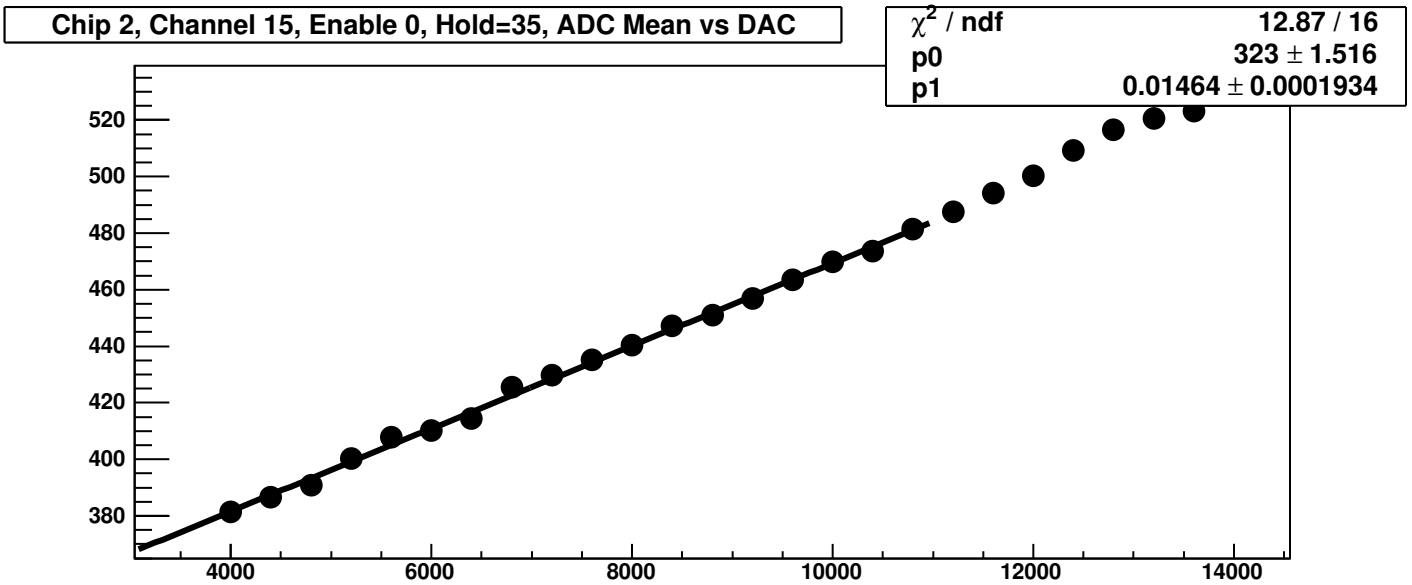

Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

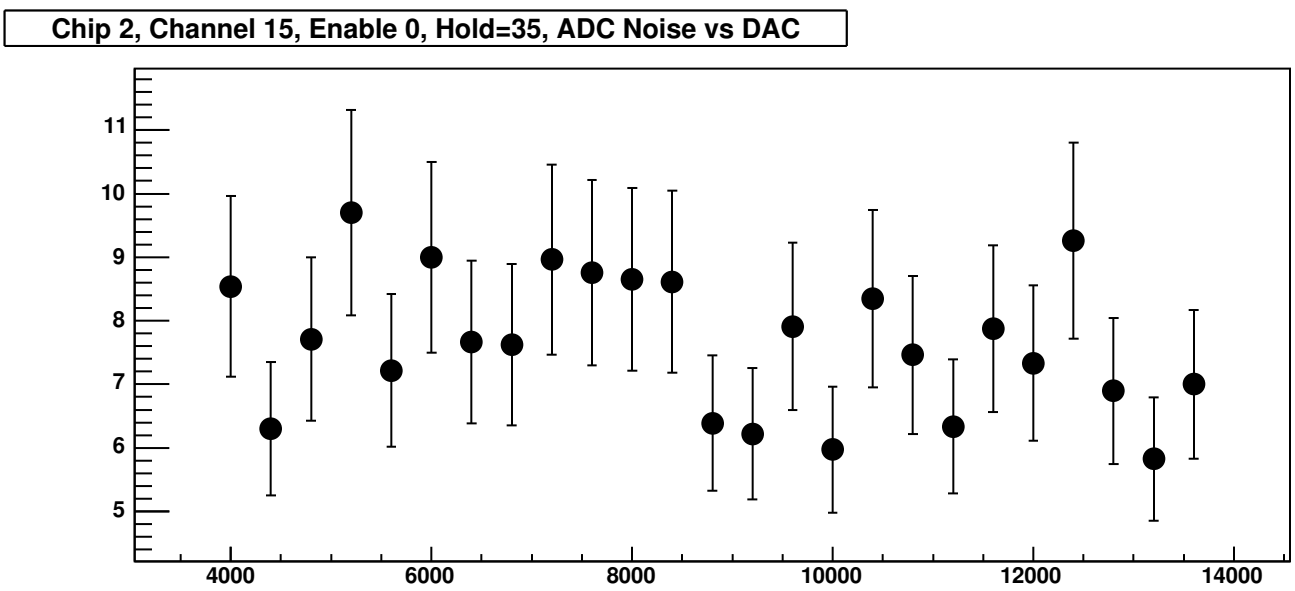

Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

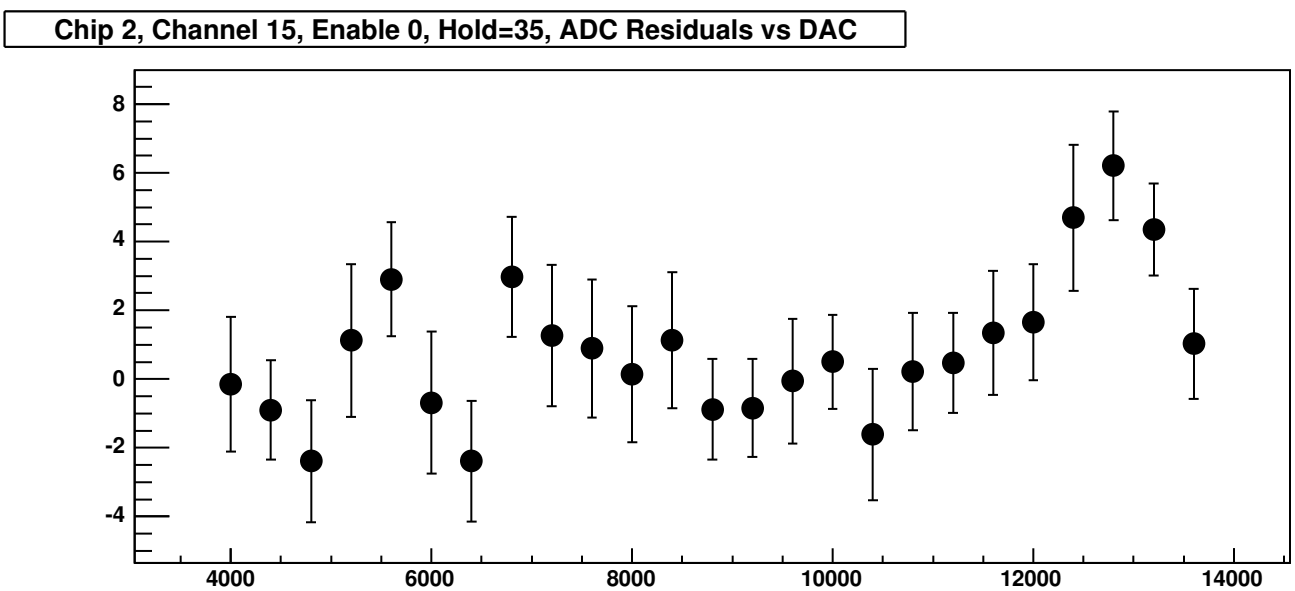

Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.38 / 16
p0 494.4 ± 1.451
p1 0.01508 ± 0.0001927
720
700
680
660
640
620
600
580
560
540
4000
6000
8000
10000
12000
14000

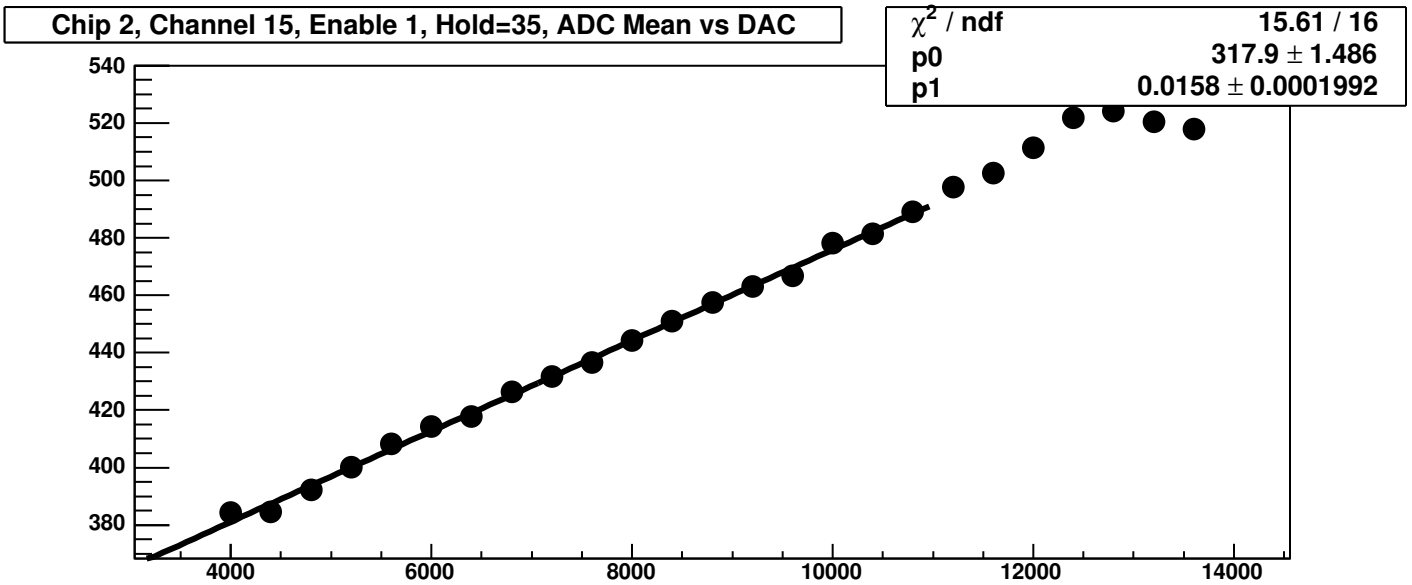

Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

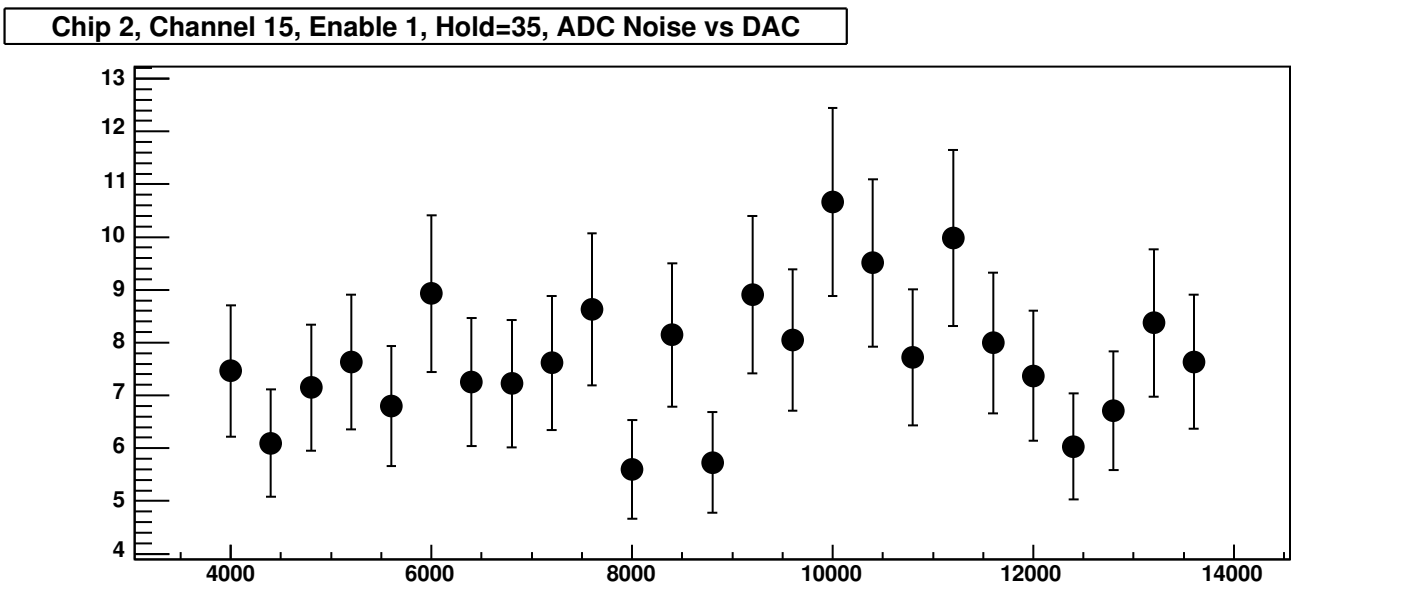

Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

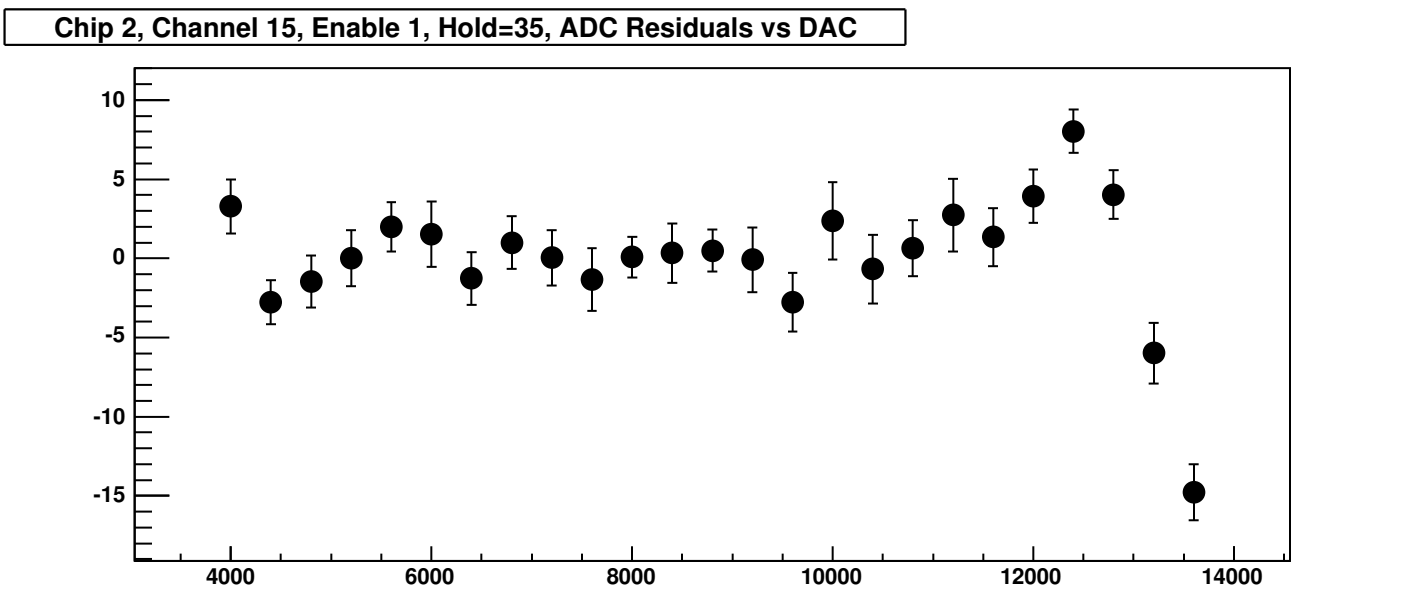

Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 12.87 / 16
p0 323 ± 1.516
p1 0.01464 ± 0.0001934
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000

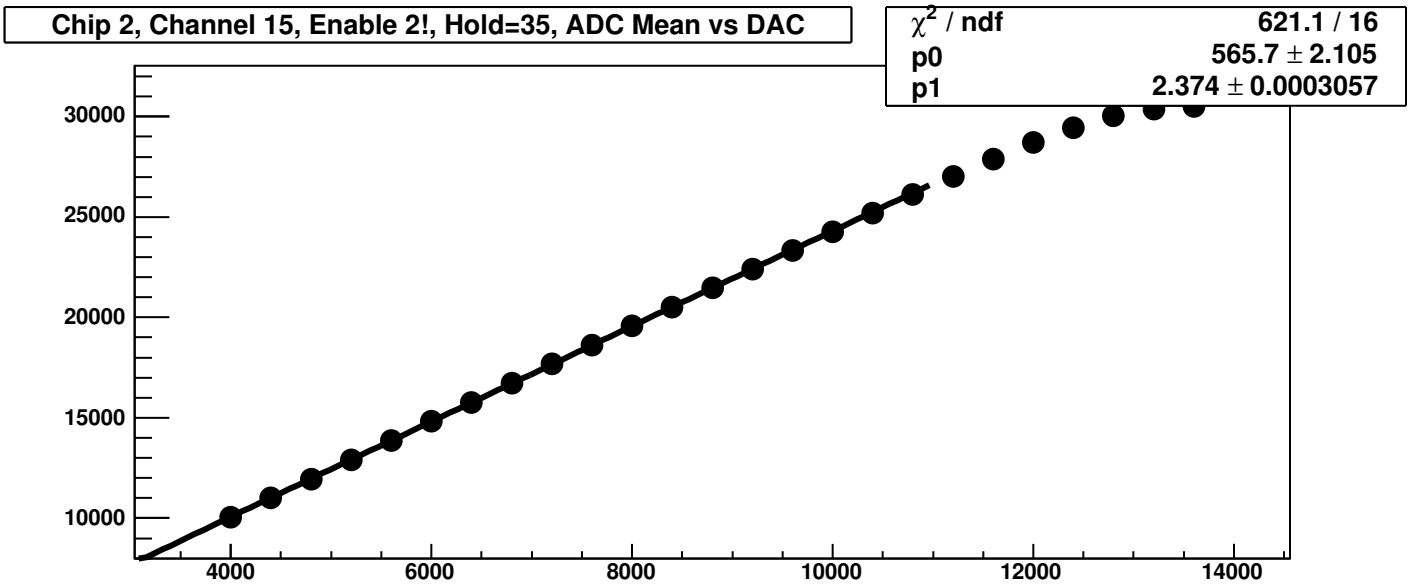

Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

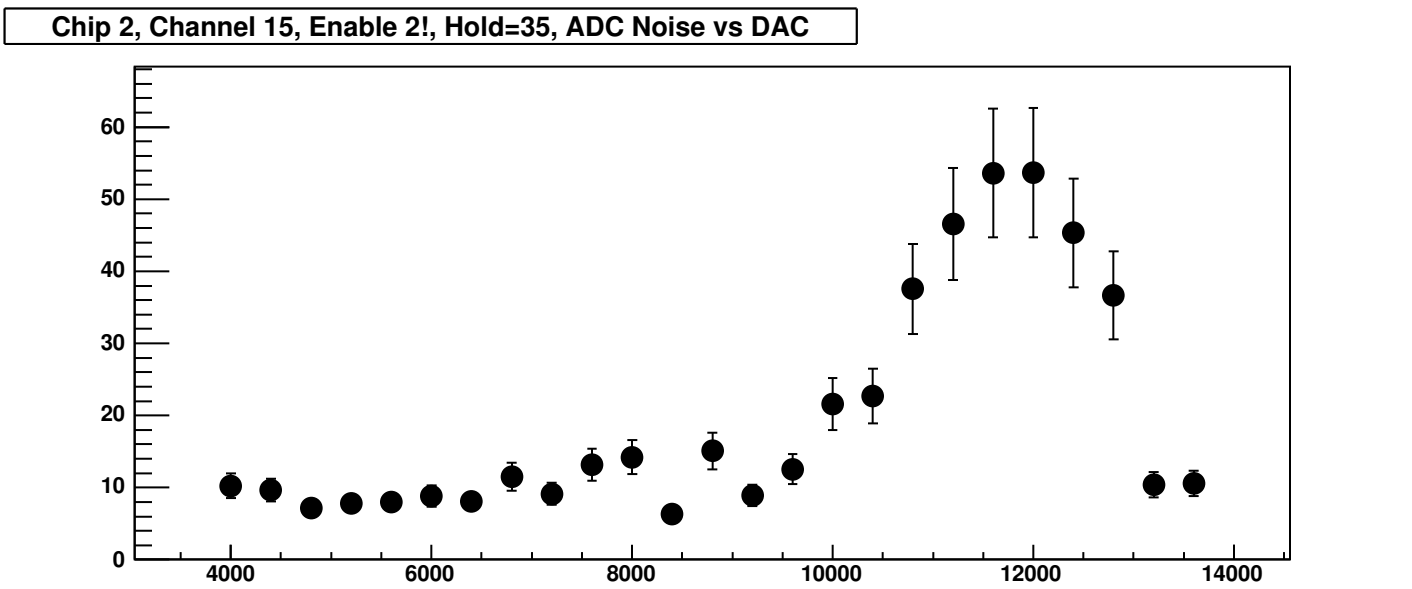

Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

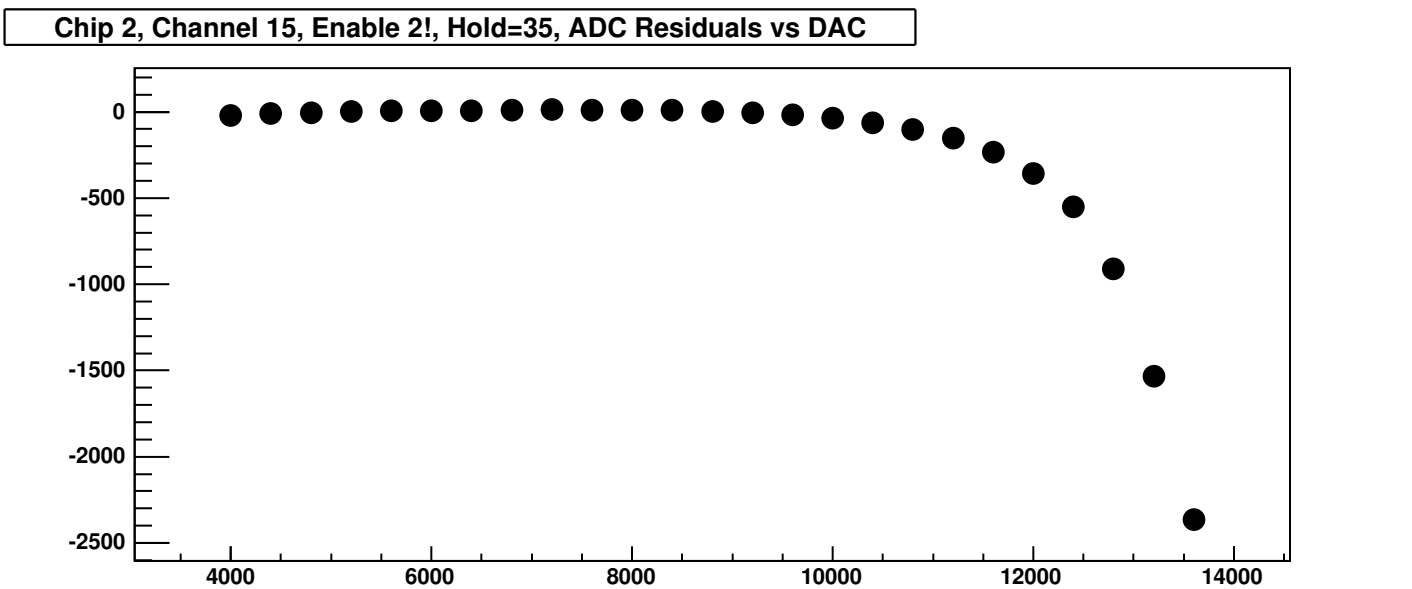

Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 15.61 / 16
p0 317.9 ± 1.486
p1 0.0158 ± 0.0001992
540
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

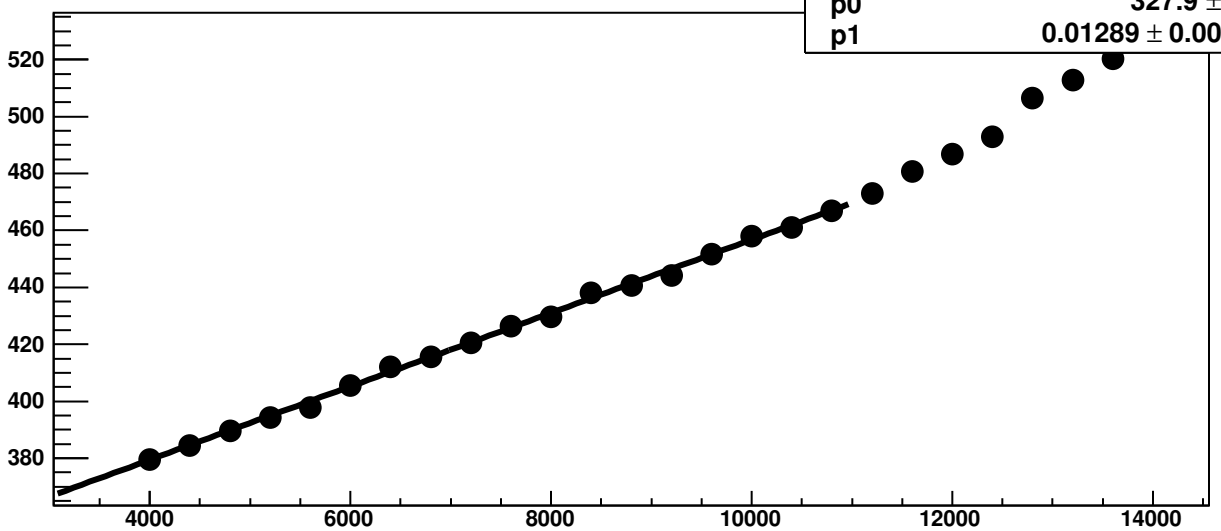

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 621.1 / 16
p0 565.7 ± 2.105
p1 2.374 ± 0.0003057
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

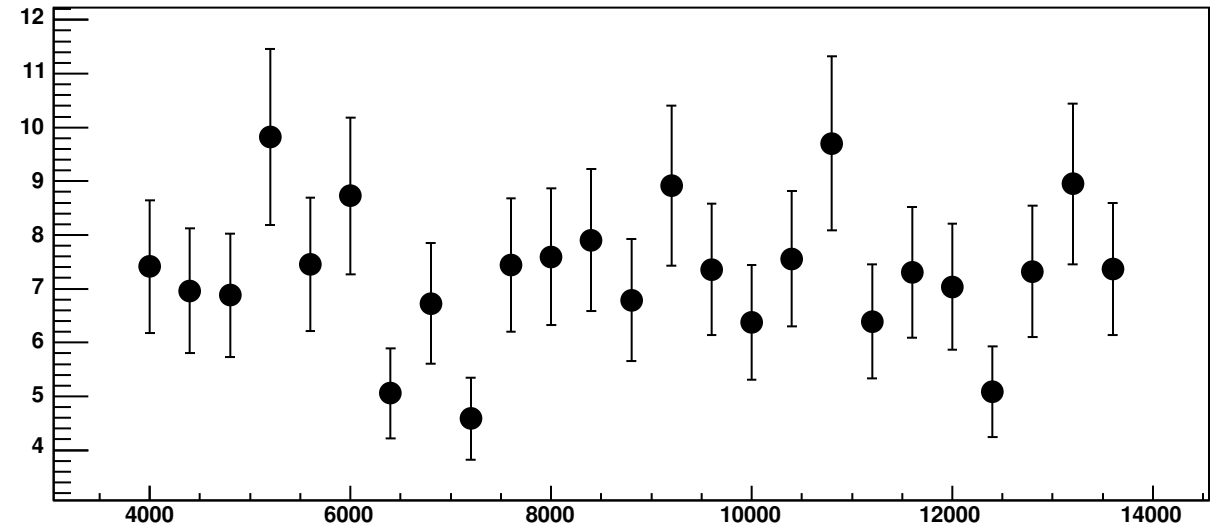

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
4000
6000
8000
10000
12000
14000

Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

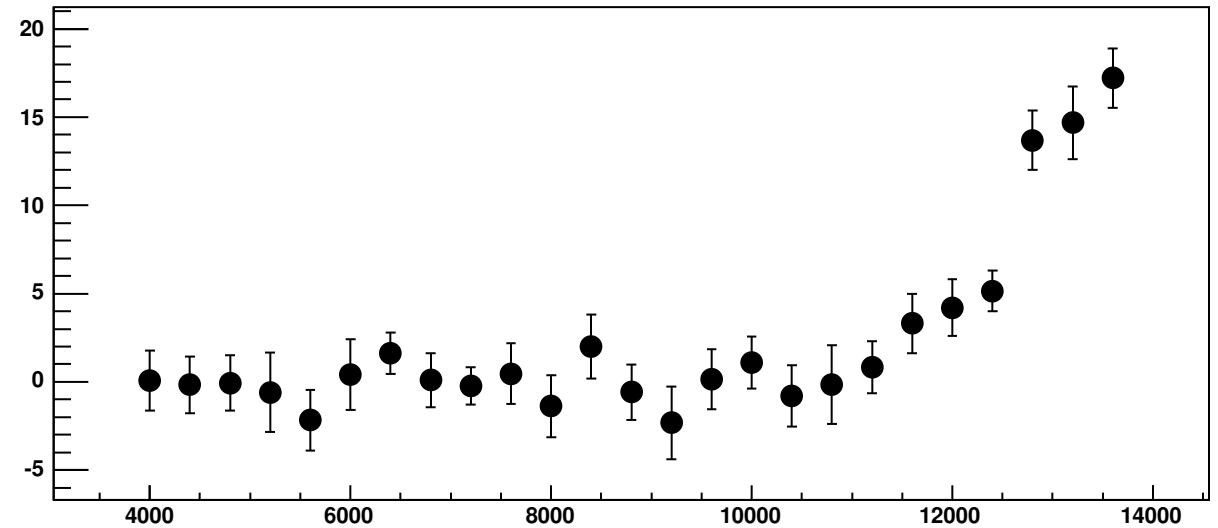
χ^2 / ndf	7.863 / 16
p0	327.9 ± 1.484
p1	0.01289 ± 0.0001967

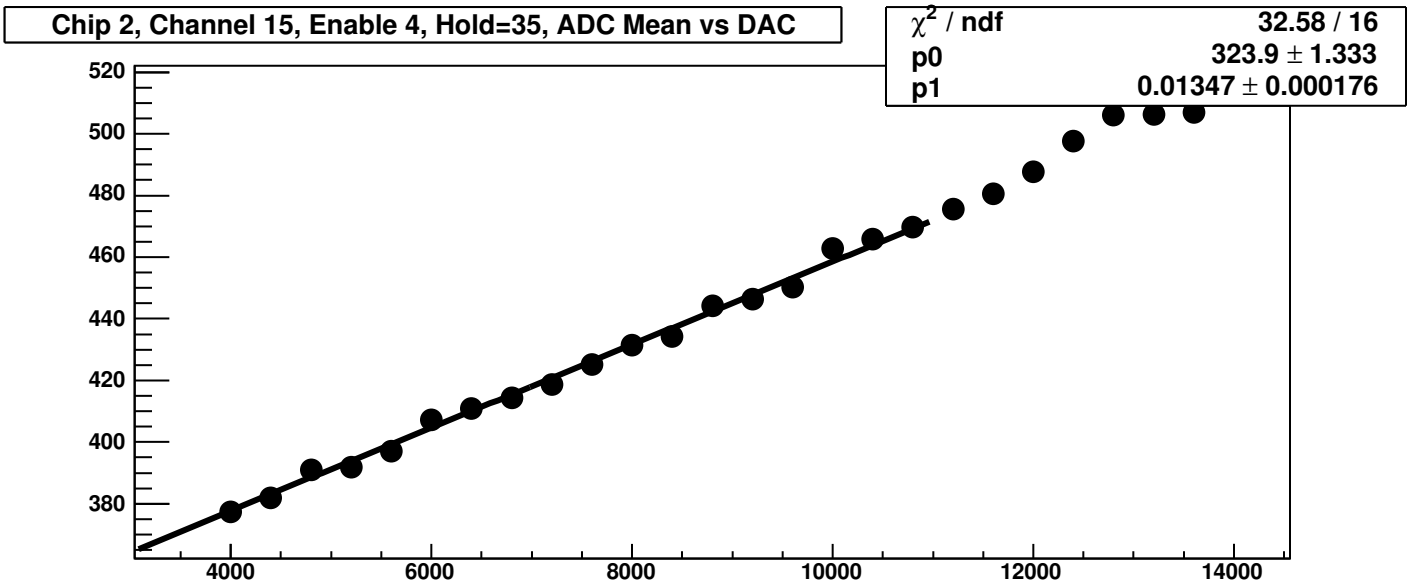


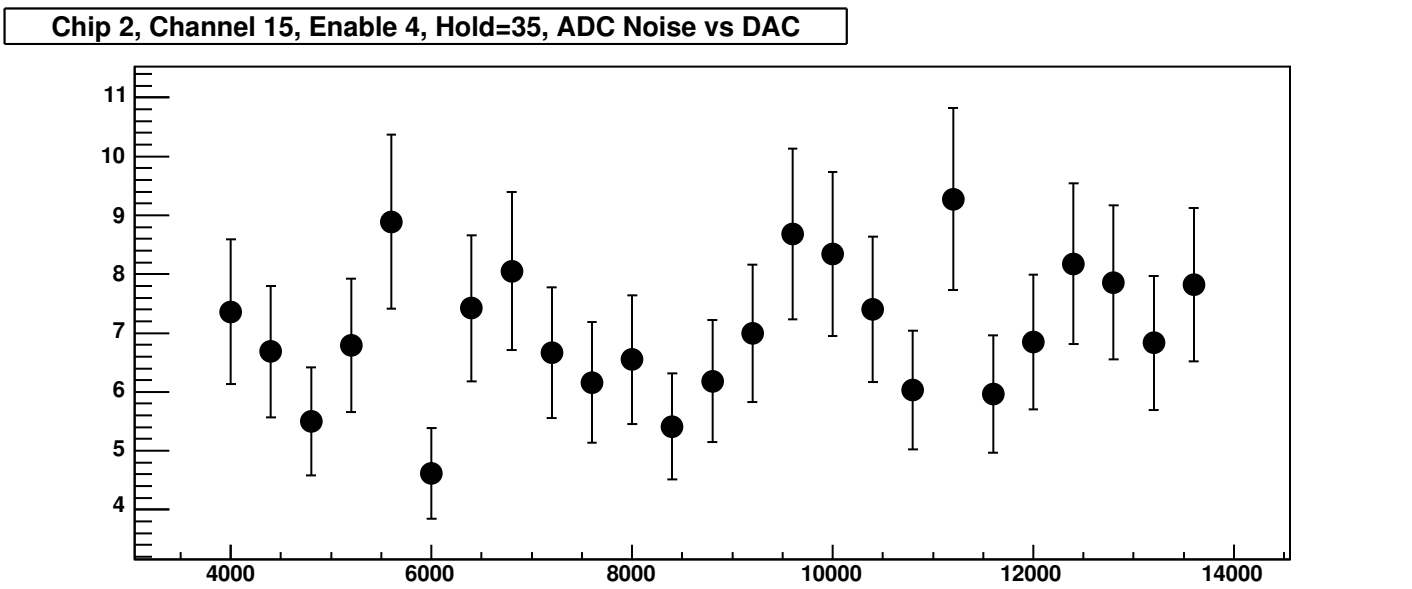
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC

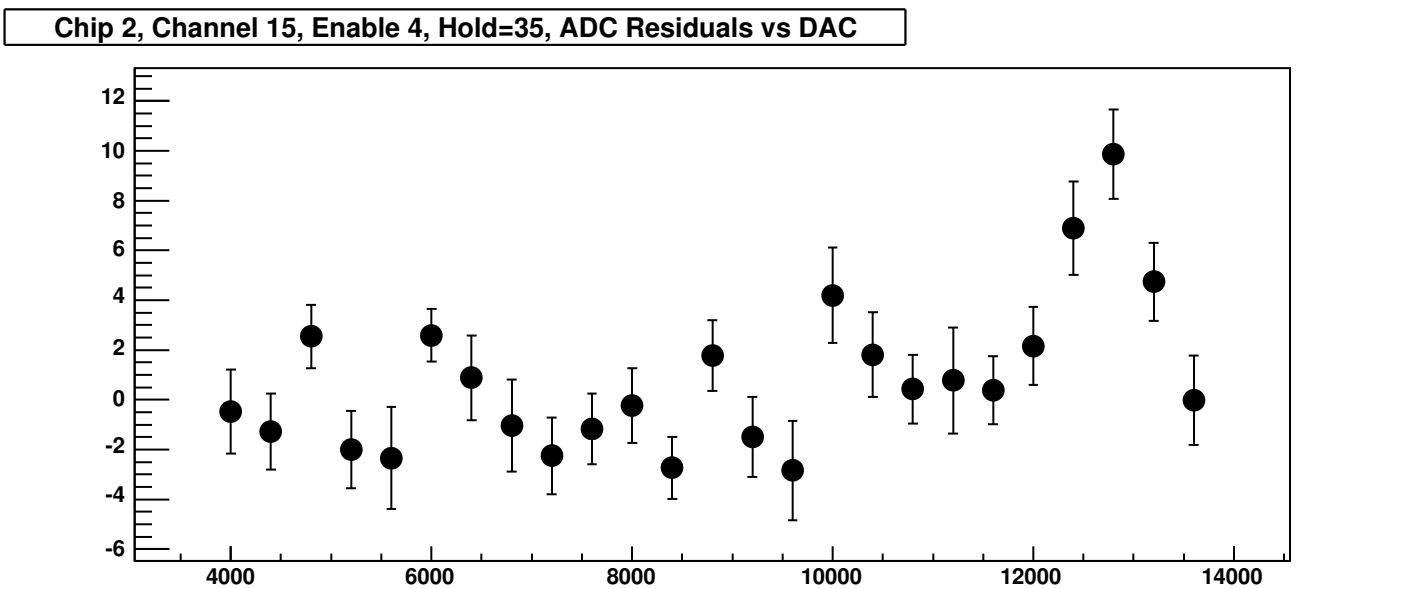


Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

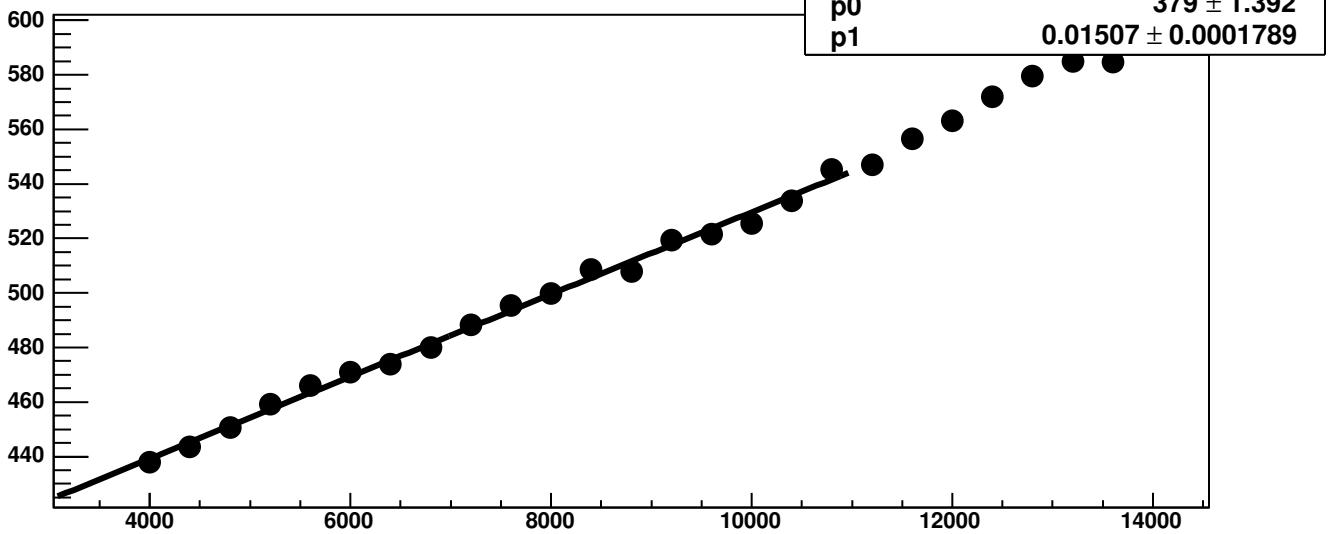



Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.58 / 16
p0 323.9 ± 1.333
p1 0.01347 ± 0.000176
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000

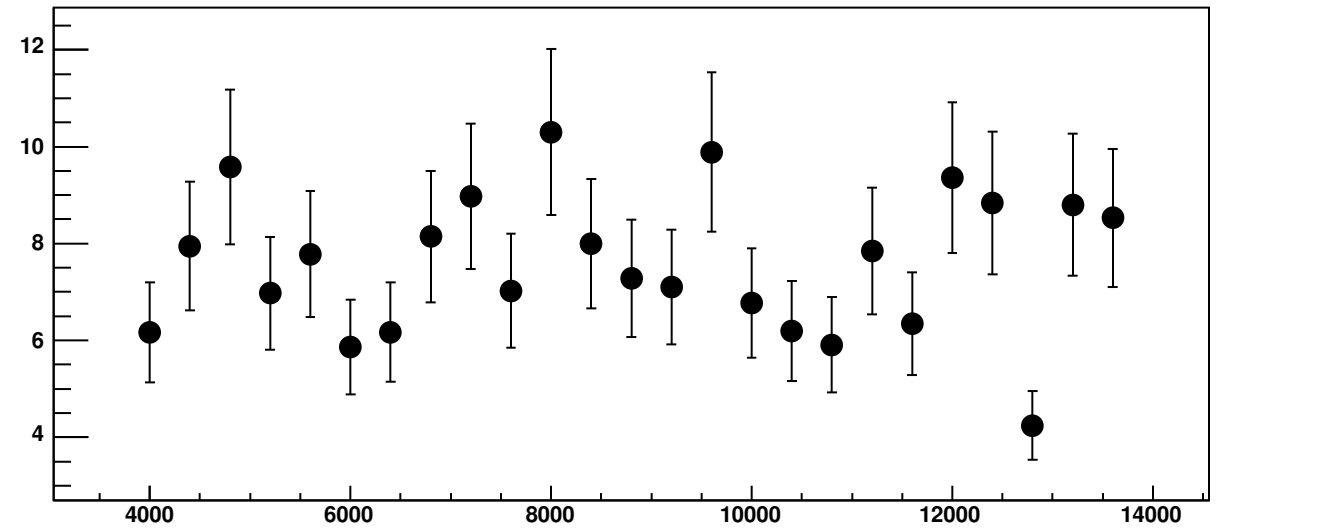

Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

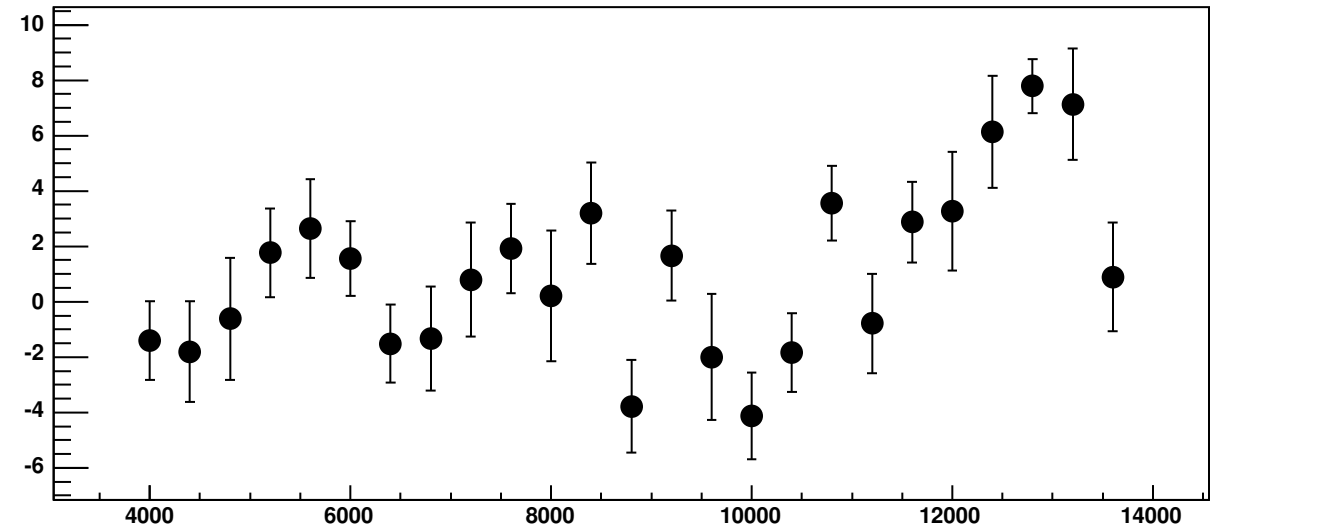
Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



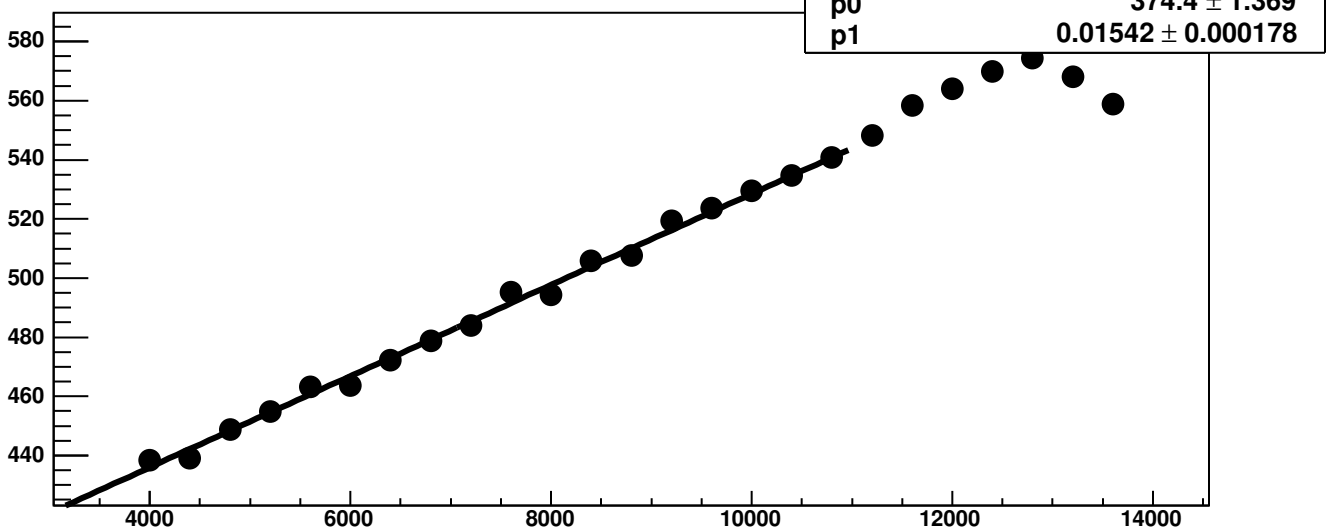
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



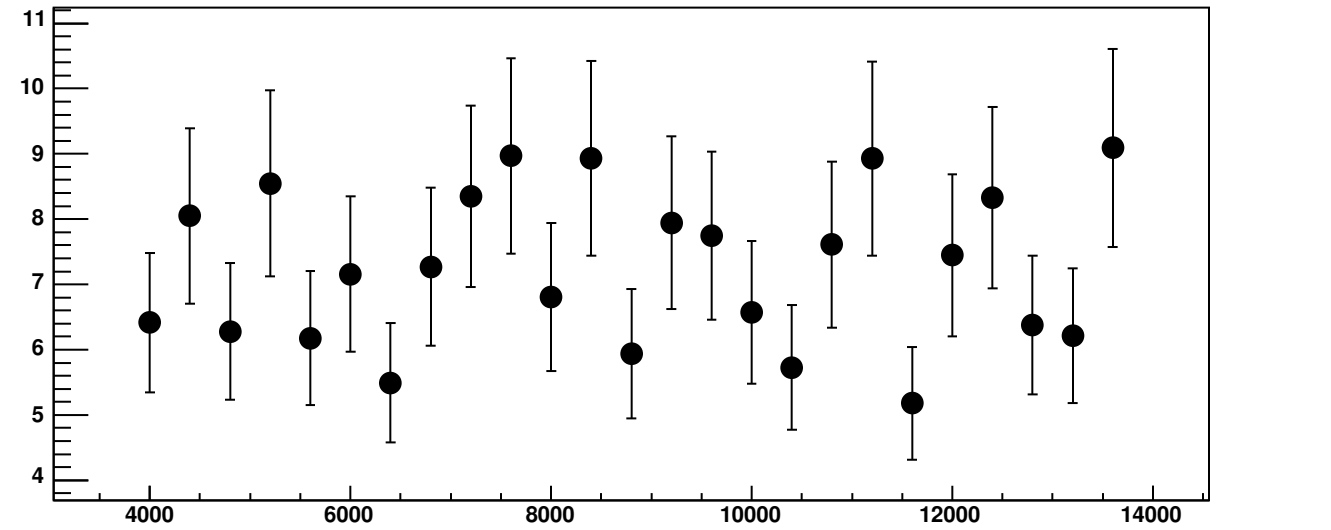
Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



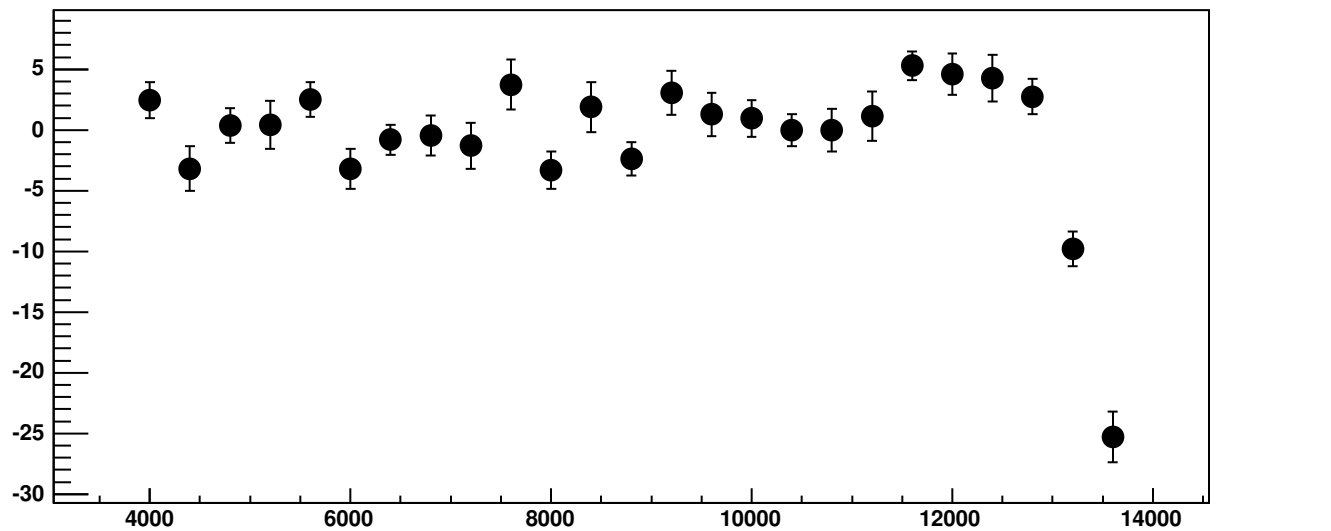
Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



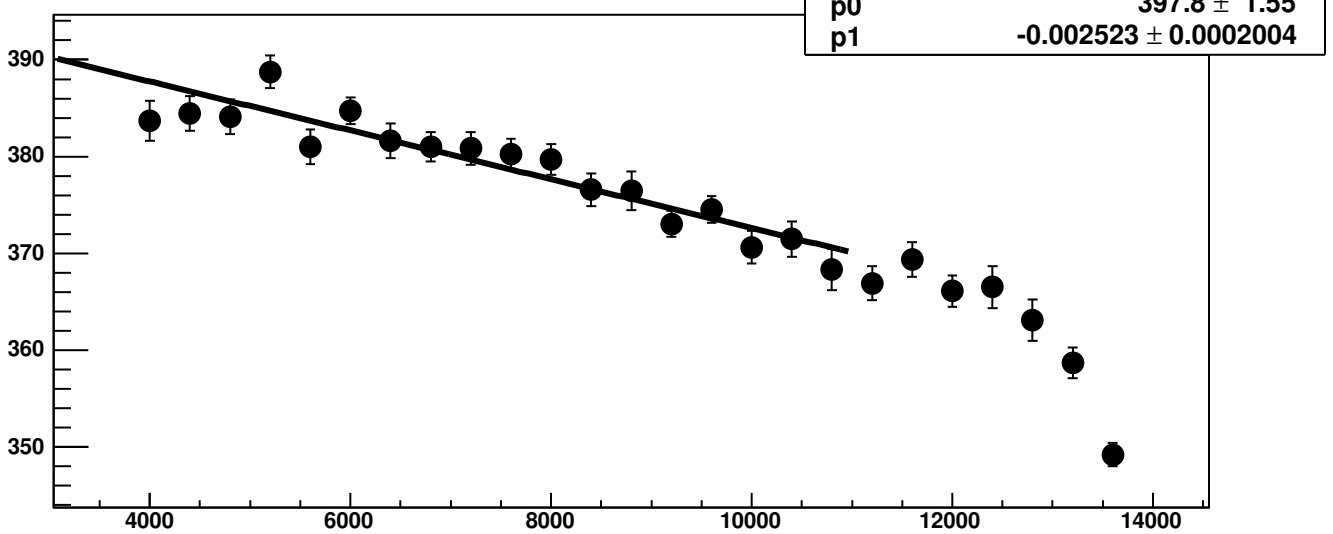
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



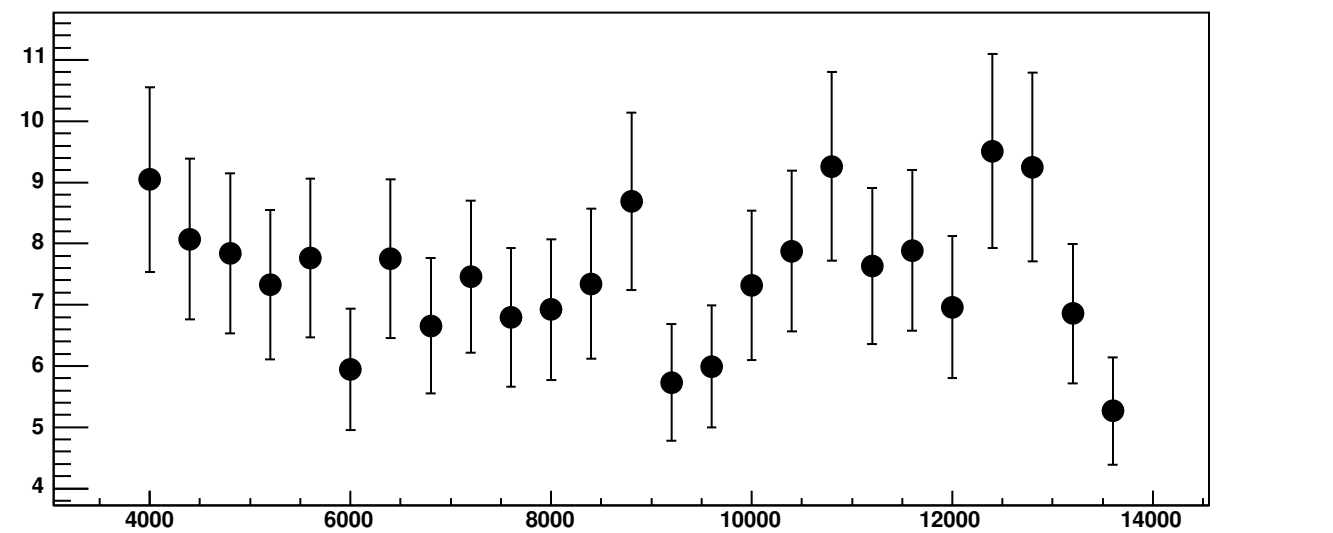
Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



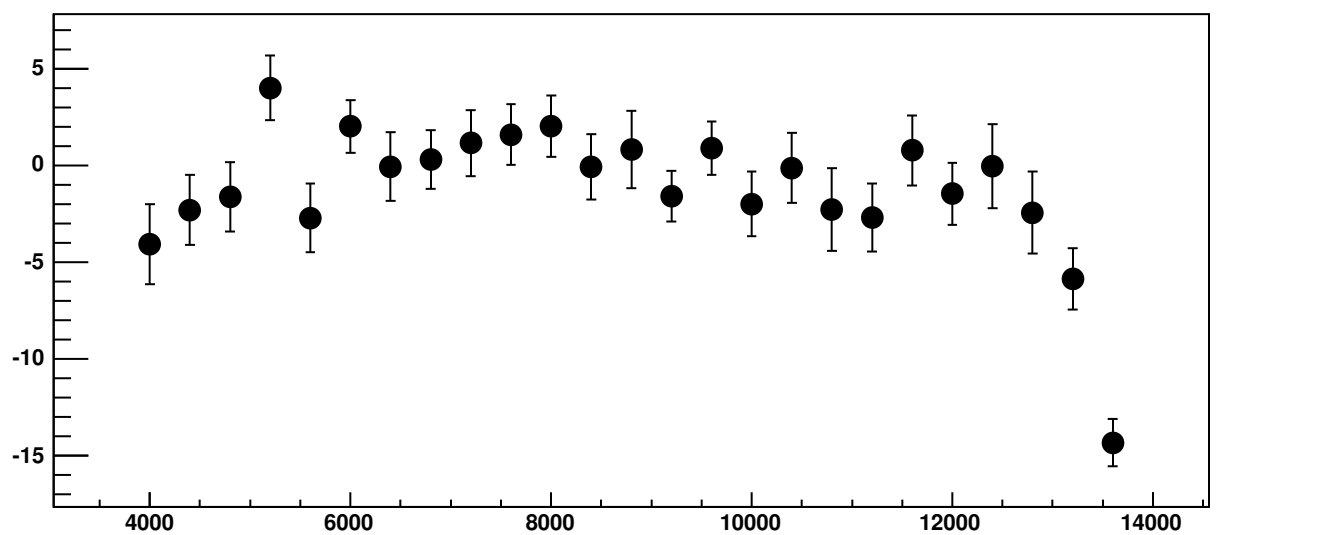
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

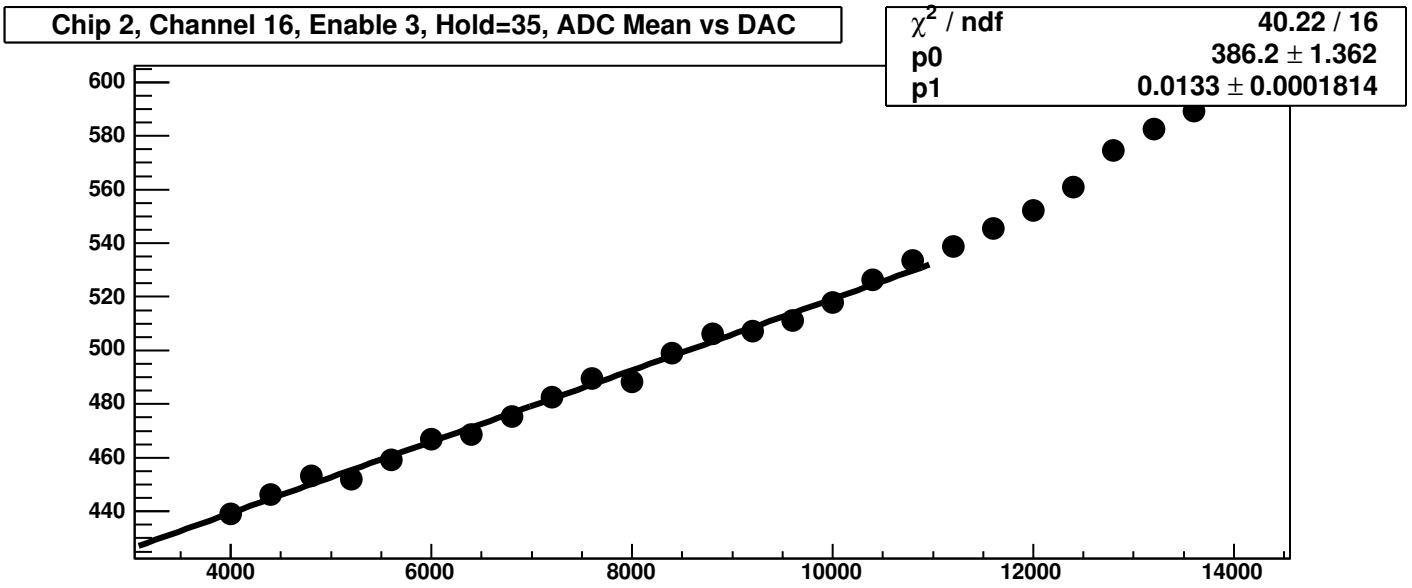


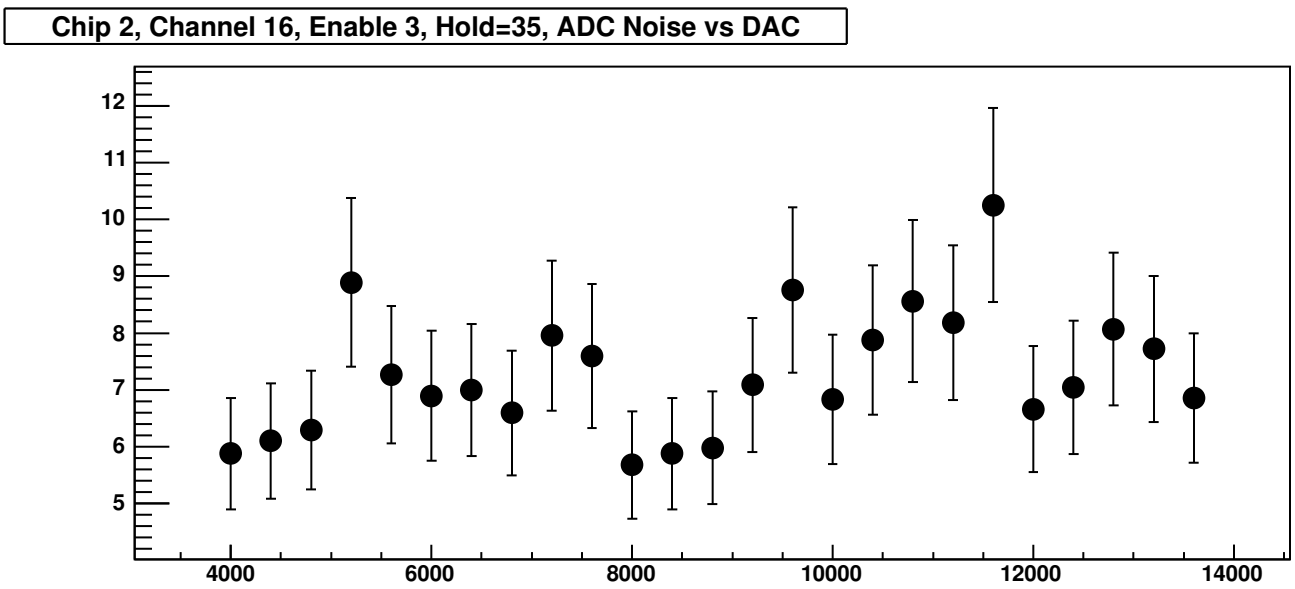
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

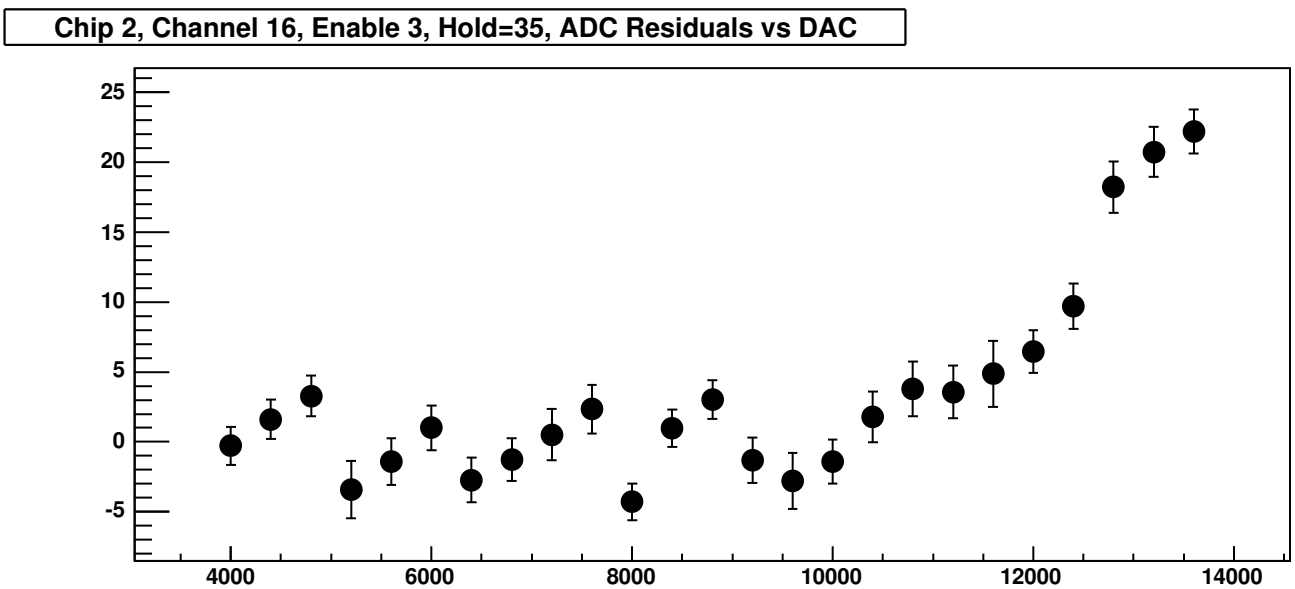


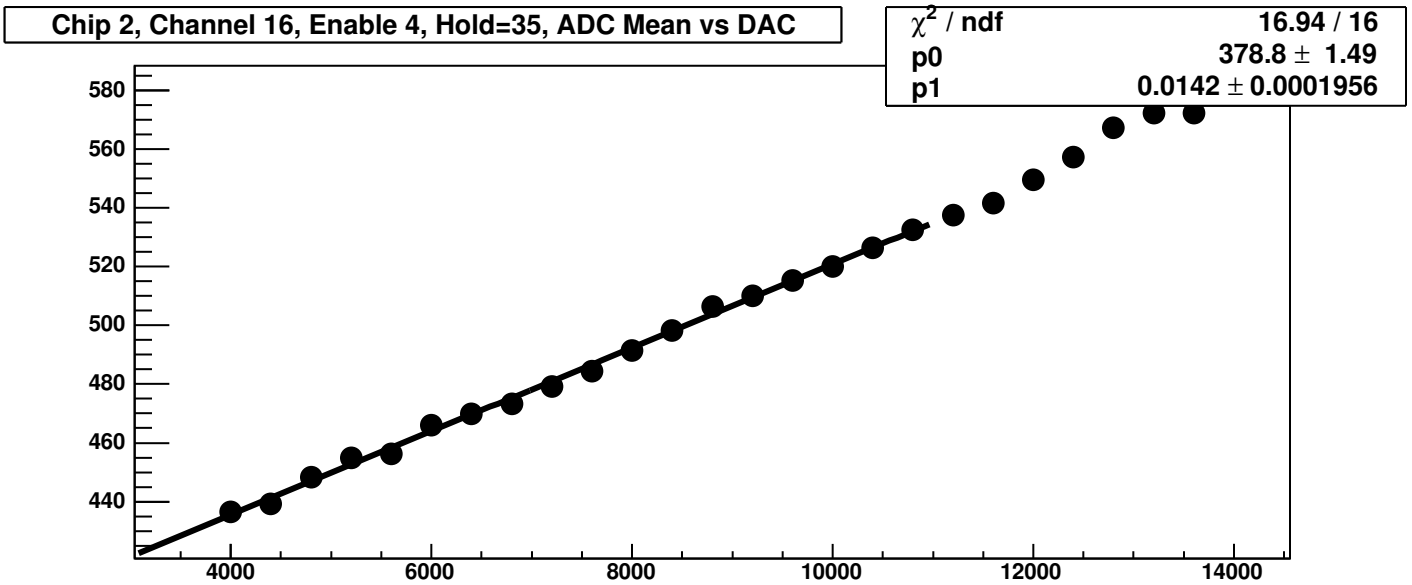
Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

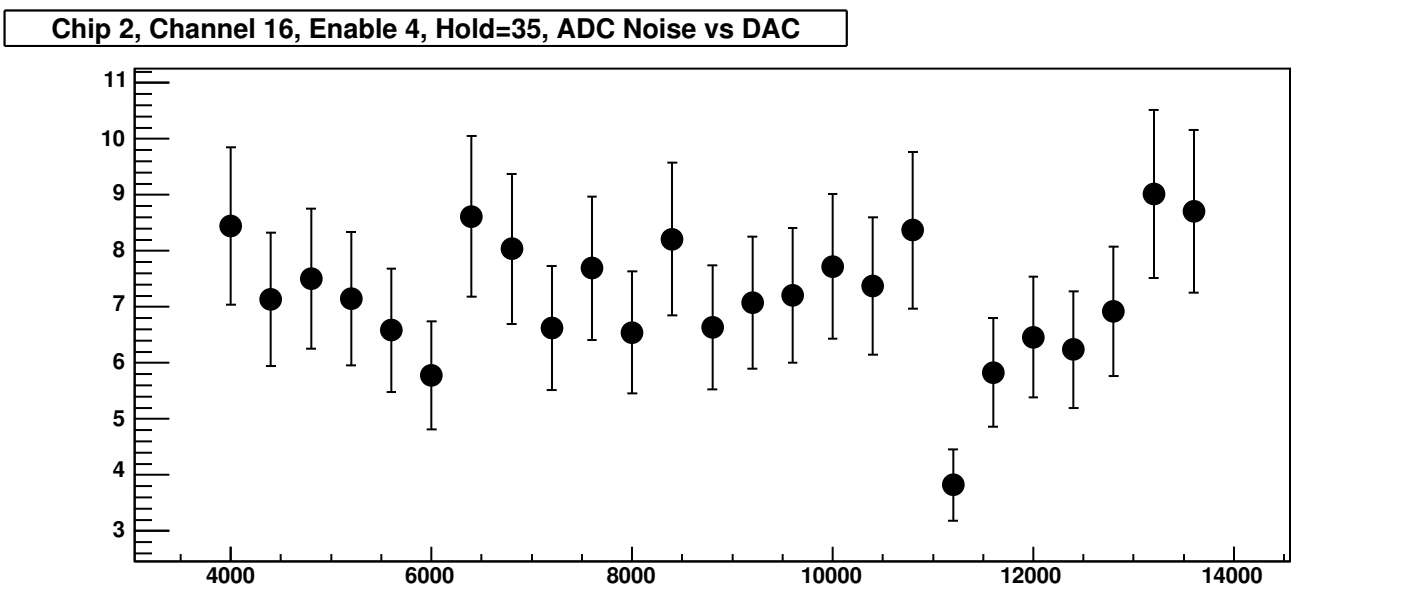


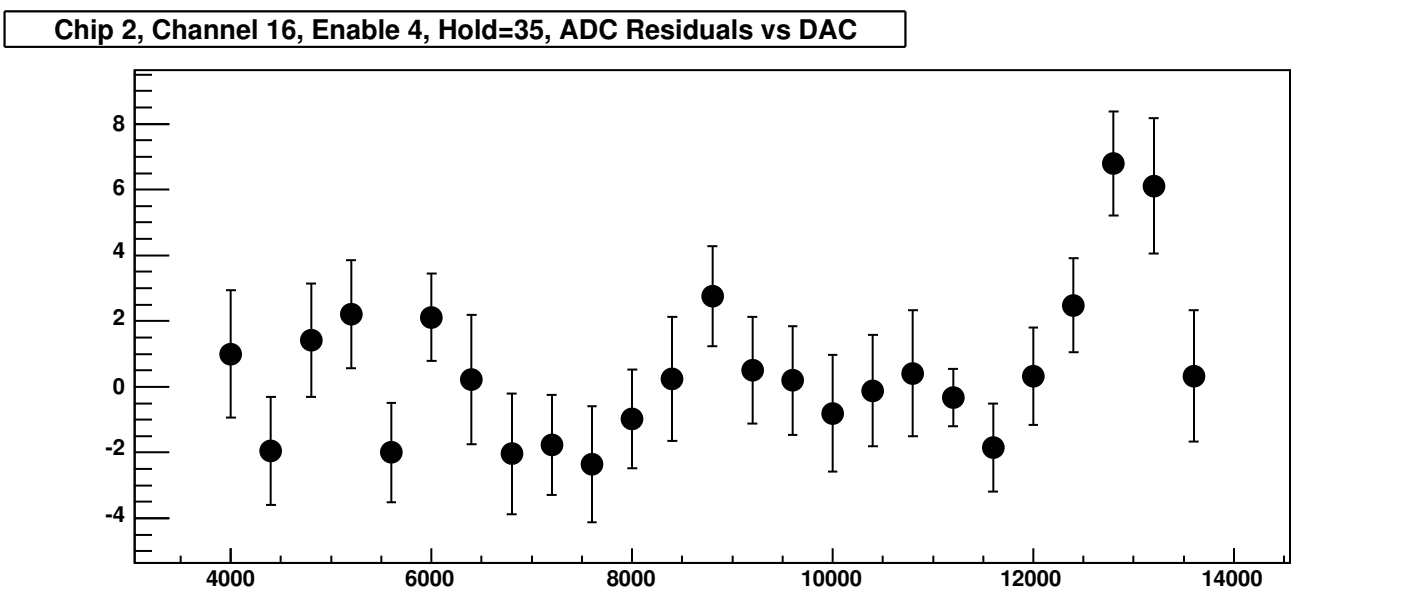

Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.22 / 16
p0 386.2 ± 1.362
p1 0.0133 ± 0.0001814
600
580
560
540
520
500
480
460
440
4000
6000
8000
10000
12000
14000

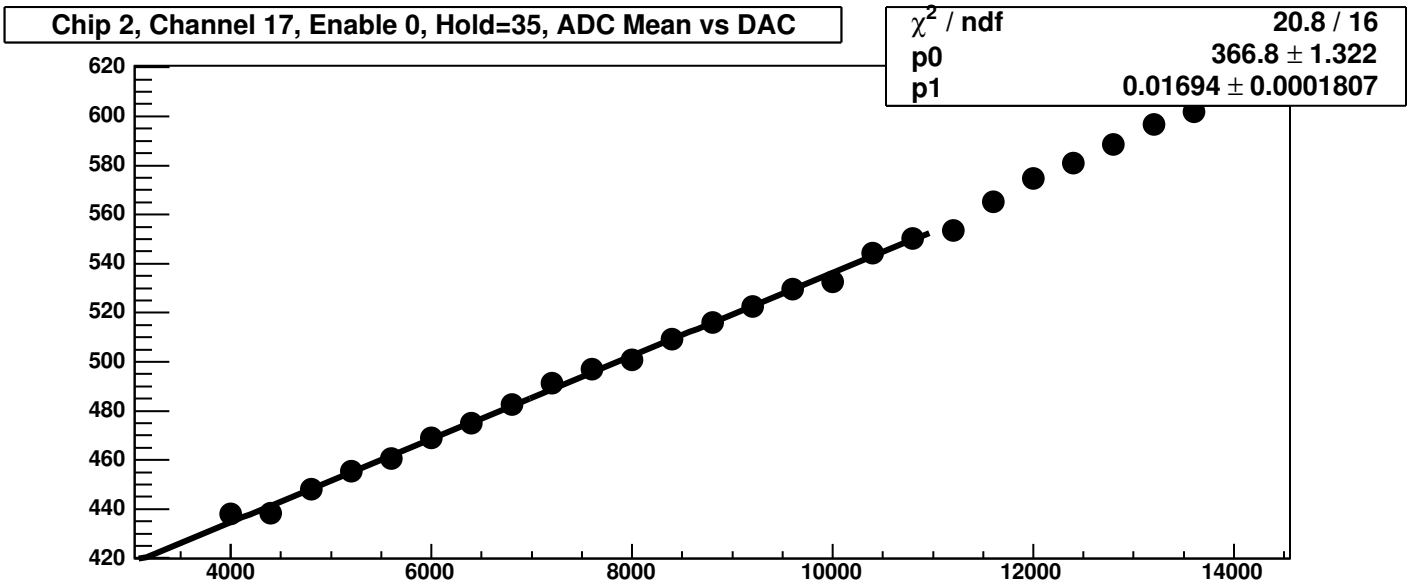

Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

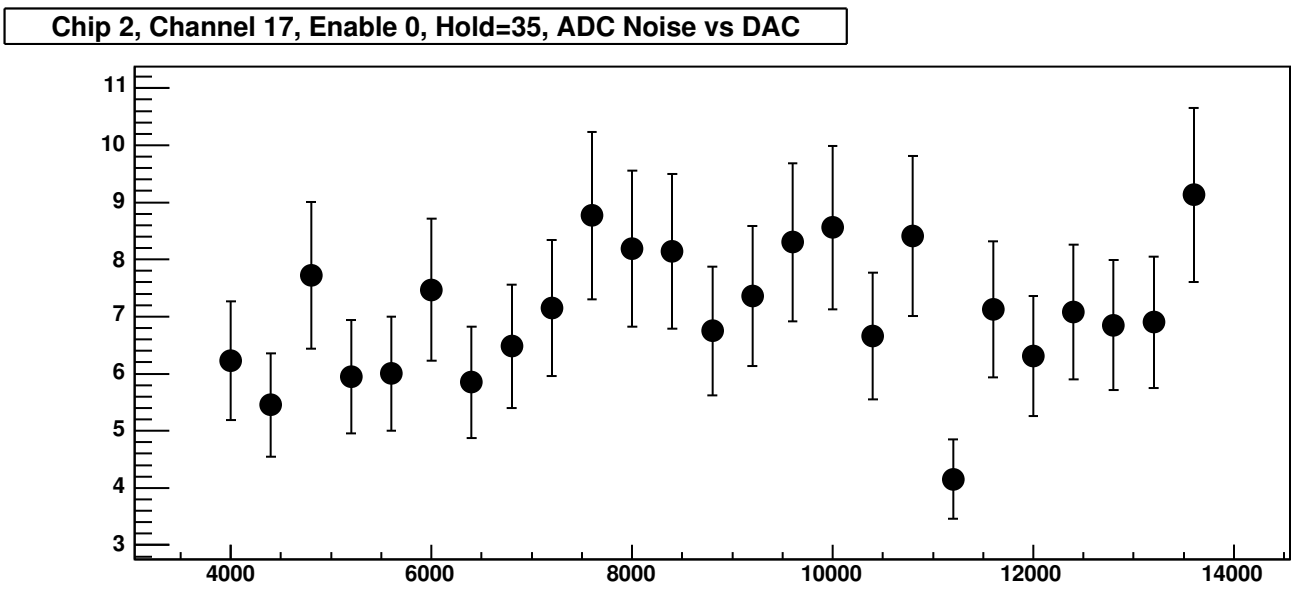

Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

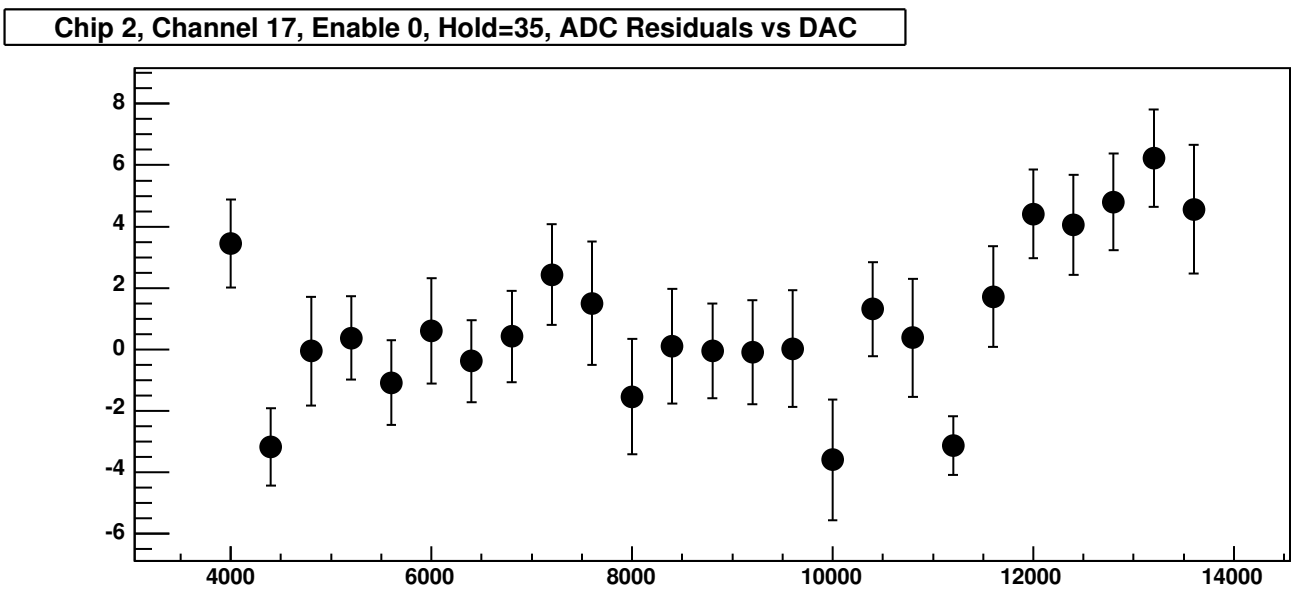

Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.94 / 16
p0 378.8 ± 1.49
p1 0.0142 ± 0.0001956
580
560
540
520
500
480
460
440
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

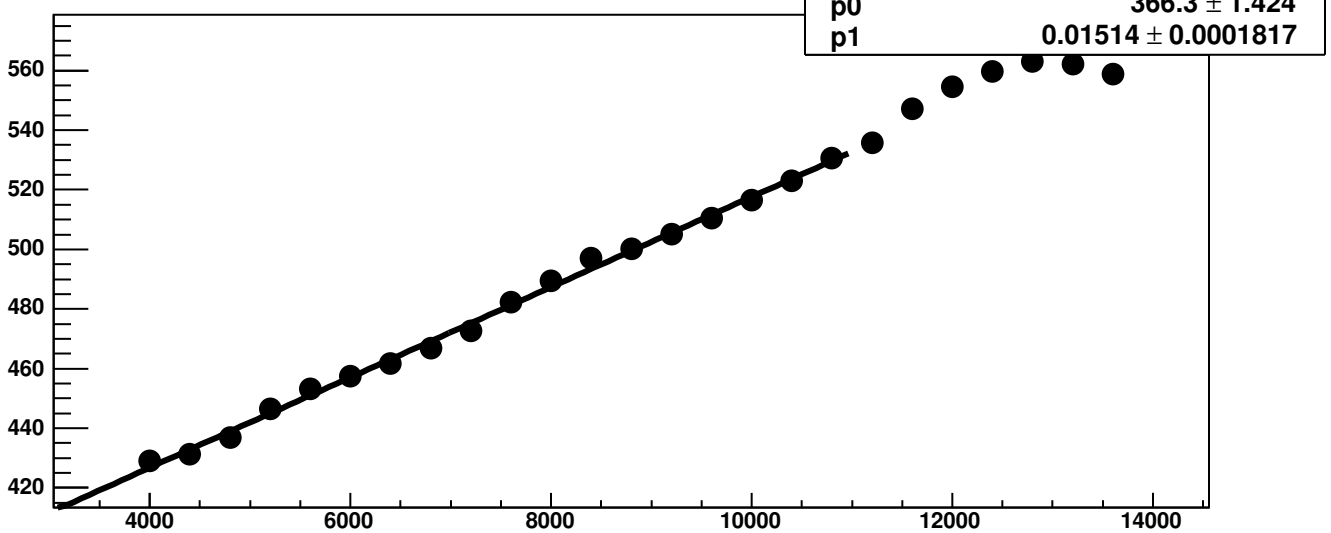

Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20.8 / 16
p0 366.8 ± 1.322
p1 0.01694 ± 0.0001807
620
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

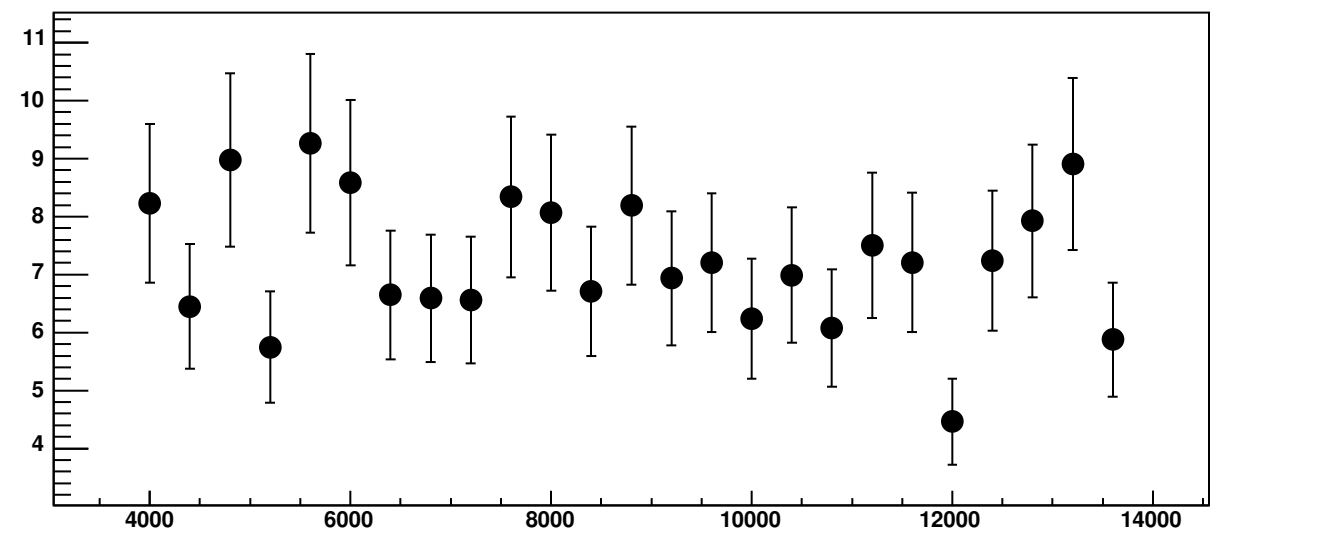

Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

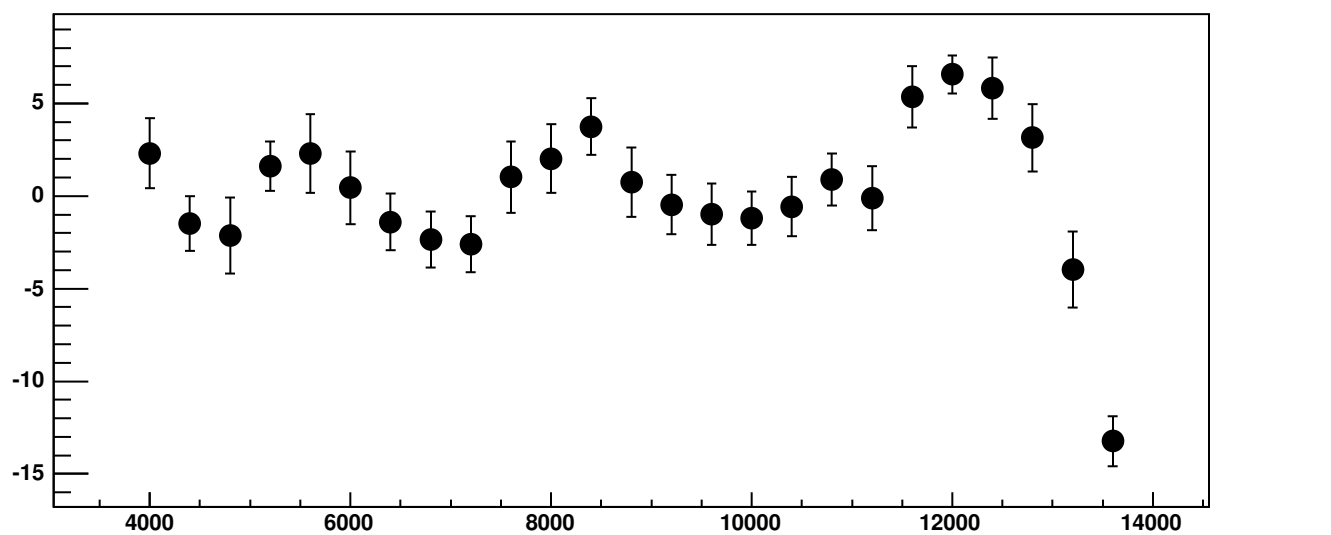
Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

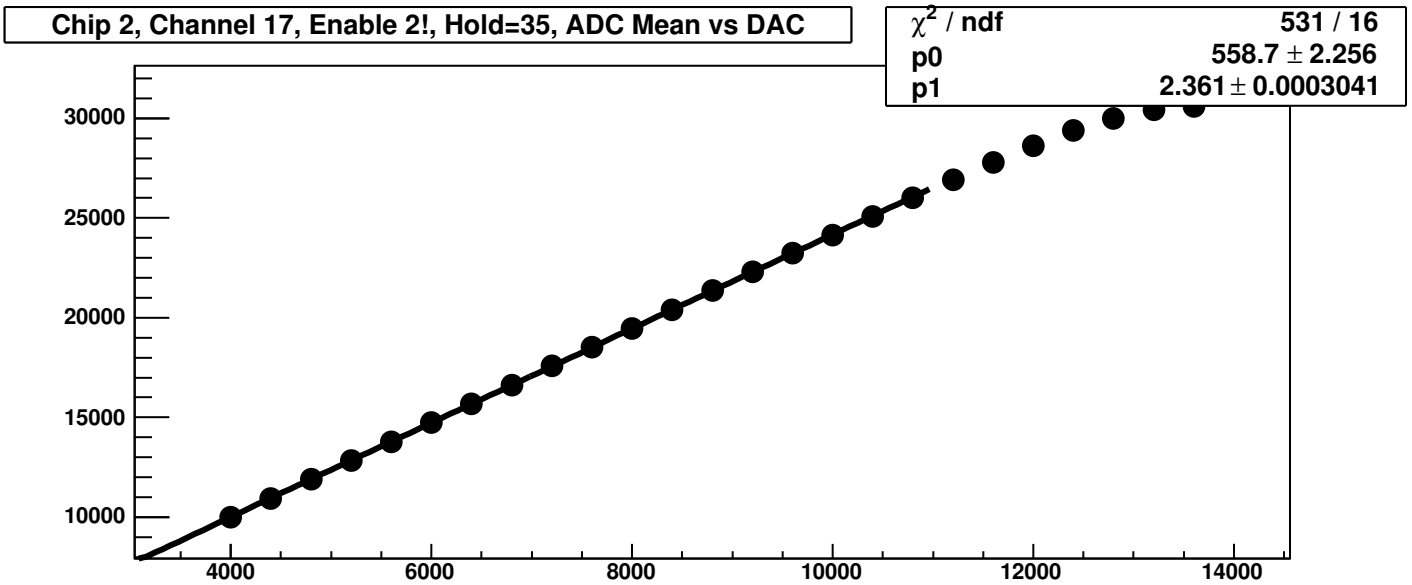


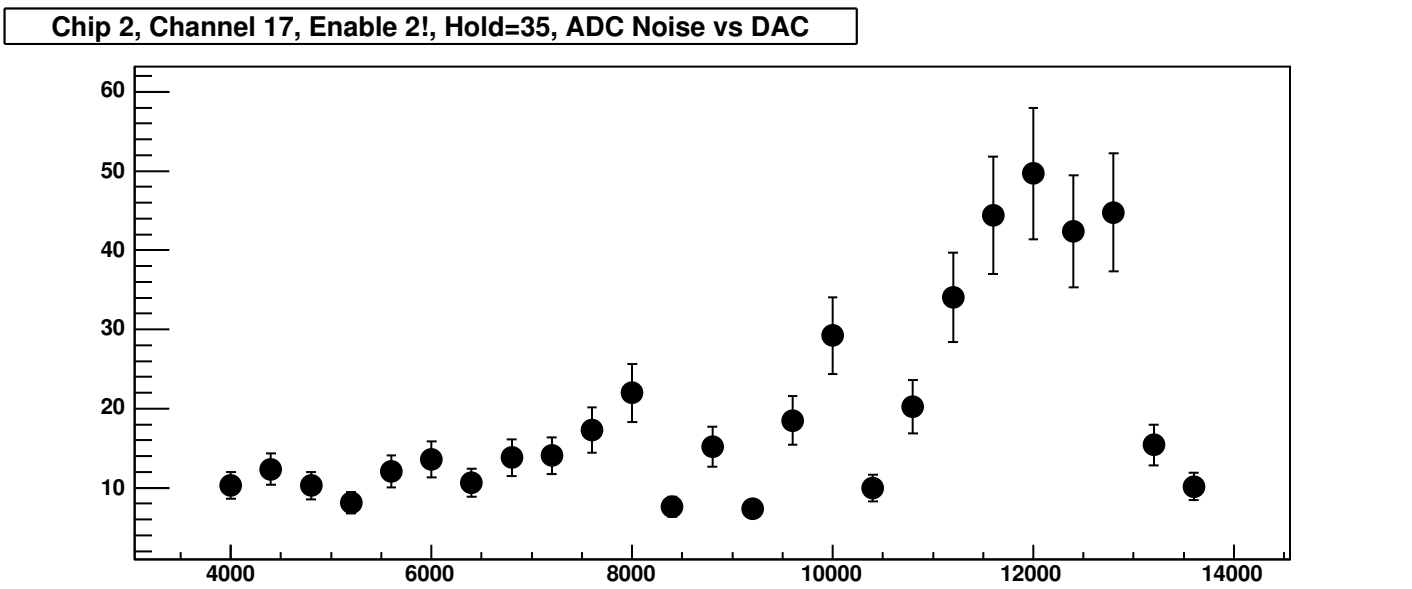
Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

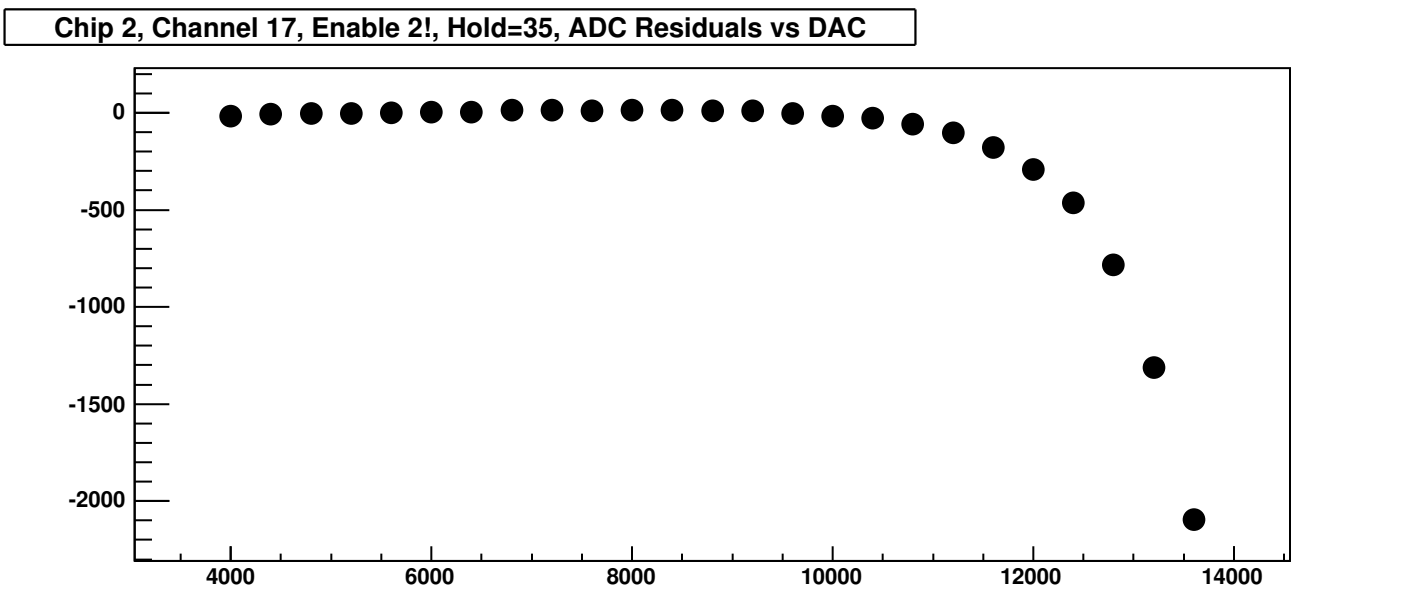


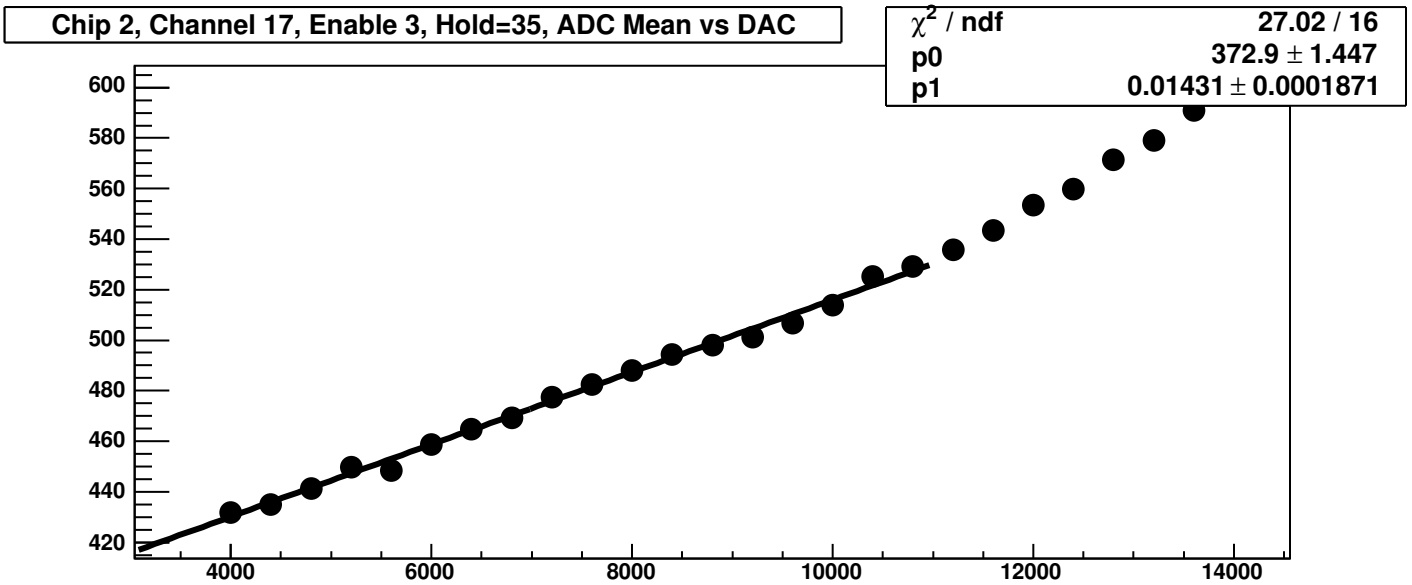
Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

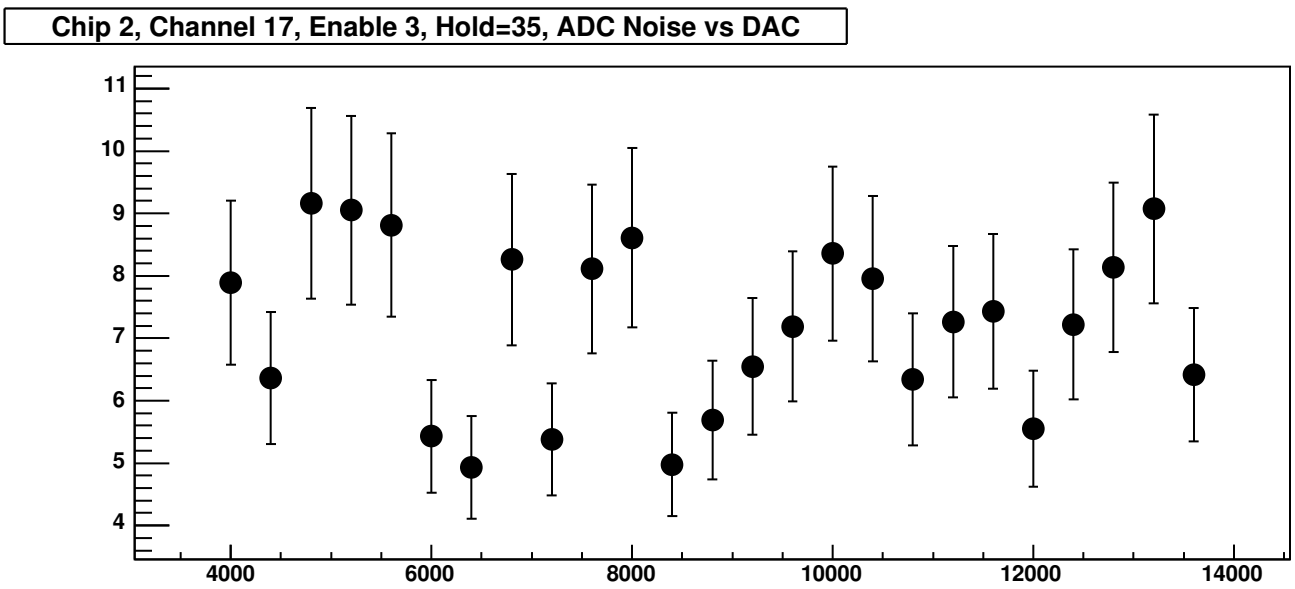


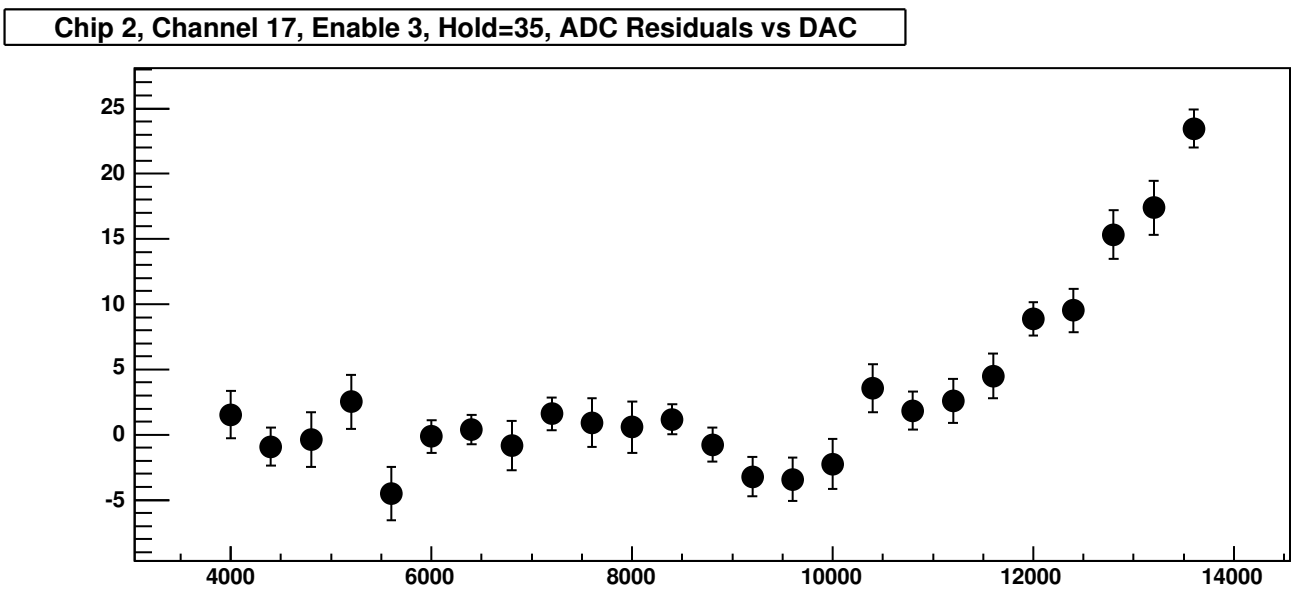

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 531 / 16
p0 558.7 ± 2.256
p1 2.361 ± 0.0003041
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
4000
6000
8000
10000
12000
14000

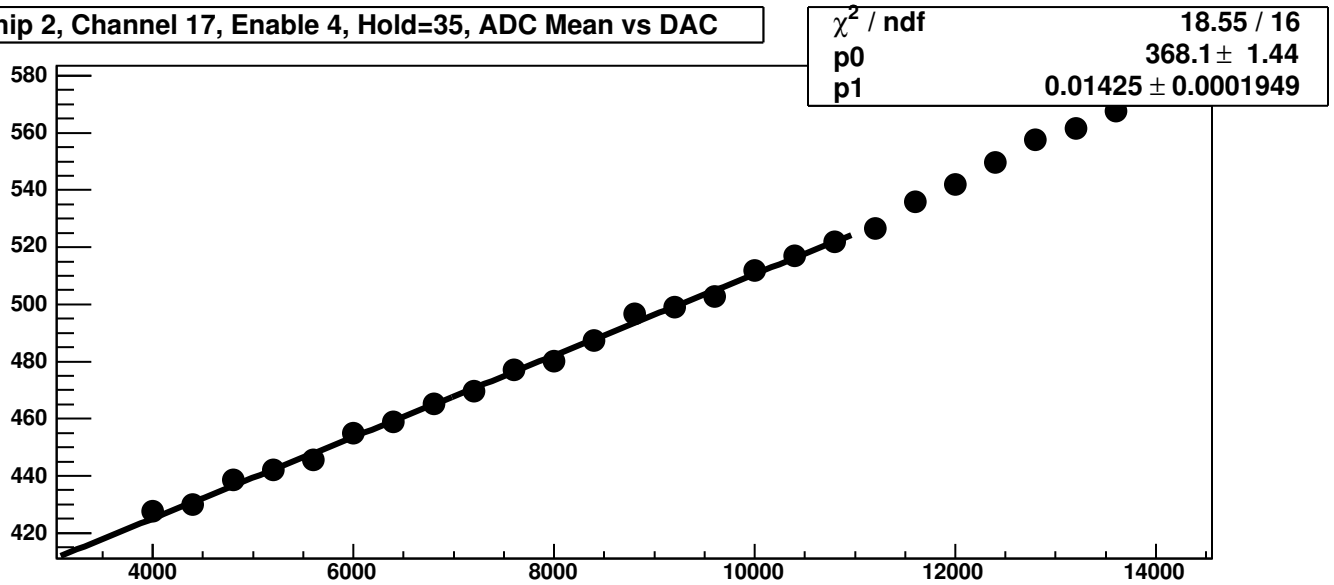

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.02 / 16
p0 372.9 ± 1.447
p1 0.01431 ± 0.0001871
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

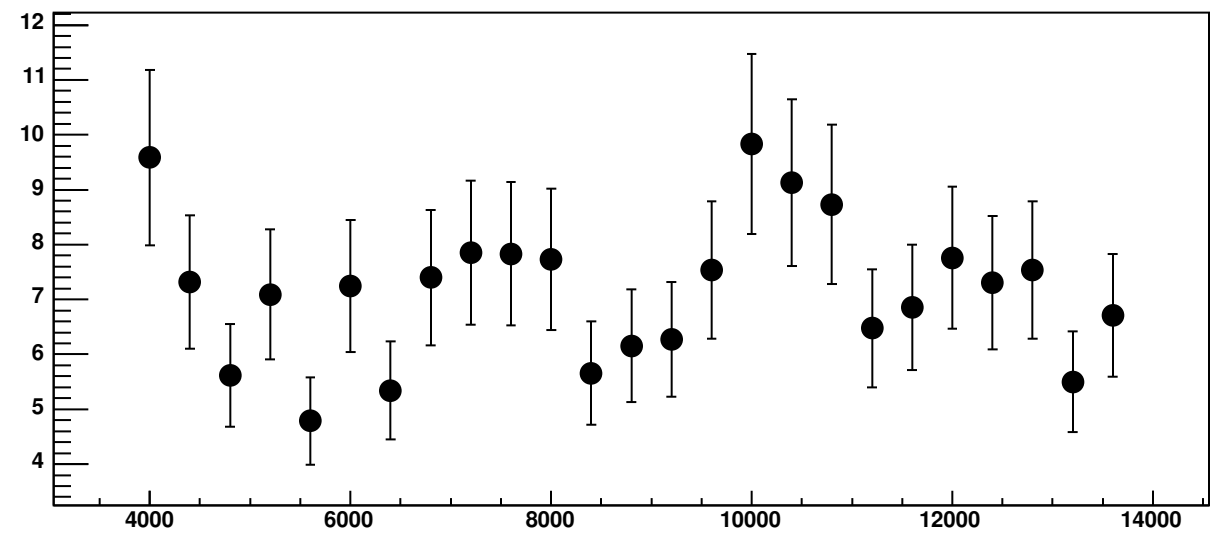

Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

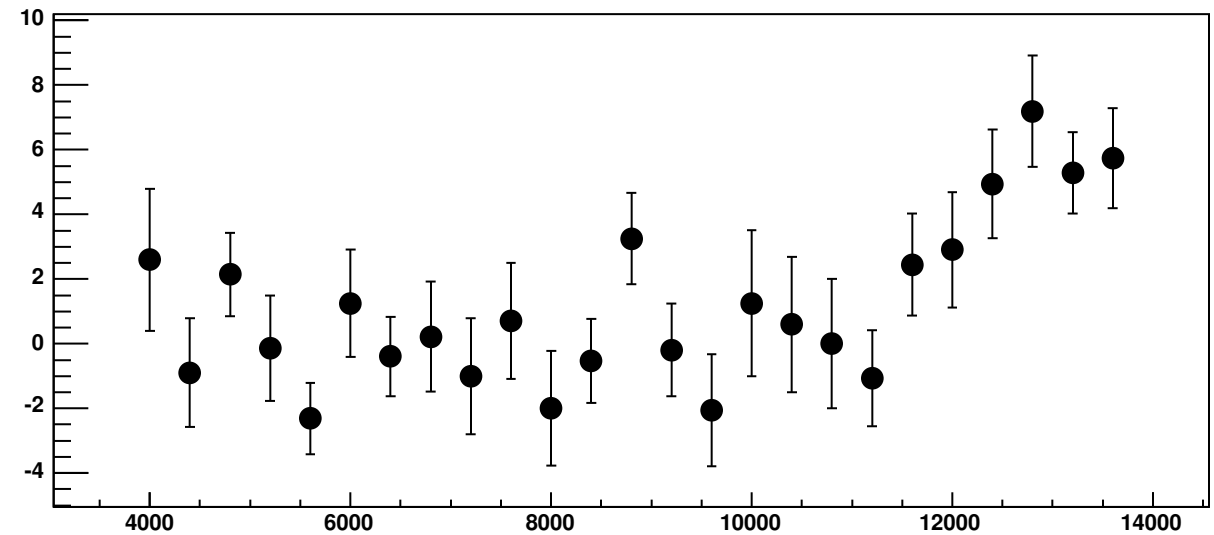
Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



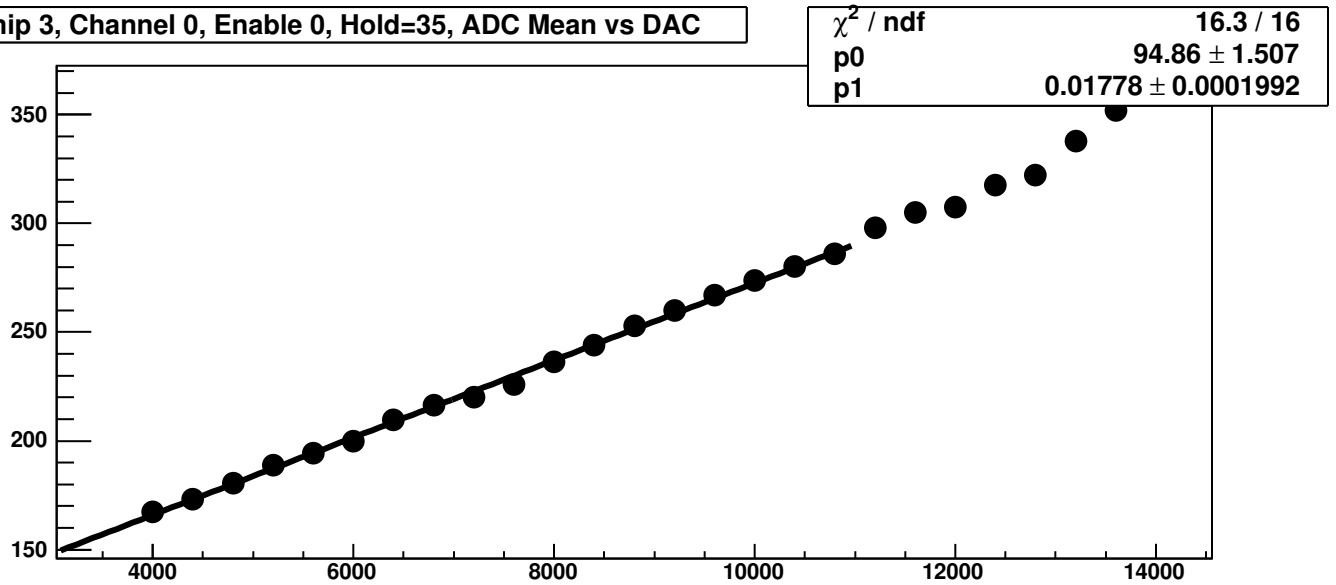
Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



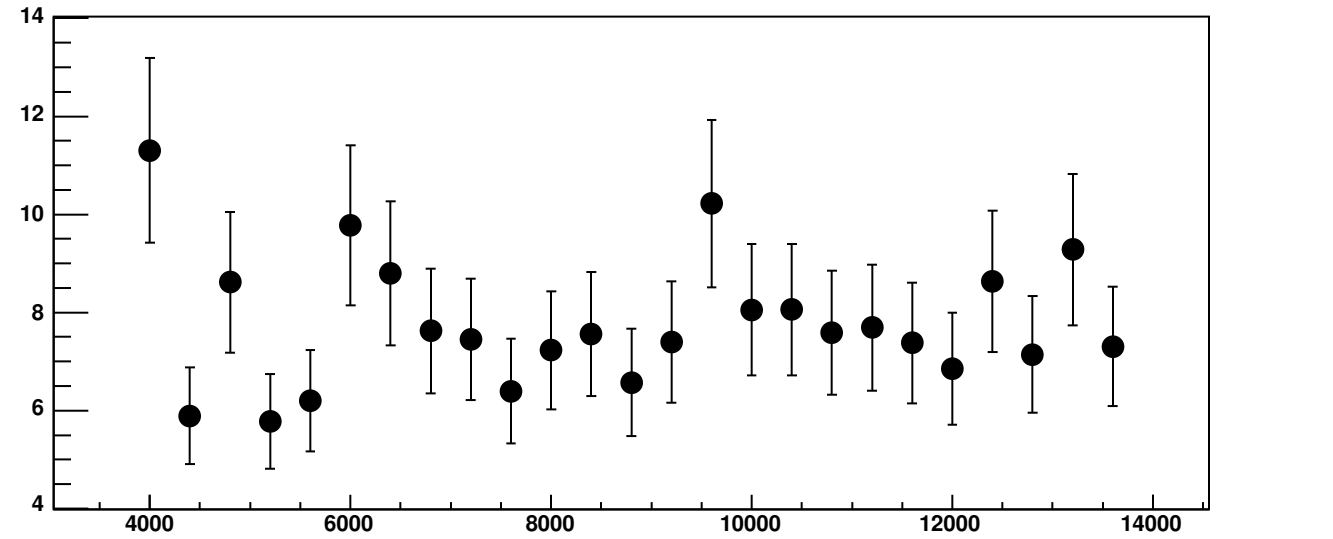
Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



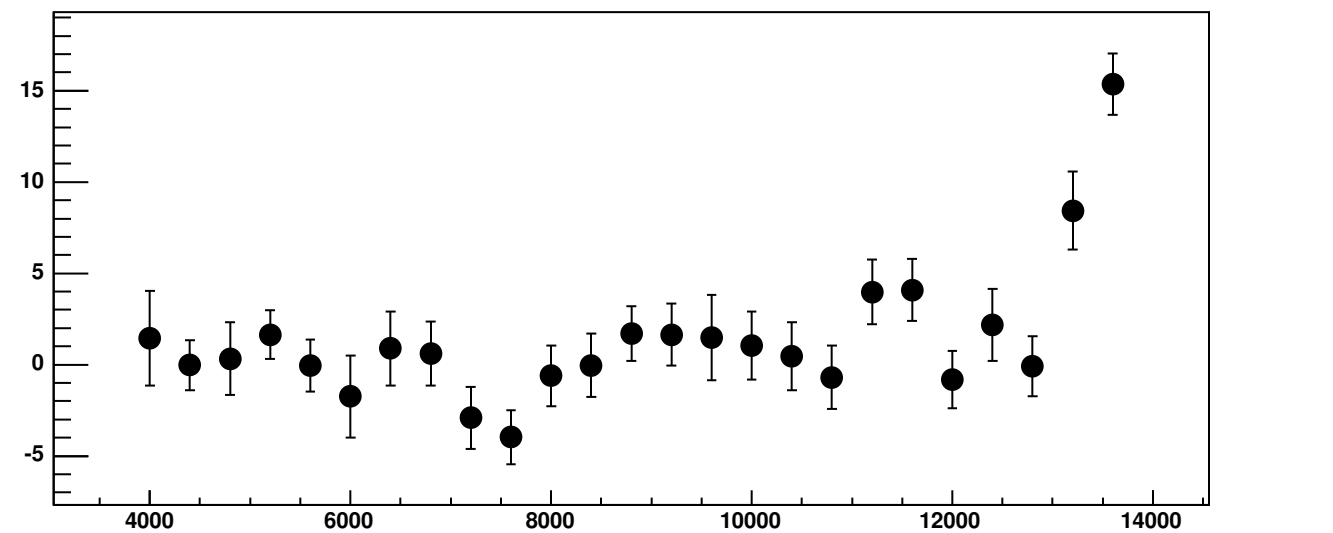
Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

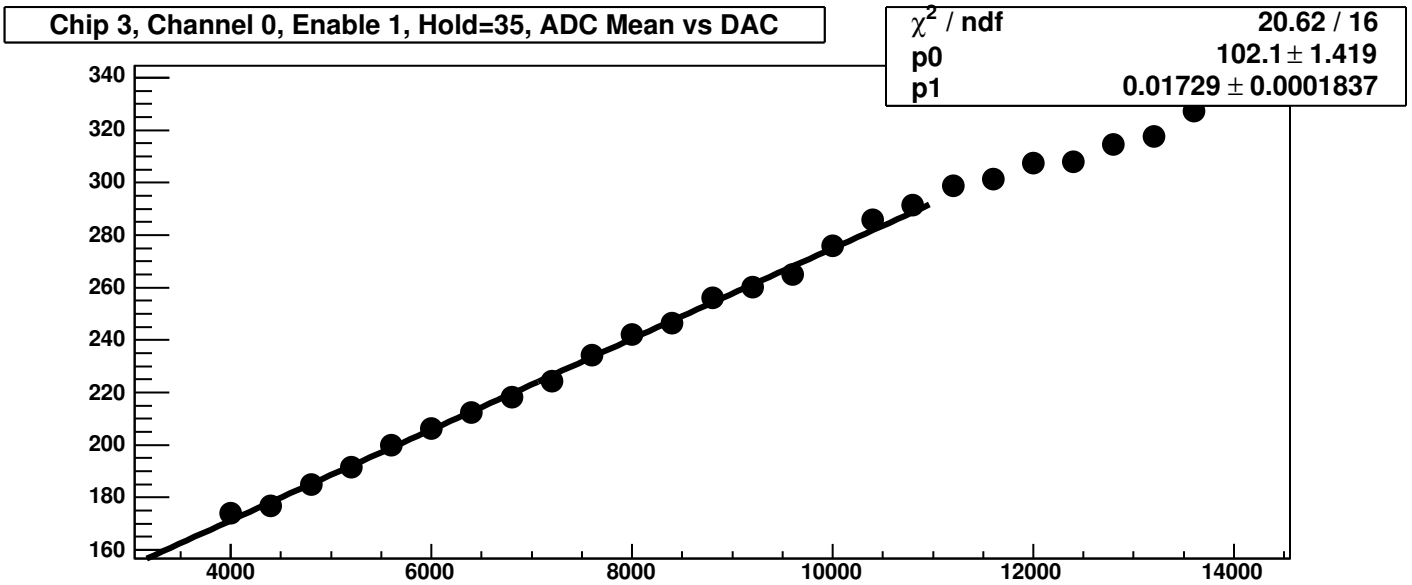


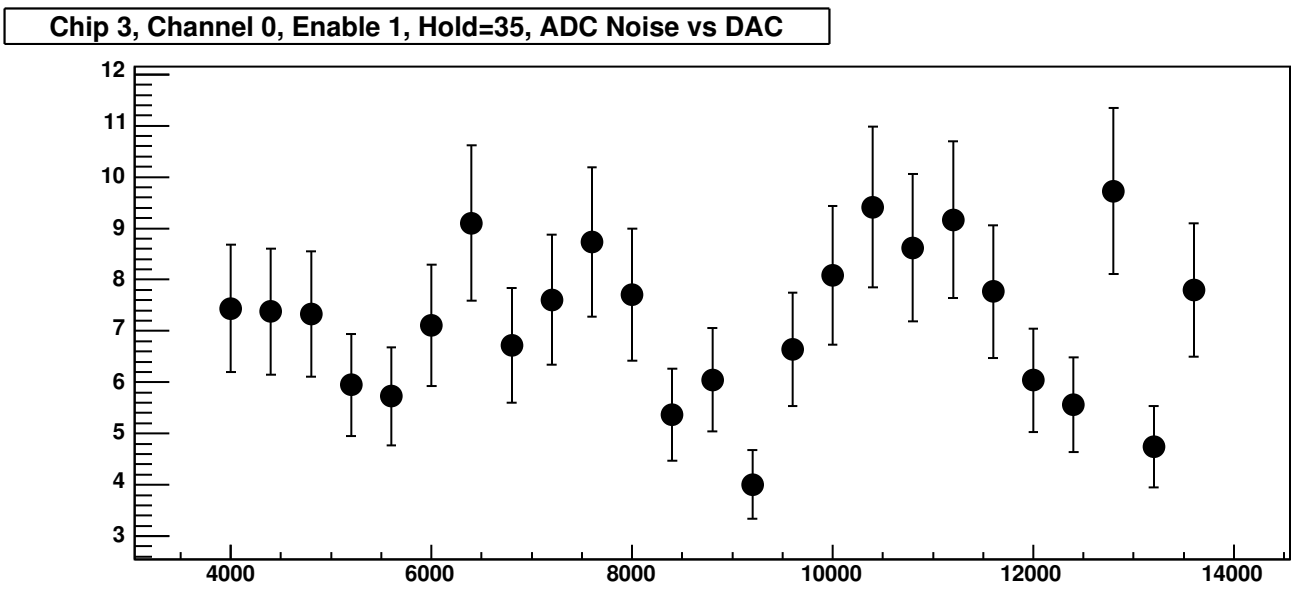
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

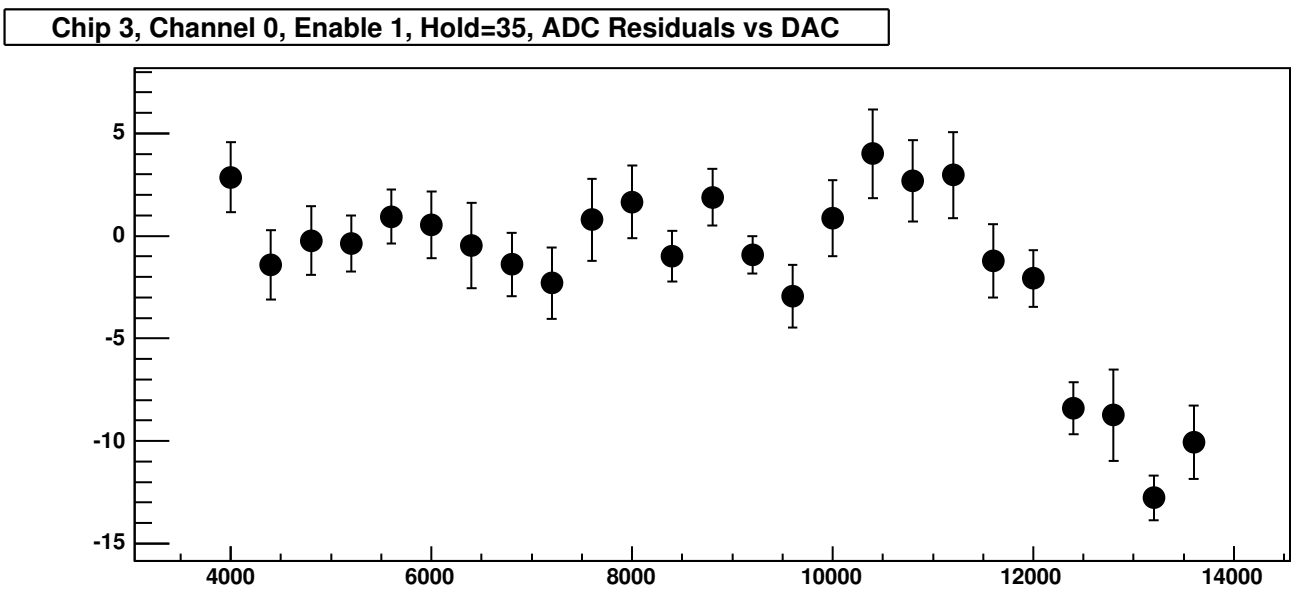


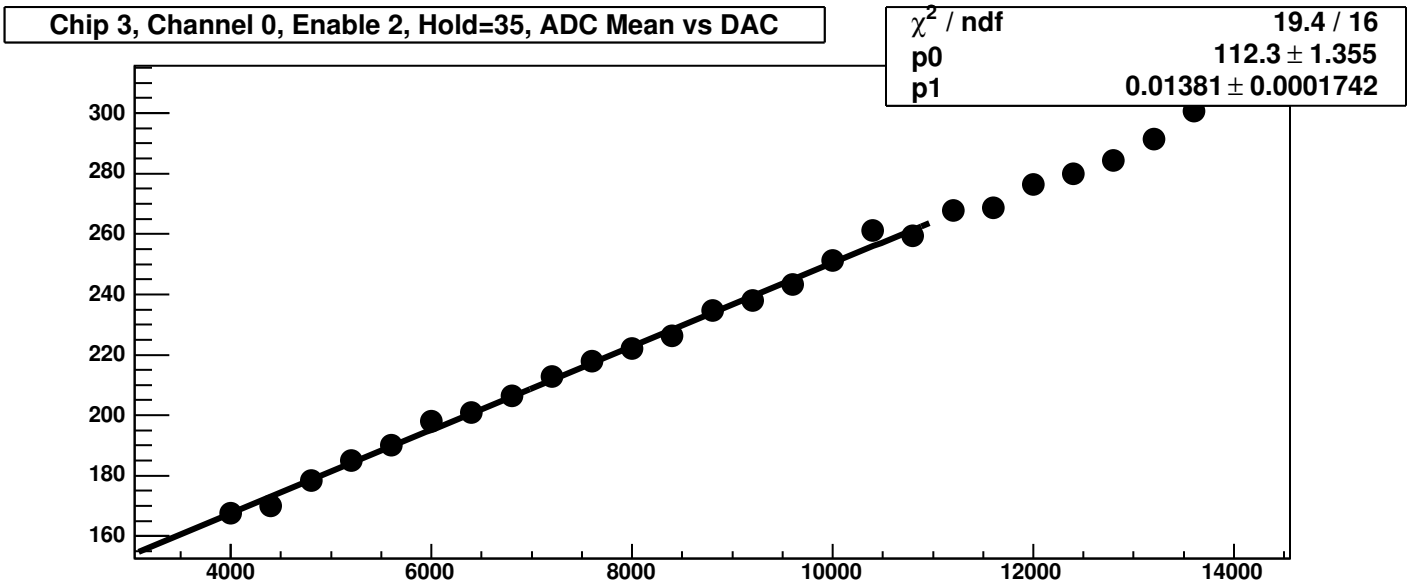
Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

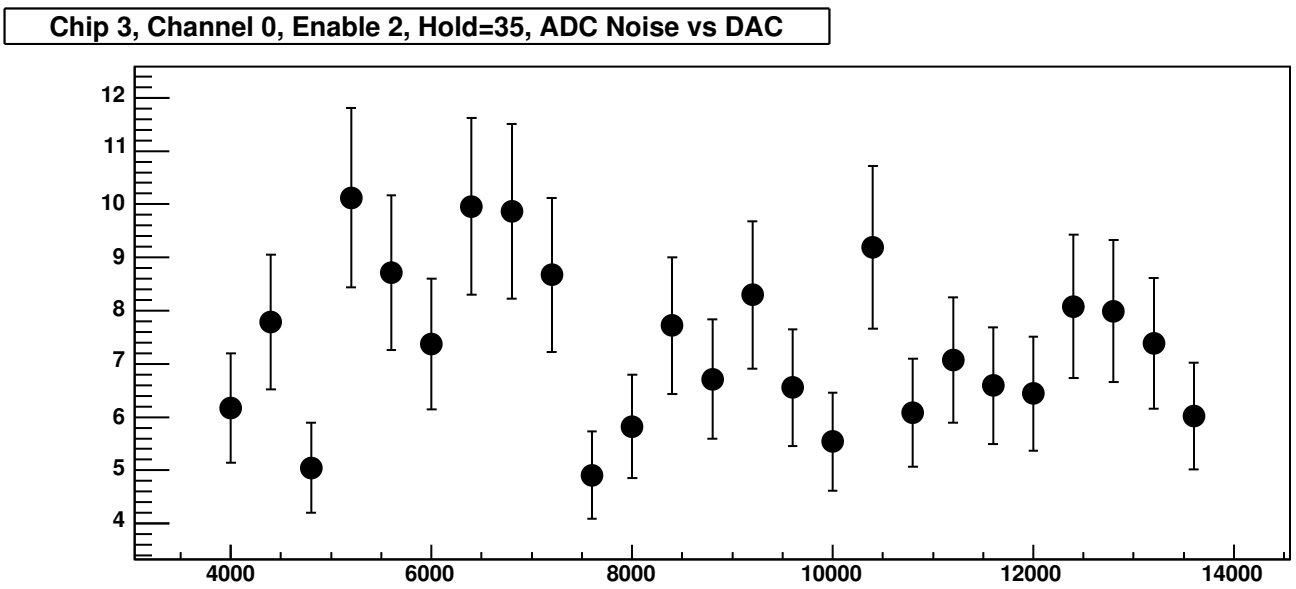


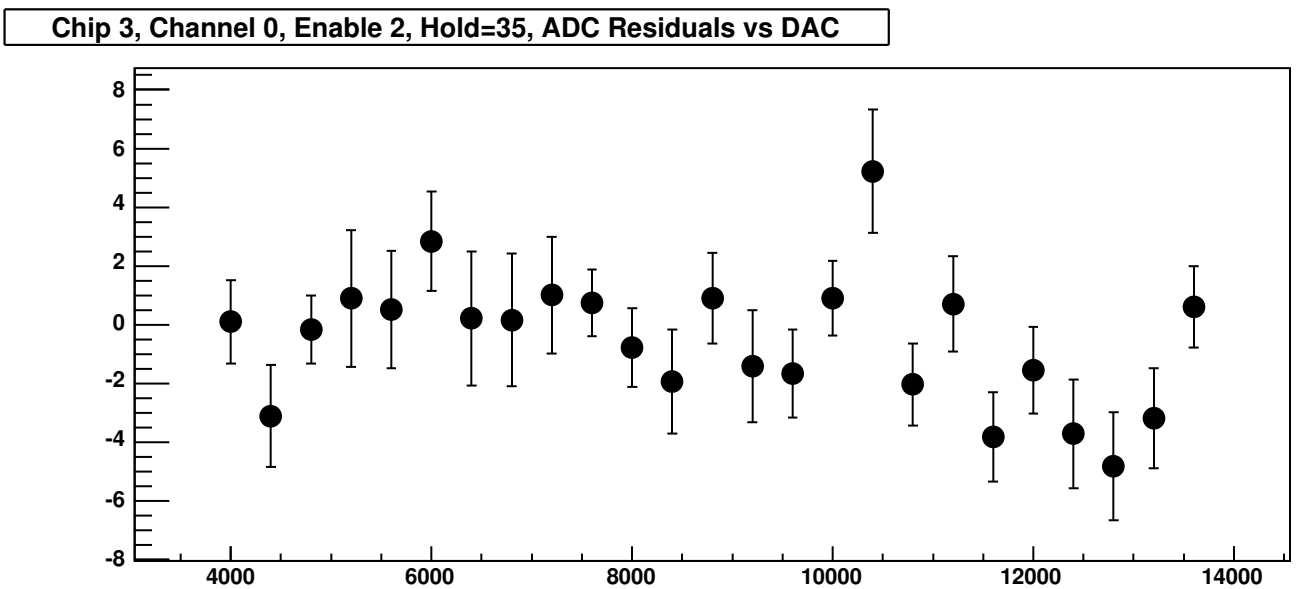

Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.62 / 16
p0 102.1 ± 1.419
p1 0.01729 ± 0.0001837
340
320
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

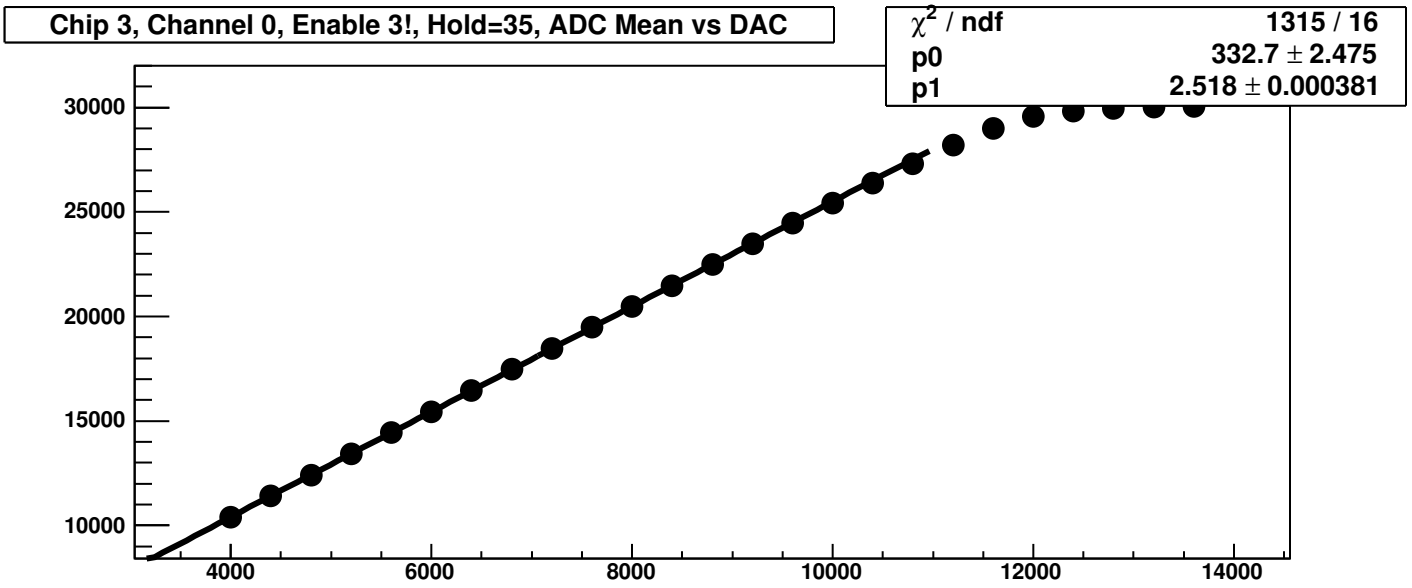

Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

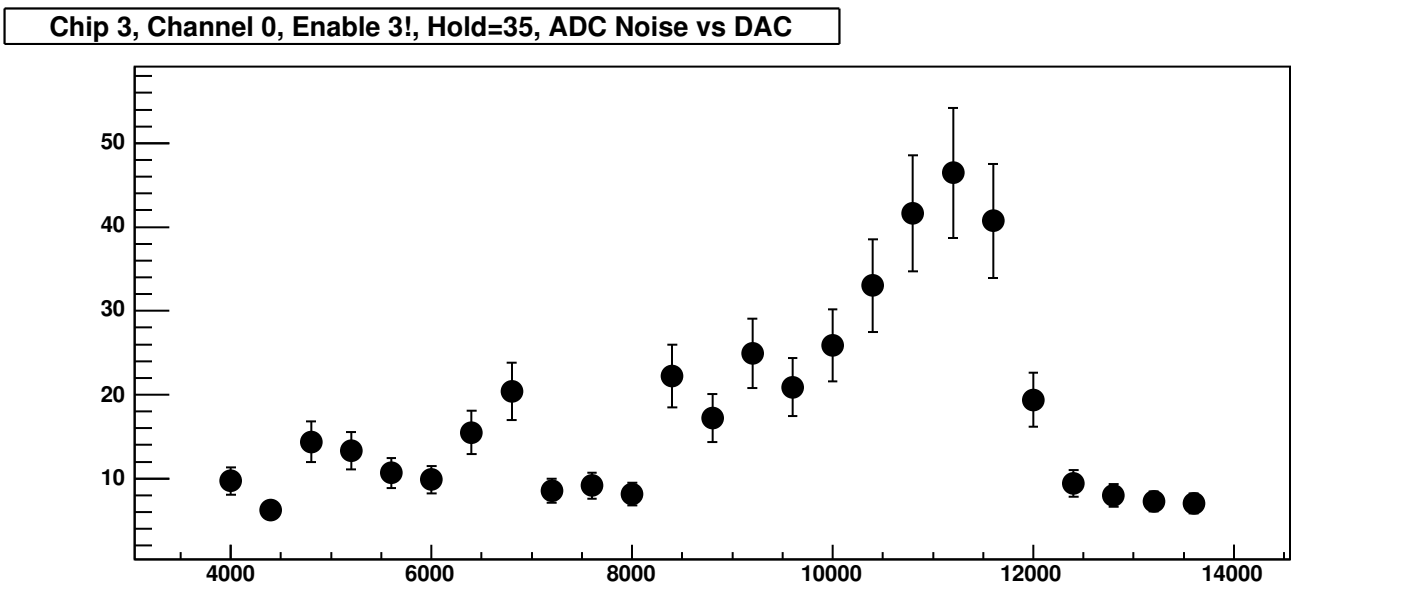

Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

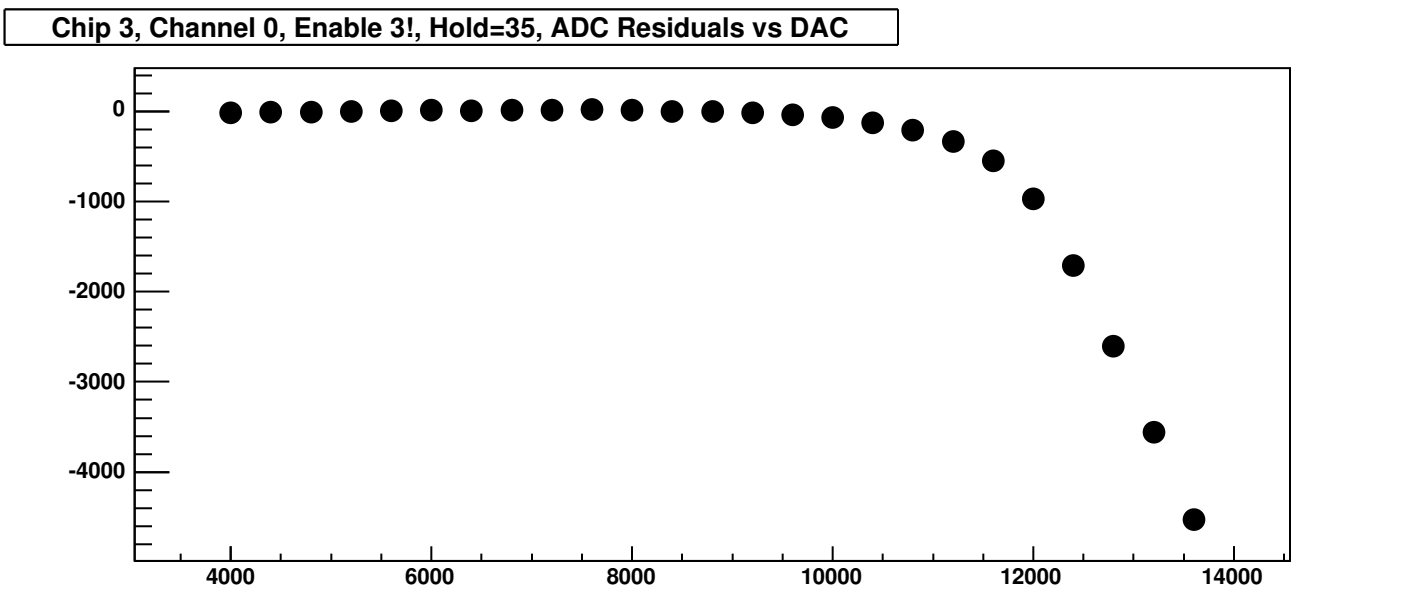

Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.4 / 16
p0 112.3 ± 1.355
p1 0.01381 ± 0.0001742
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

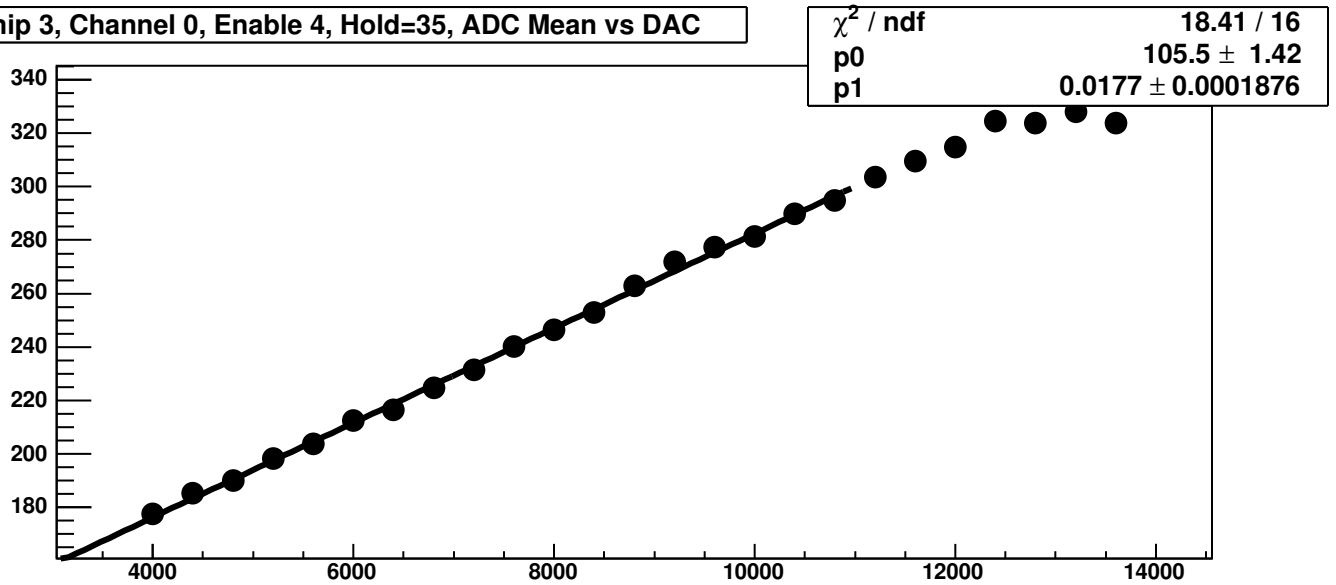

Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1315 / 16
p0 332.7 ± 2.475
p1 2.518 ± 0.000381
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

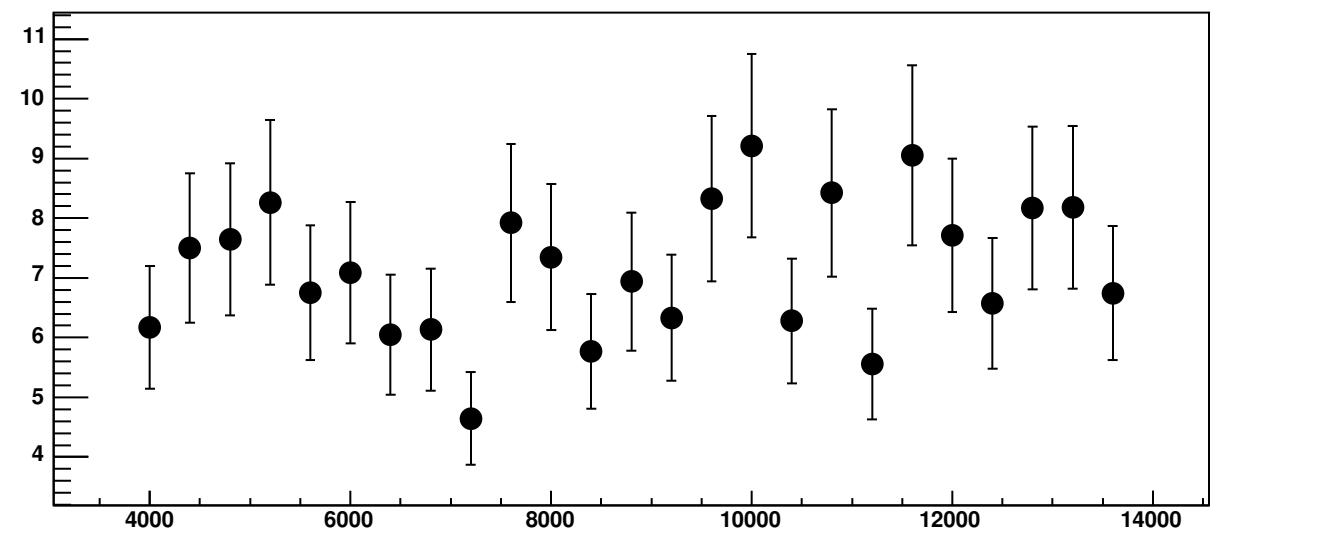

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

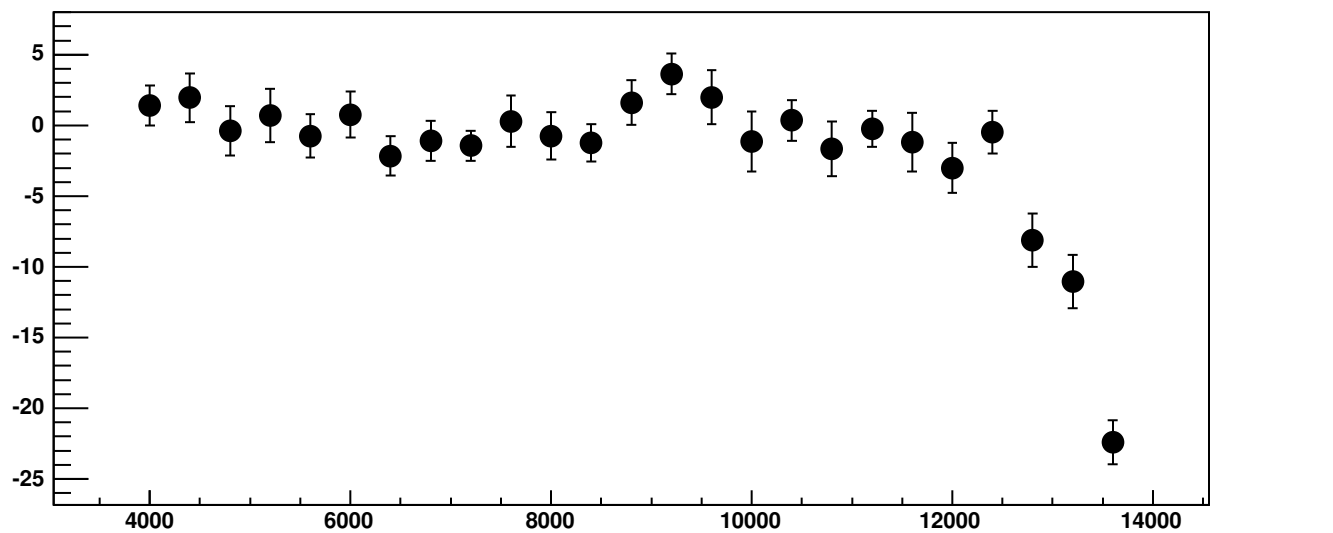
Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

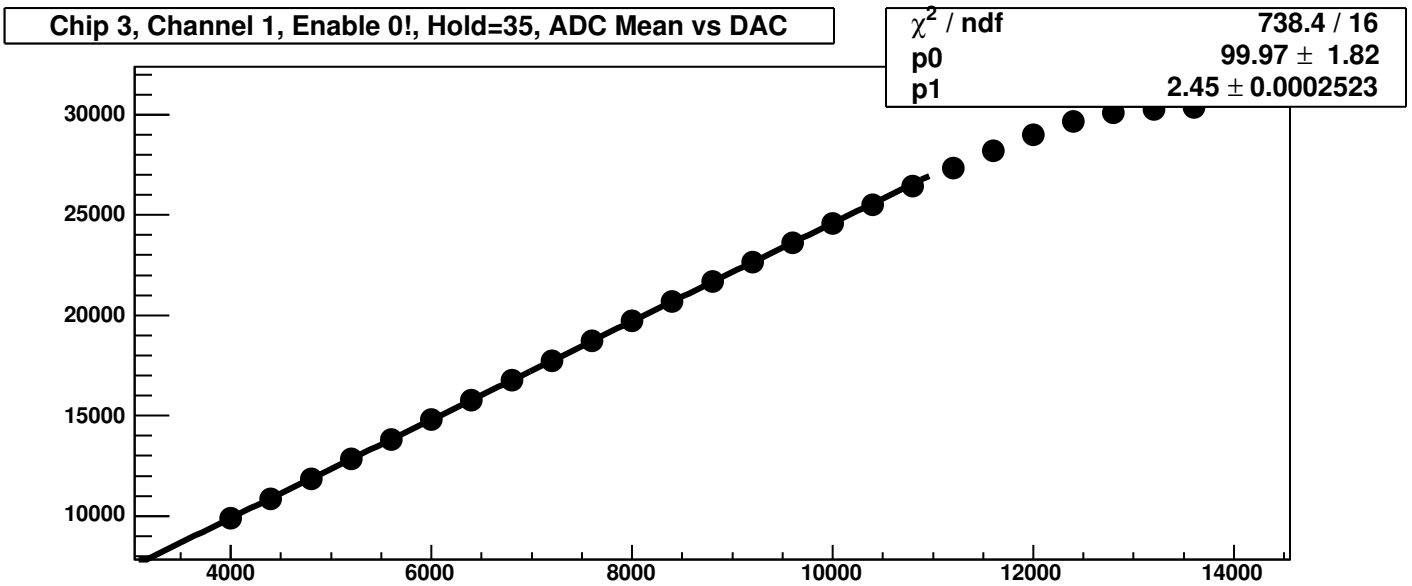


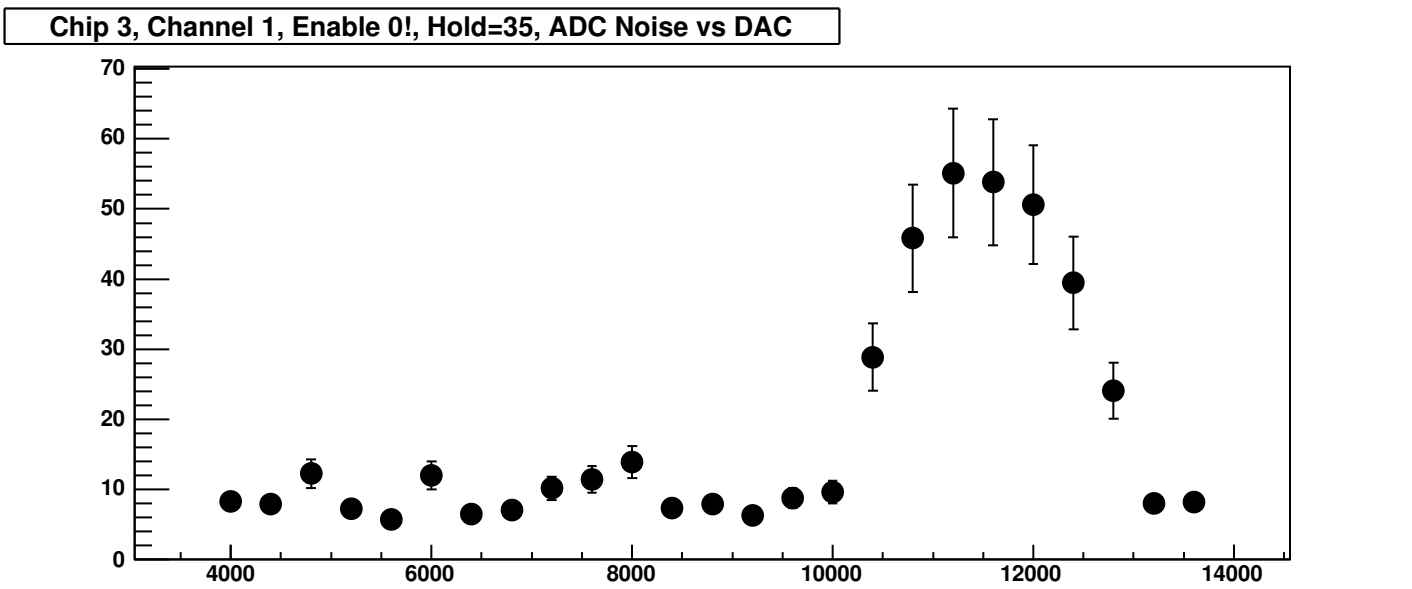
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

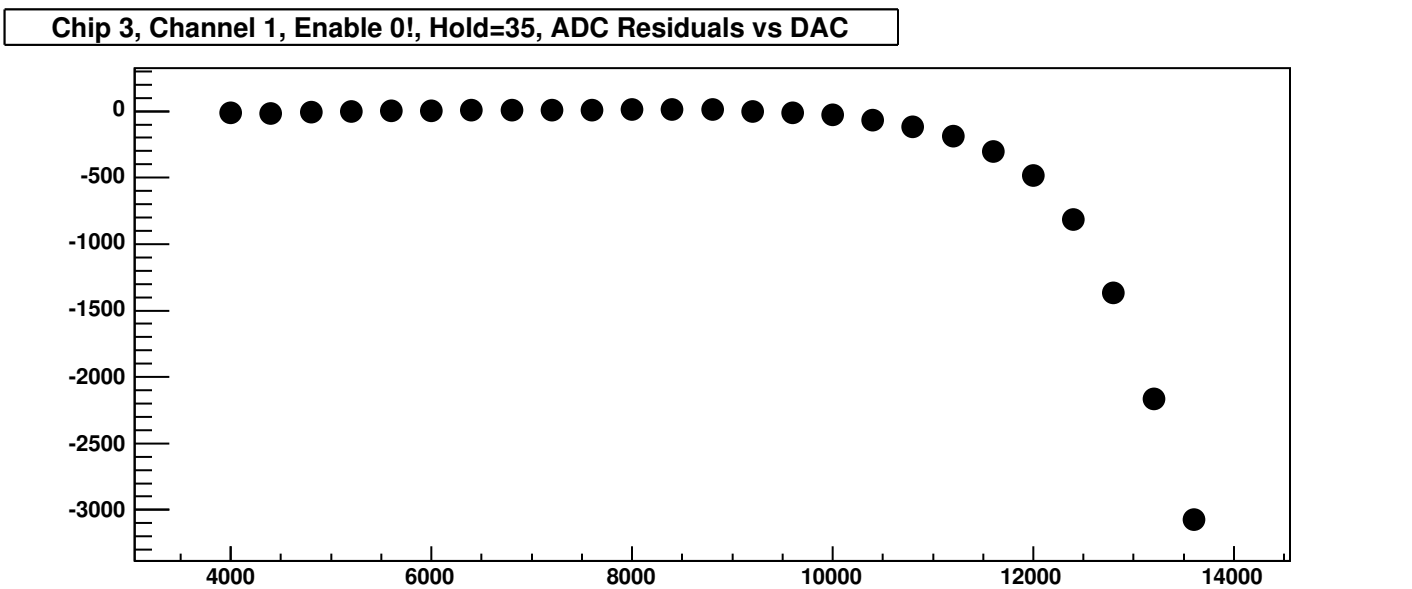


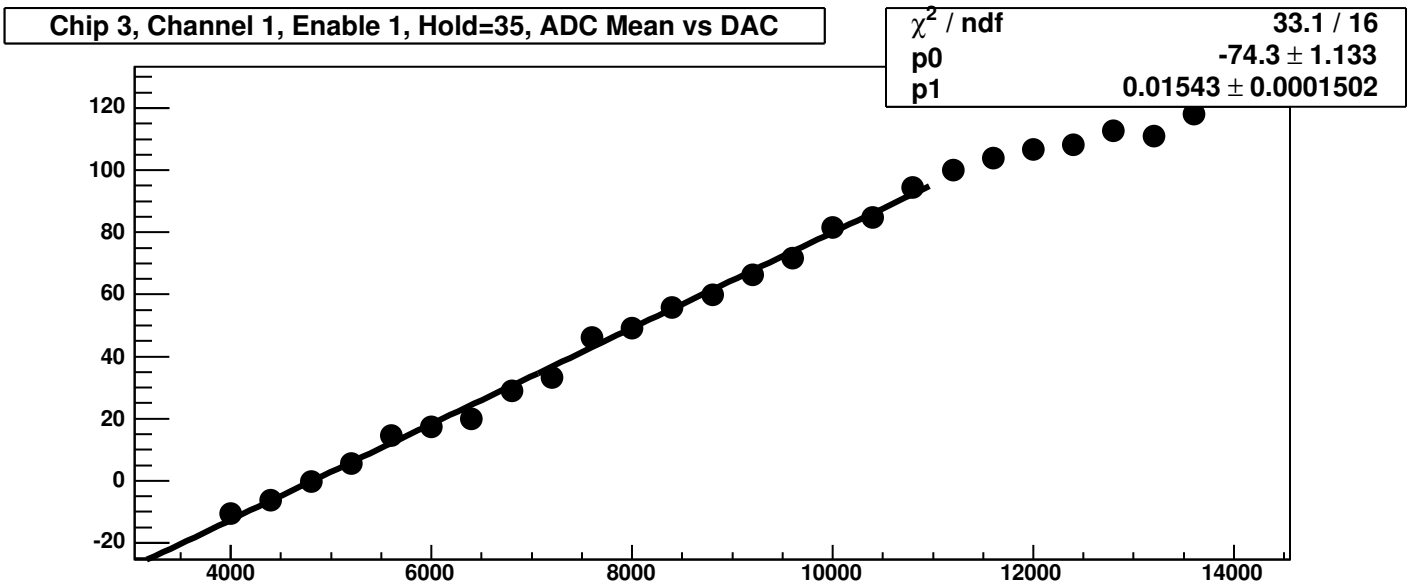
Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

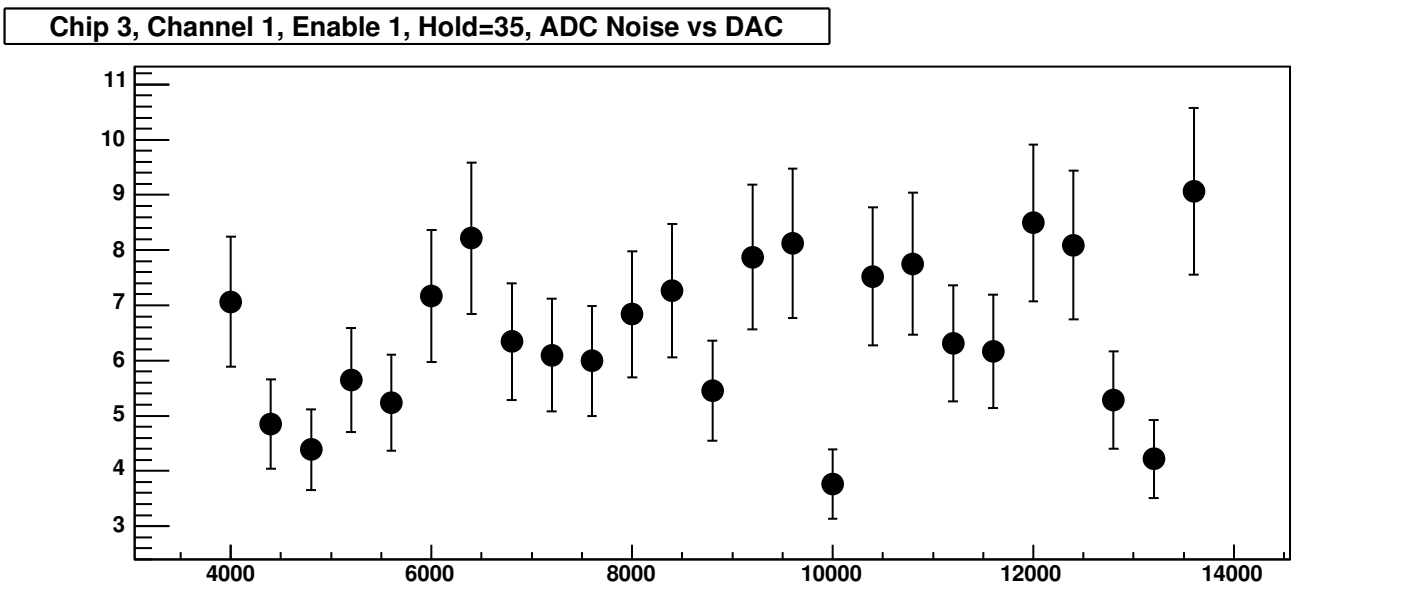


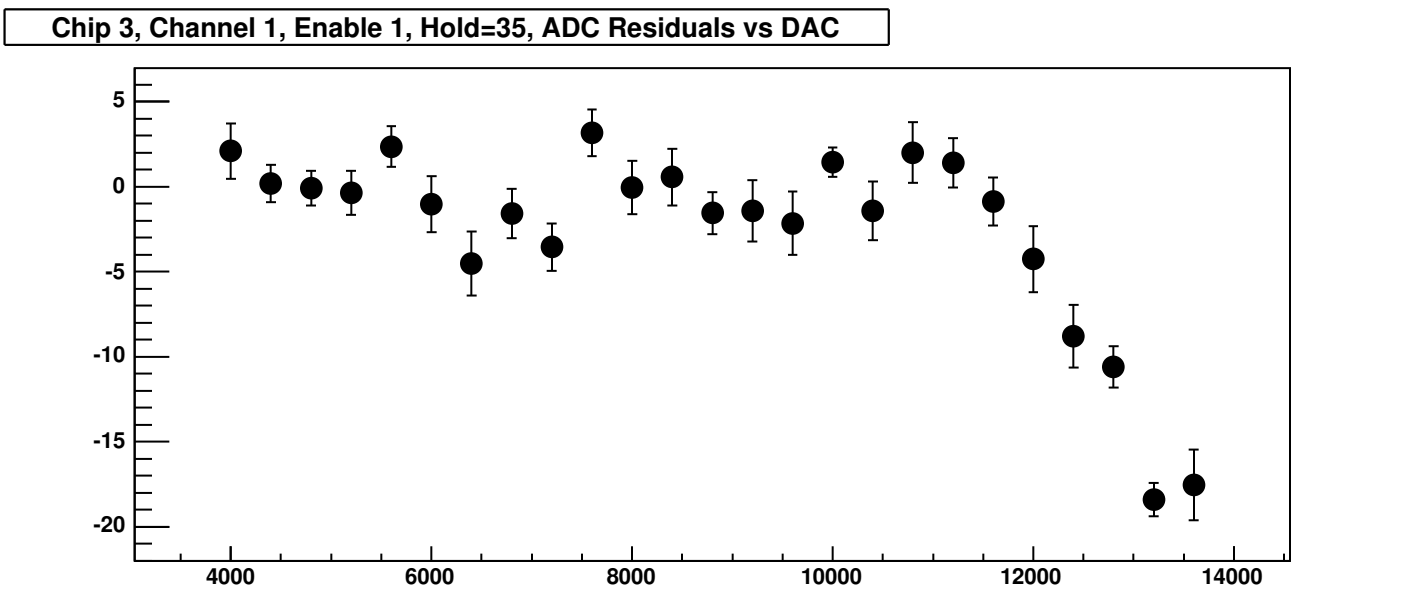

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 738.4 / 16
p0 99.97 ± 1.82
p1 2.45 ± 0.0002523
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

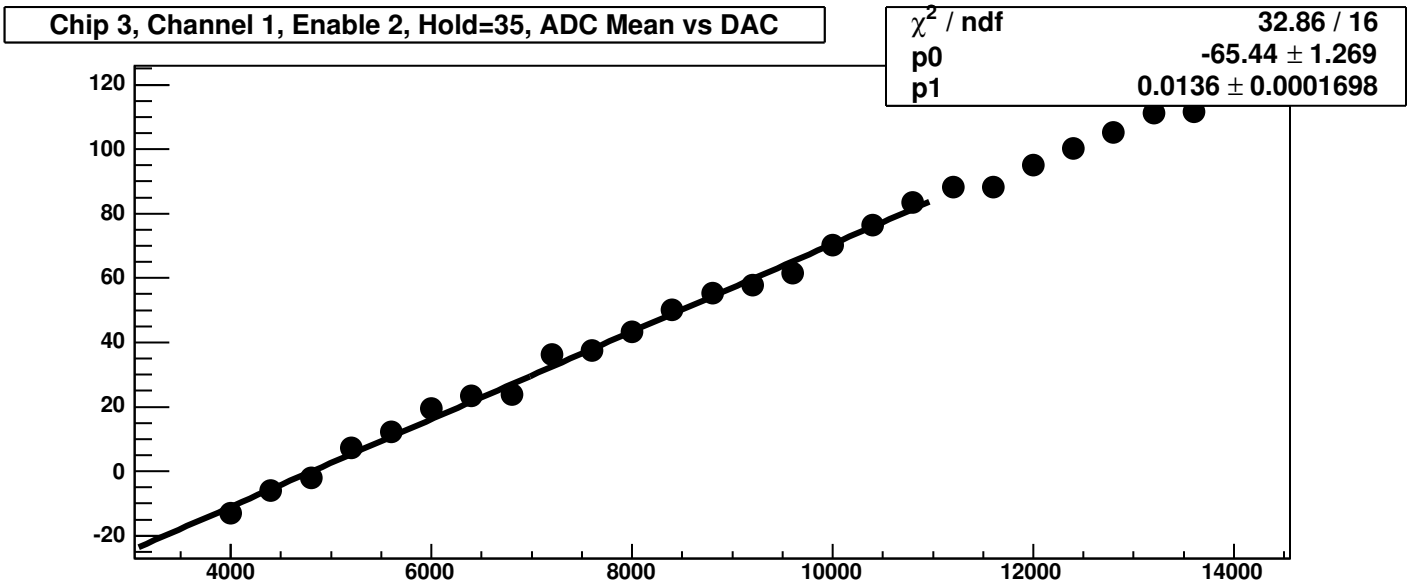

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

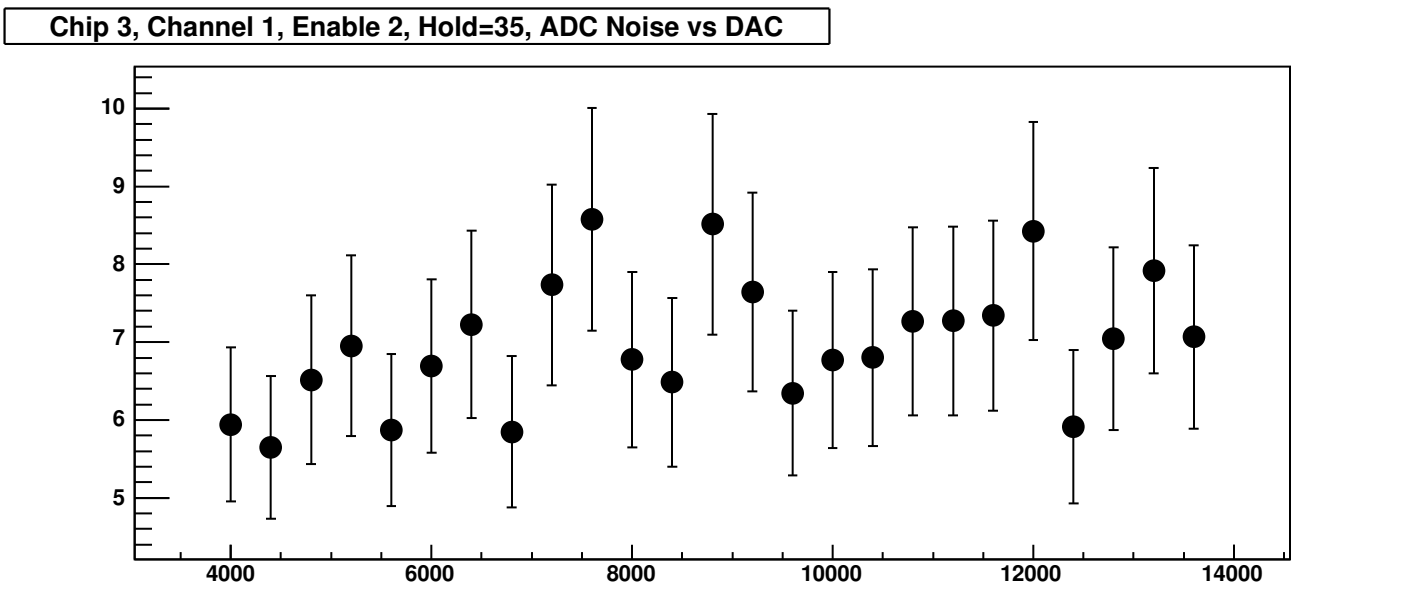

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
4000
6000
8000
10000
12000
14000

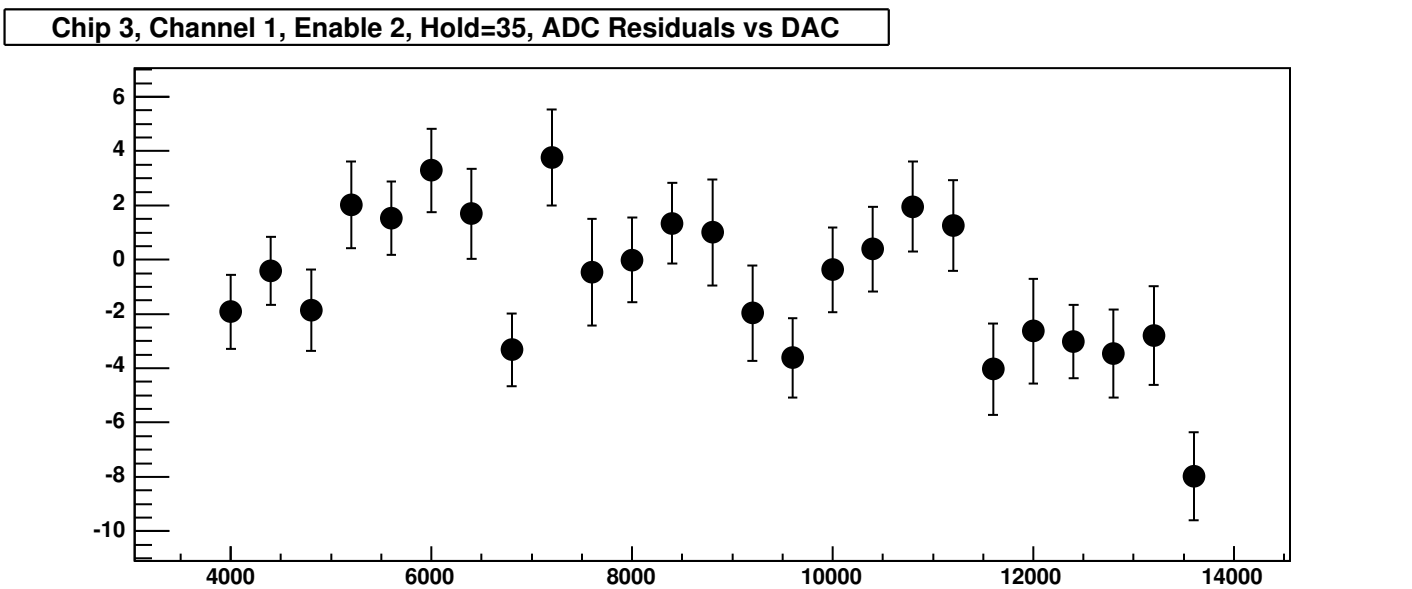

Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.1 / 16
p0 -74.3 ± 1.133
p1 0.01543 ± 0.0001502
120
100
80
60
40
20
0
-20
4000
6000
8000
10000
12000
14000

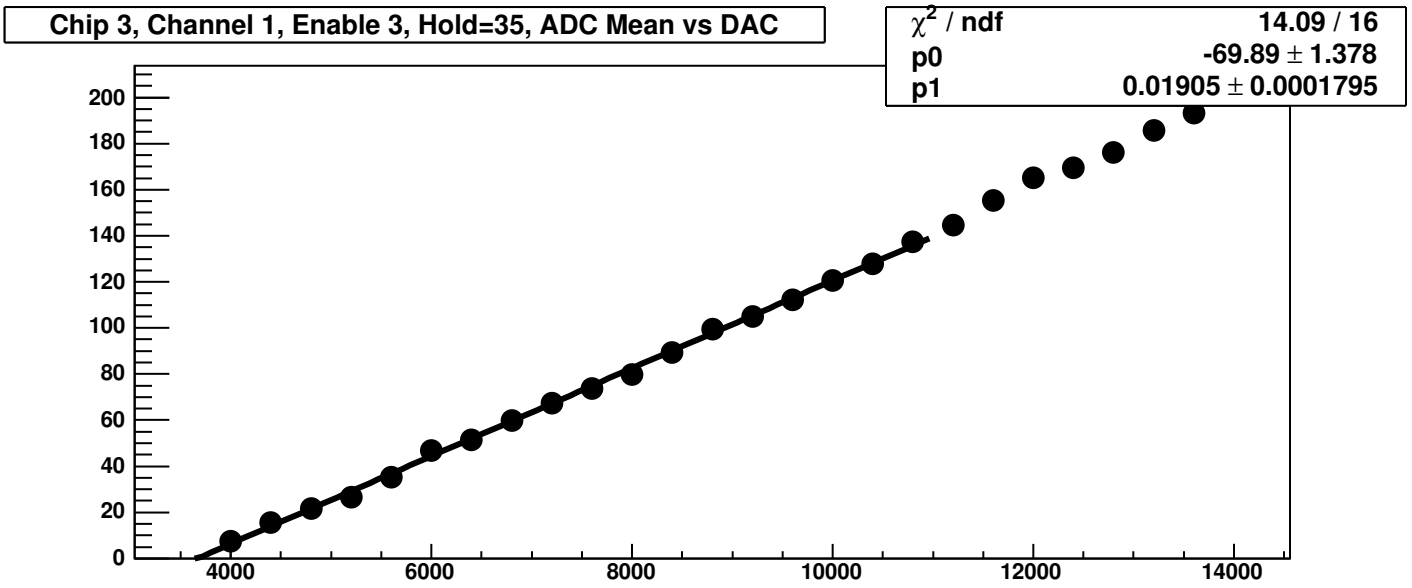

Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

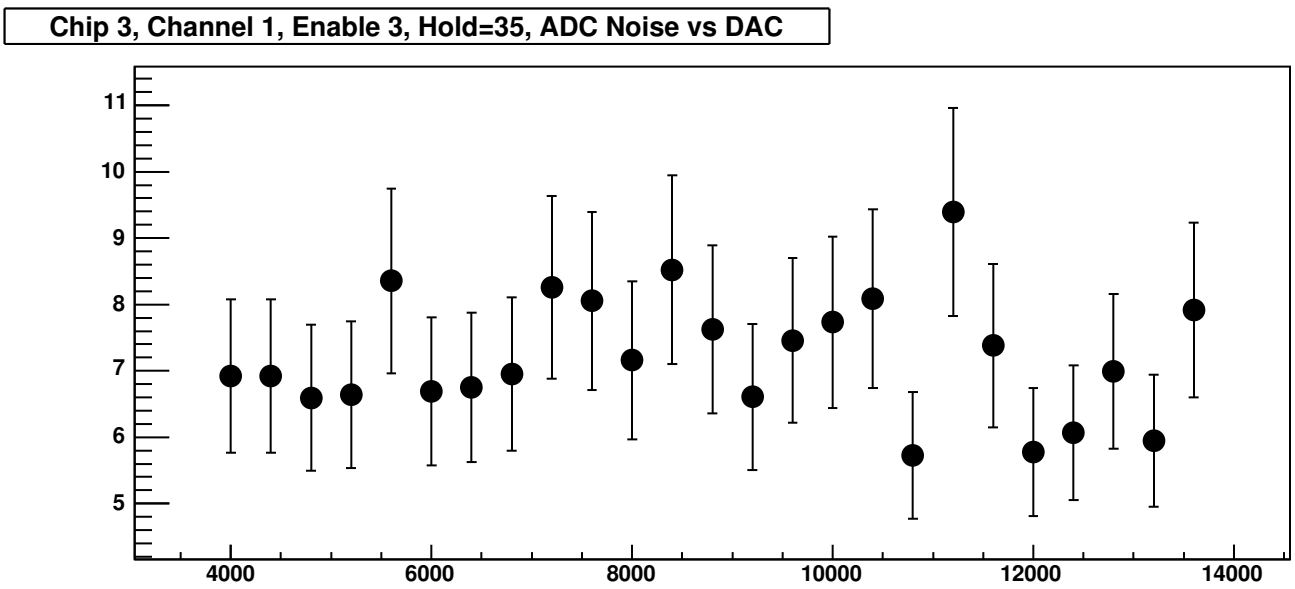

Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

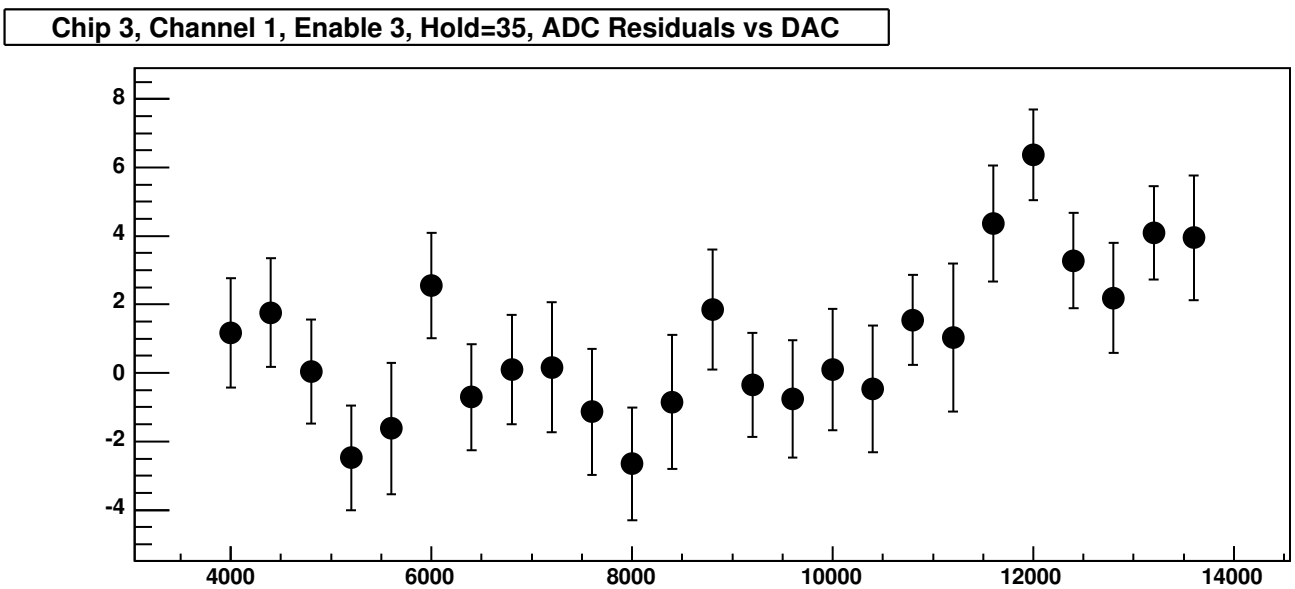

Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.86 / 16
p0 -65.44 ± 1.269
p1 0.0136 ± 0.0001698
120
100
80
60
40
20
0
-20
4000
6000
8000
10000
12000
14000

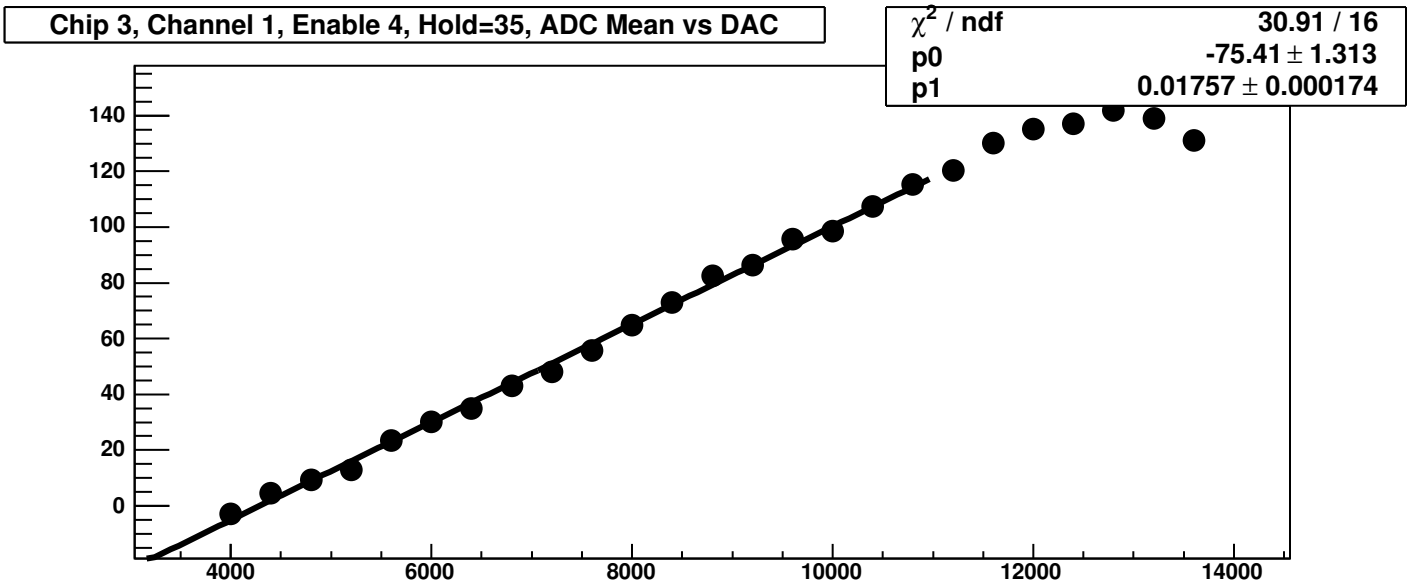

Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

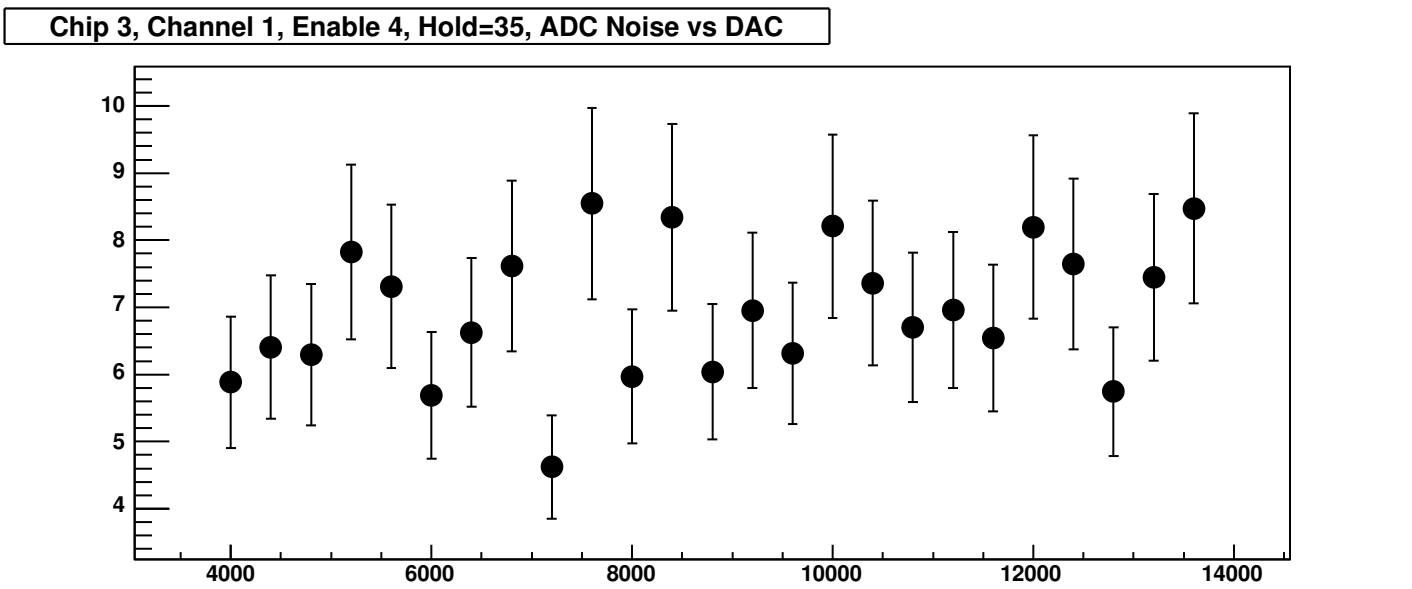

Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

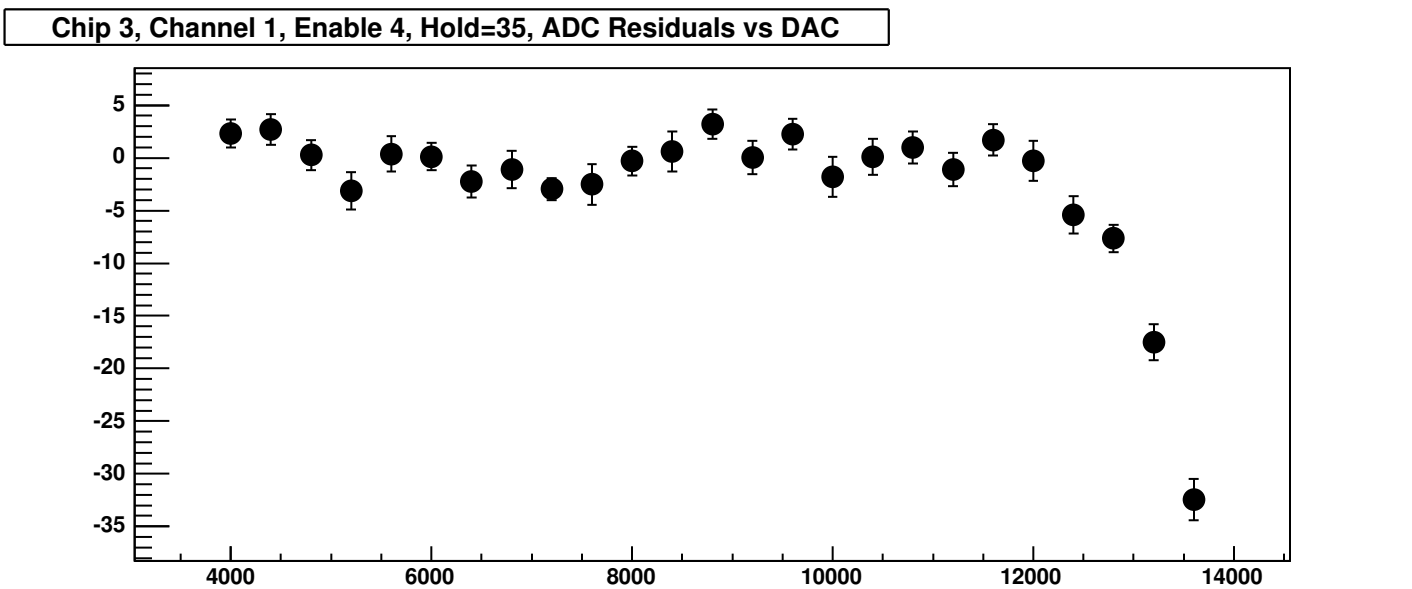

Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.09 / 16
p0 -69.89 ± 1.378
p1 0.01905 ± 0.0001795
200
180
160
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

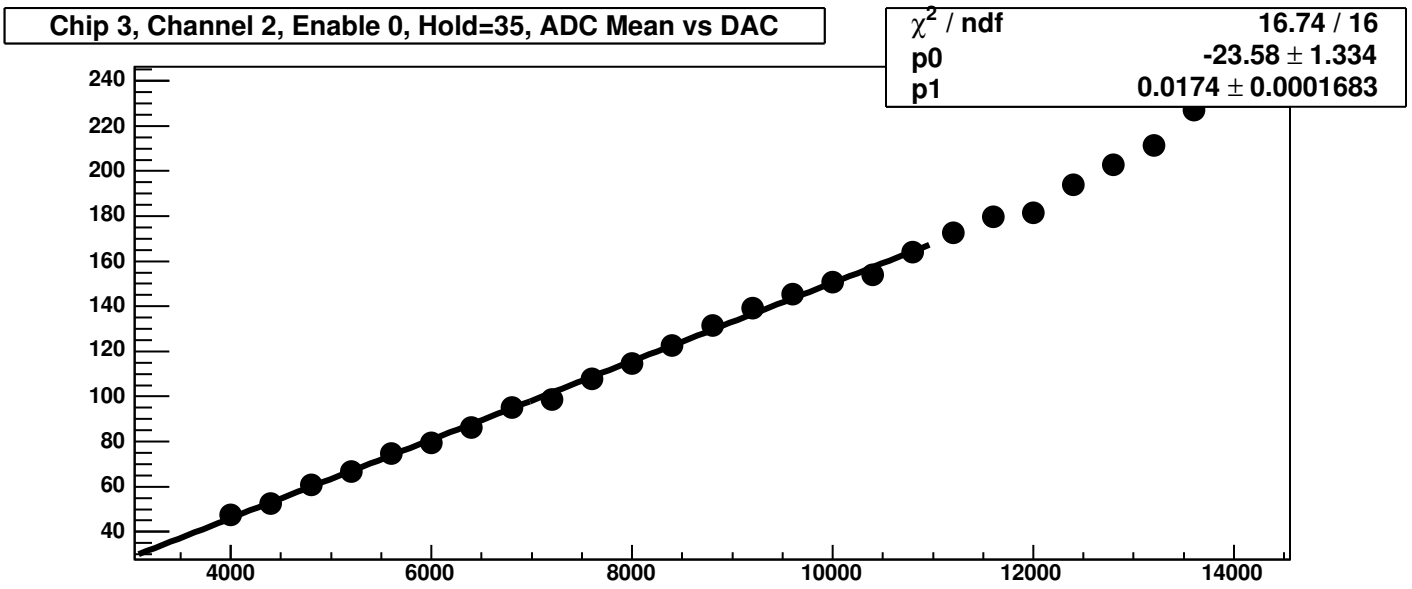

Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

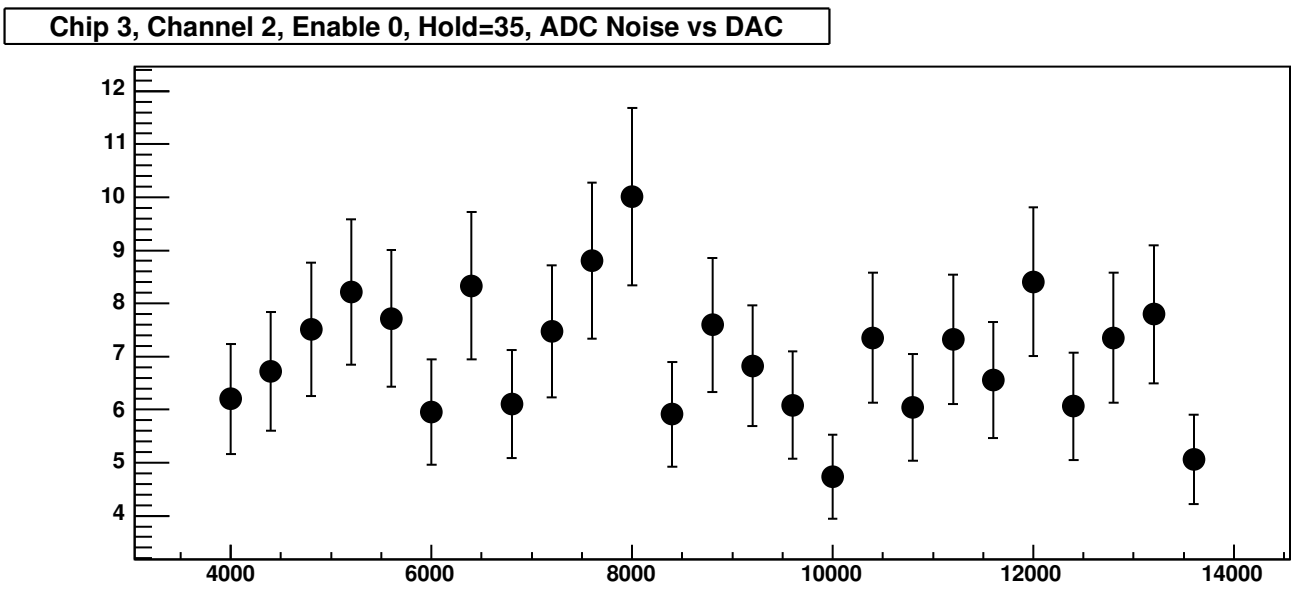

Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

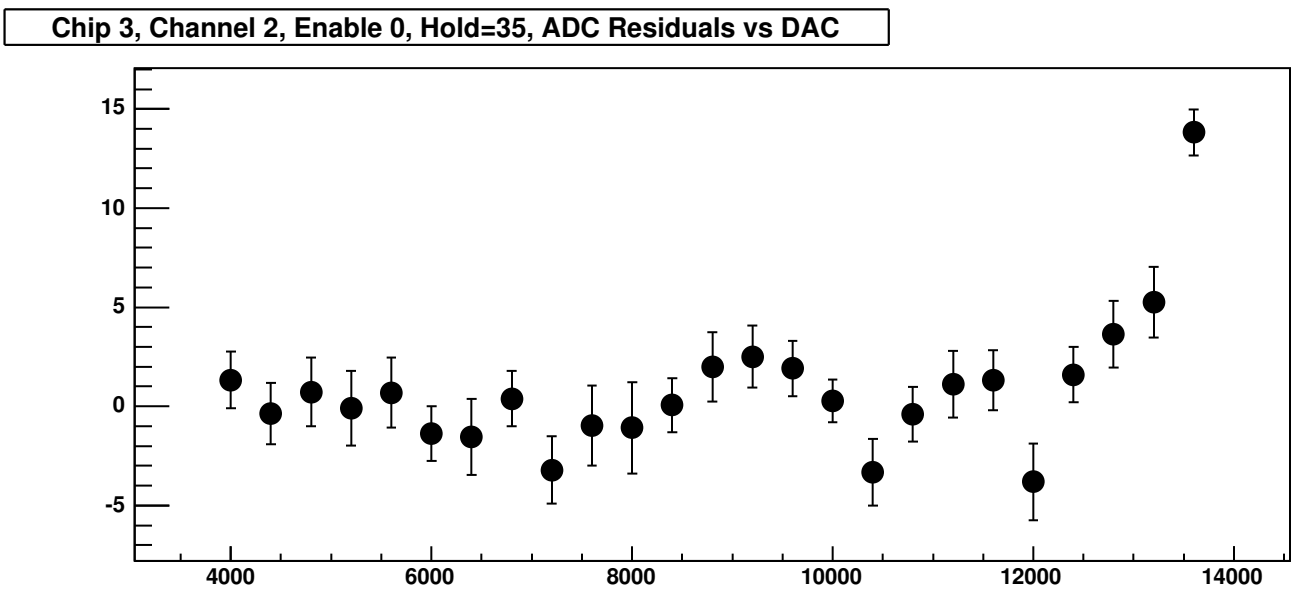

Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.91 / 16
p0 -75.41 ± 1.313
p1 0.01757 ± 0.000174
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

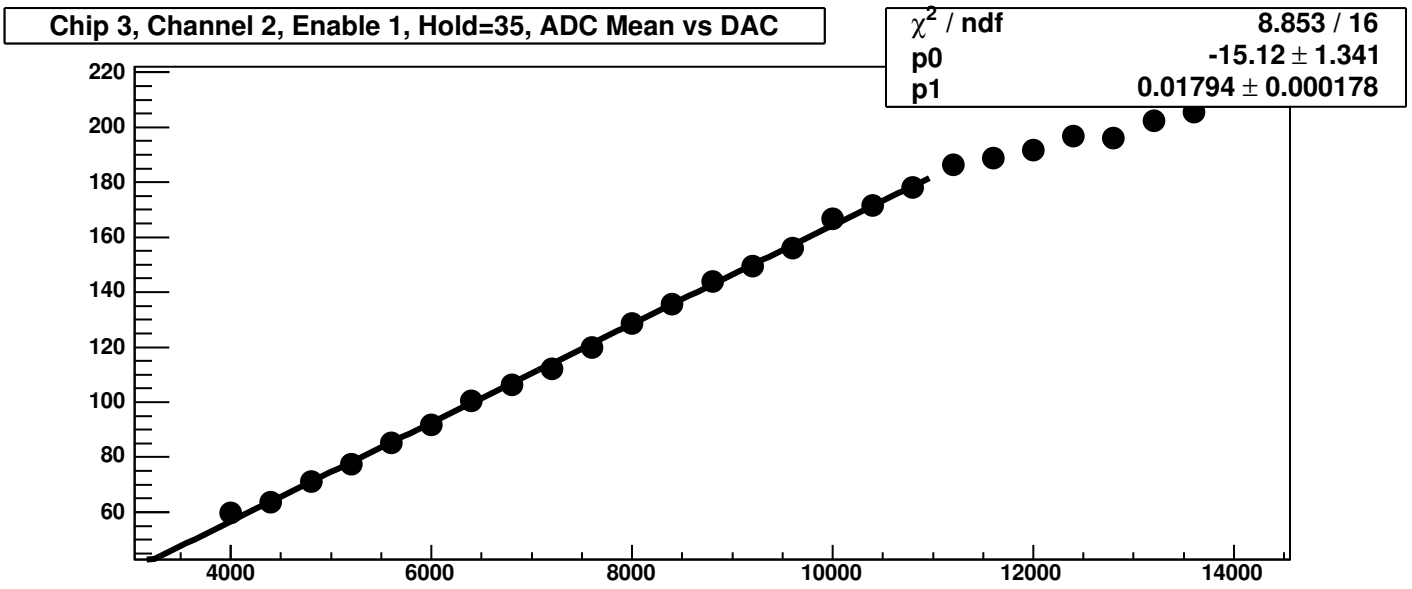

Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

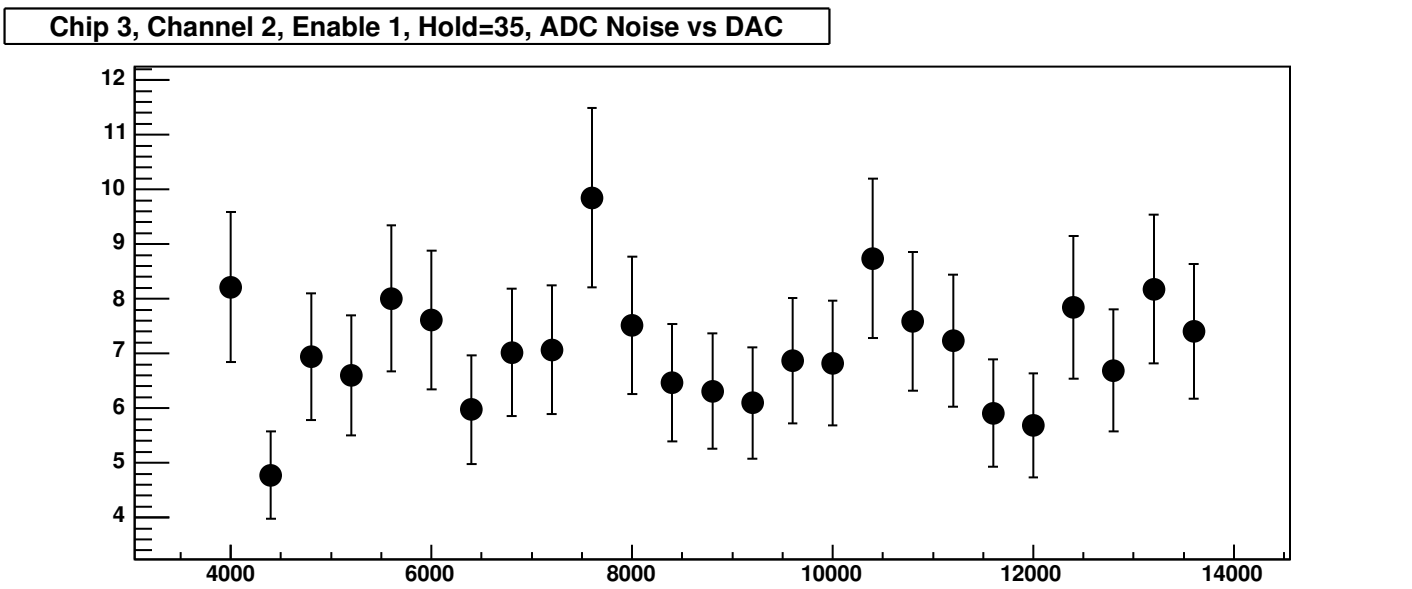

Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

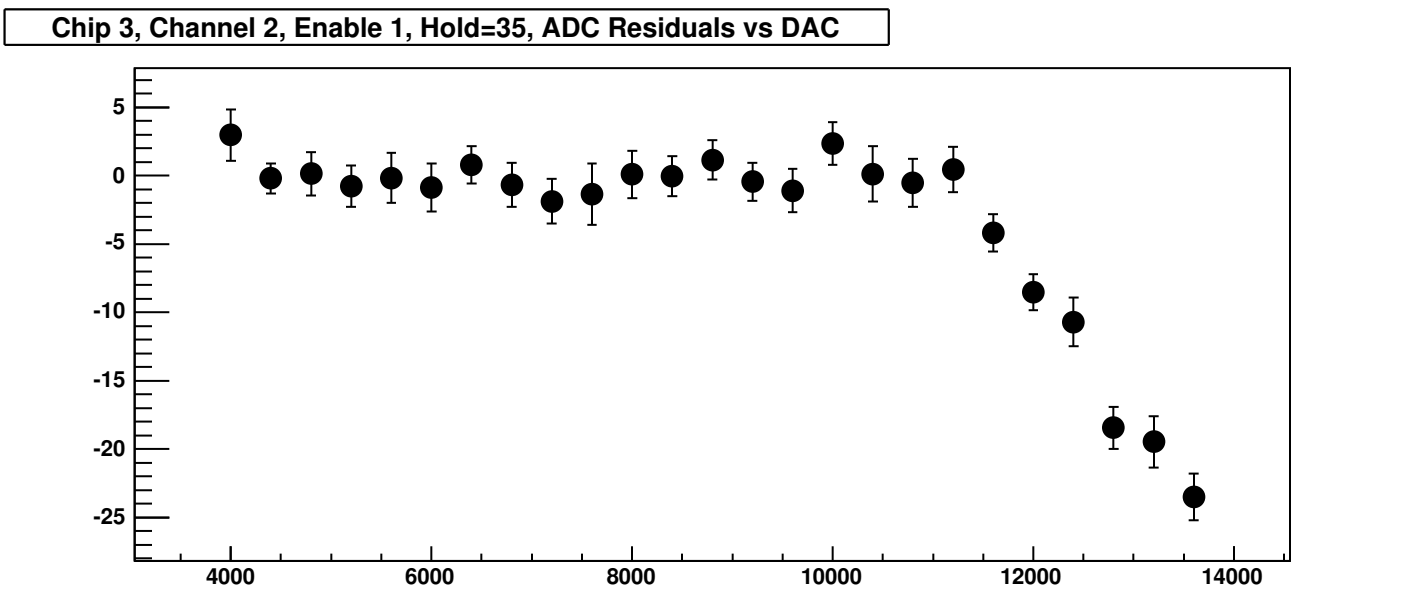

Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 16.74 / 16
p0 -23.58 ± 1.334
p1 0.0174 ± 0.0001683
240
220
200
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

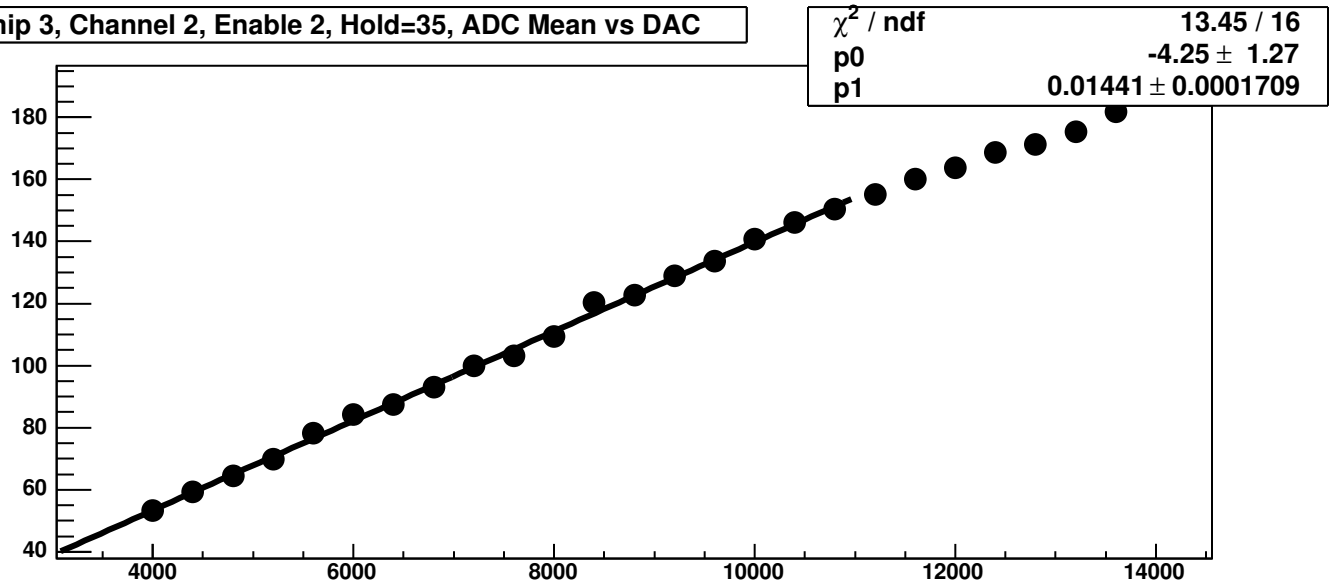

Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 8.853 / 16
p0 -15.12 ± 1.341
p1 0.01794 ± 0.000178
220
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

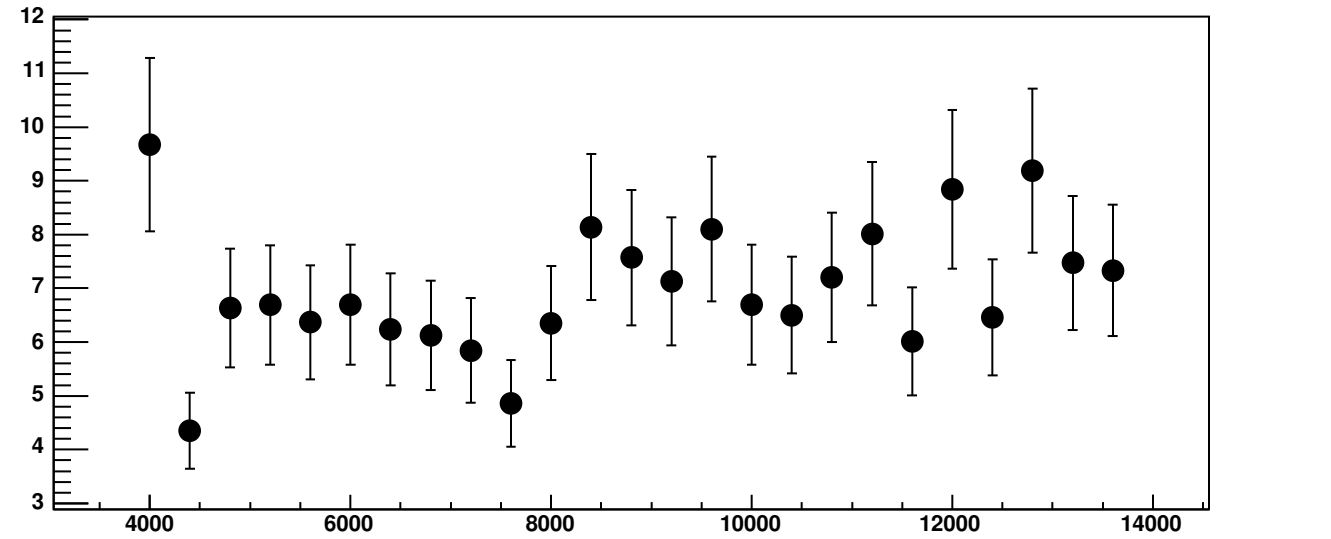

Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

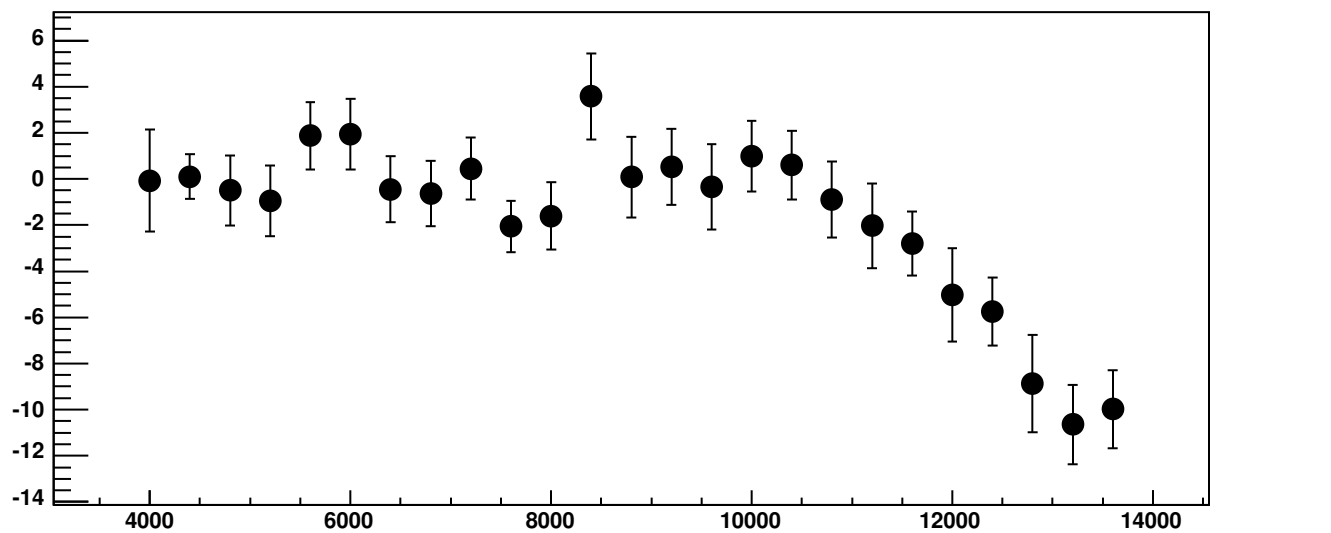
Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

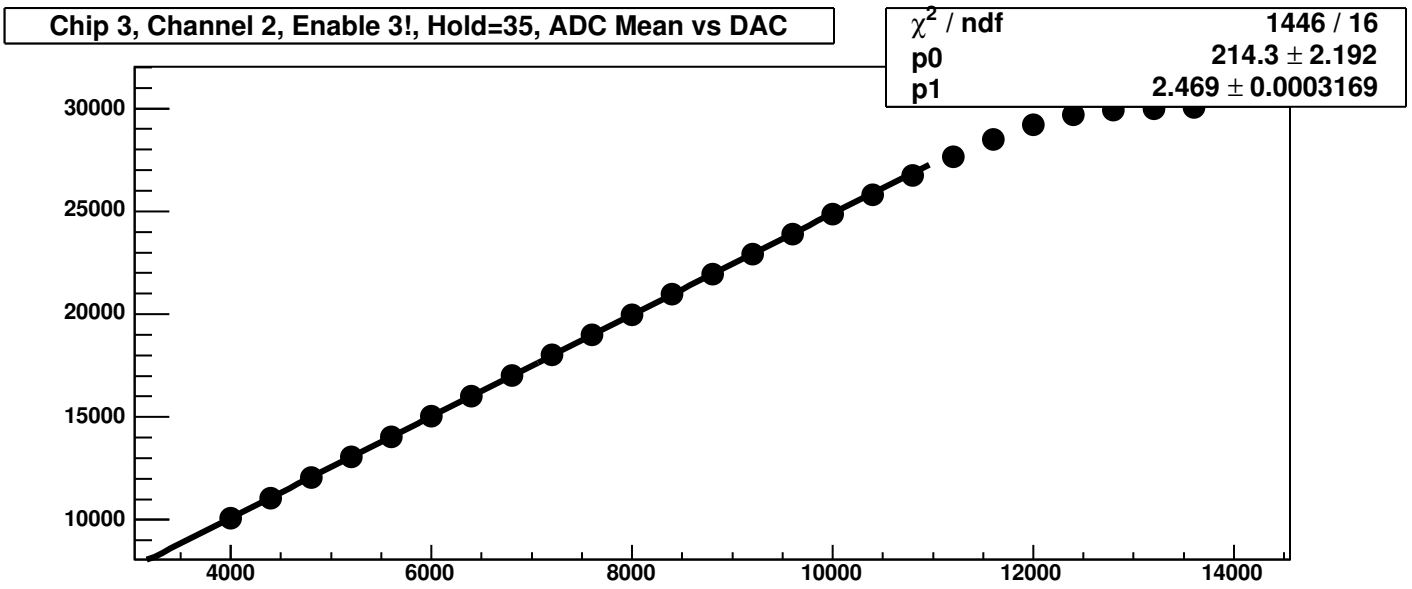


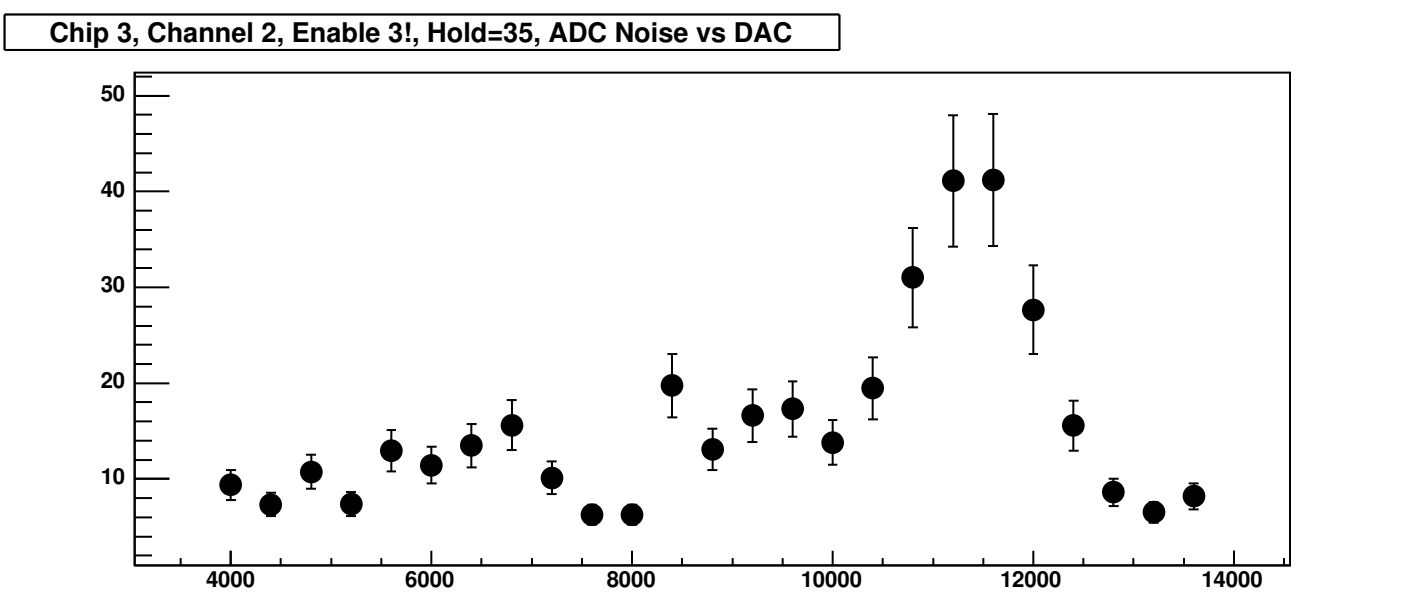
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

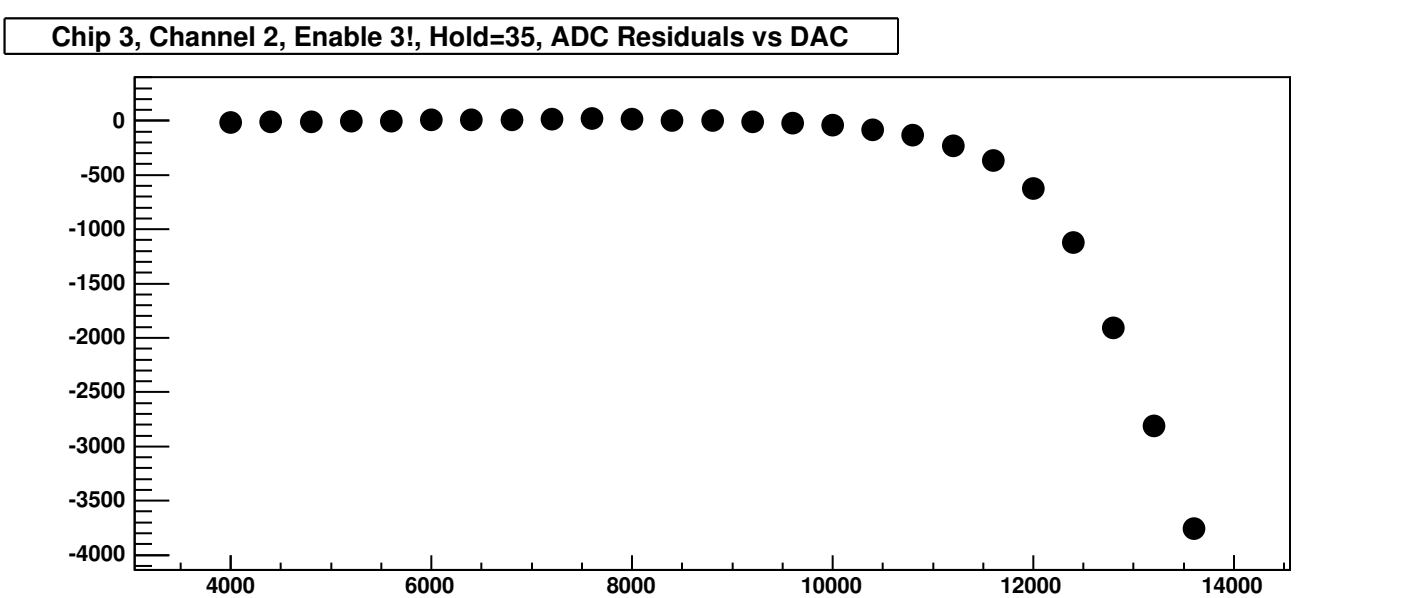


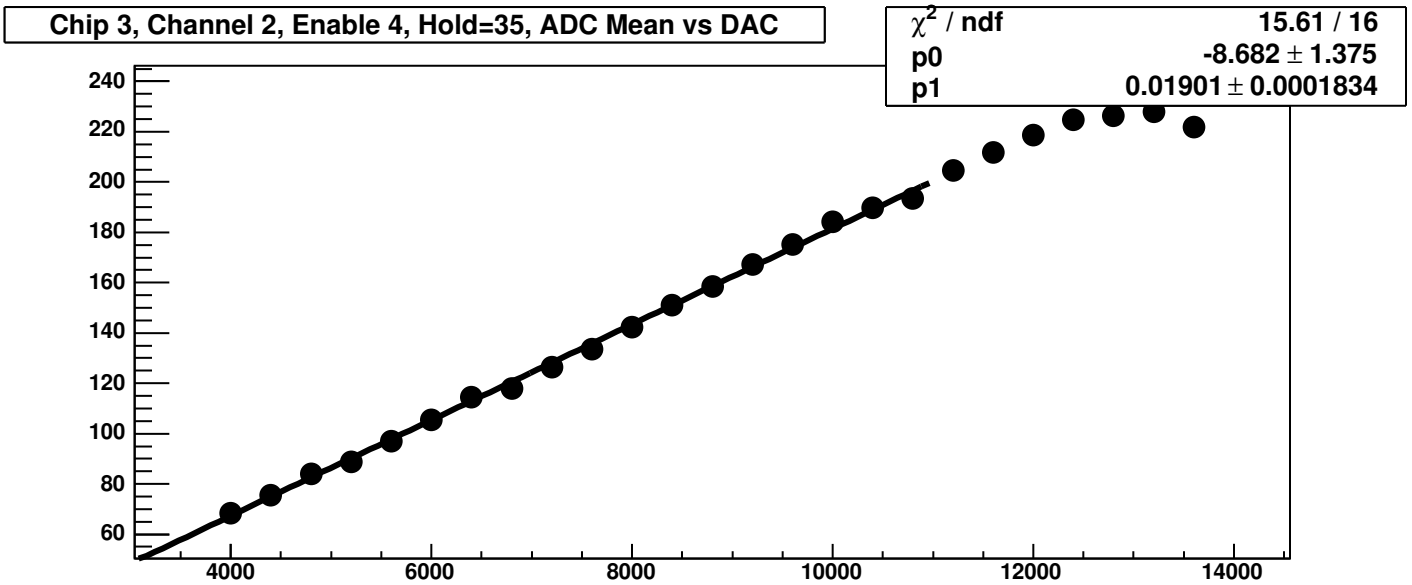
Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

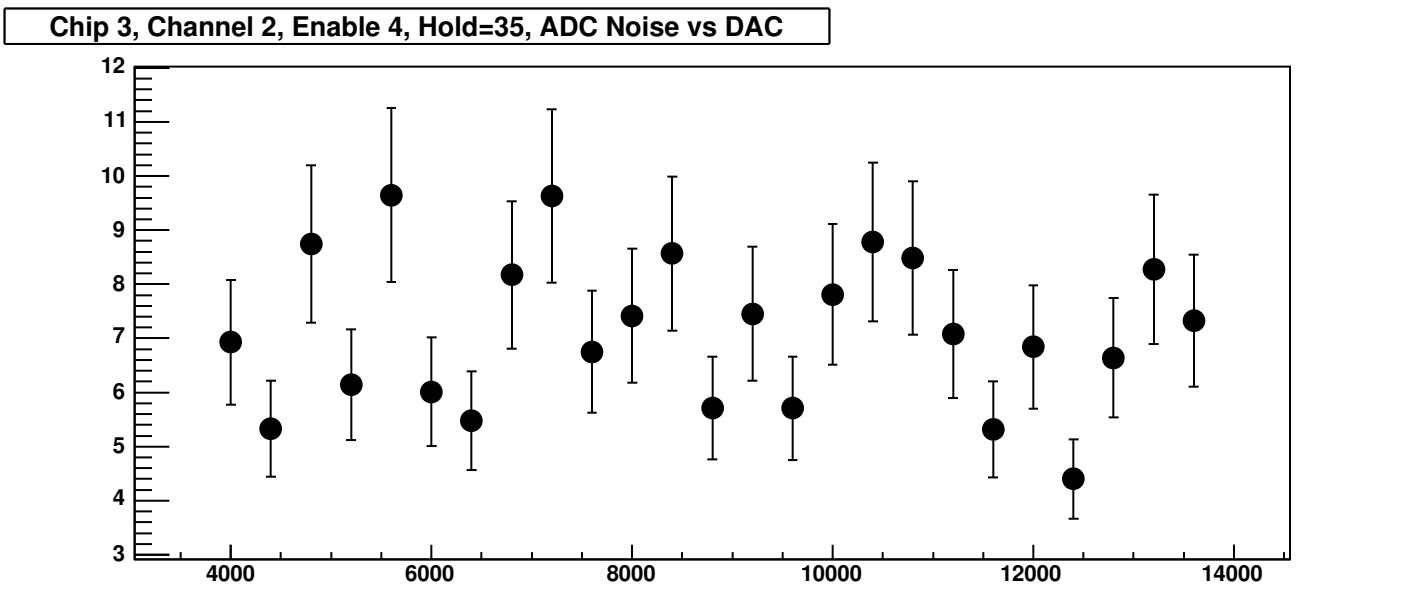


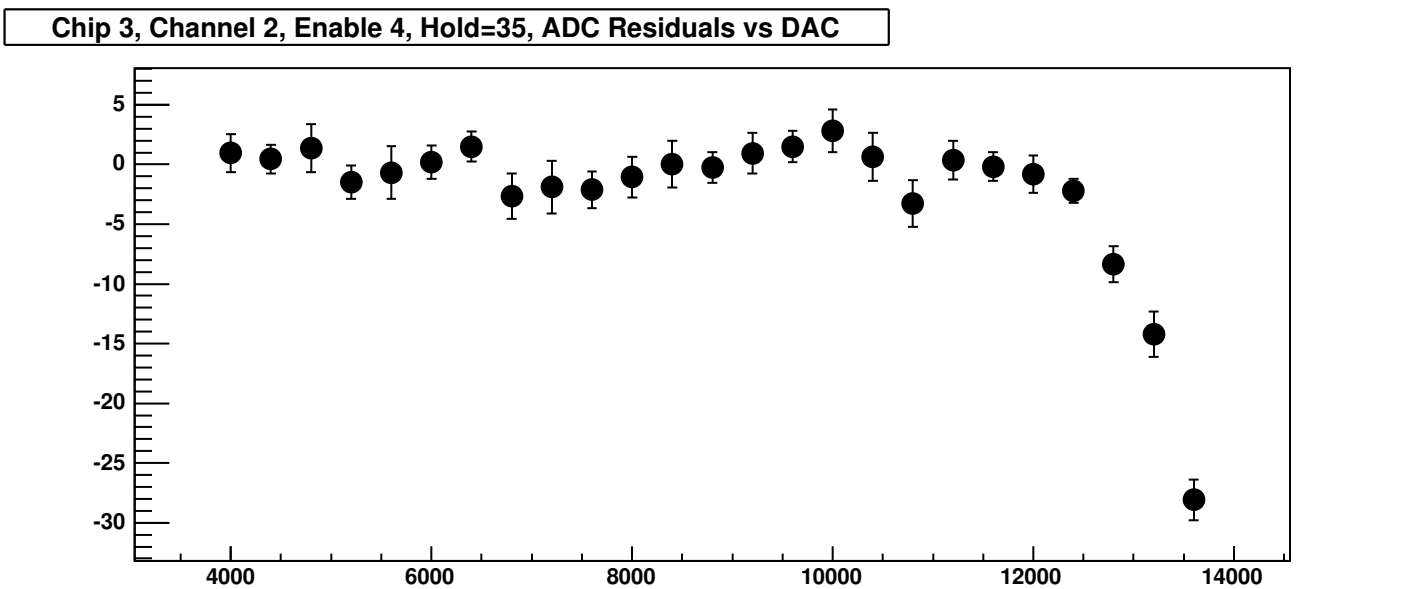

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 1446 / 16
p0 214.3 ± 2.192
p1 2.469 ± 0.0003169
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

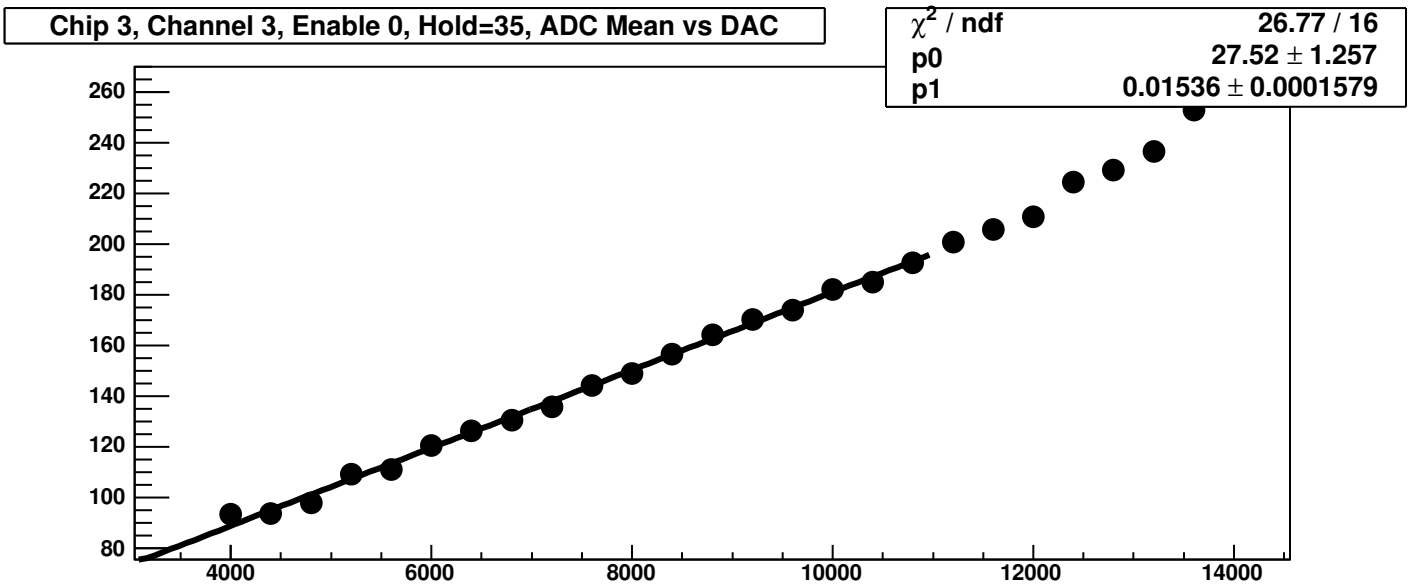

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

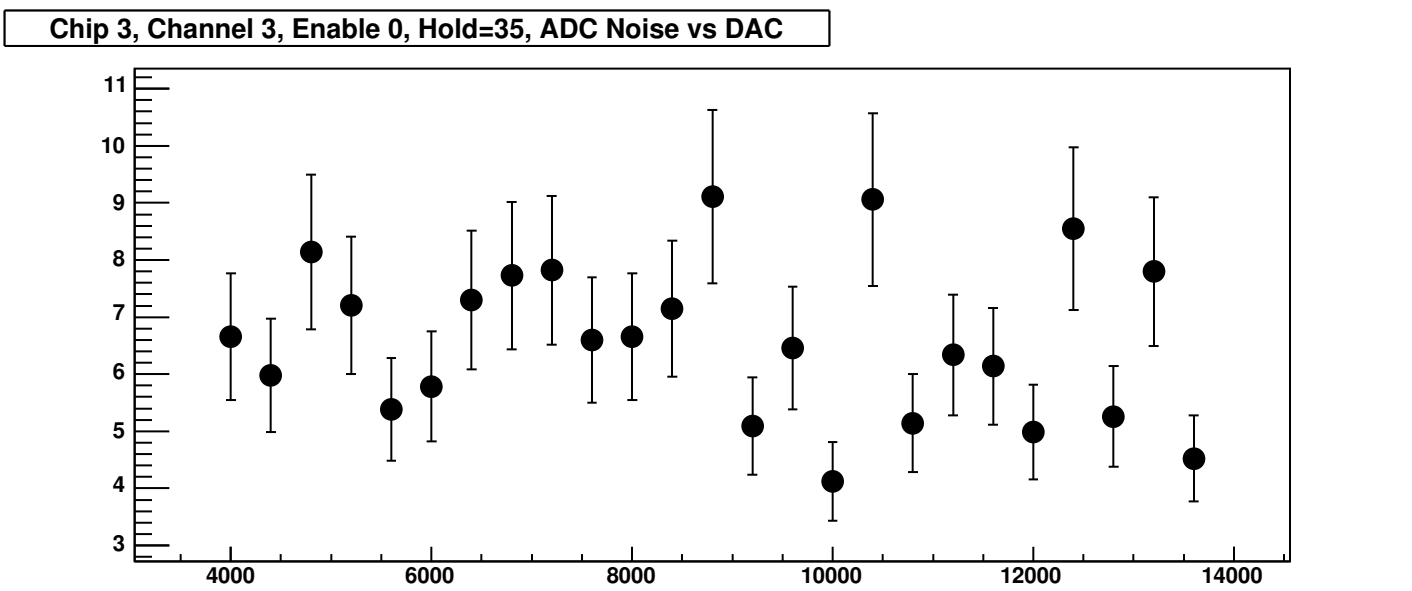

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

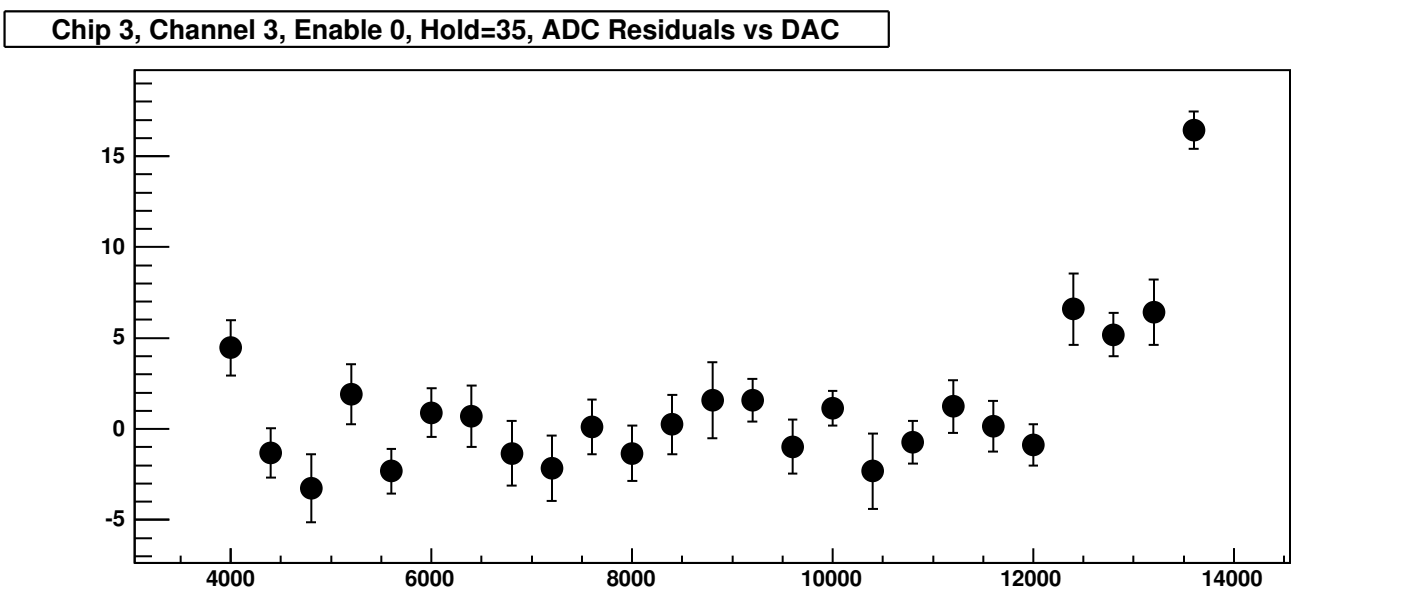

Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.61 / 16
p0 -8.682 ± 1.375
p1 0.01901 ± 0.0001834
240
220
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

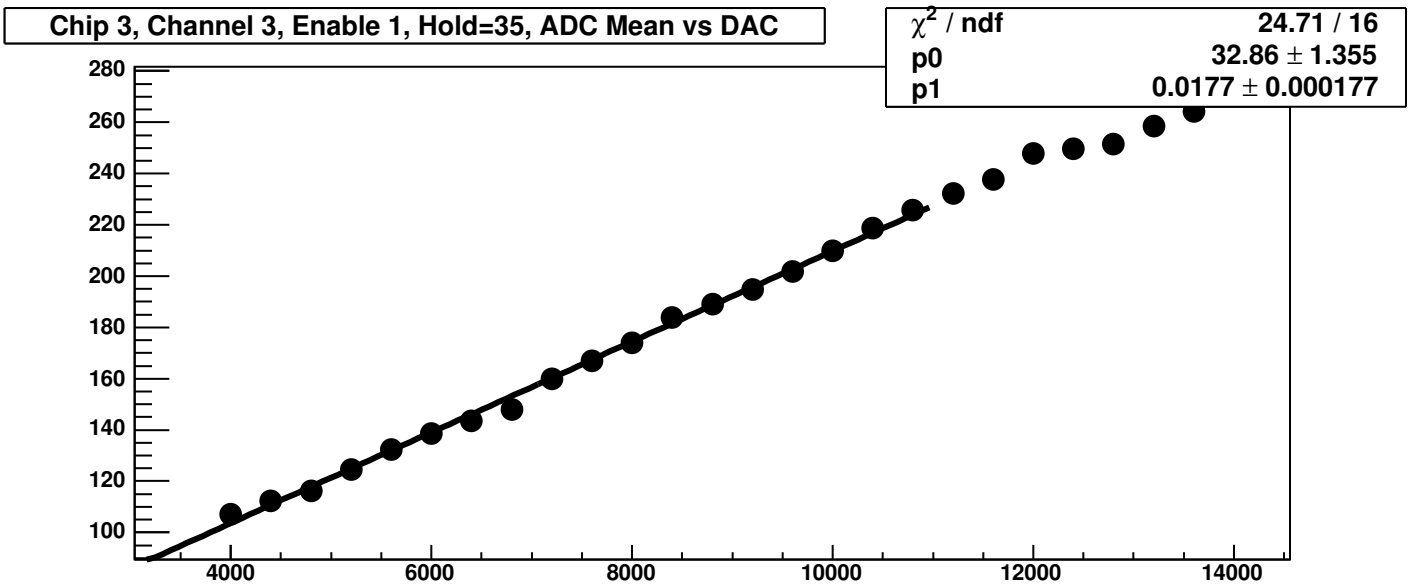

Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

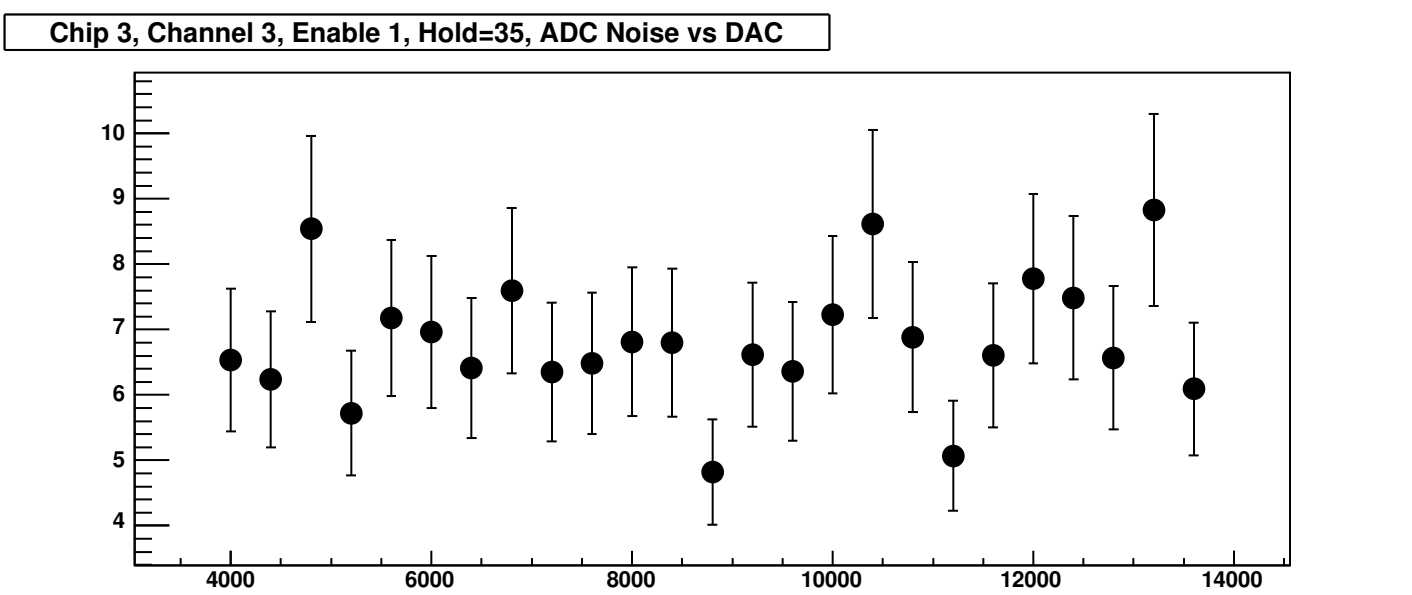

Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

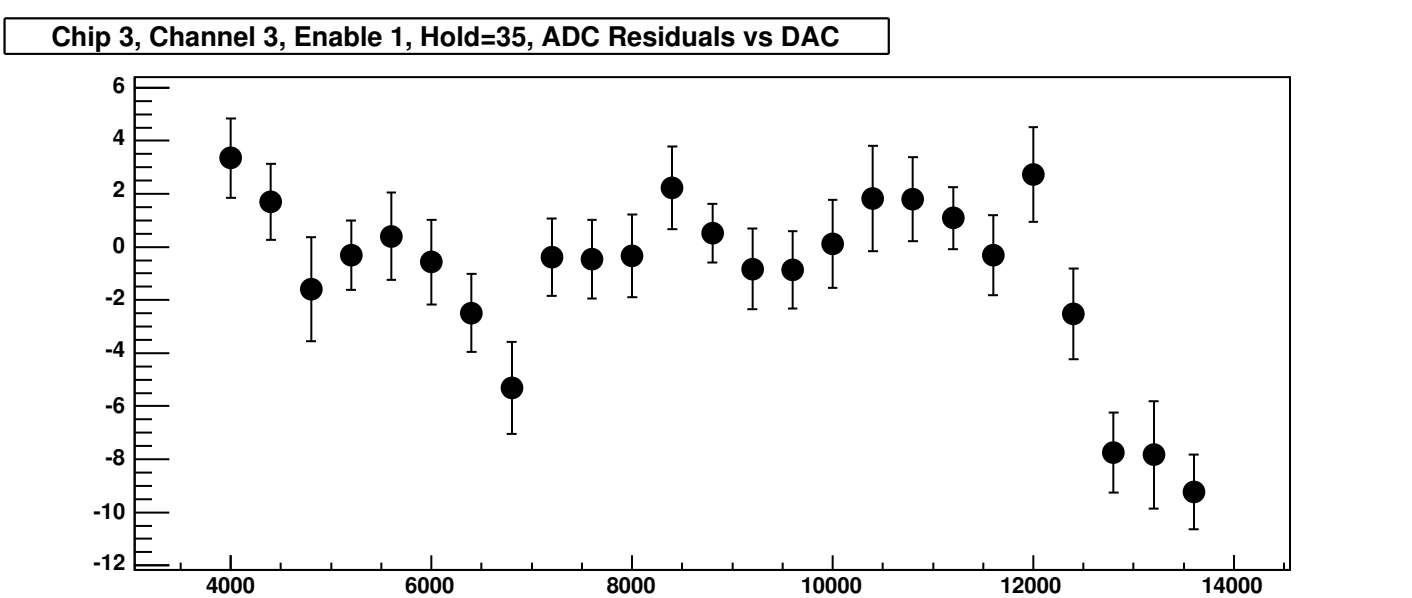

Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 26.77 / 16
p0 27.52 ± 1.257
p1 0.01536 ± 0.0001579
260
240
220
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

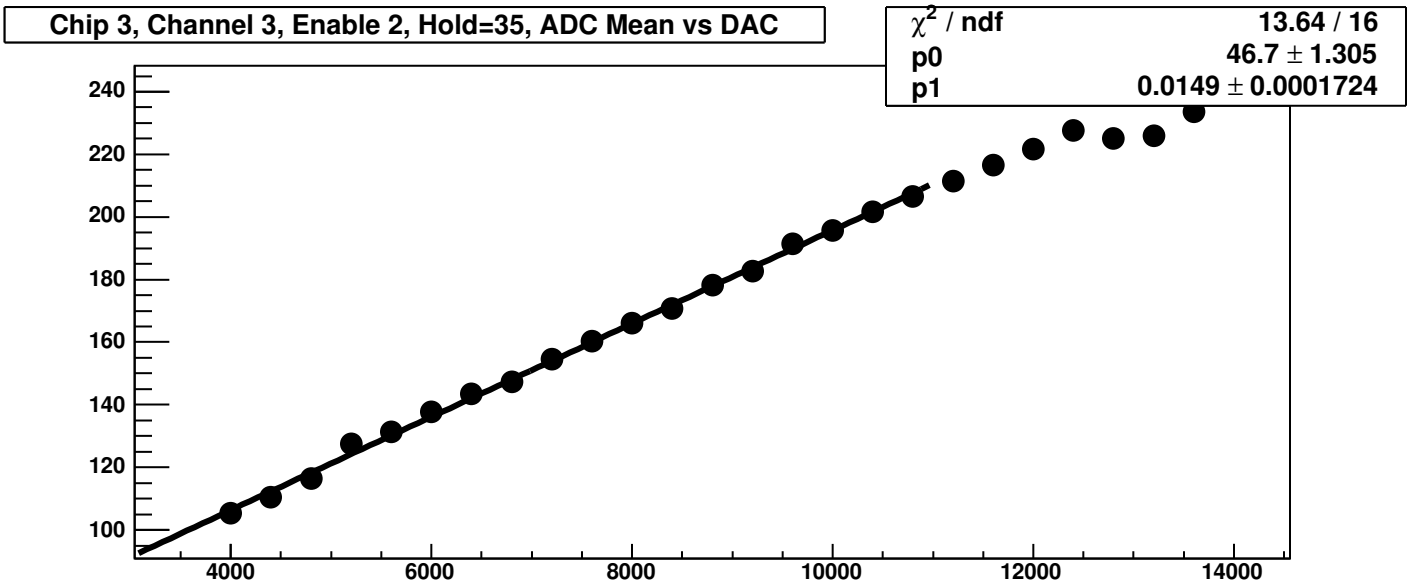

Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

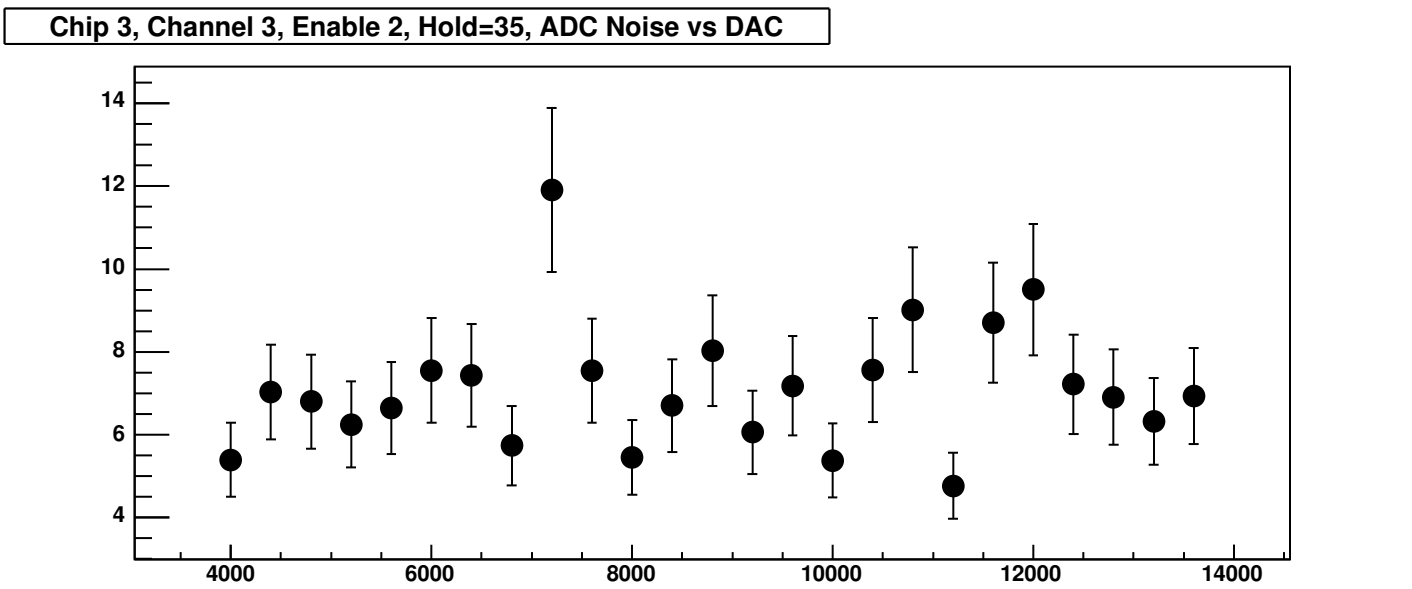

Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

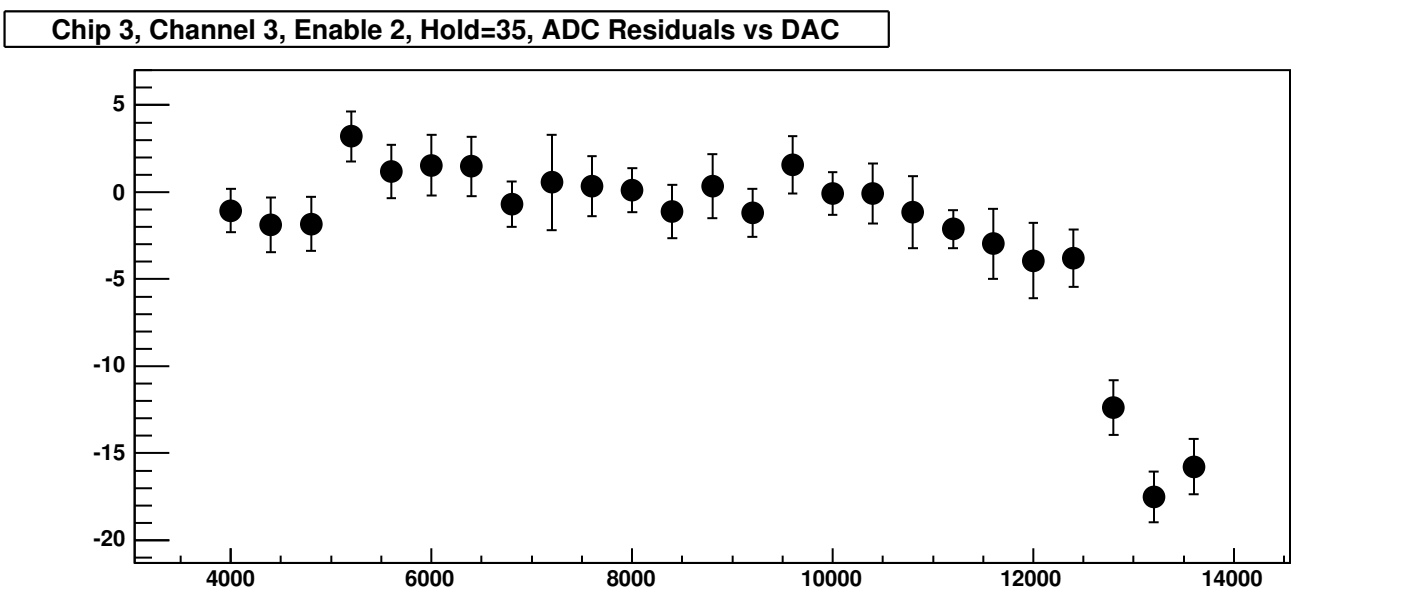

Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.71 / 16
p0 32.86 ± 1.355
p1 0.0177 ± 0.000177
280
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000


Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

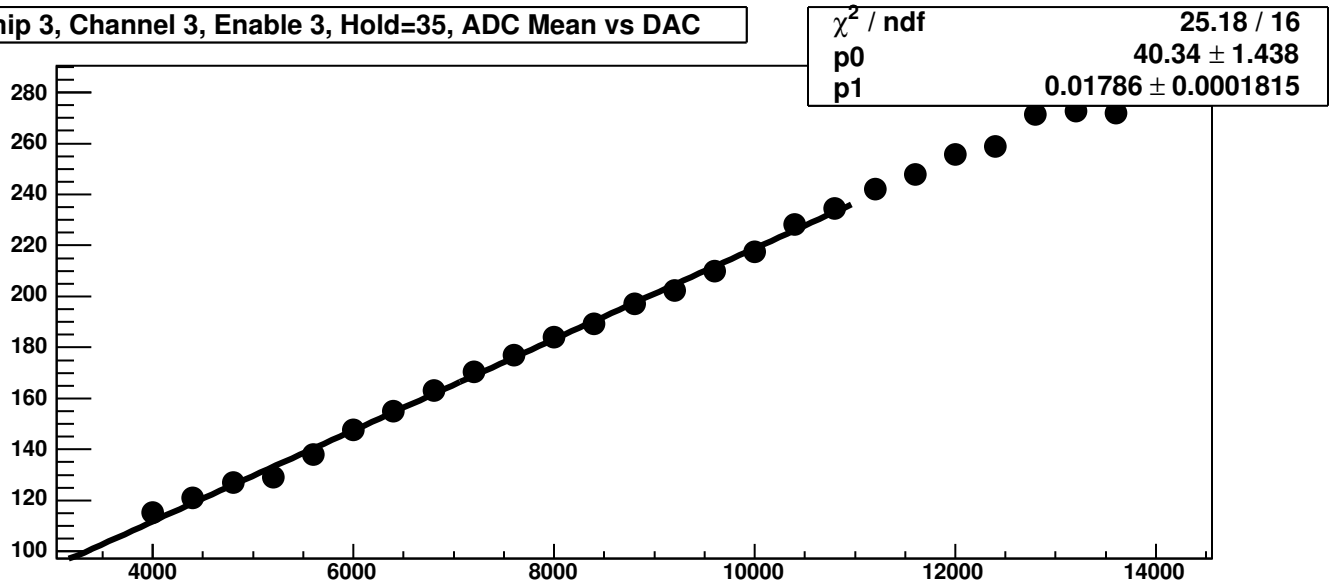

Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000


Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 13.64 / 16
p0 46.7 ± 1.305
p1 0.0149 ± 0.0001724
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

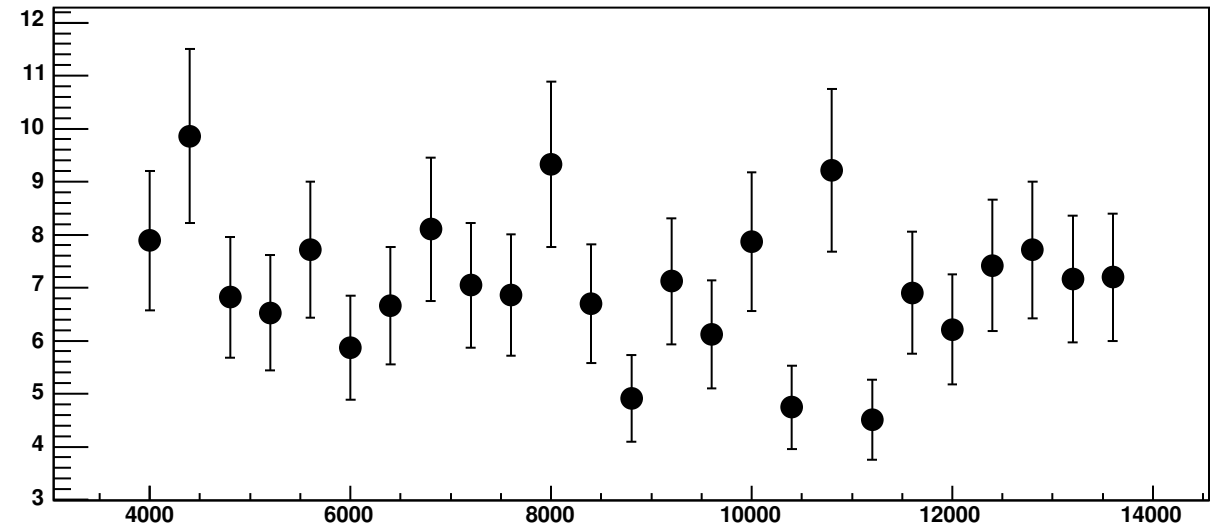

Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

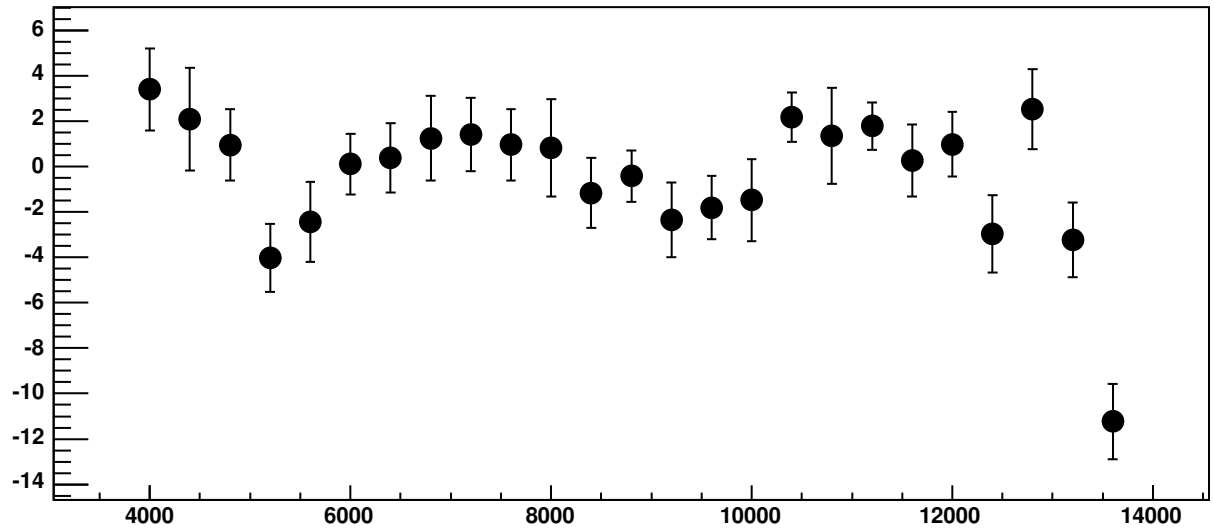
Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

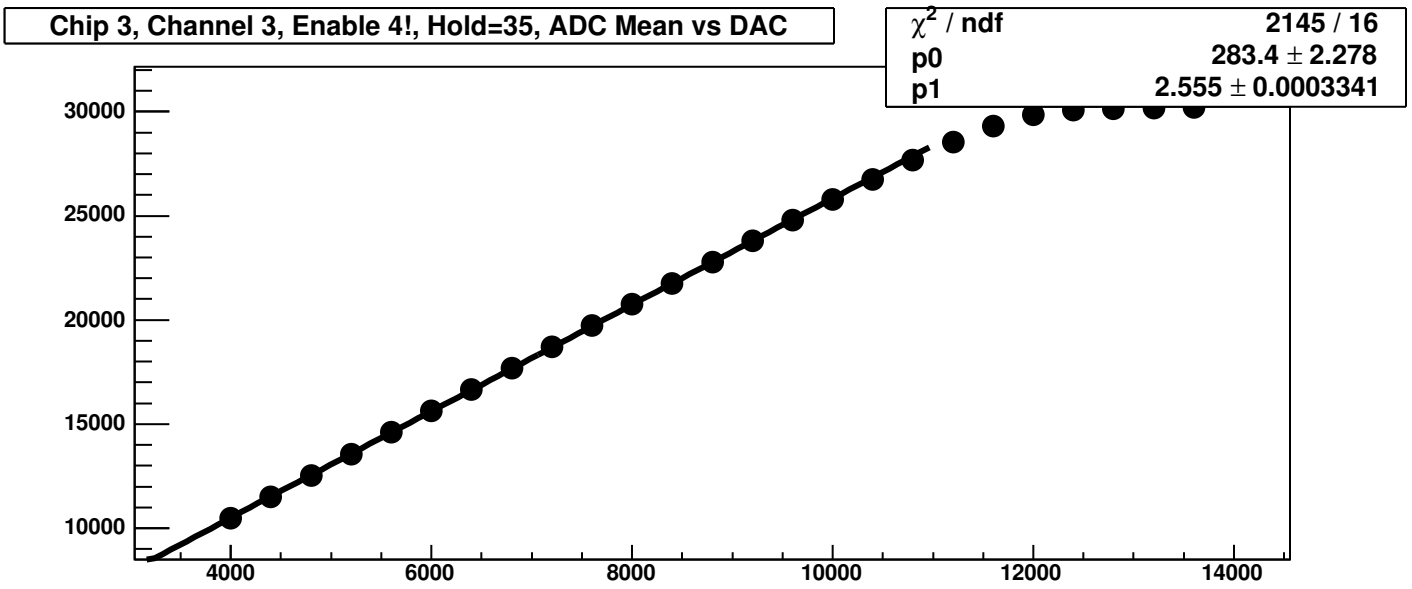


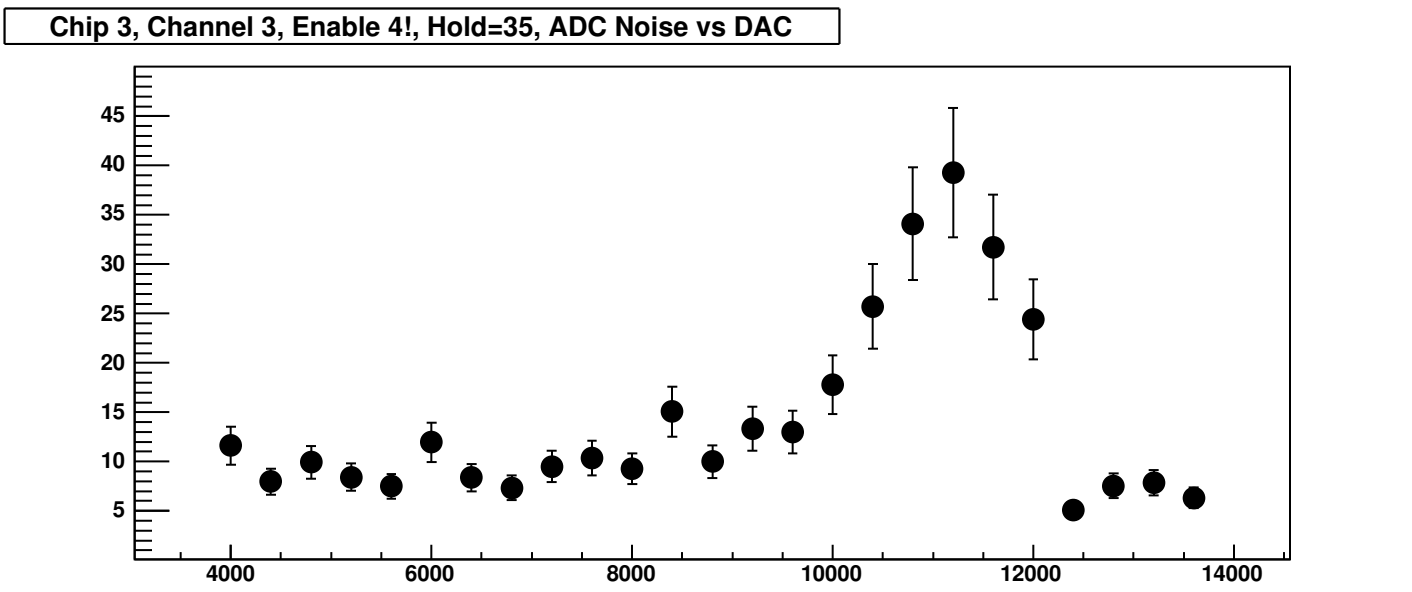
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

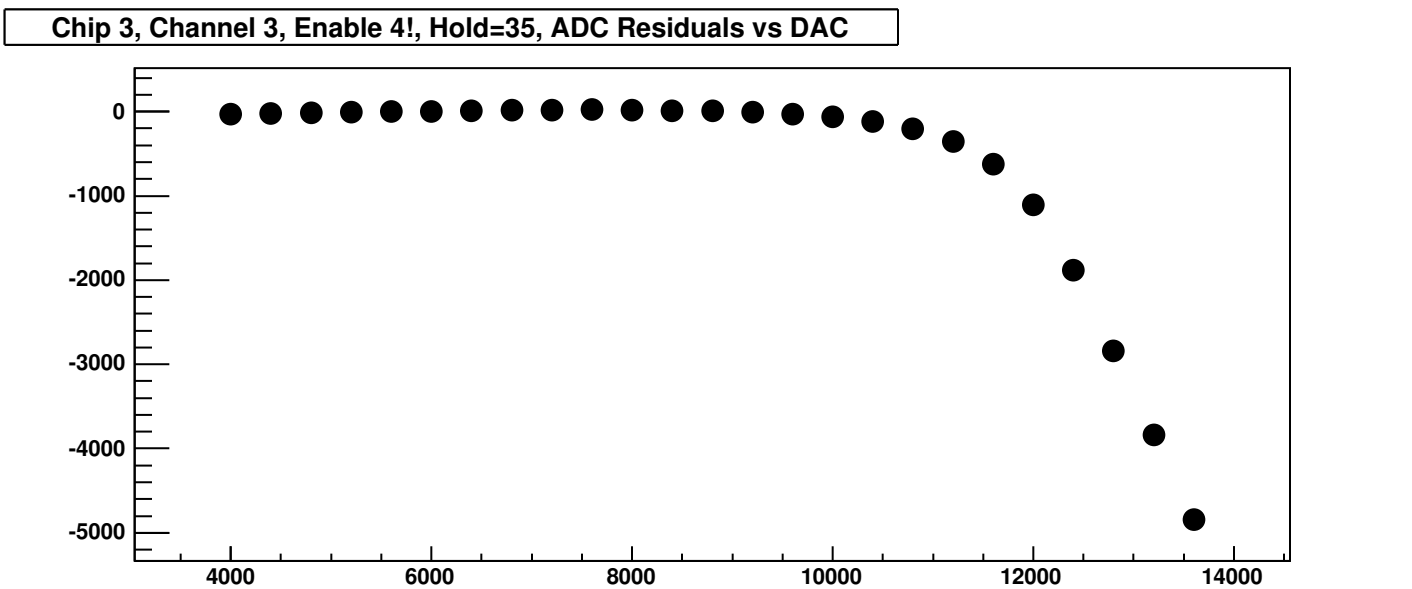


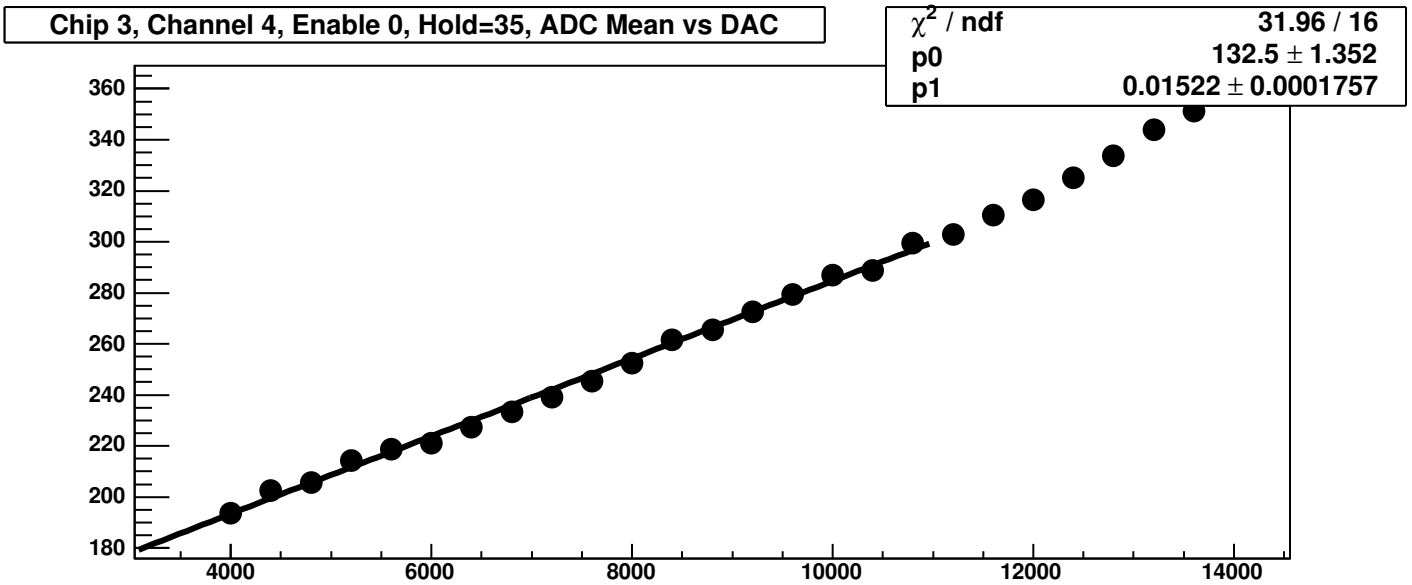
Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

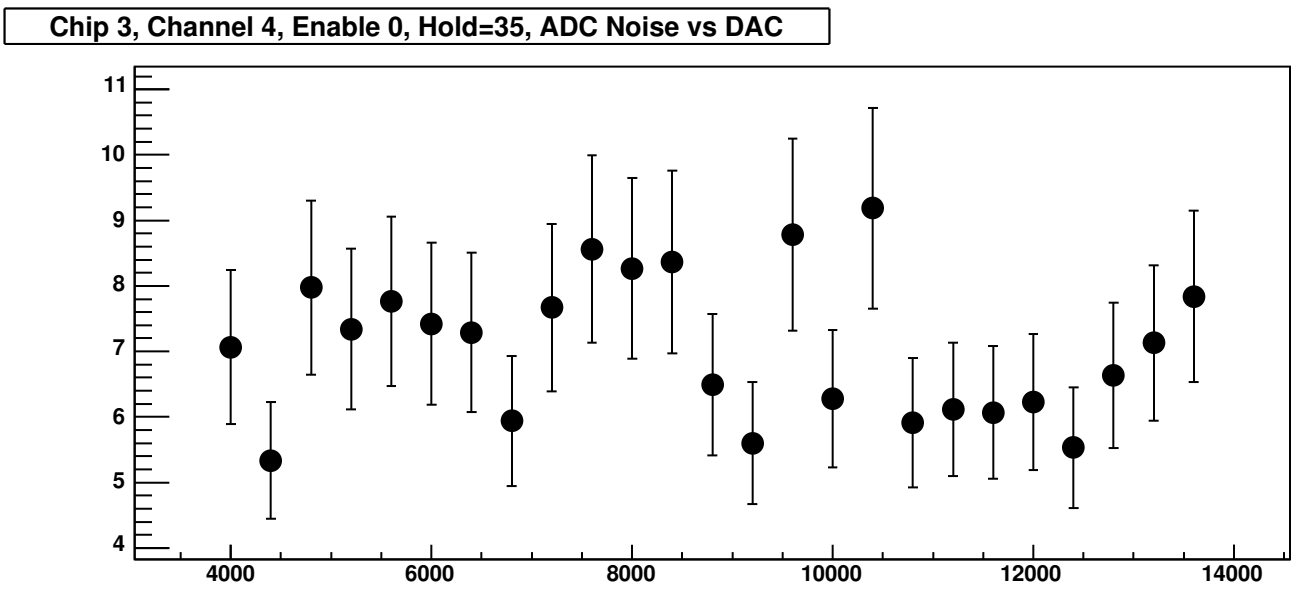


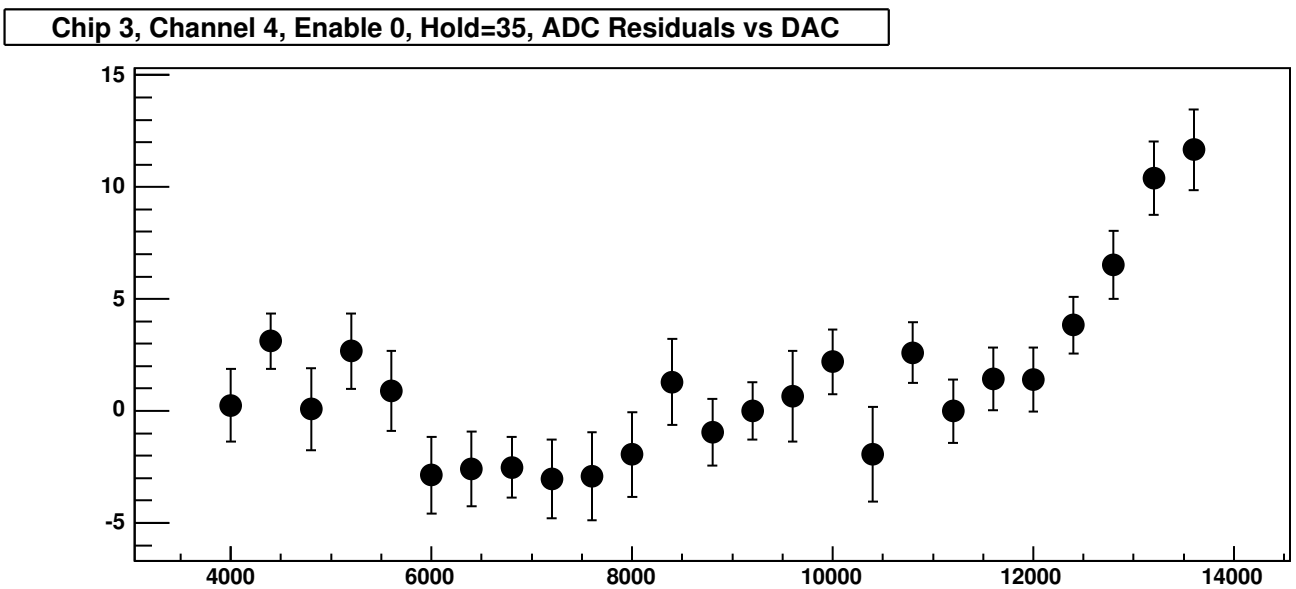

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2145 / 16
p0 283.4 ± 2.278
p1 2.555 ± 0.0003341
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

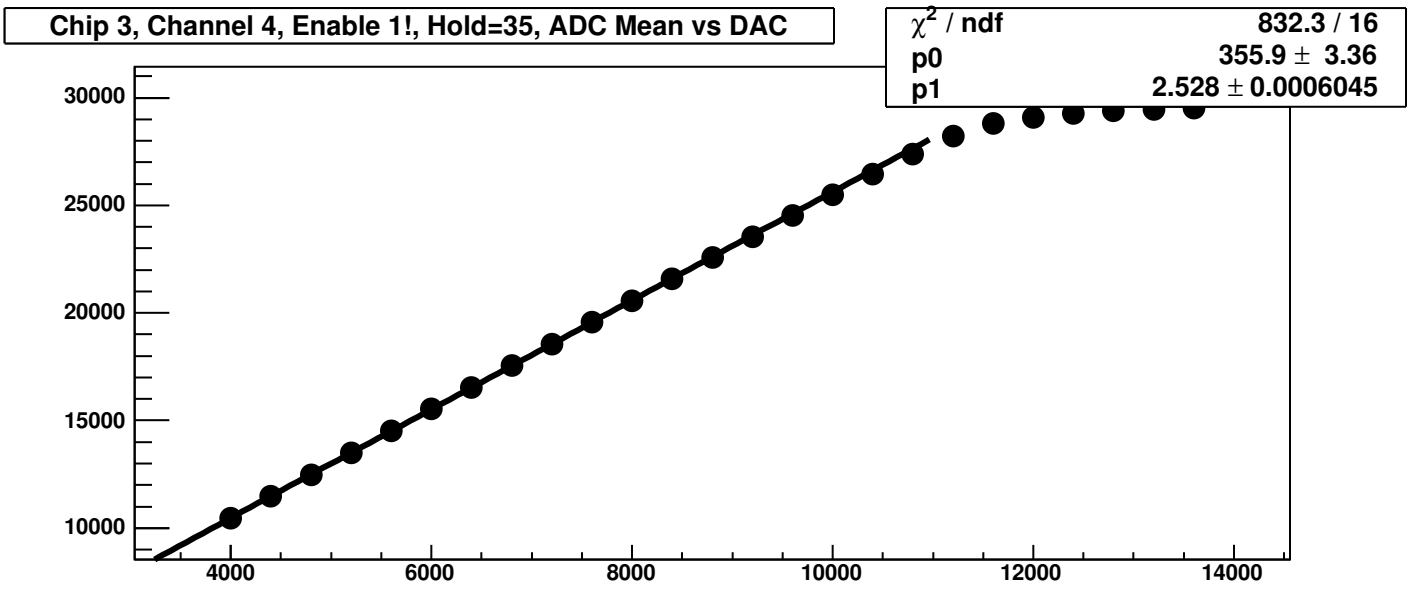

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

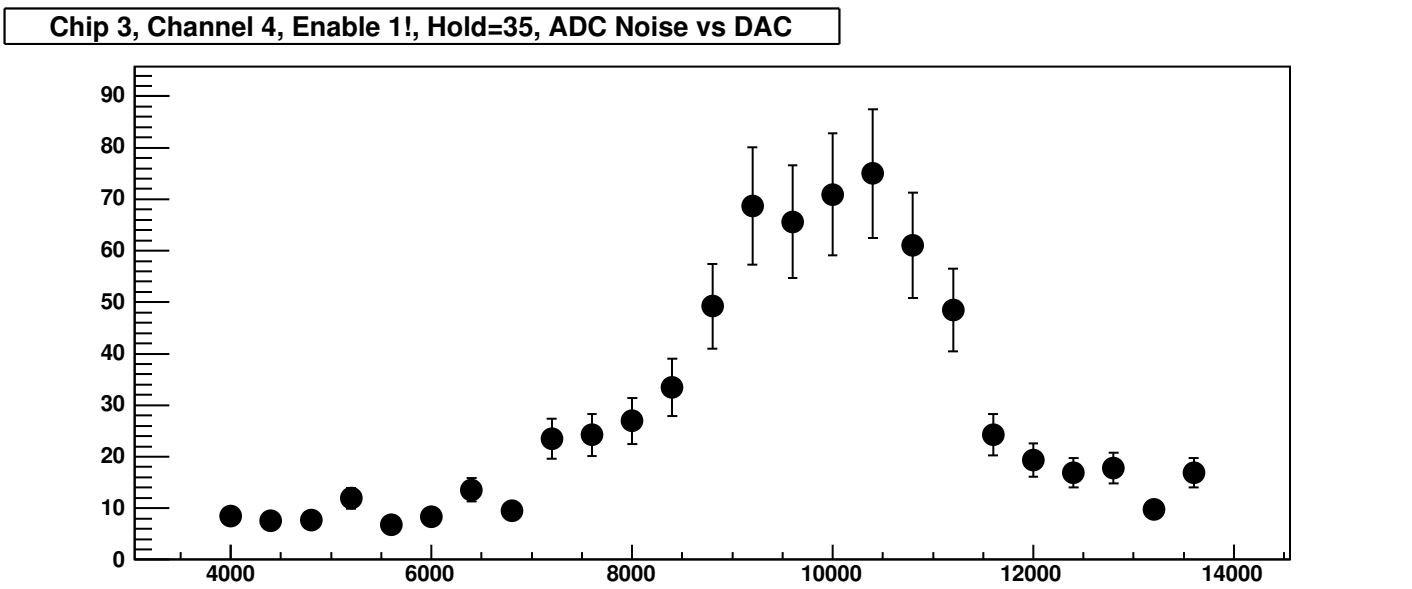

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

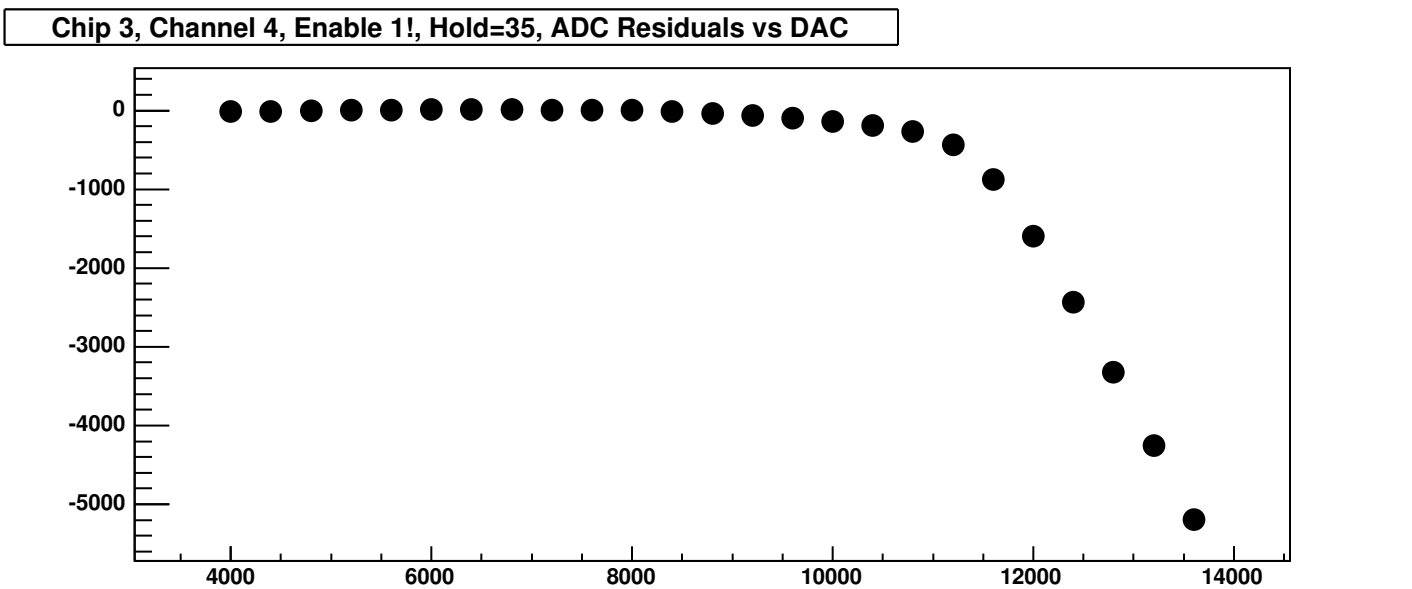

Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 31.96 / 16
p0 132.5 ± 1.352
p1 0.01522 ± 0.0001757
360
340
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

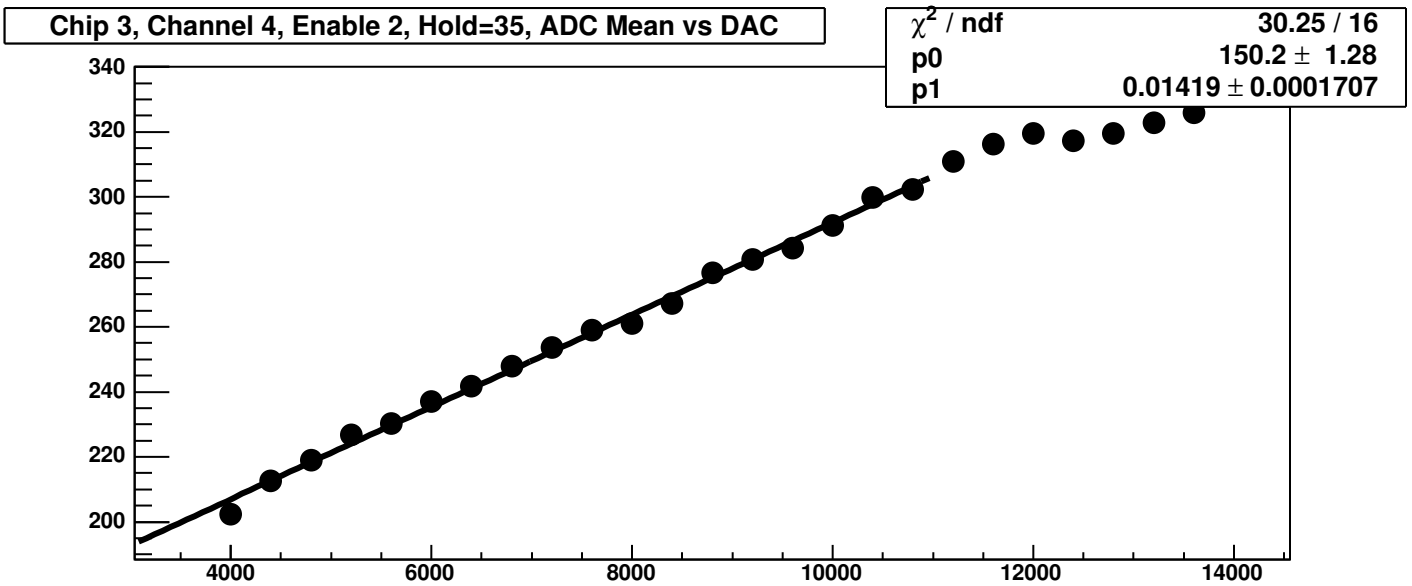

Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

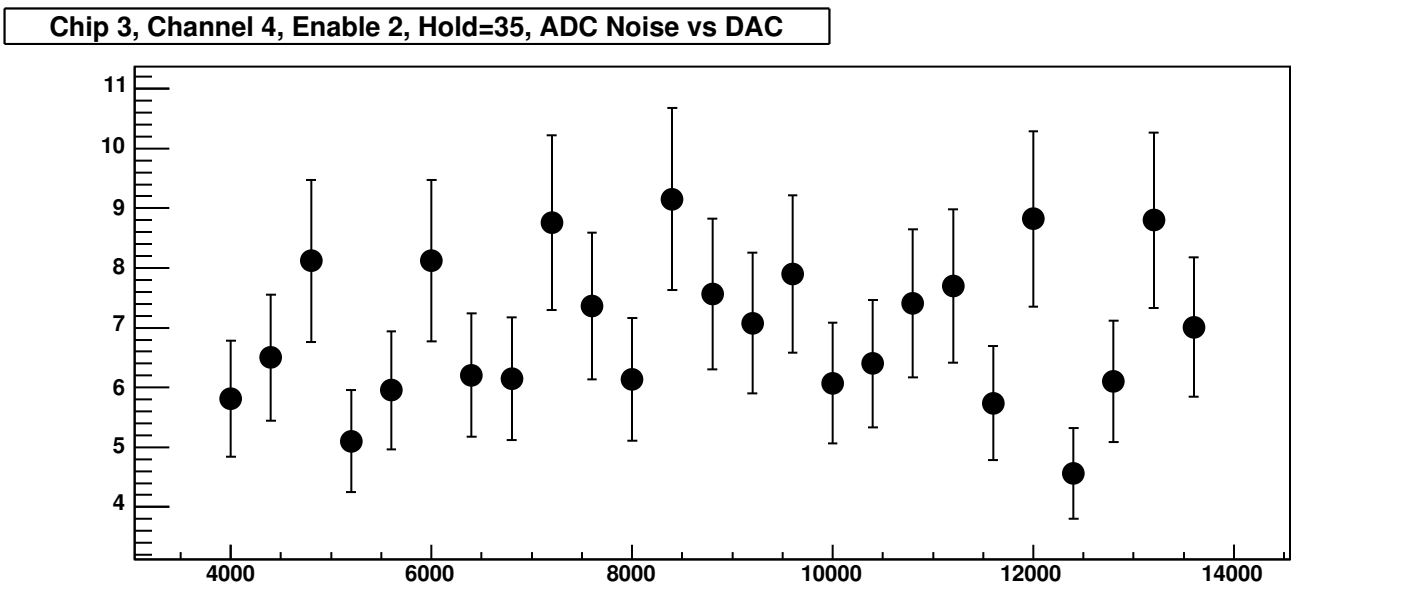

Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

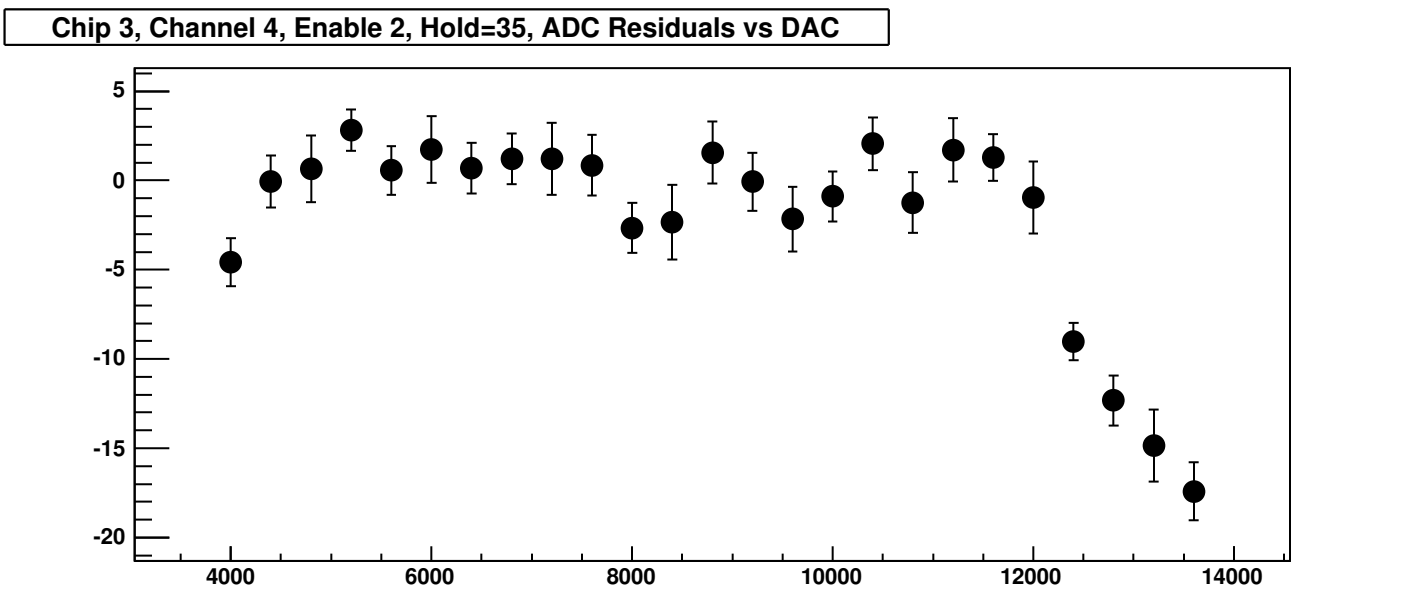

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 832.3 / 16
p0 355.9 ± 3.36
p1 2.528 ± 0.0006045
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

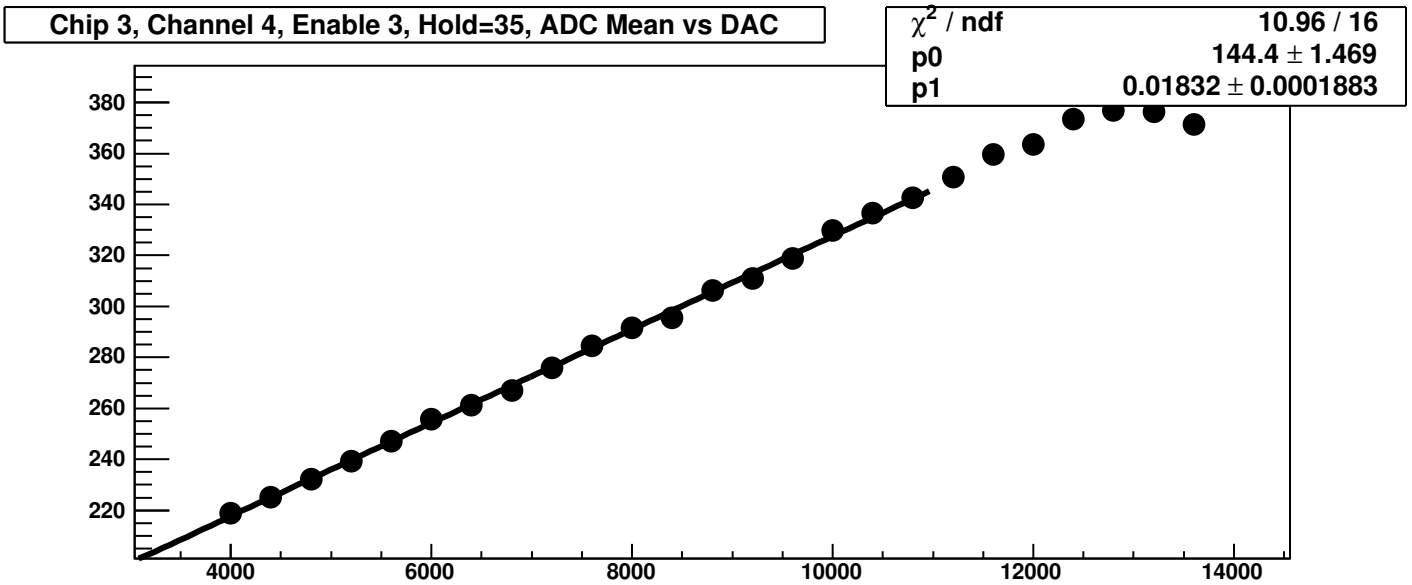

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

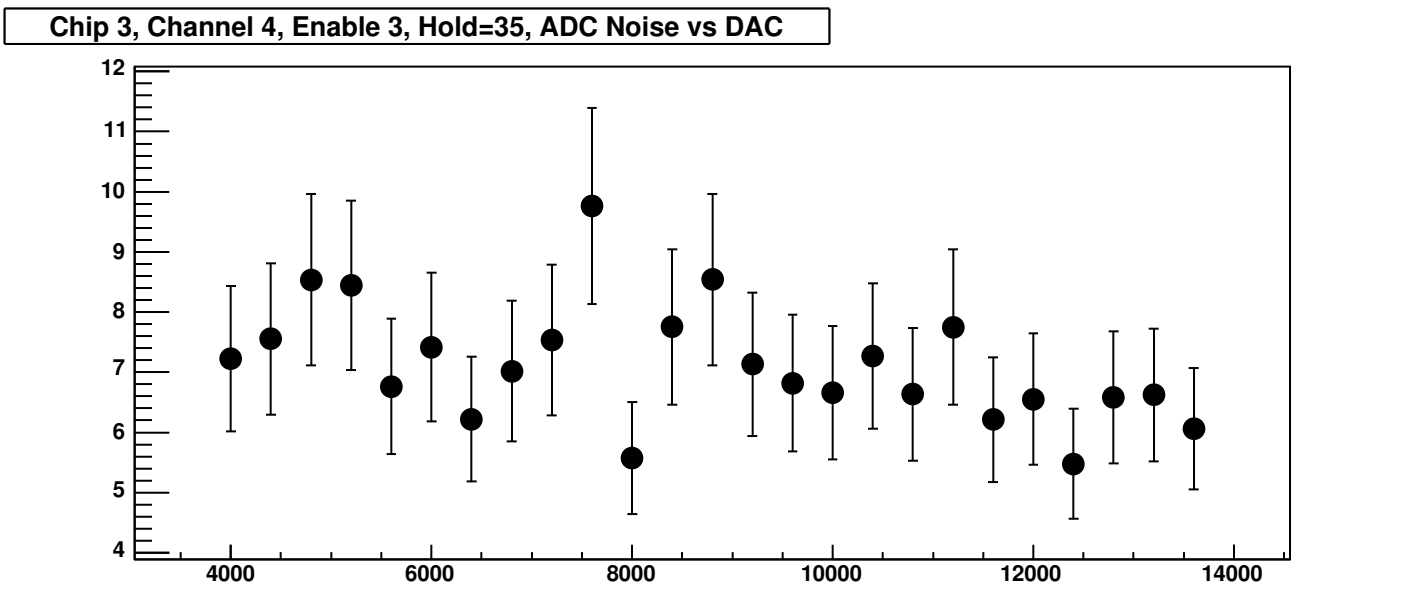

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

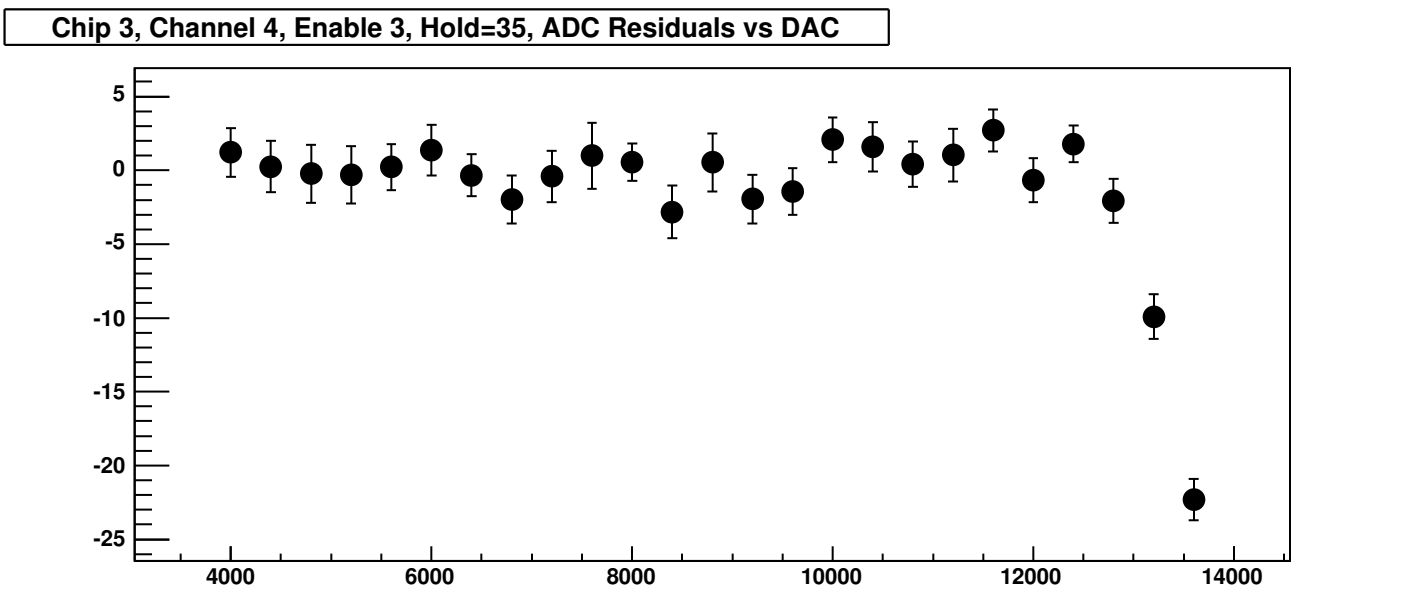

Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.25 / 16
p0 150.2 ± 1.28
p1 0.01419 ± 0.0001707
340
320
300
280
260
240
220
200
4000
6000
8000
10000
12000
14000

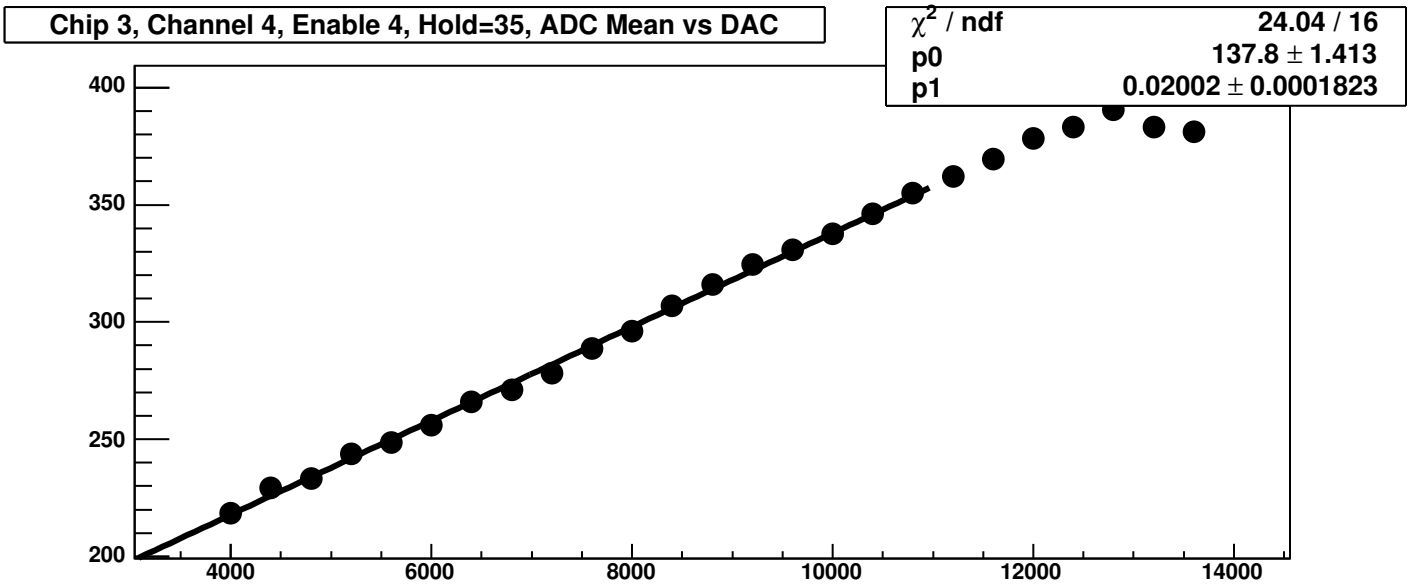

Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

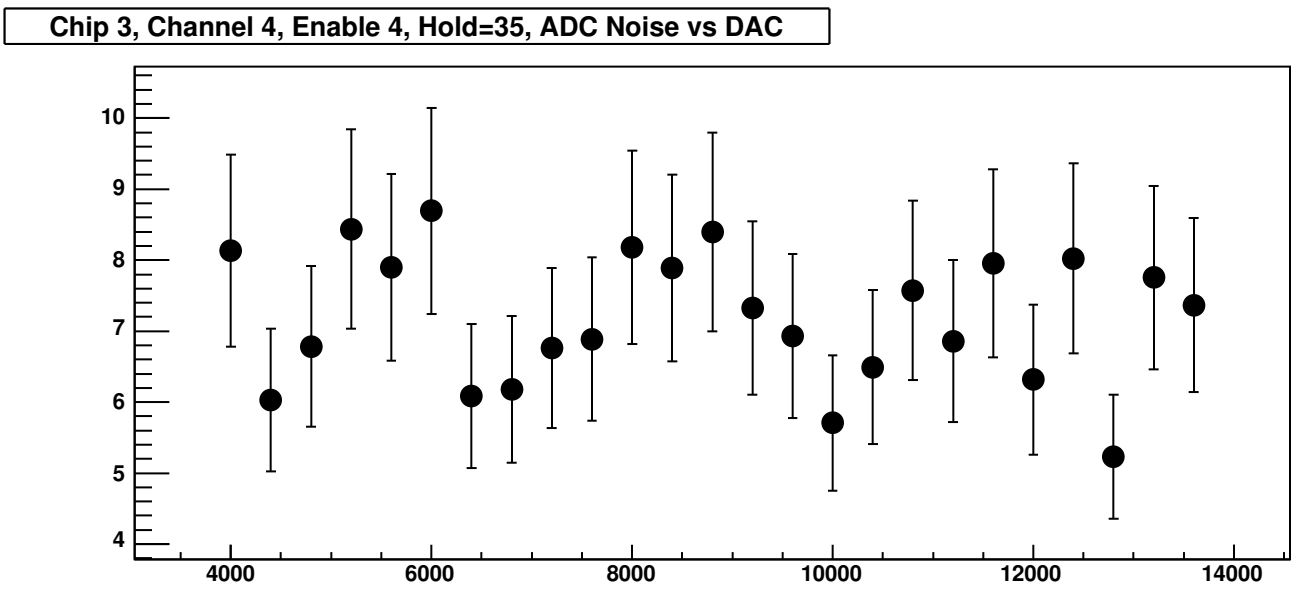

Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

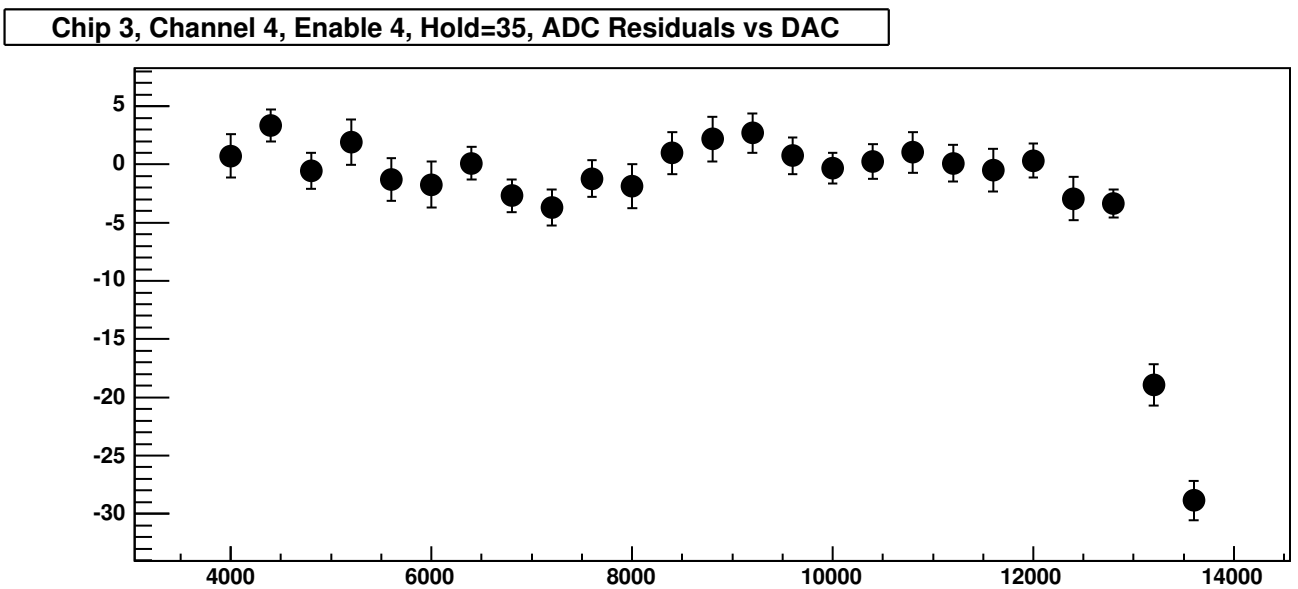

Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 10.96 / 16
p0 144.4 ± 1.469
p1 0.01832 ± 0.0001883
380
360
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

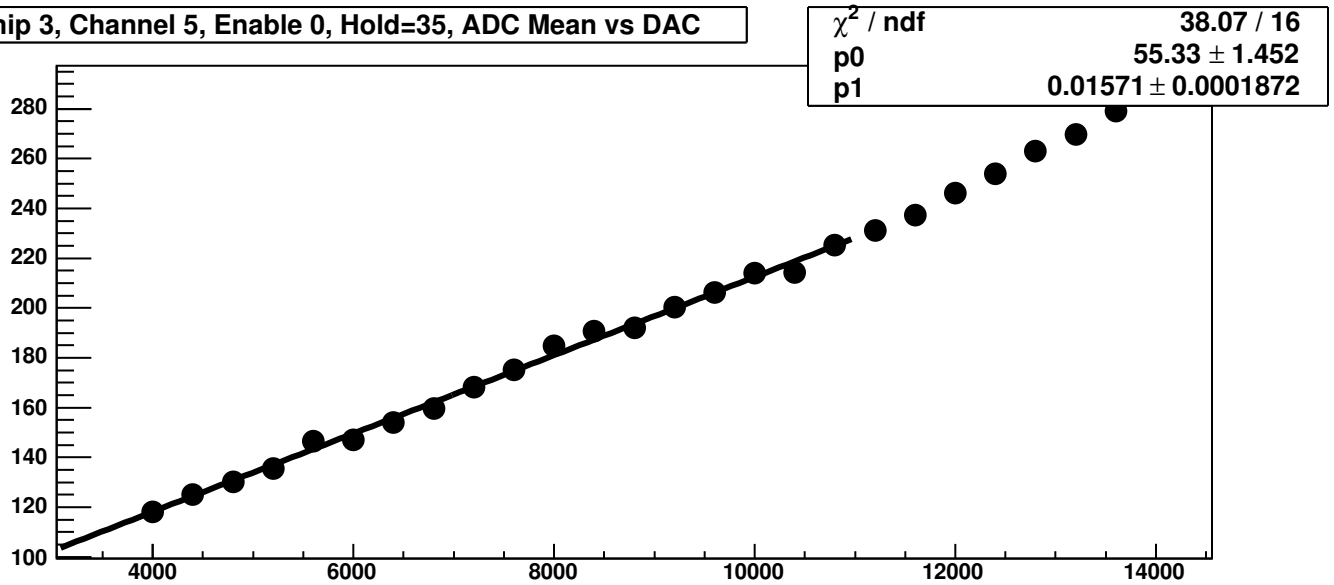

Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.04 / 16
p0 137.8 ± 1.413
p1 0.02002 ± 0.0001823
400
350
300
250
200
4000
6000
8000
10000
12000
14000

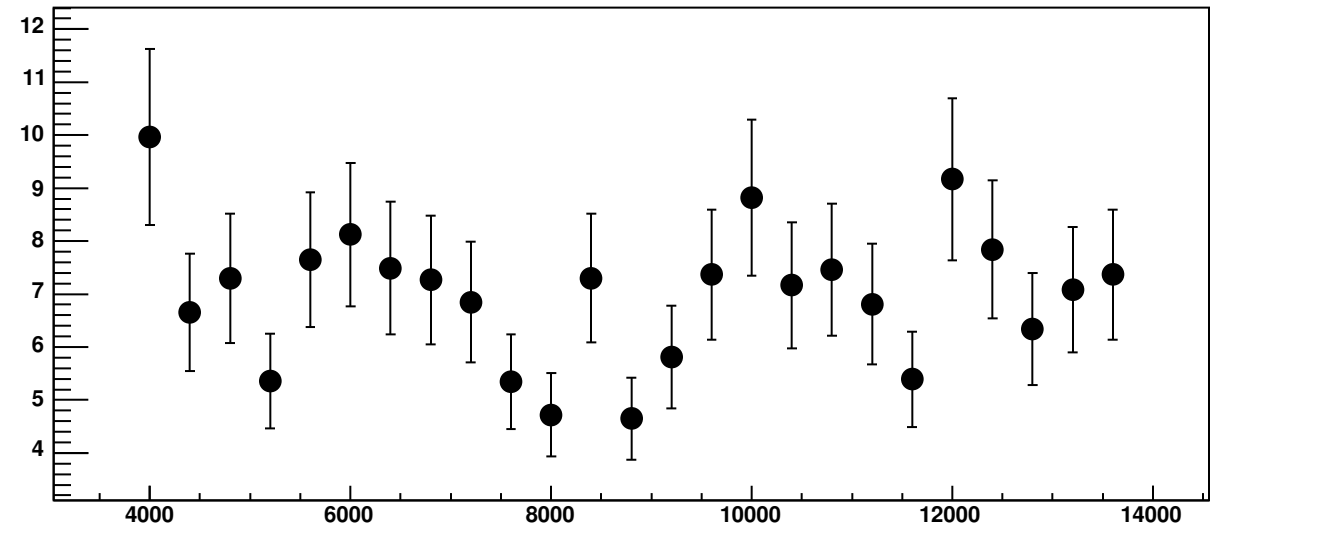

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

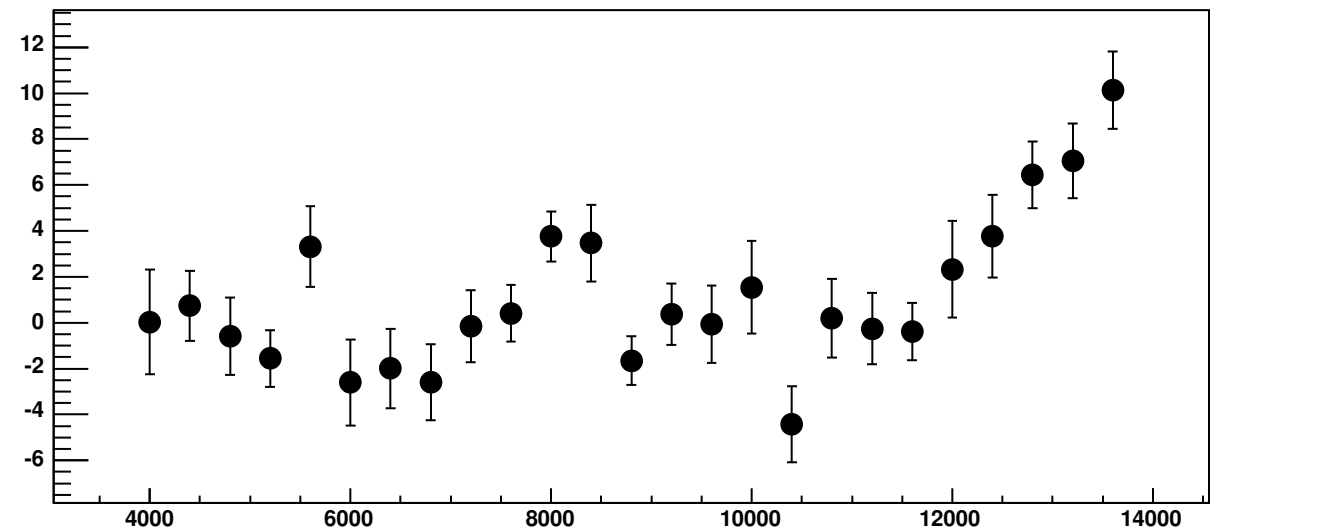
Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

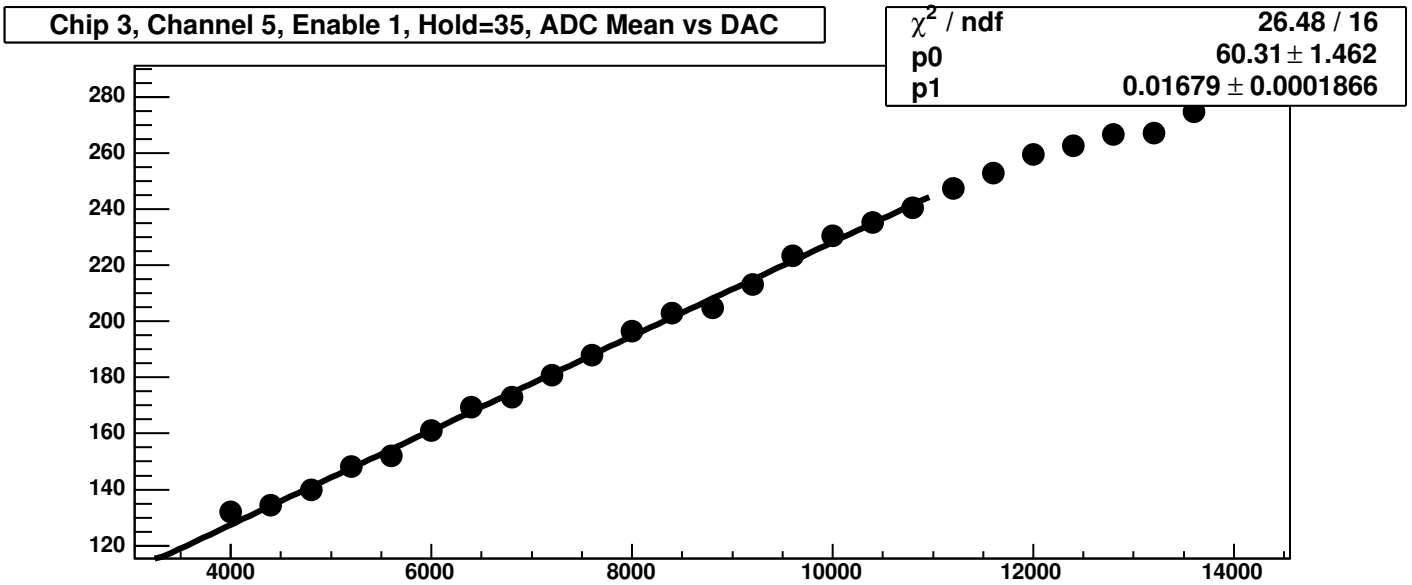


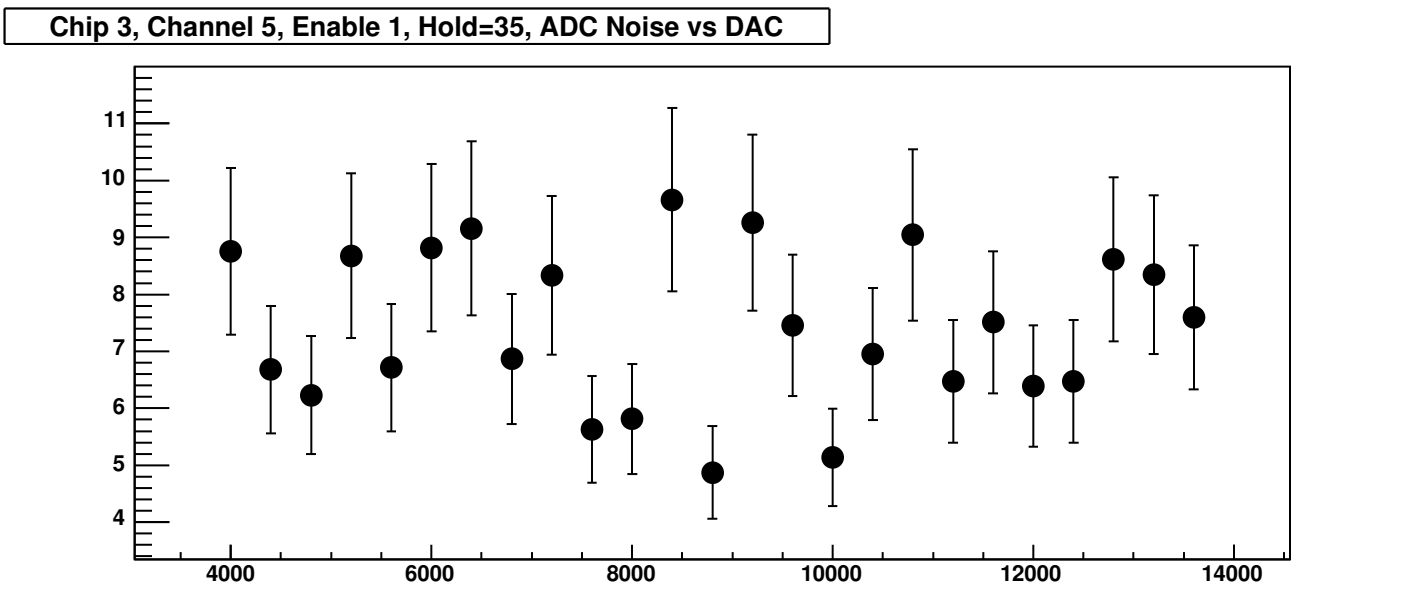
Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

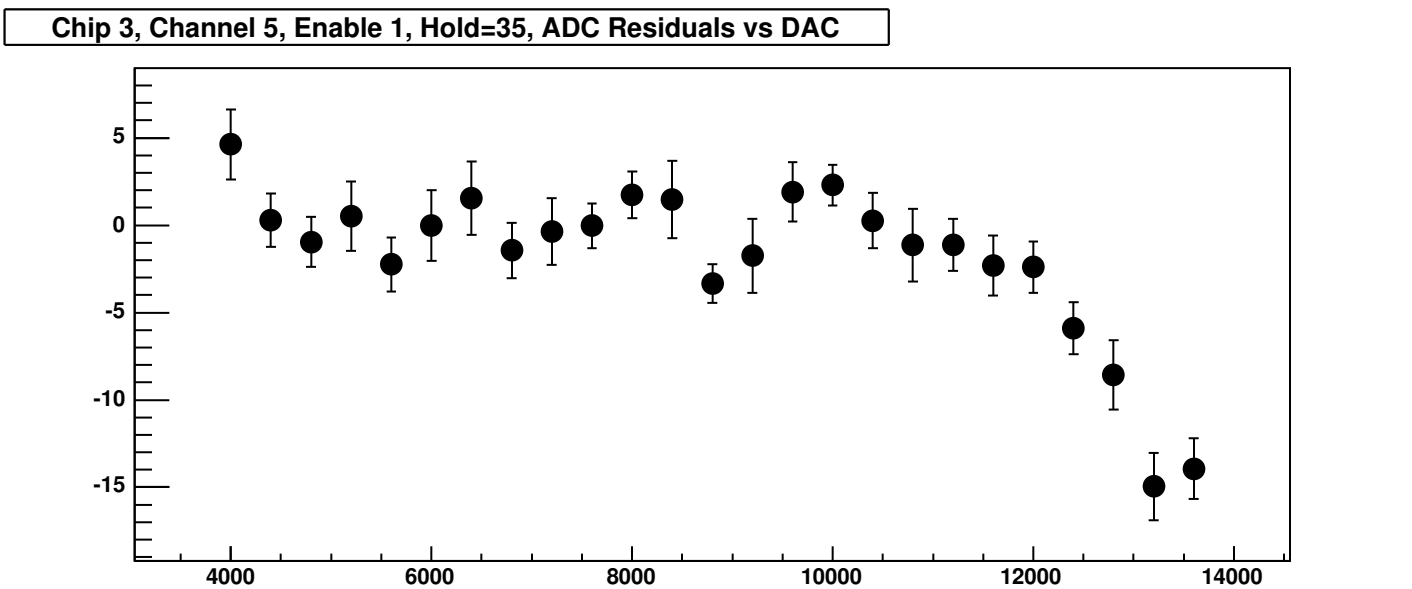


Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

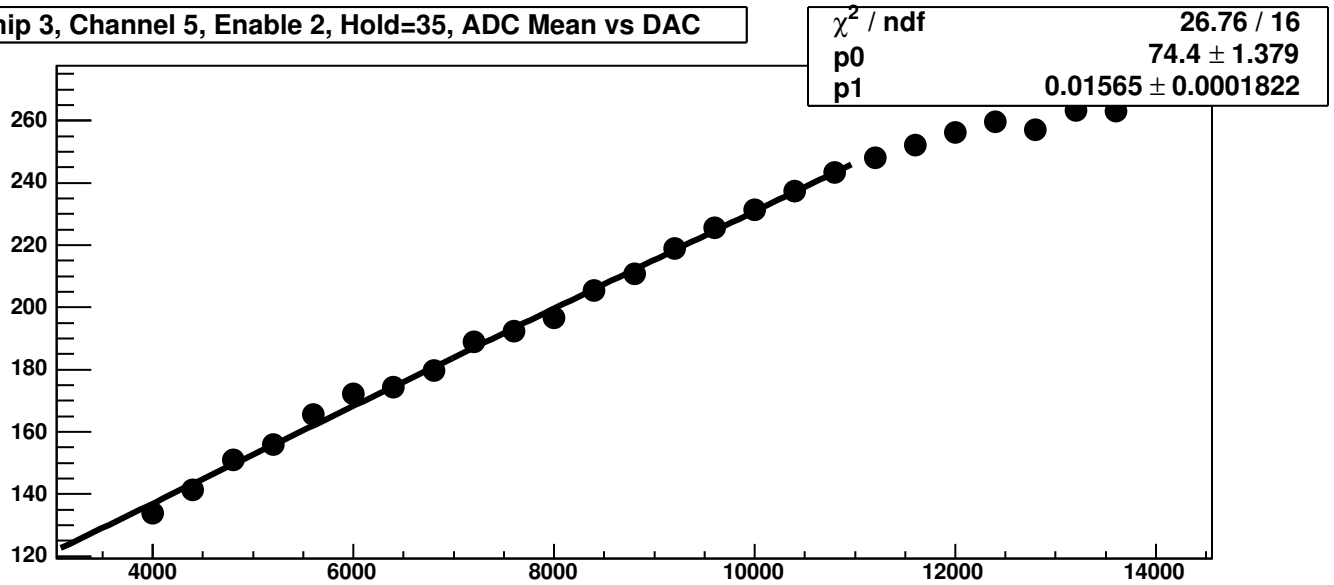



Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.48 / 16
p0 60.31 ± 1.462
p1 0.01679 ± 0.0001866
280
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000

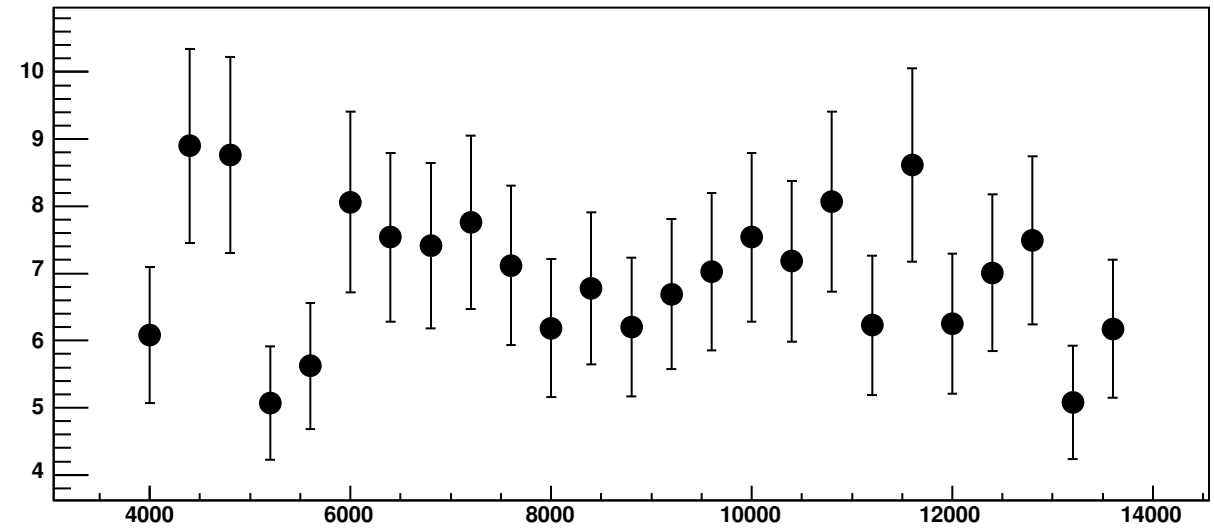

Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

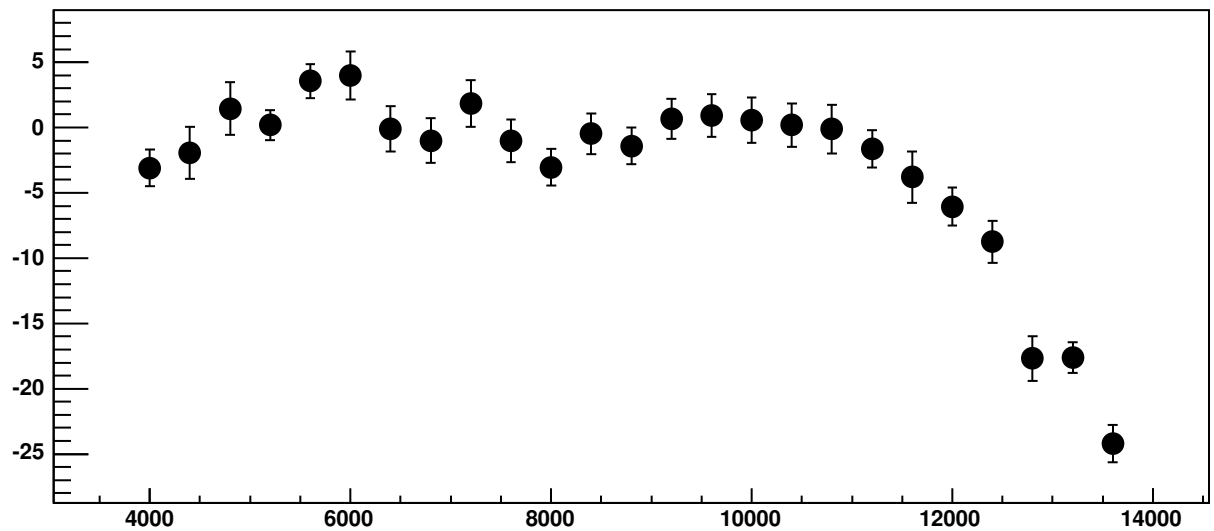
Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

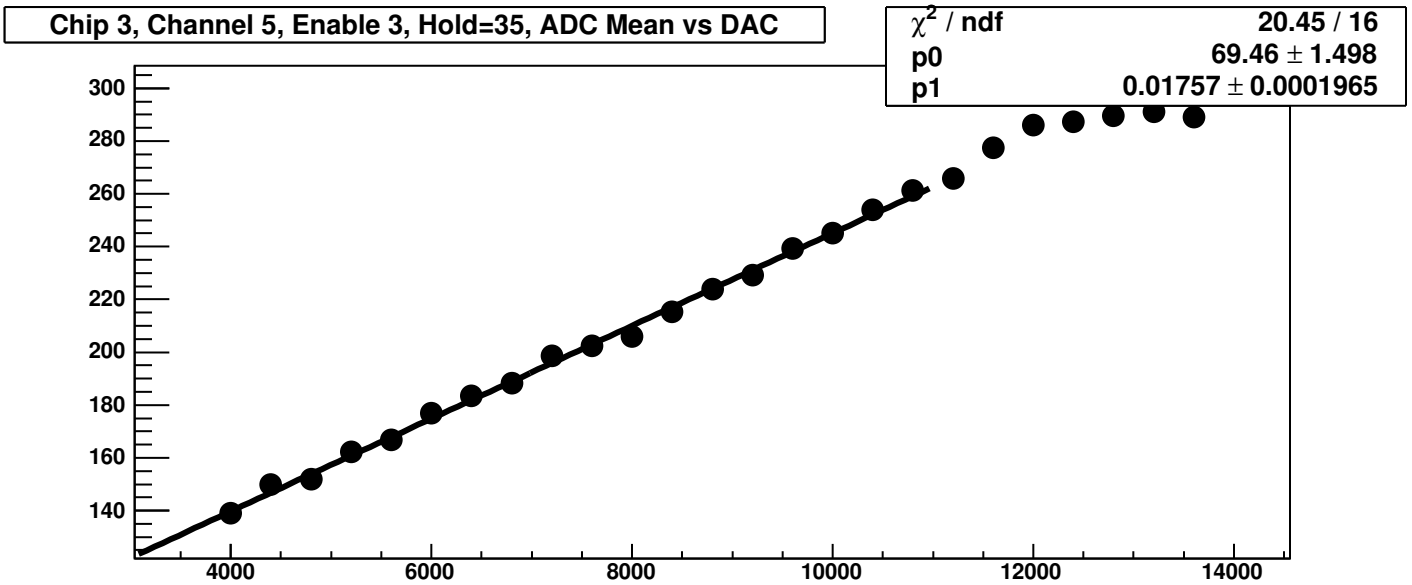


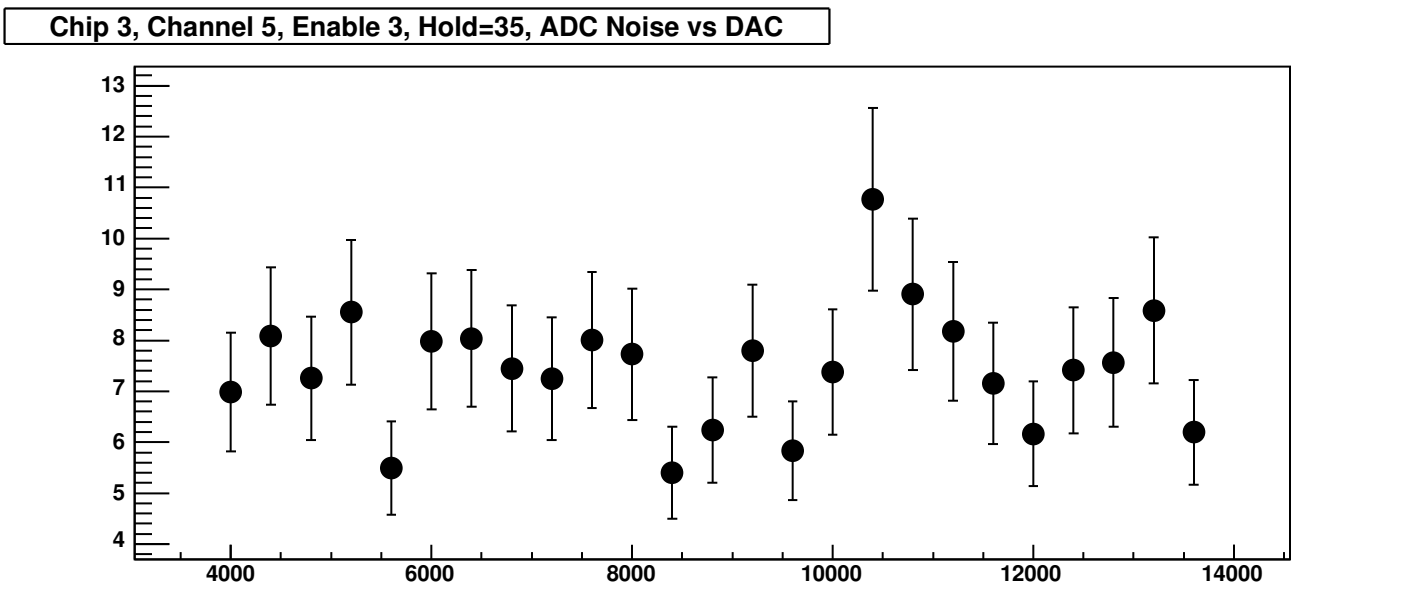
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

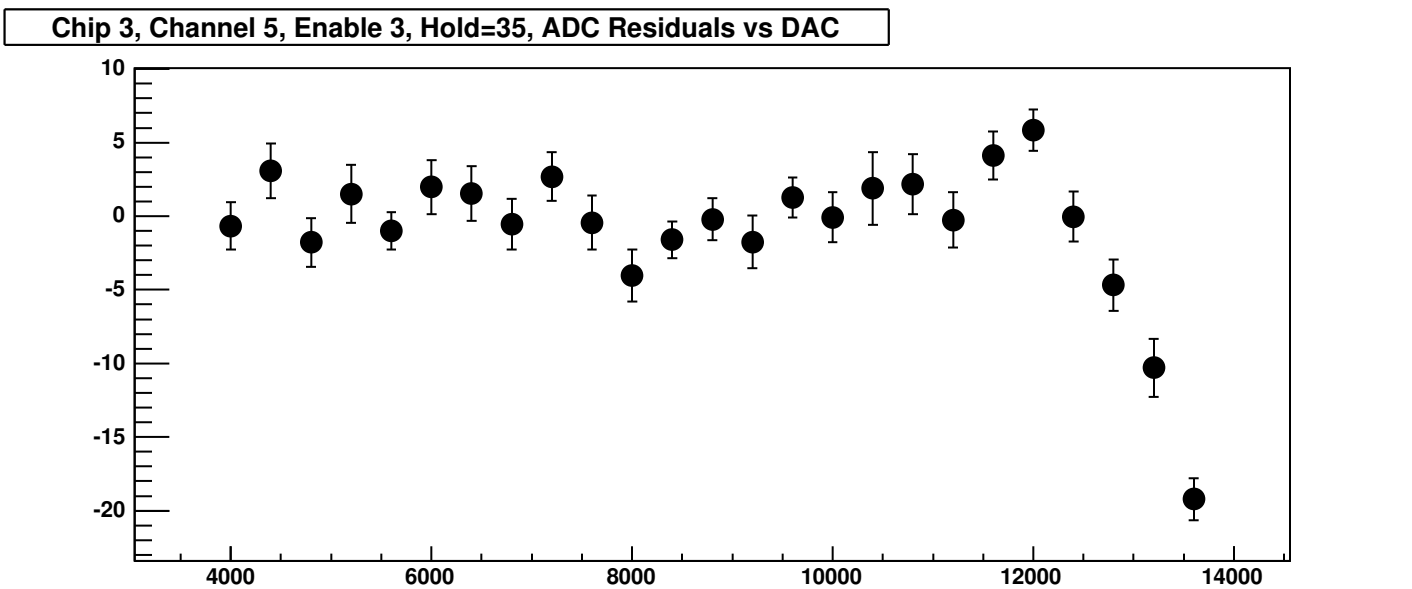


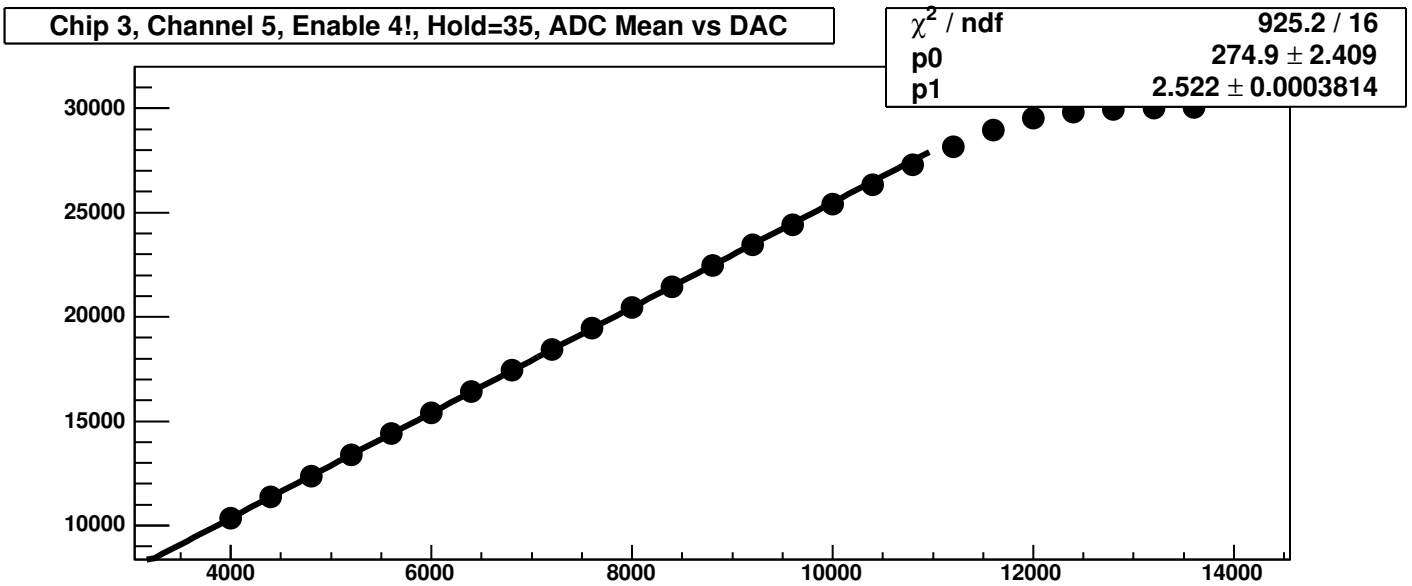
Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

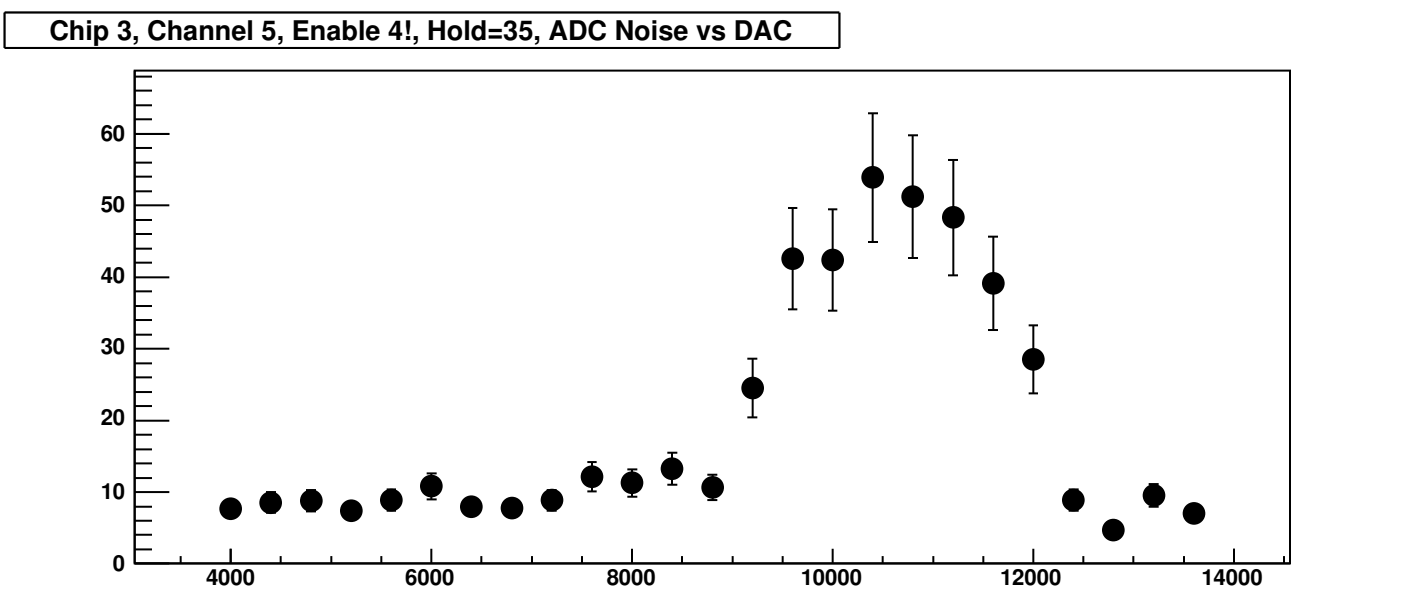


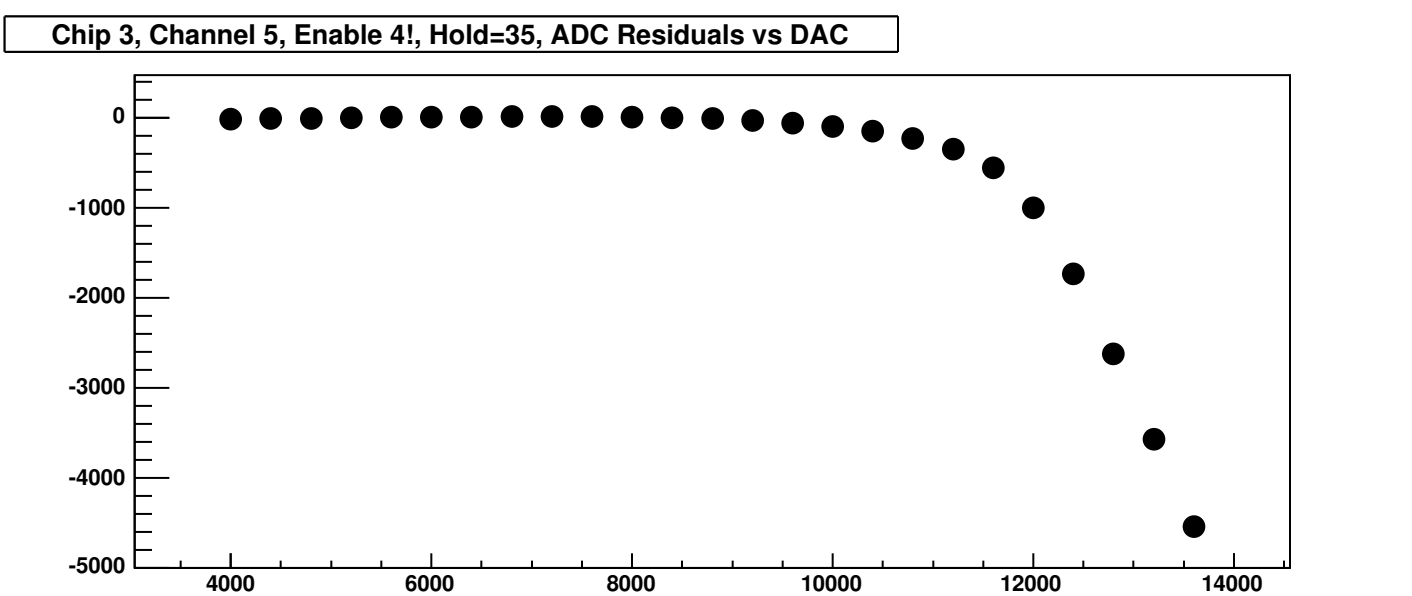

Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.45 / 16
p0 69.46 ± 1.498
p1 0.01757 ± 0.0001965
300
280
260
240
220
200
180
160
140
4000
6000
8000
10000
12000
14000

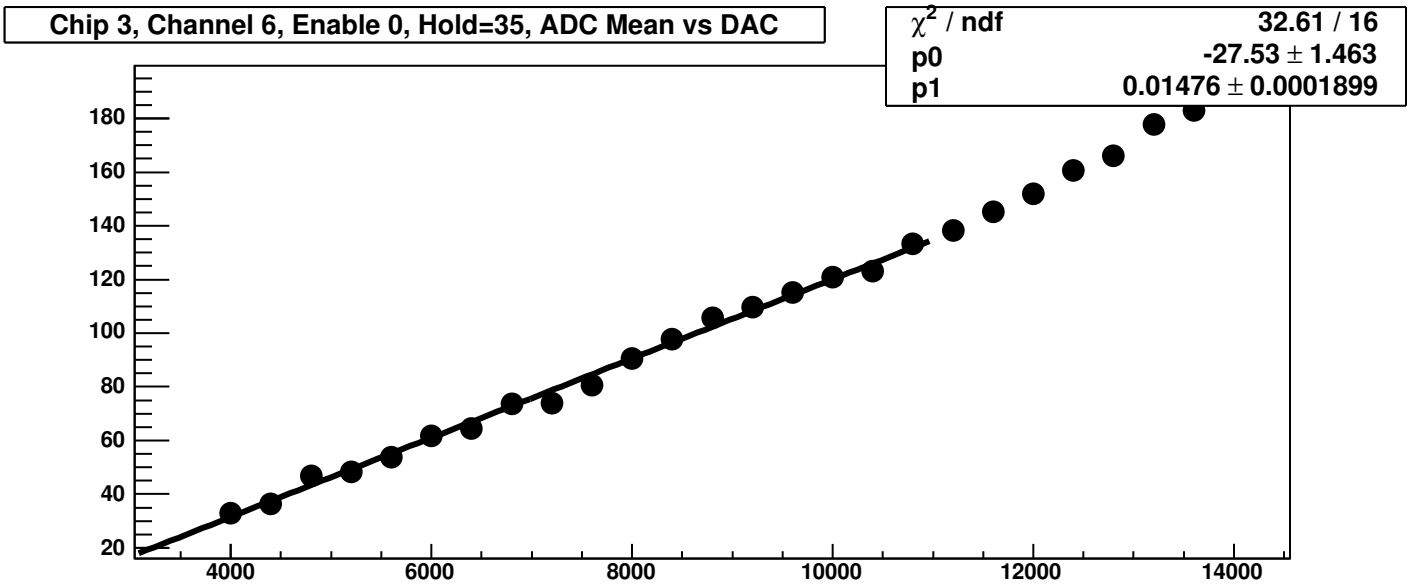

Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

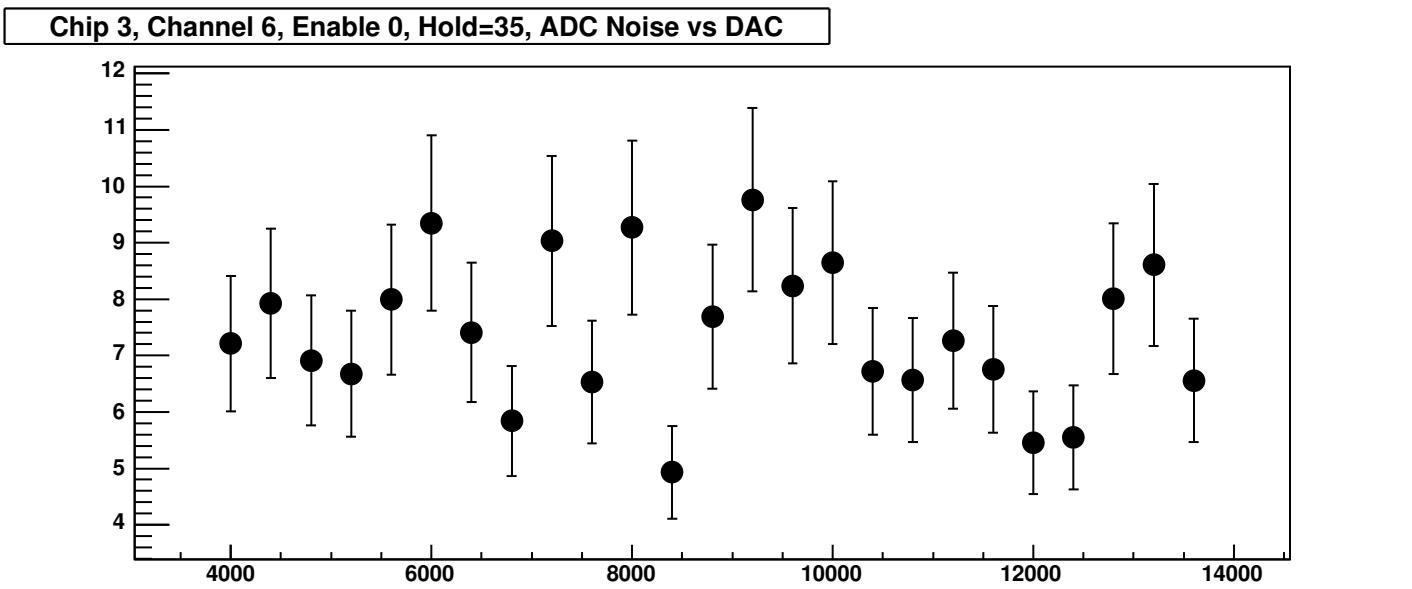

Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

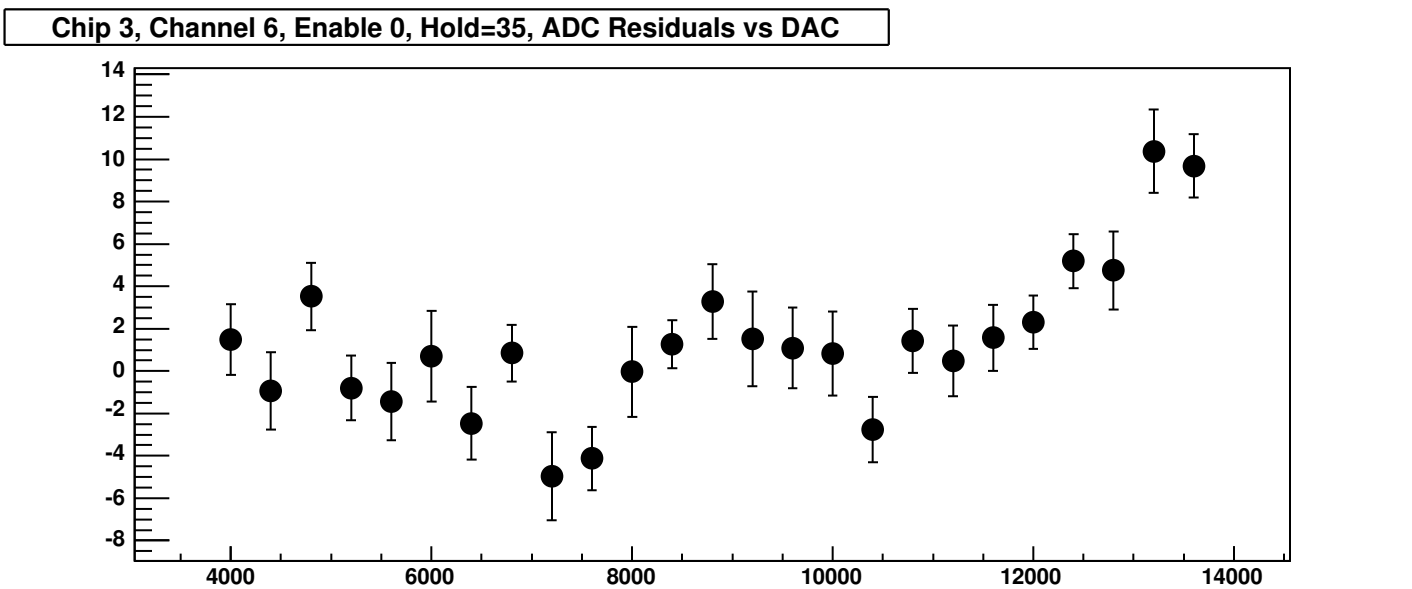

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 925.2 / 16
p0 274.9 ± 2.409
p1 2.522 ± 0.0003814
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

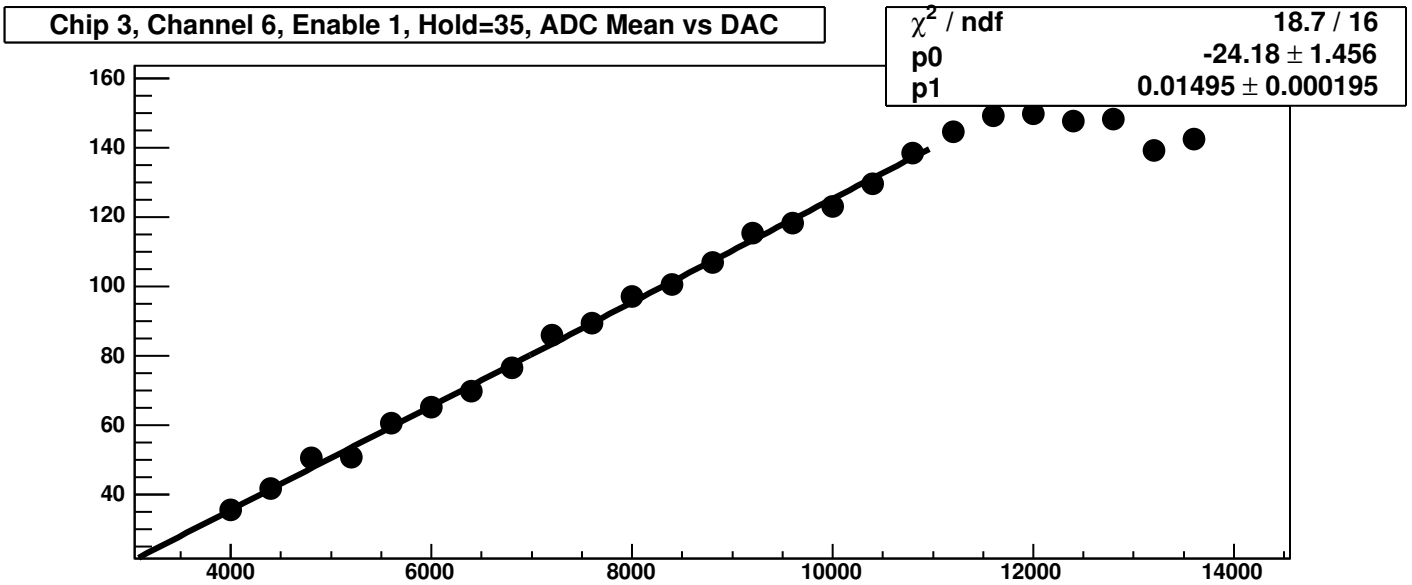

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

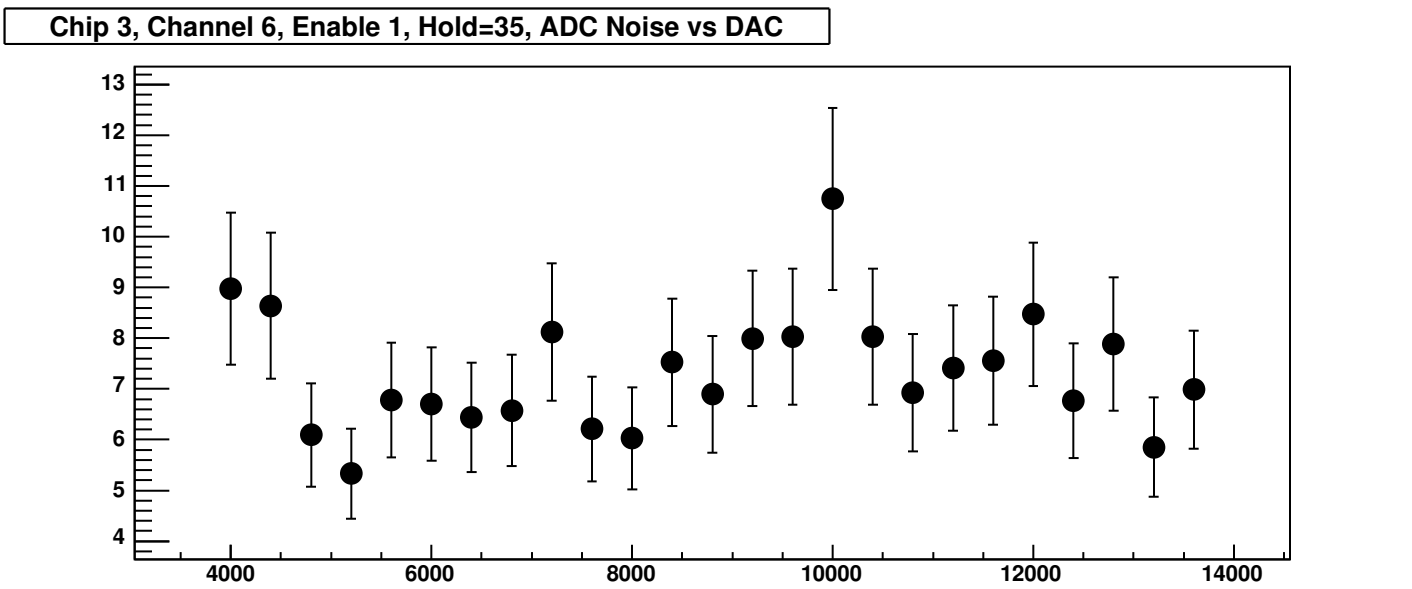

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

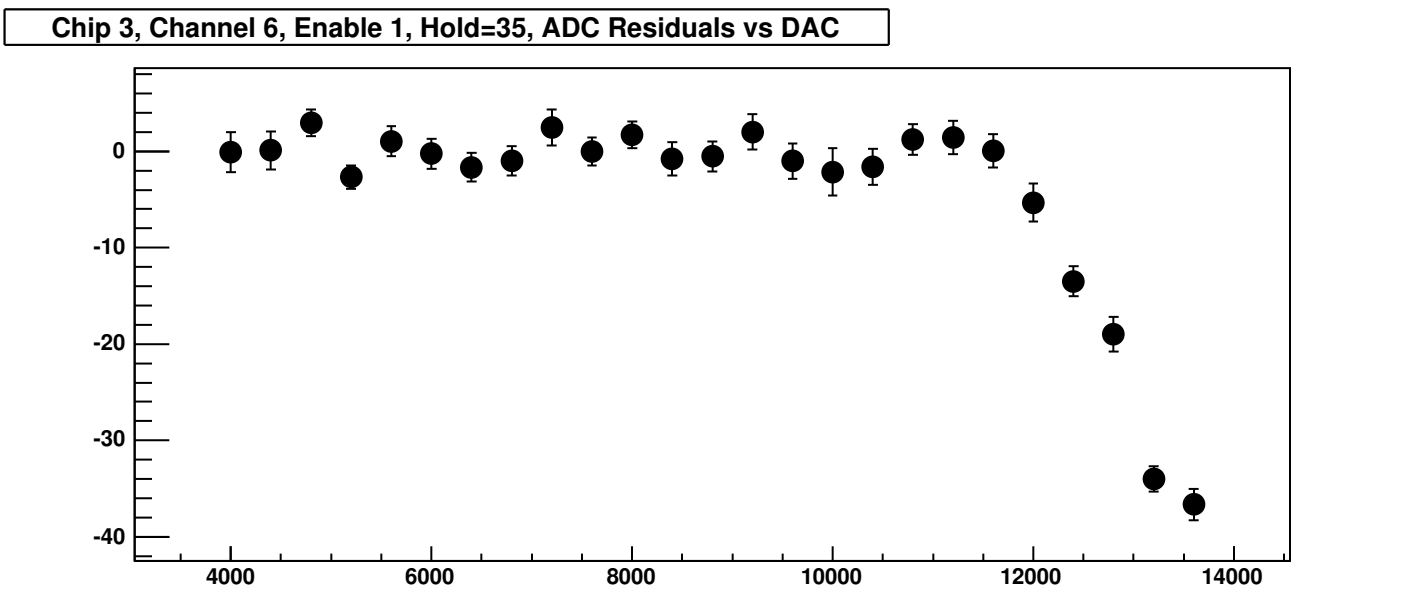

Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 32.61 / 16
p0 -27.53 ± 1.463
p1 0.01476 ± 0.0001899
180
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000

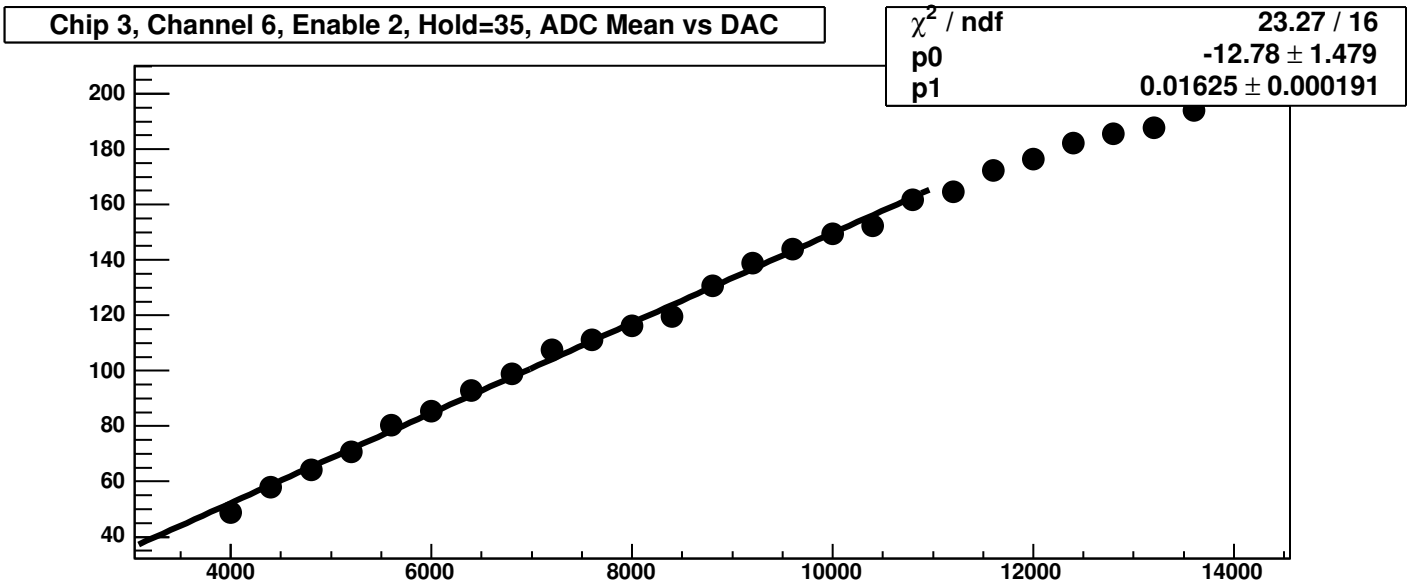

Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

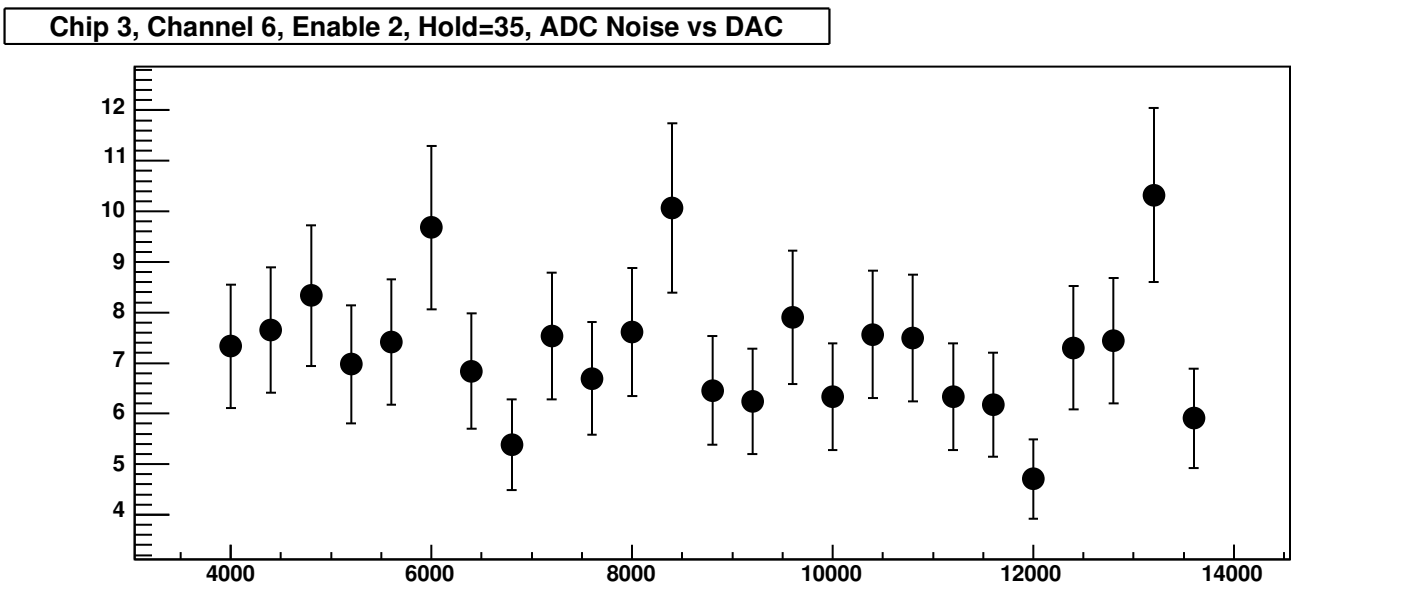

Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

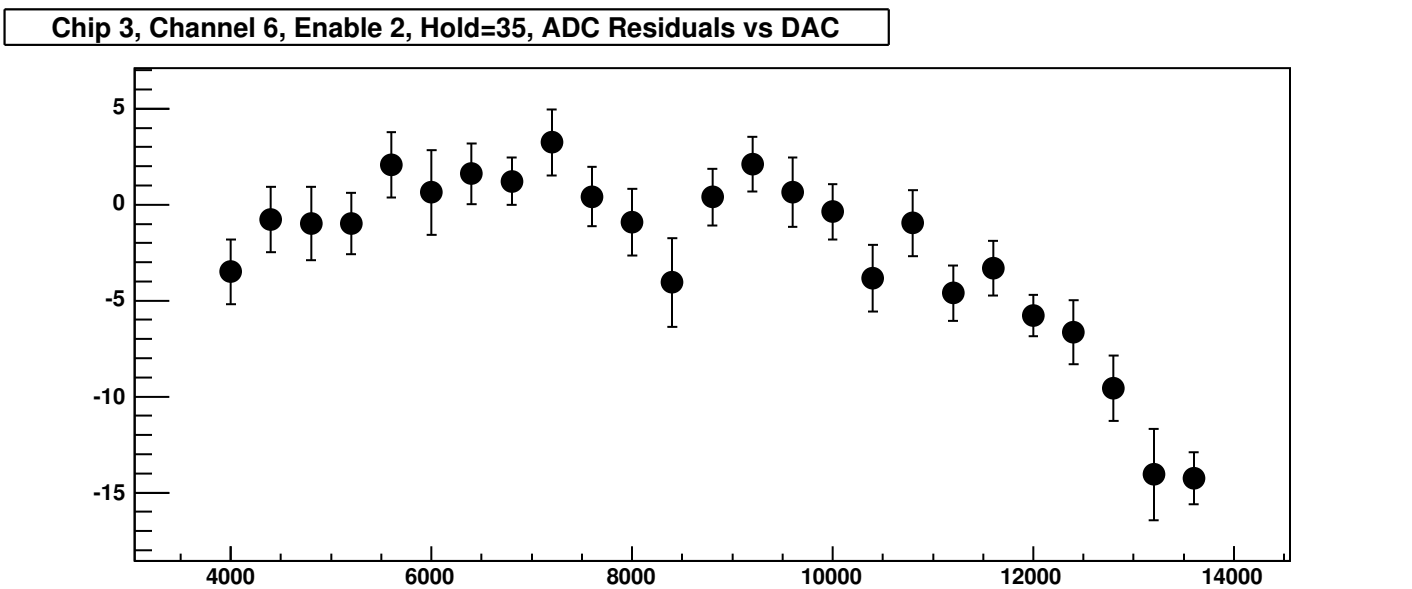

Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.7 / 16
p0 -24.18 ± 1.456
p1 0.01495 ± 0.000195
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000


Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

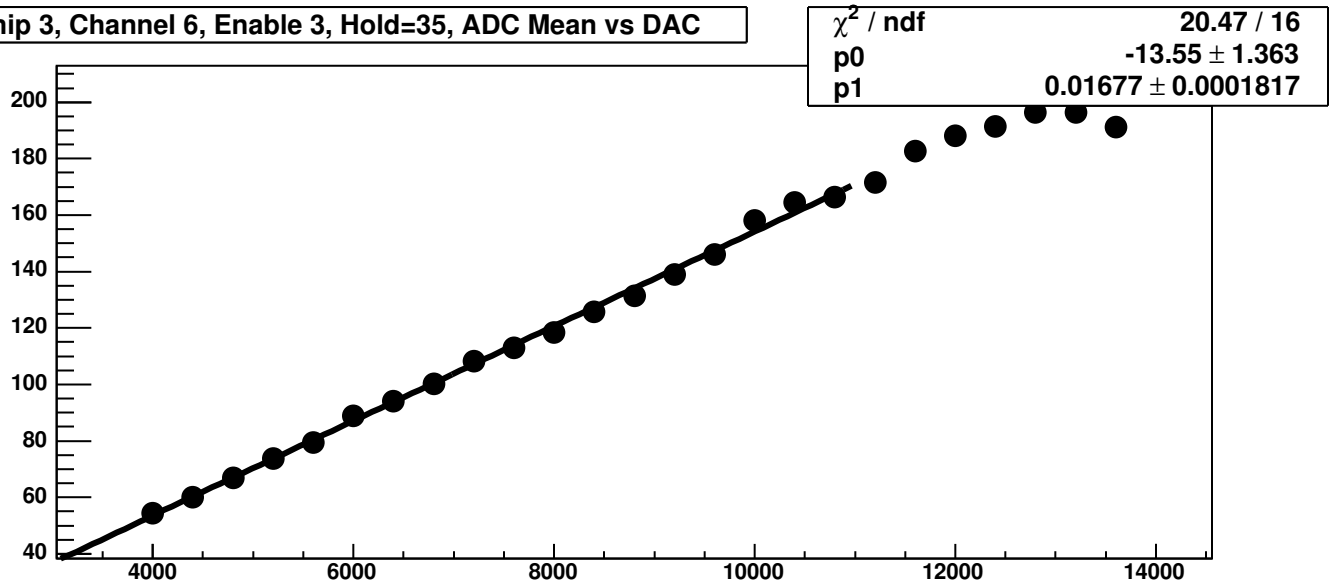

Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000


Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.27 / 16
p0 -12.78 ± 1.479
p1 0.01625 ± 0.000191
200
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

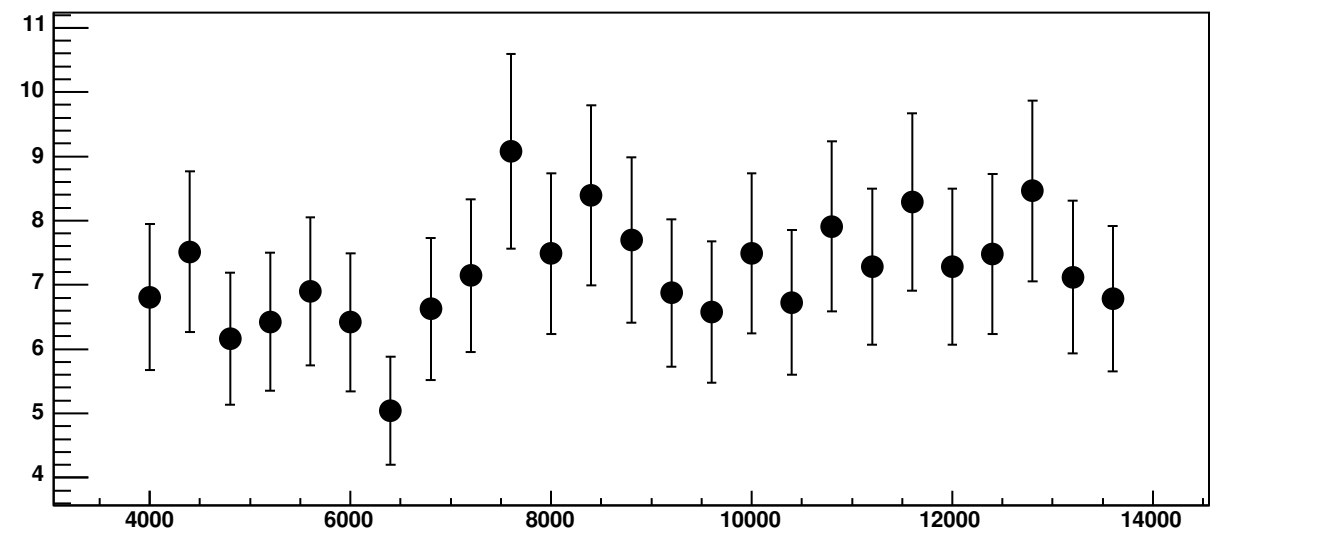

Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

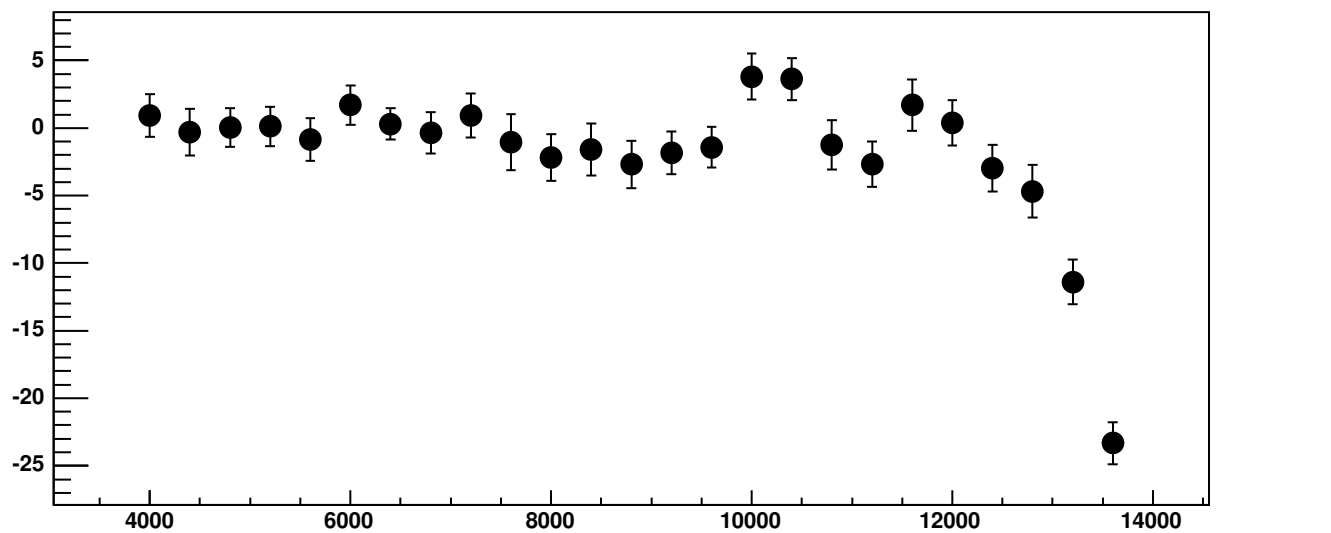
Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

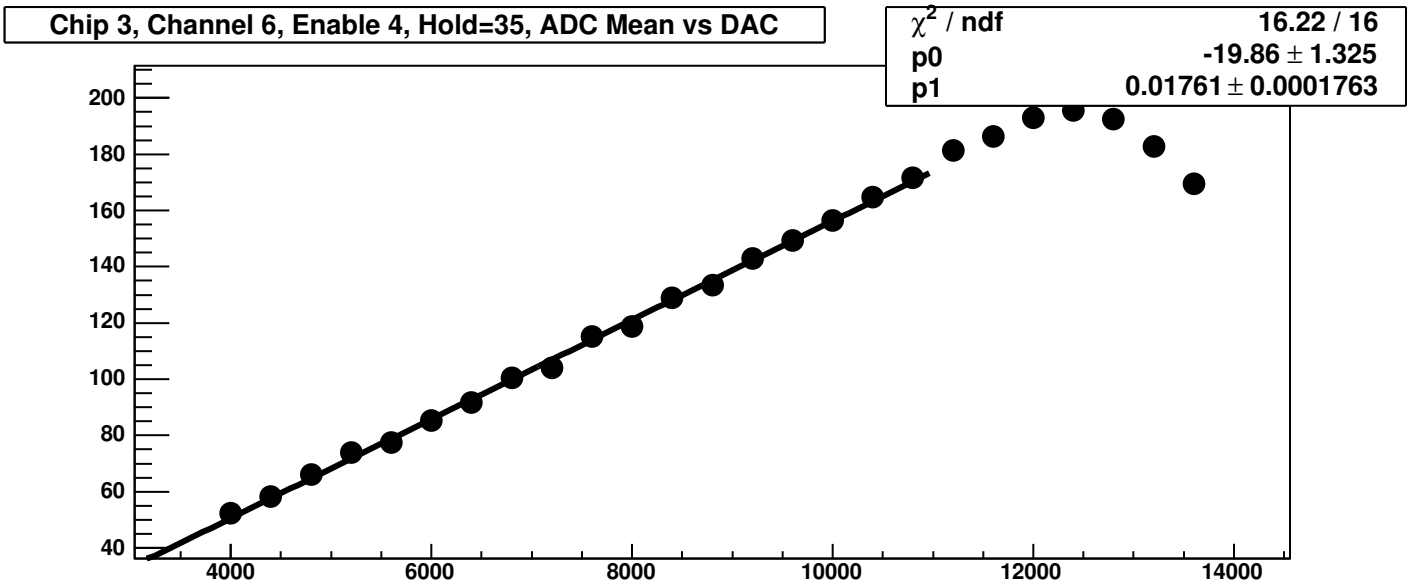


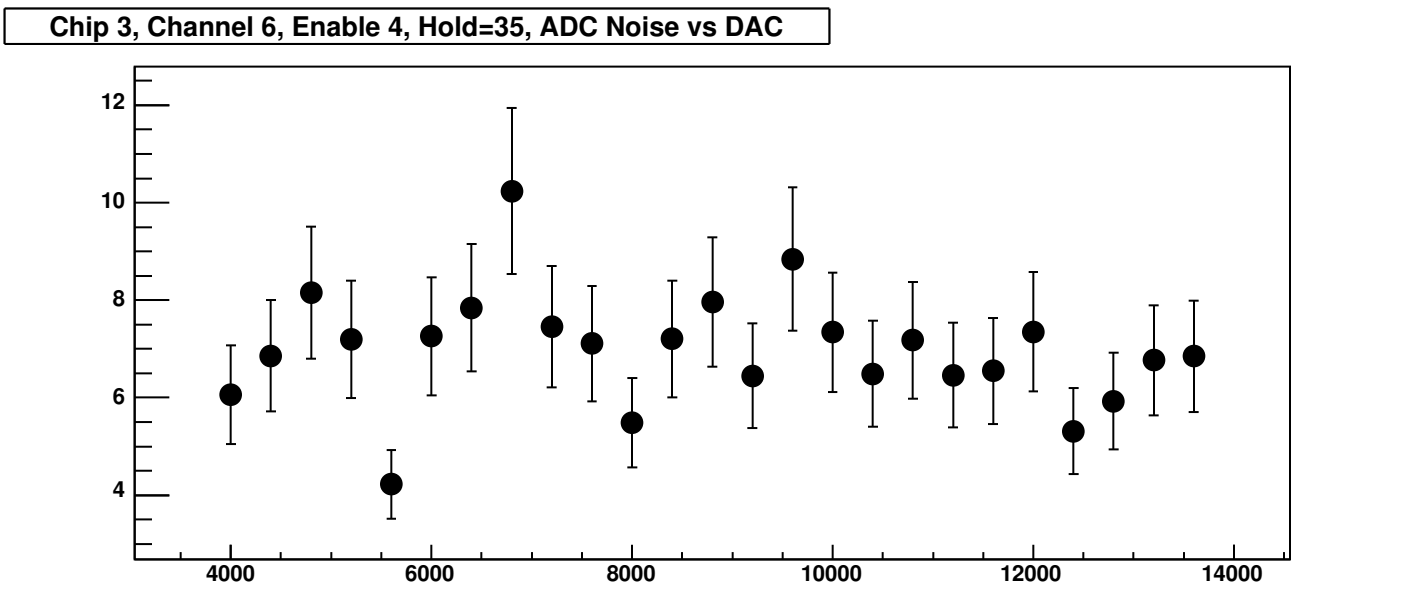
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

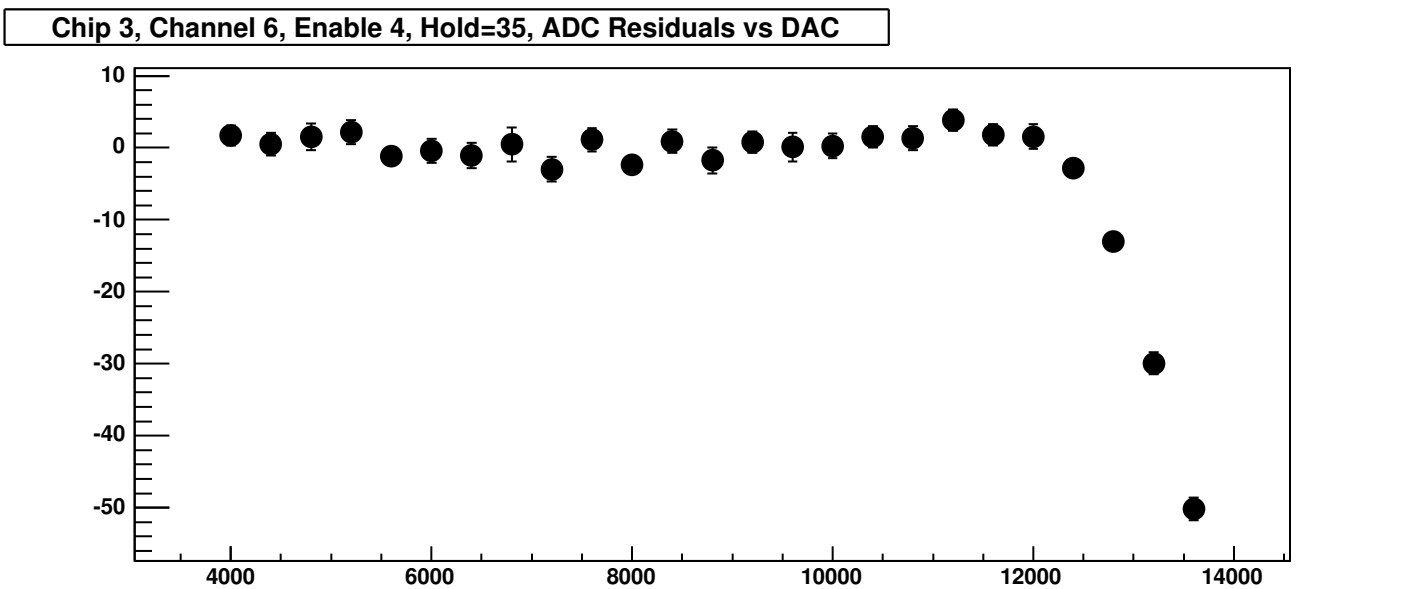


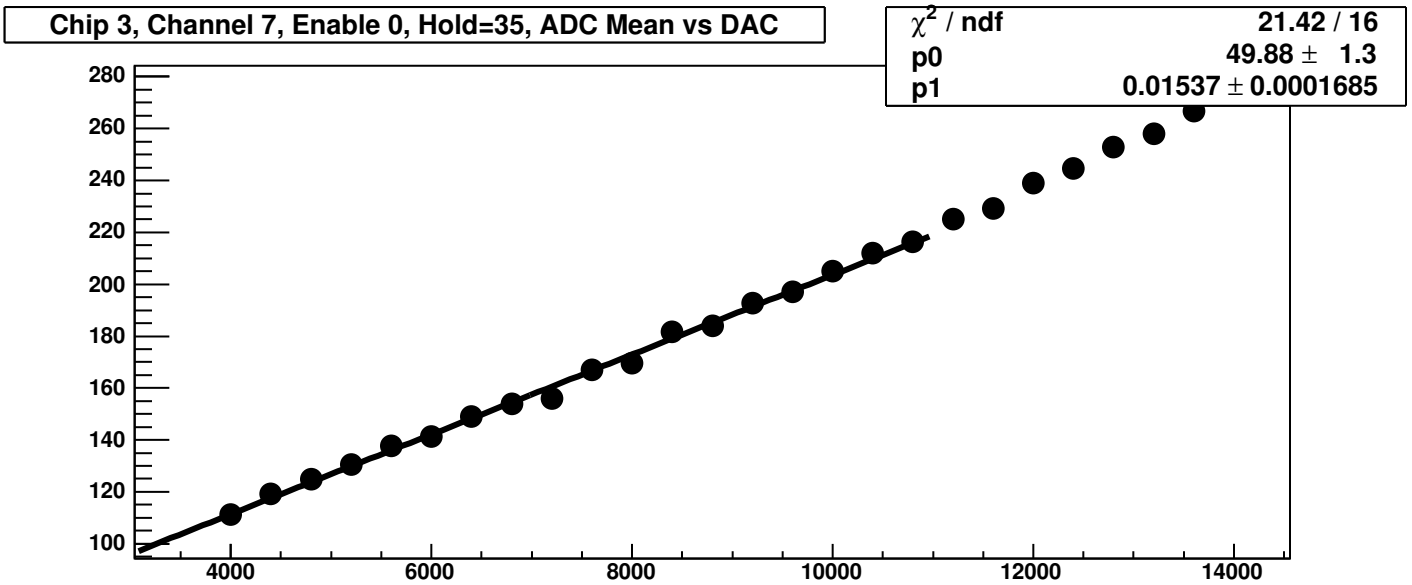
Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

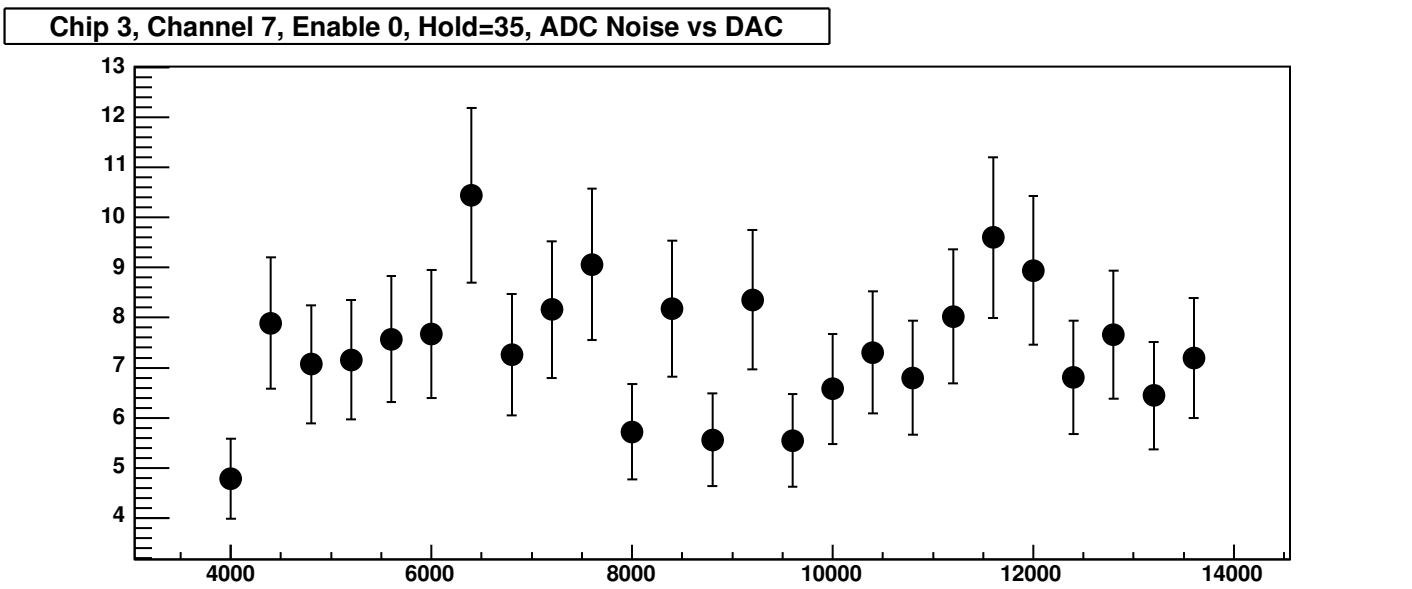


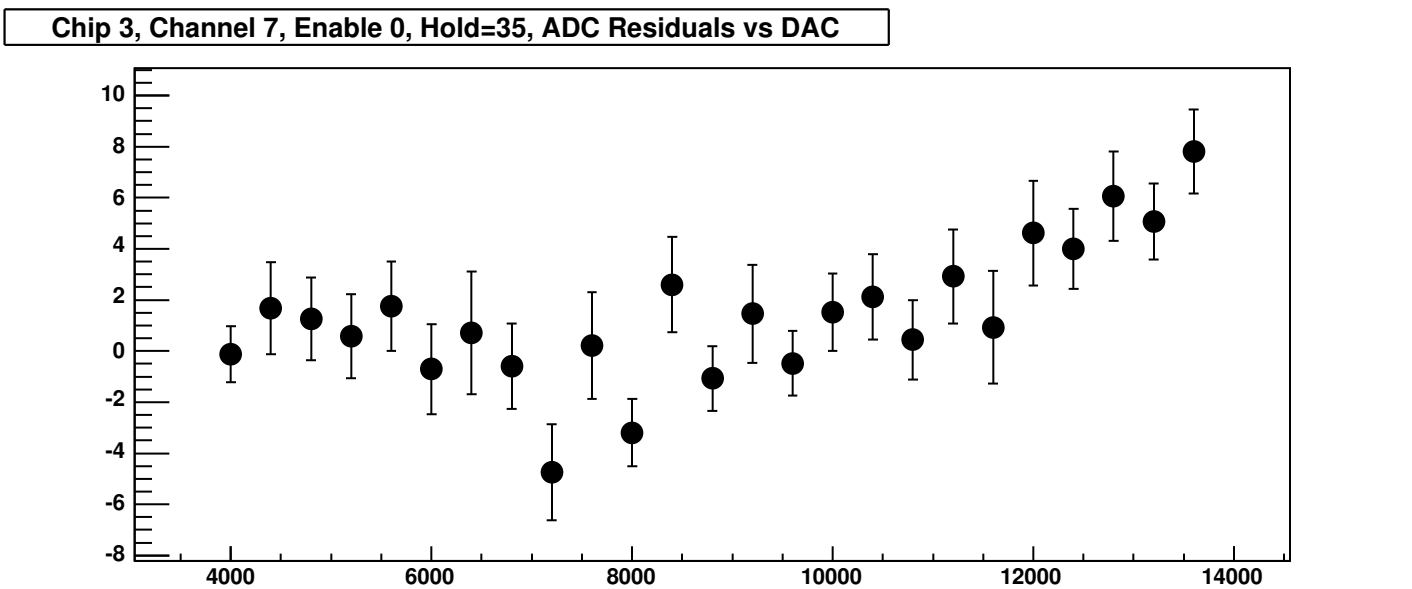

Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.22 / 16
p0 -19.86 ± 1.325
p1 0.01761 ± 0.0001763
200
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

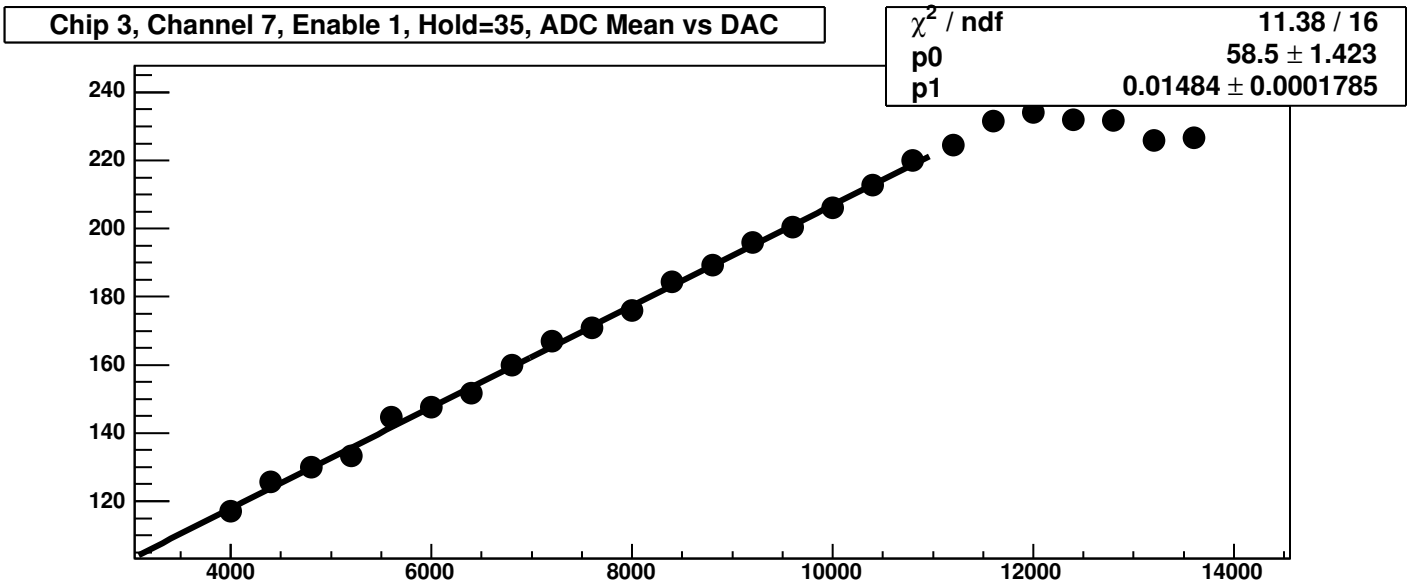

Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

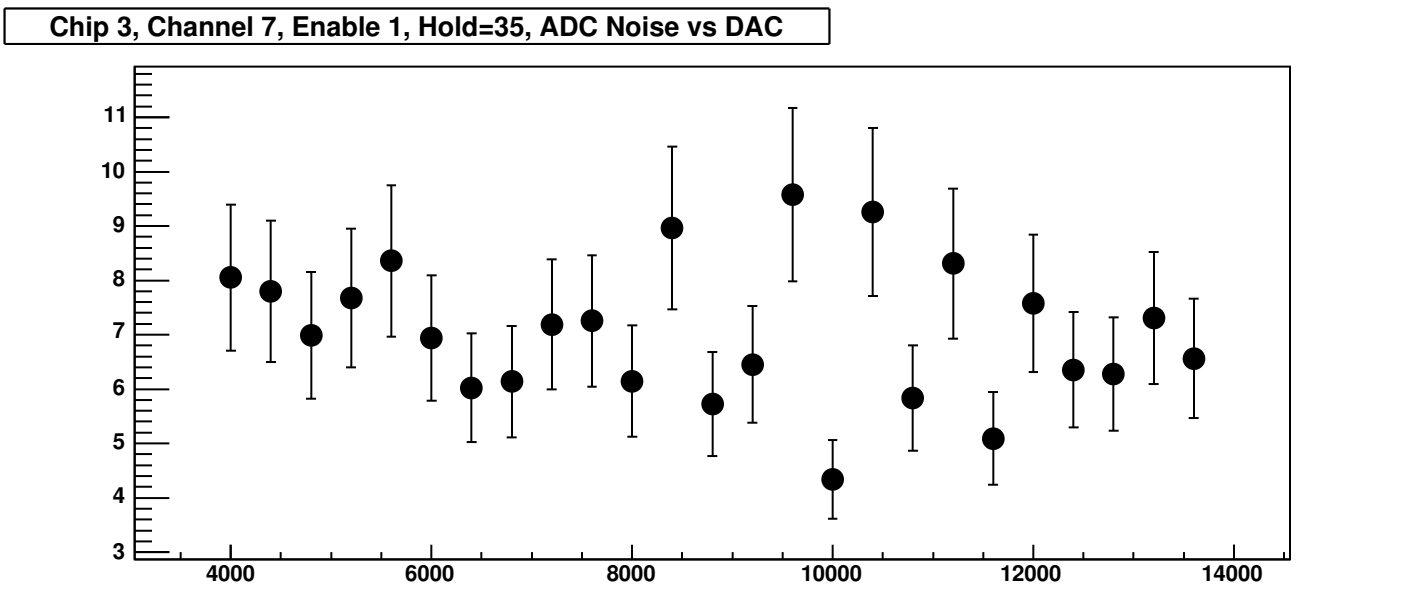

Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

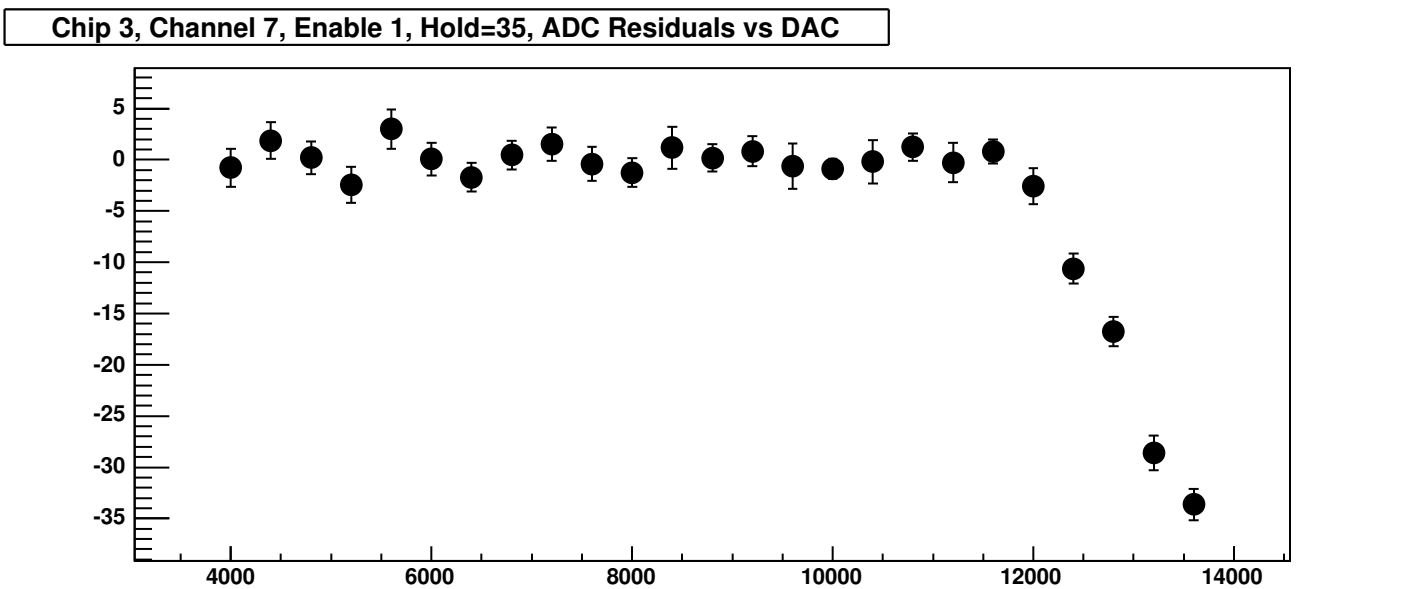

Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 21.42 / 16
p0 49.88 ± 1.3
p1 0.01537 ± 0.0001685
280
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

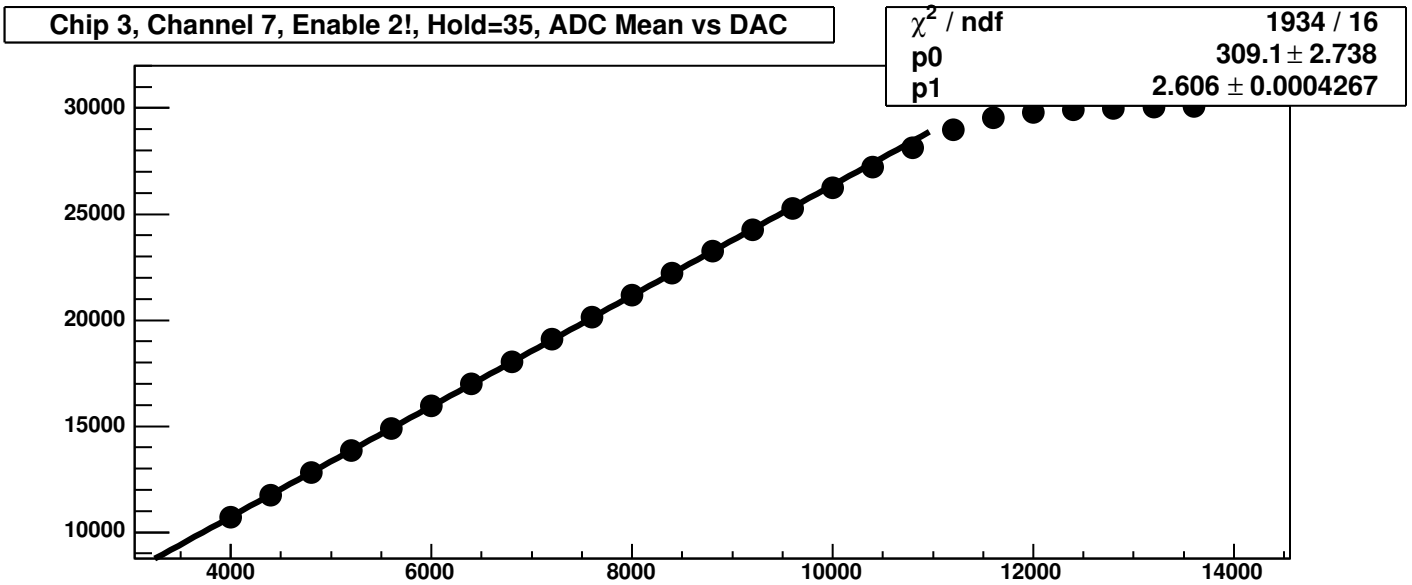

Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

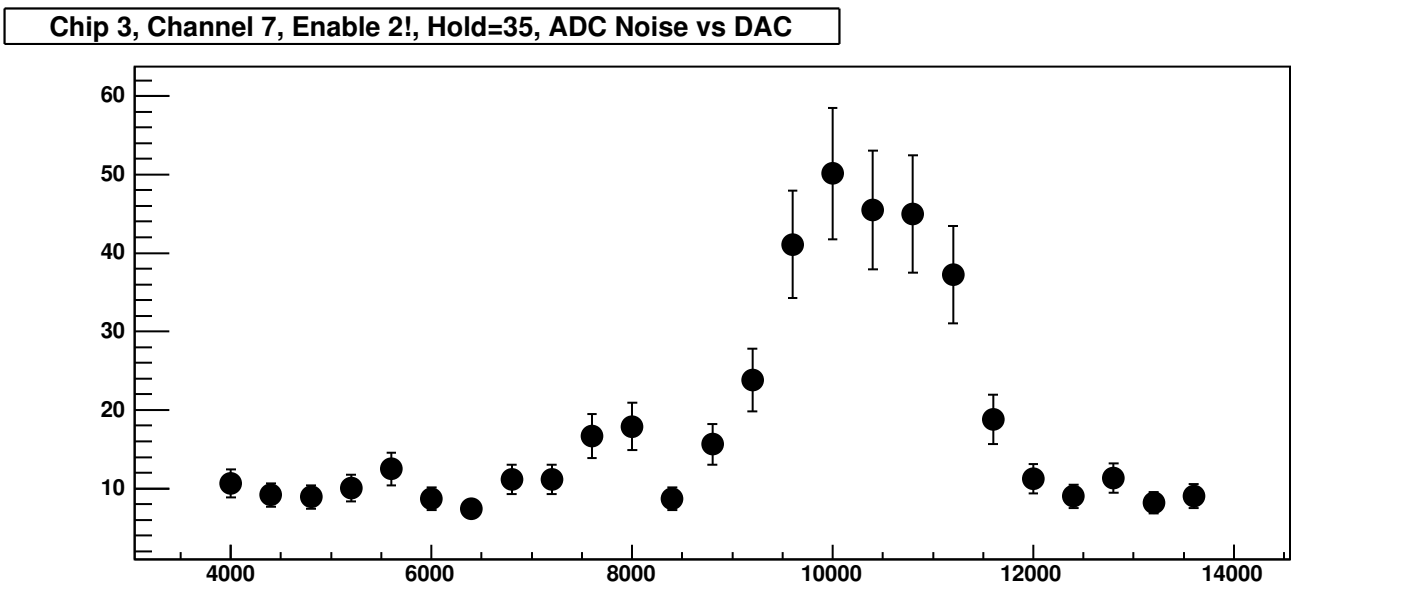

Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

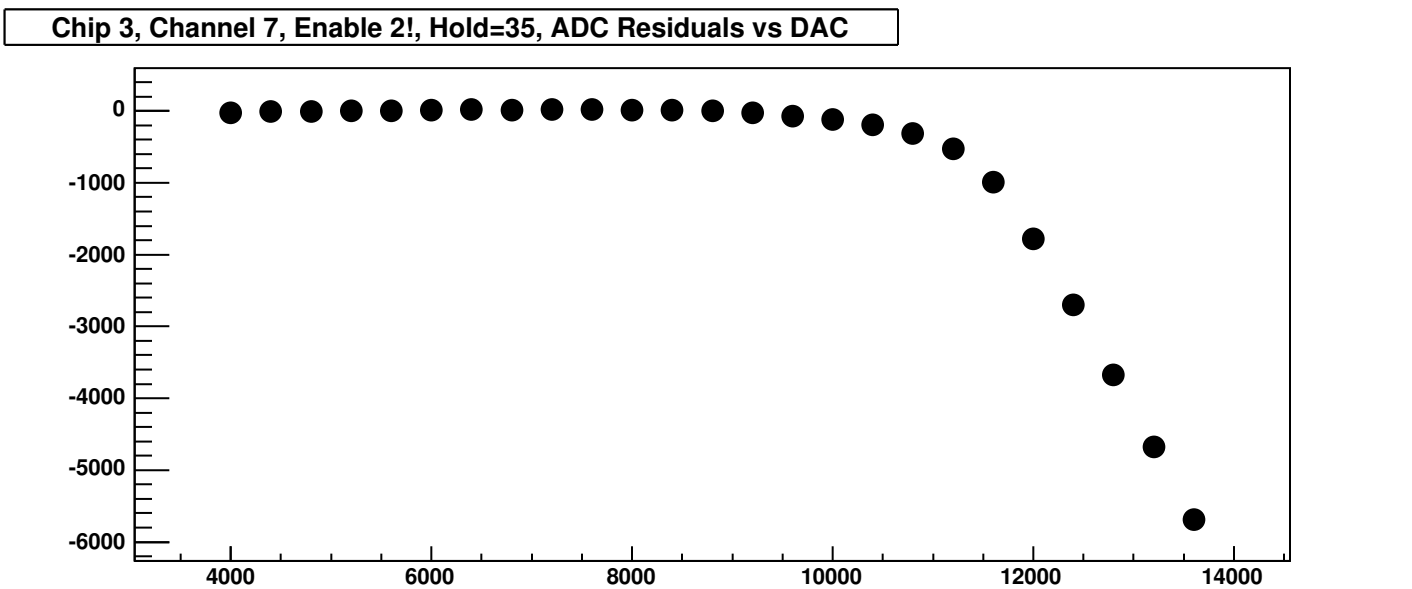

Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 11.38 / 16
p0 58.5 ± 1.423
p1 0.01484 ± 0.0001785
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000

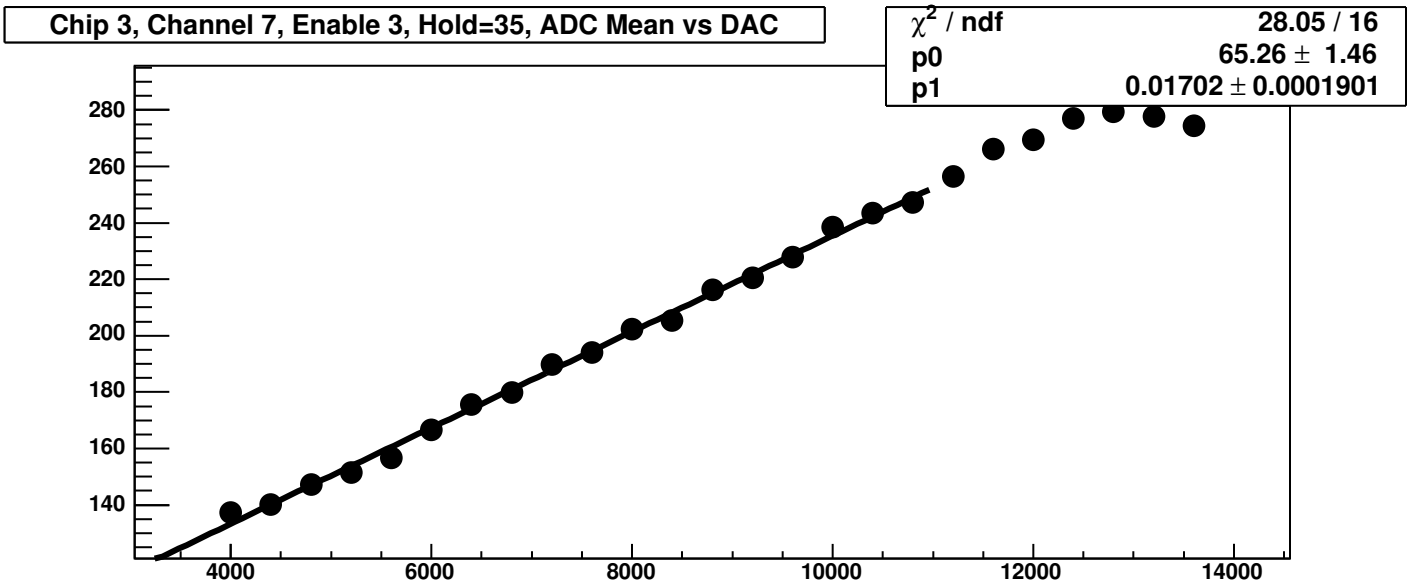

Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

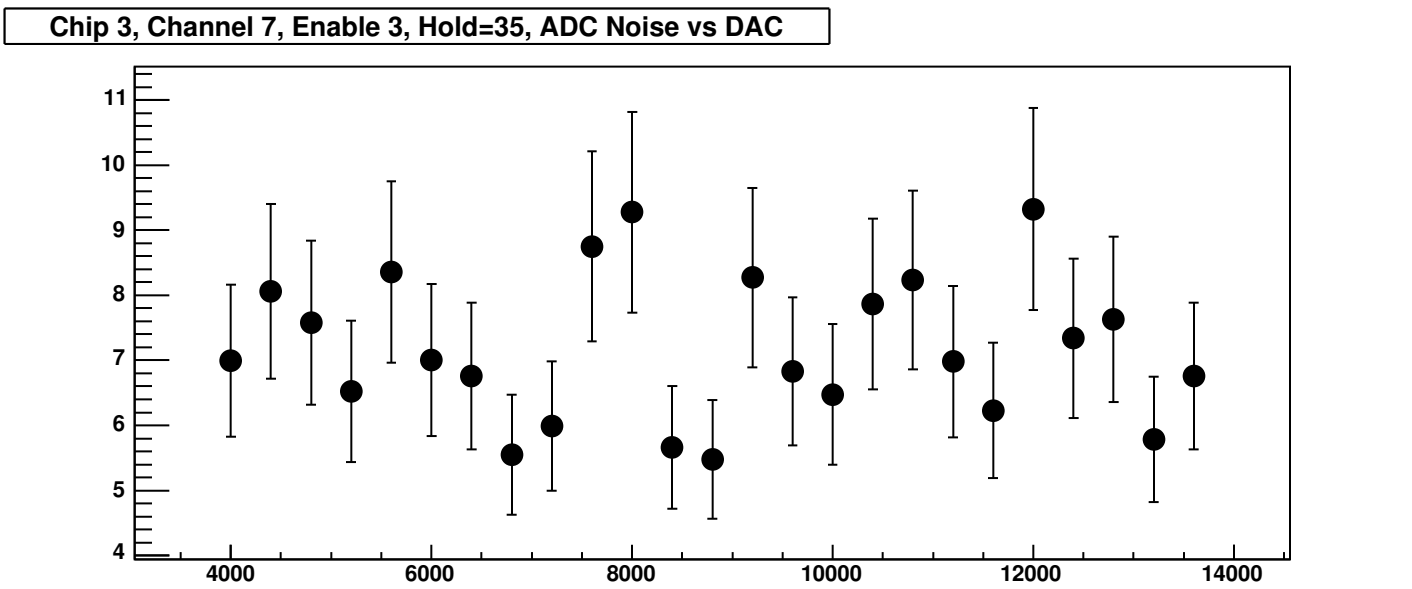

Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

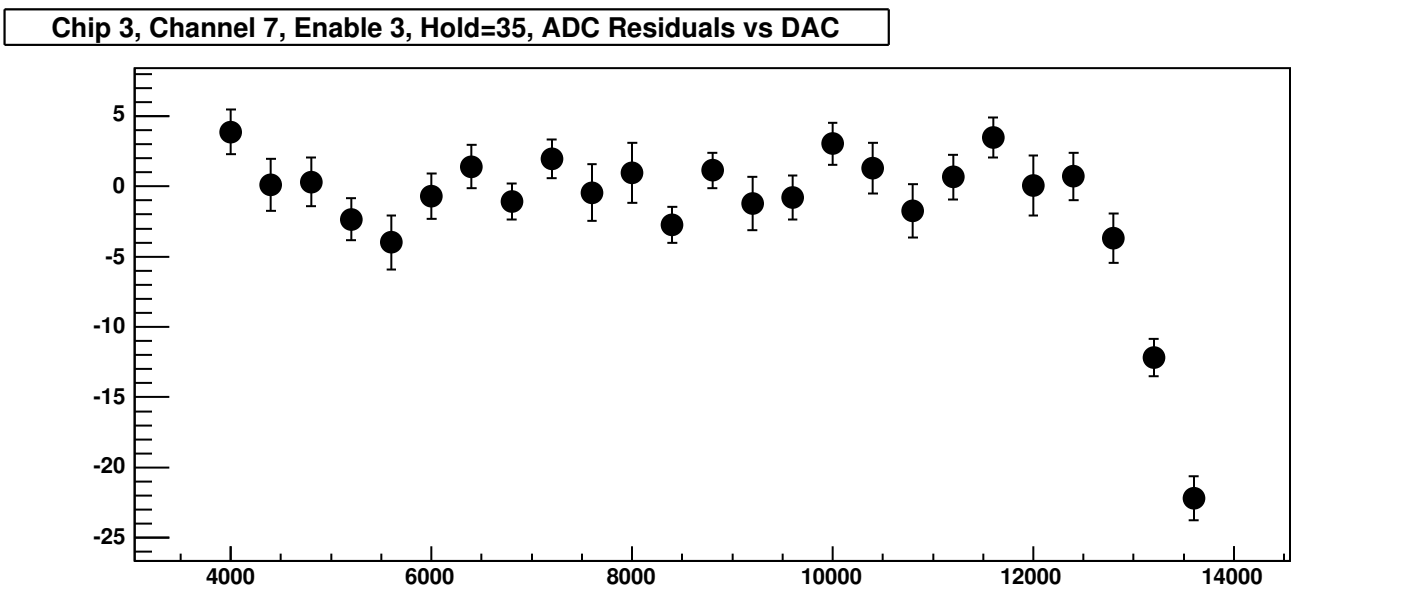

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1934 / 16
p0 309.1 ± 2.738
p1 2.606 ± 0.0004267
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
4000
6000
8000
10000
12000
14000

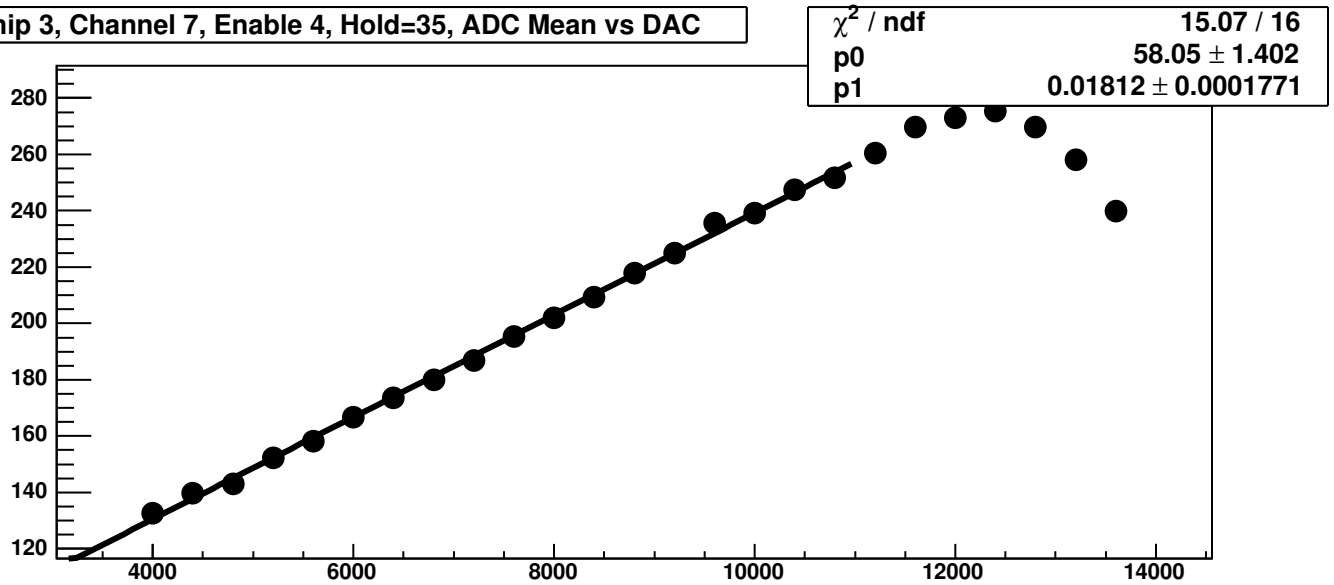

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.05 / 16
p0 65.26 ± 1.46
p1 0.01702 ± 0.0001901
280
260
240
220
200
180
160
140
4000
6000
8000
10000
12000
14000

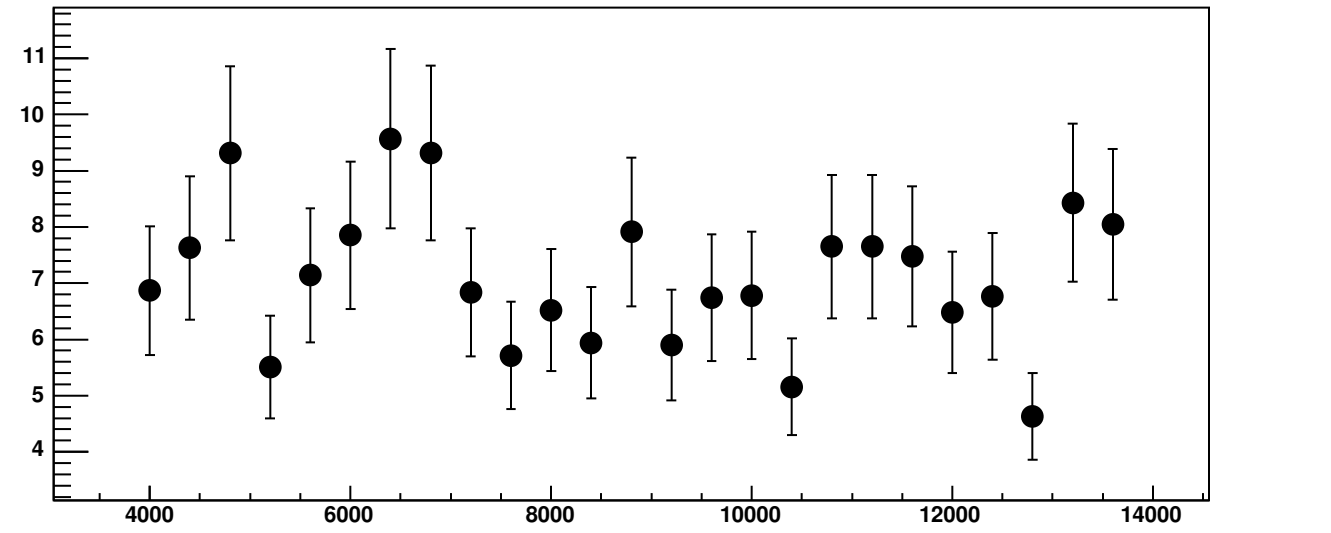

Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

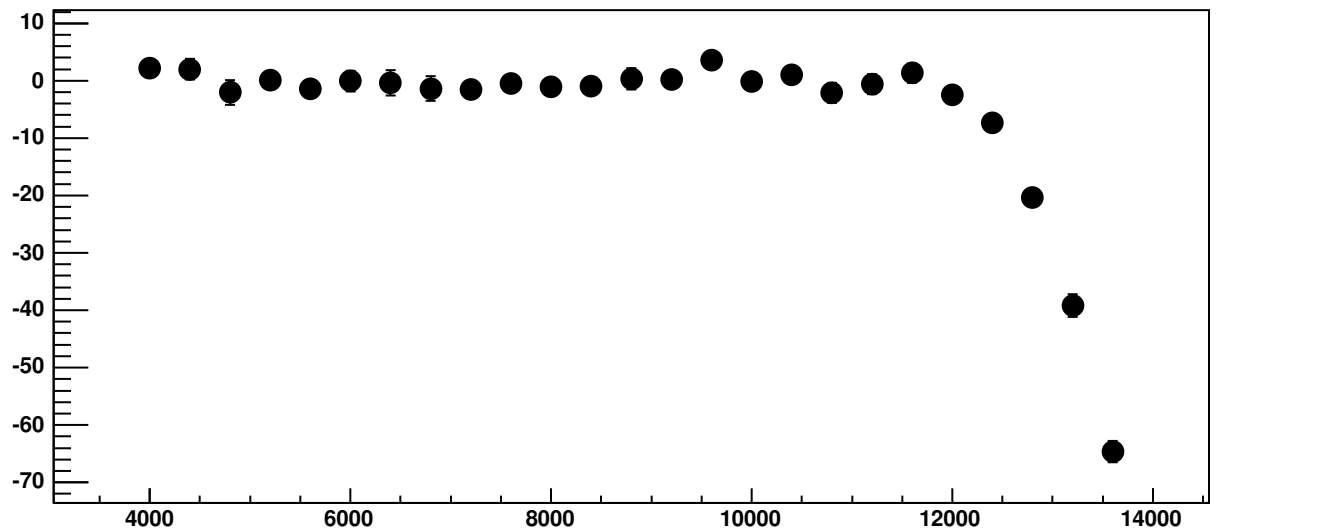
Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

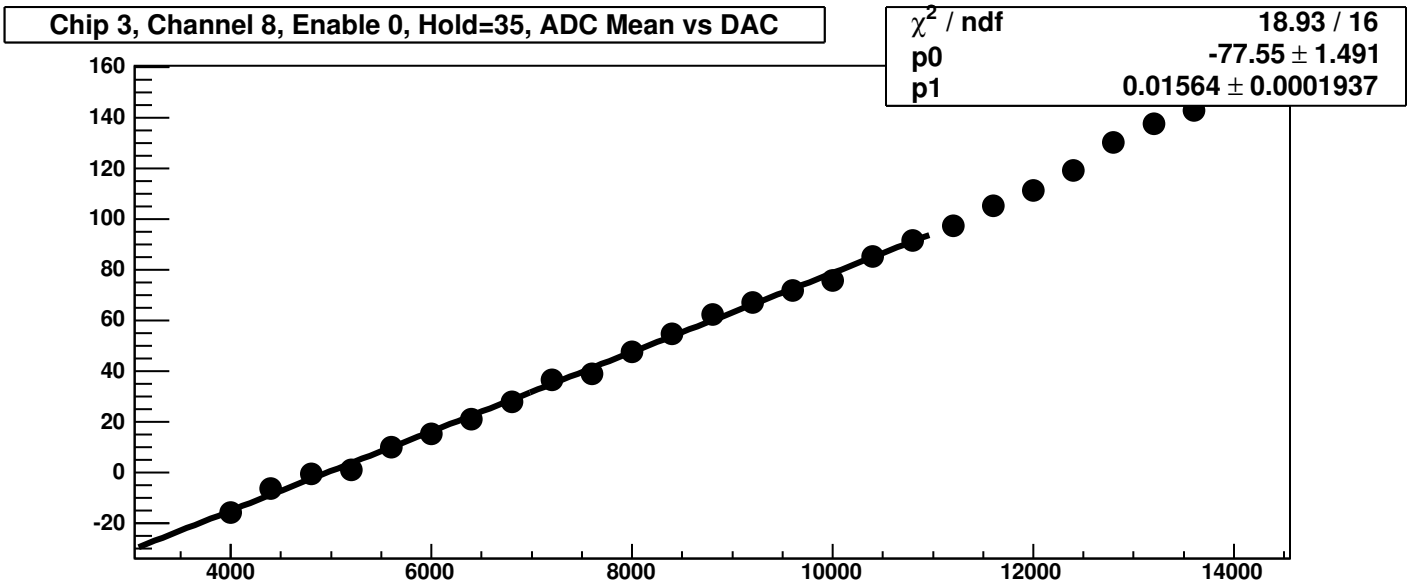


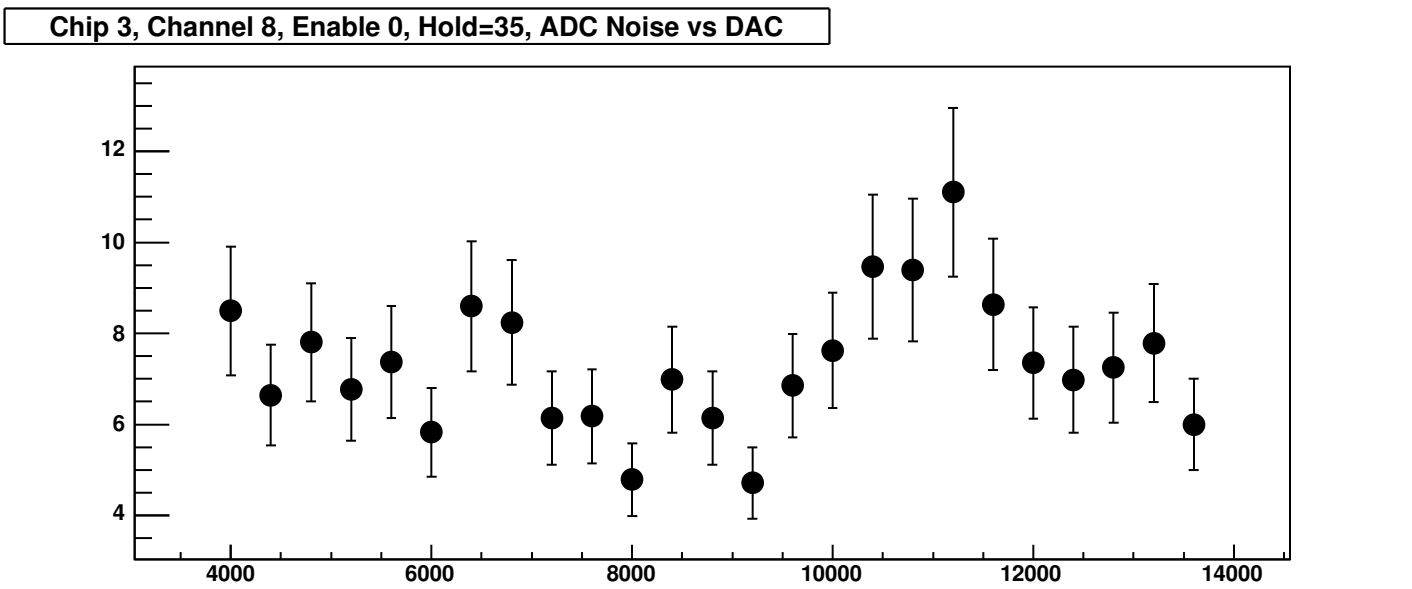
Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

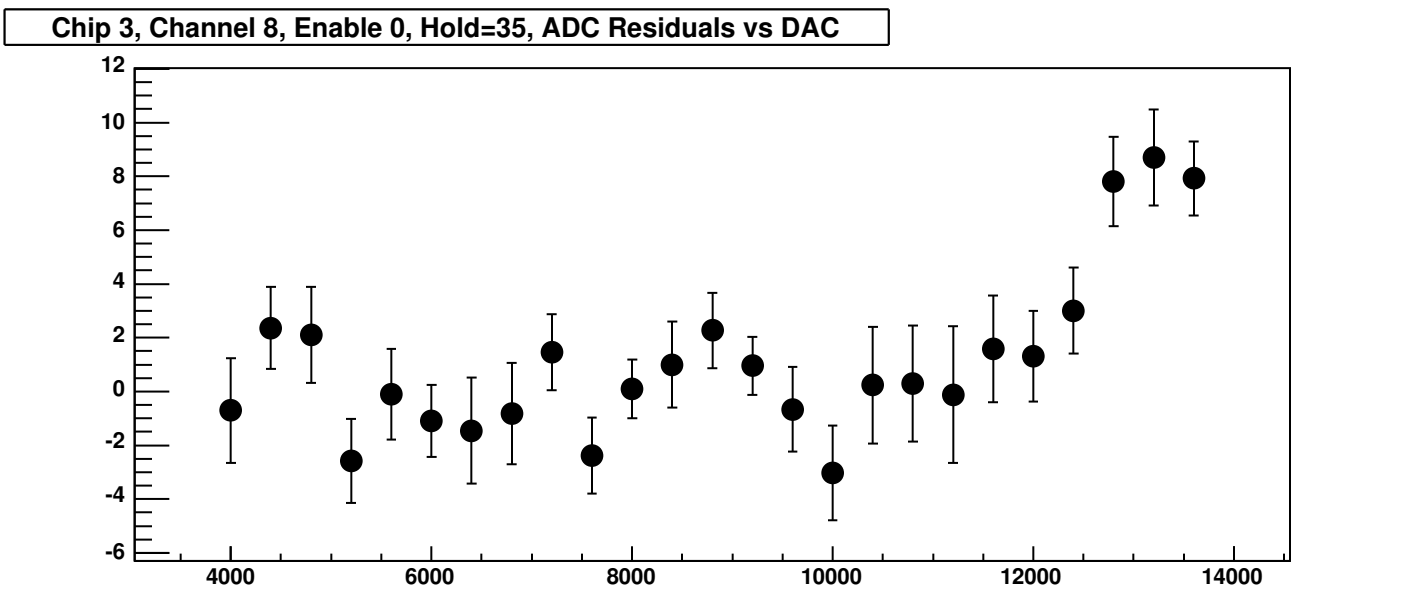


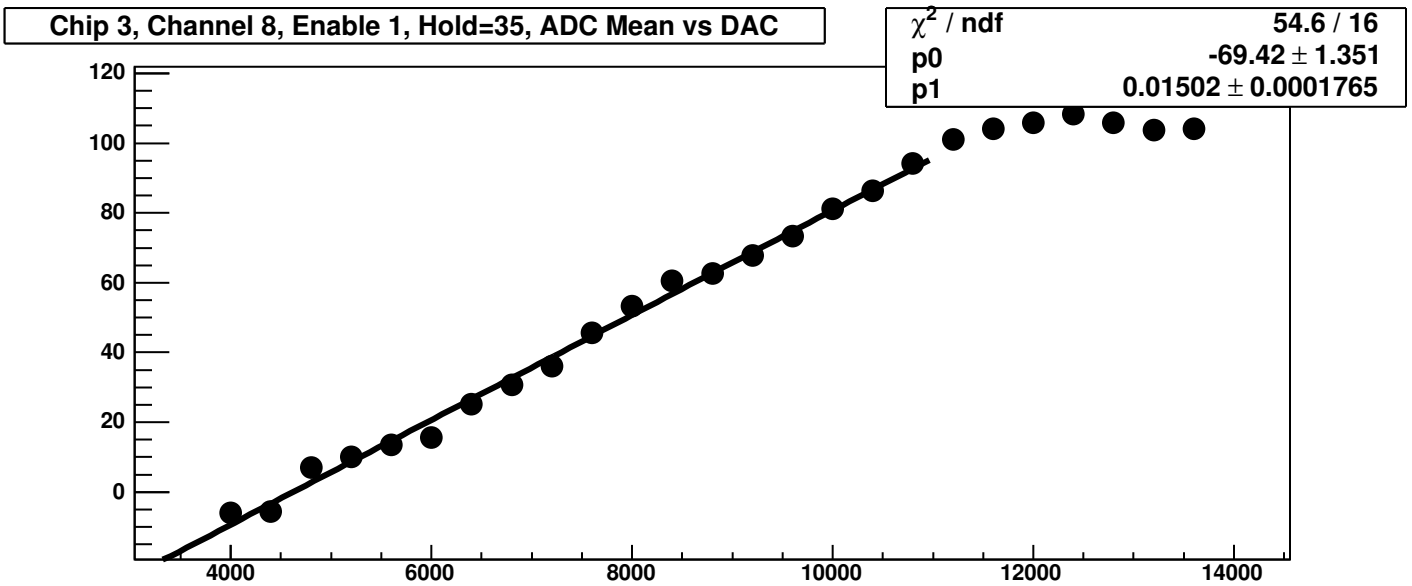
Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

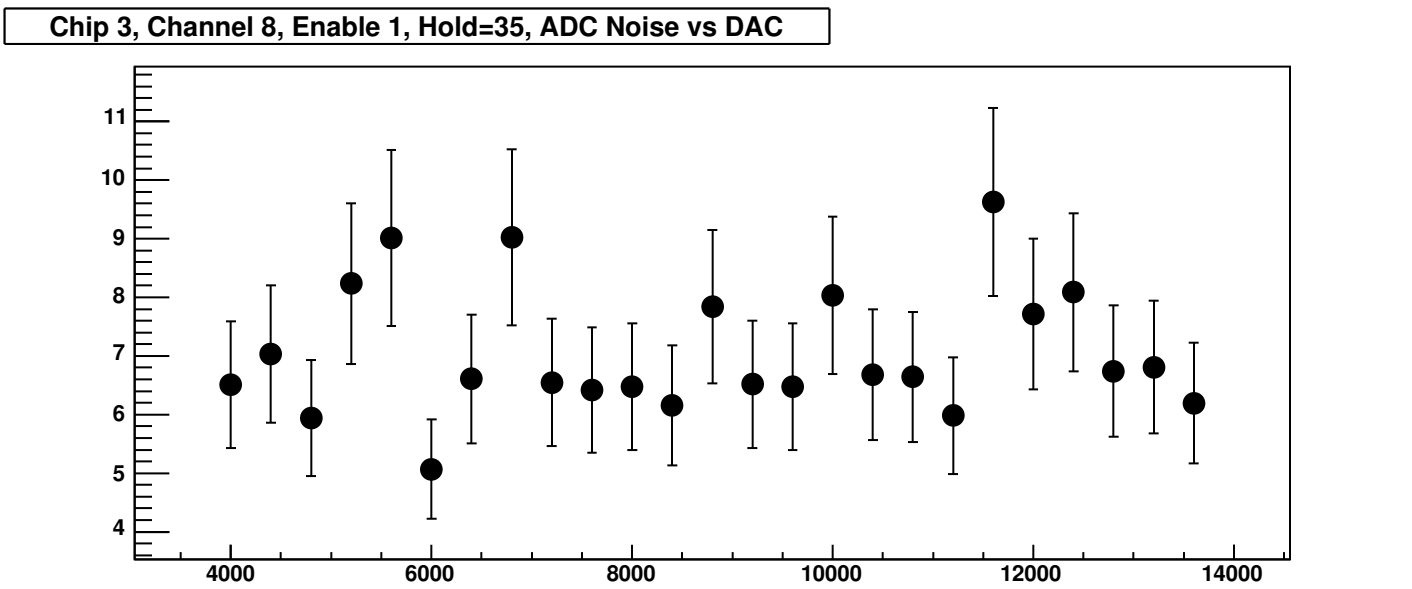


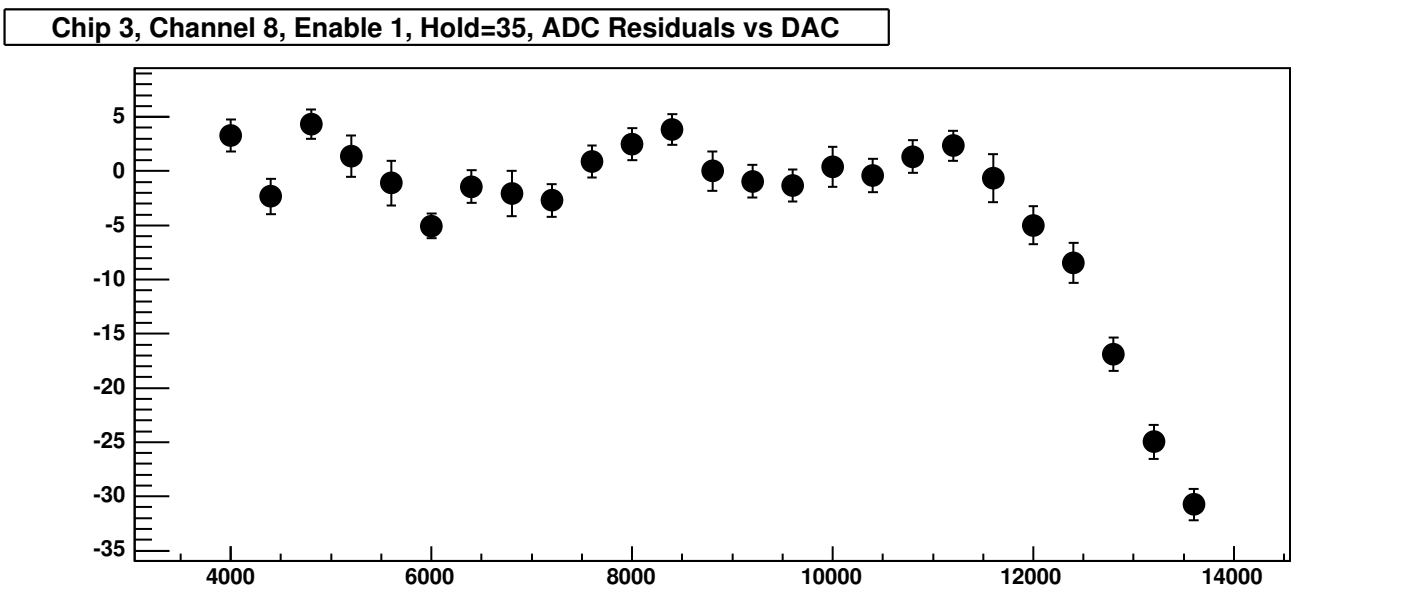

Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18.93 / 16
p0 -77.55 ± 1.491
p1 0.01564 ± 0.0001937
160
140
120
100
80
60
40
20
0
-20
4000
6000
8000
10000
12000
14000

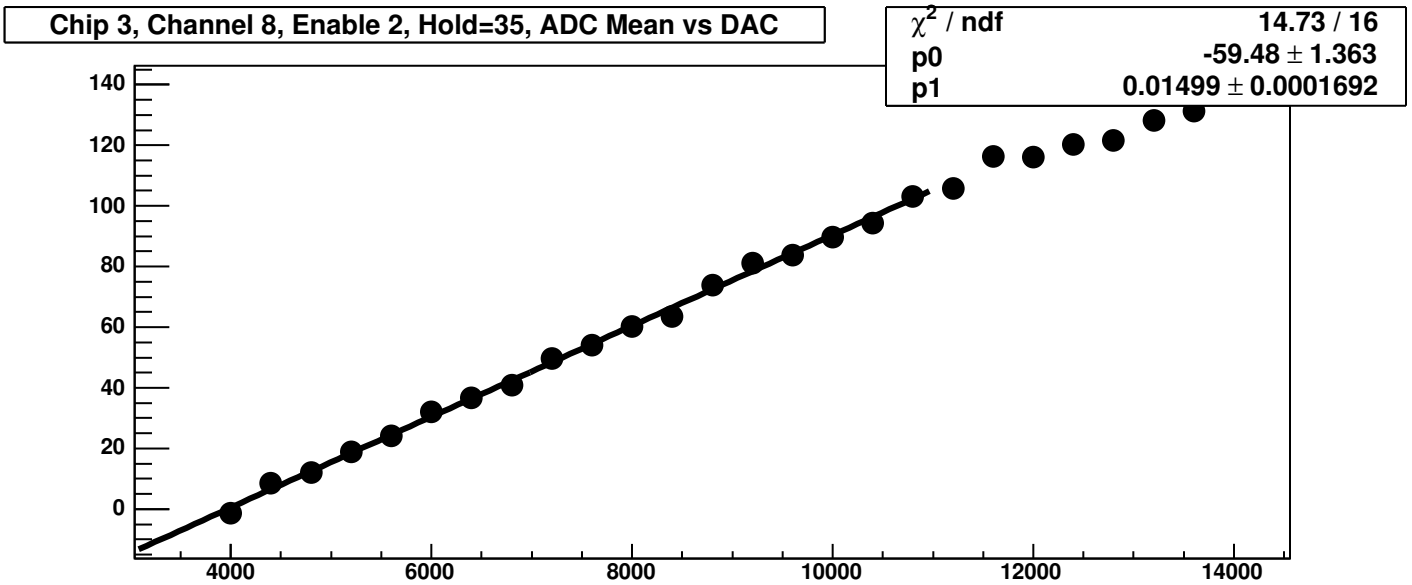

Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

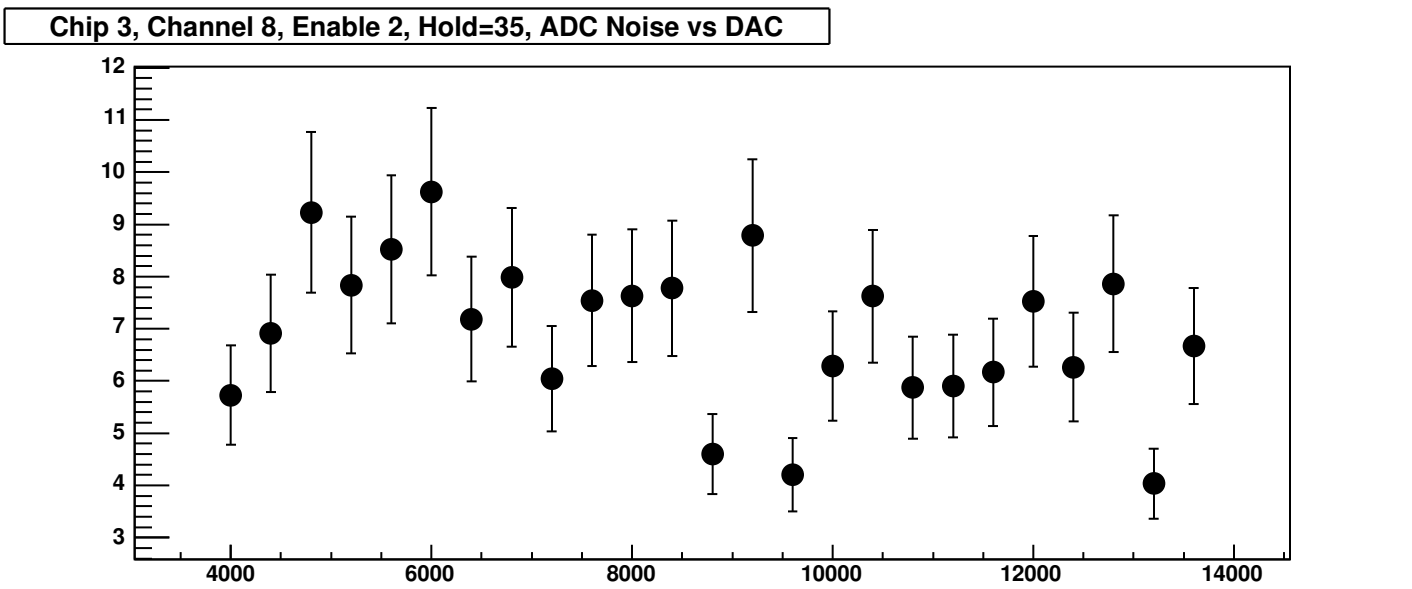

Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

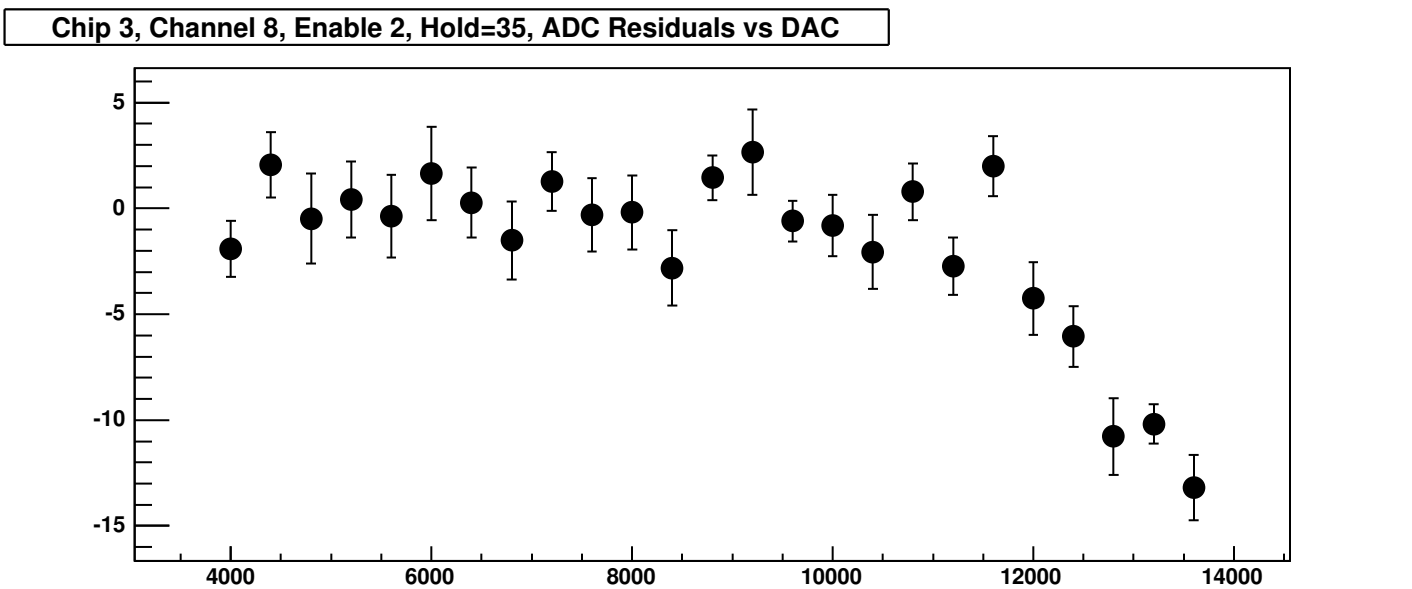

Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.6 / 16
p0 -69.42 ± 1.351
p1 0.01502 ± 0.0001765
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

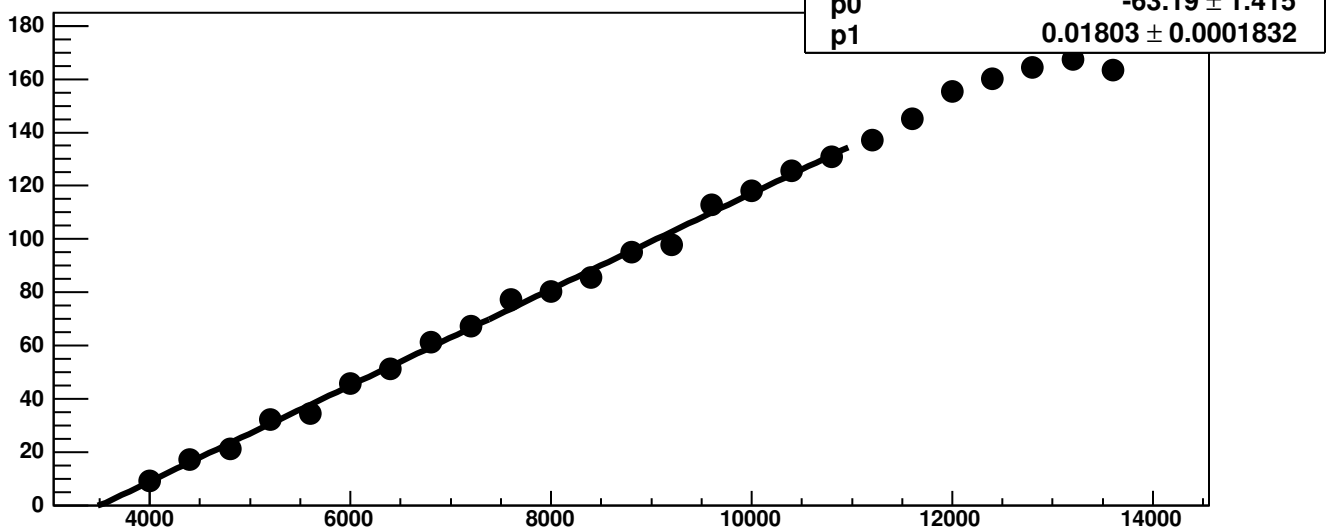

Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 14.73 / 16
p0 -59.48 ± 1.363
p1 0.01499 ± 0.0001692
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

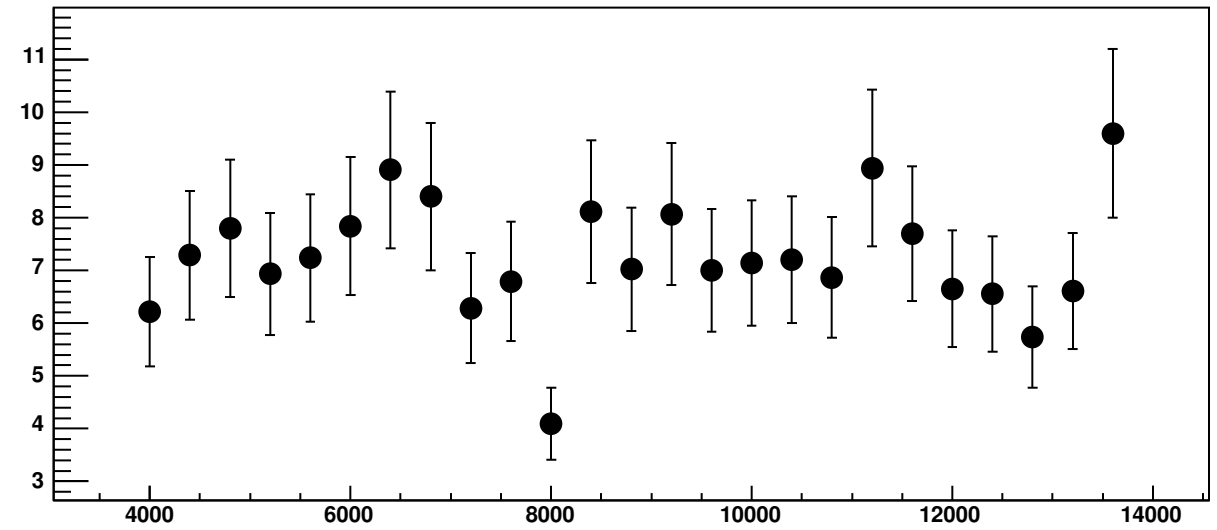

Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

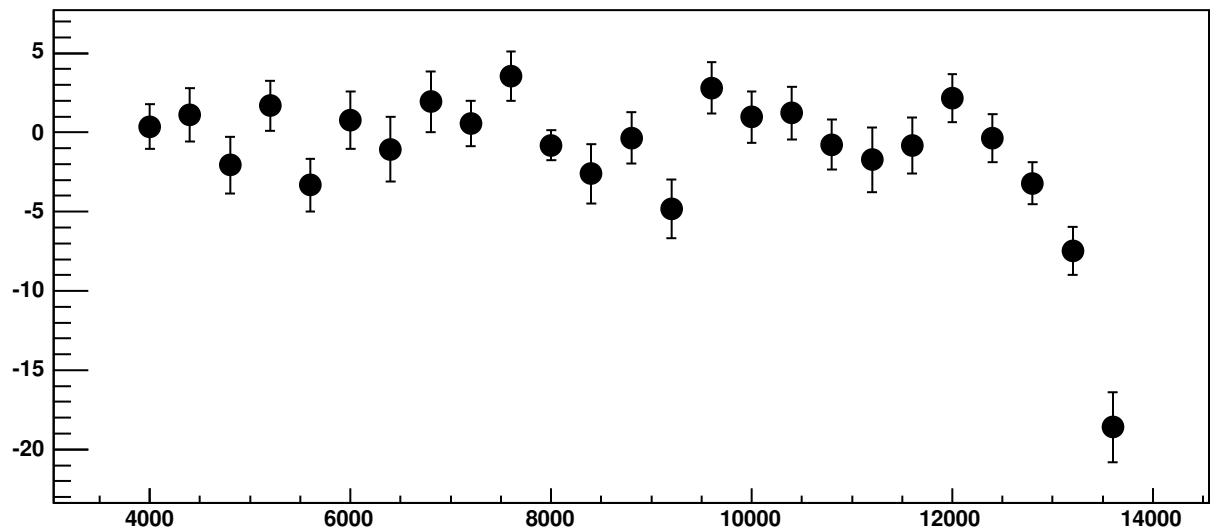
Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

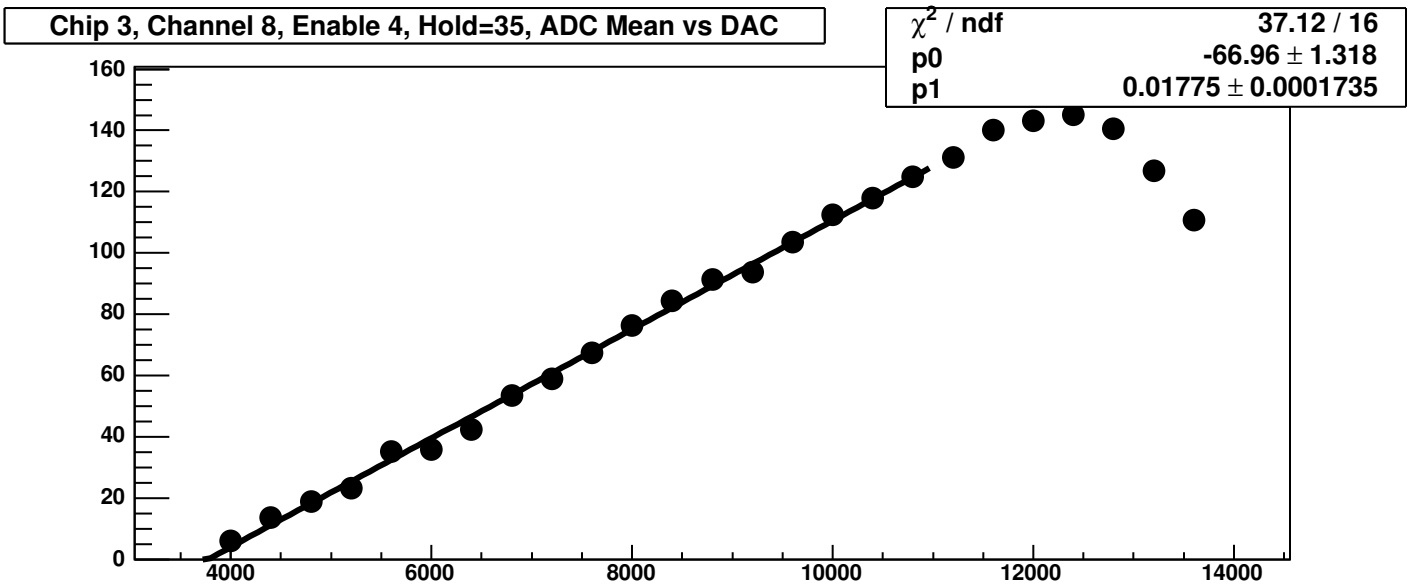


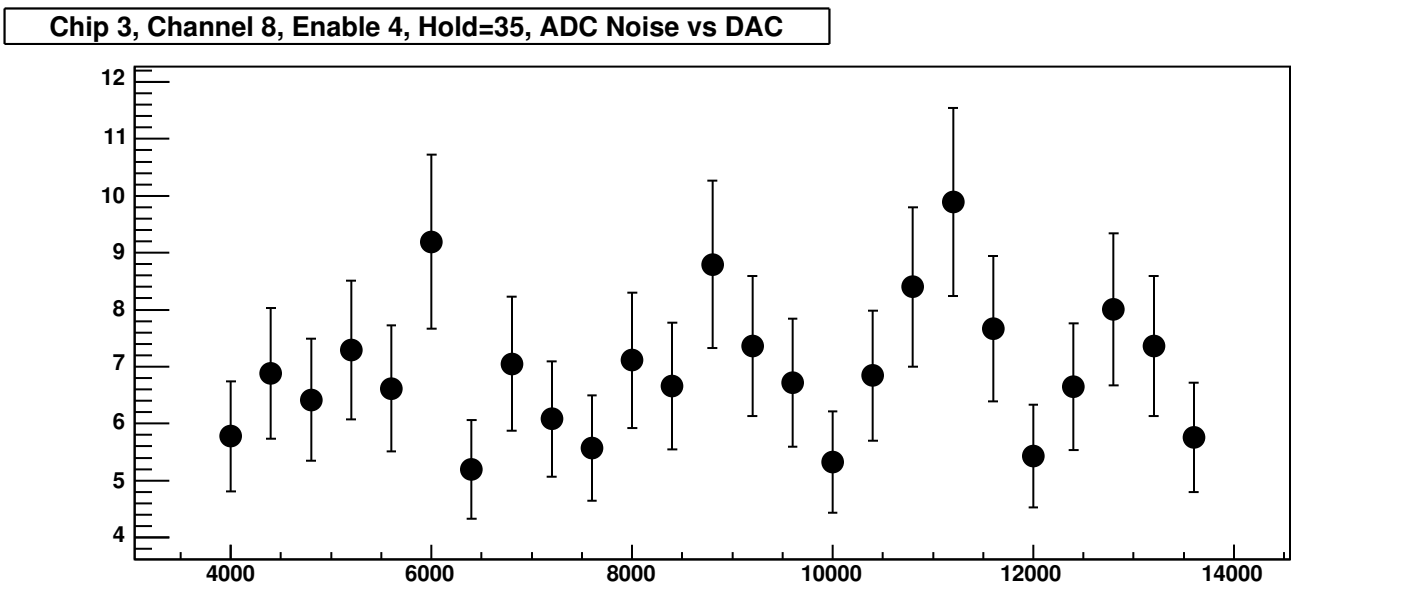
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

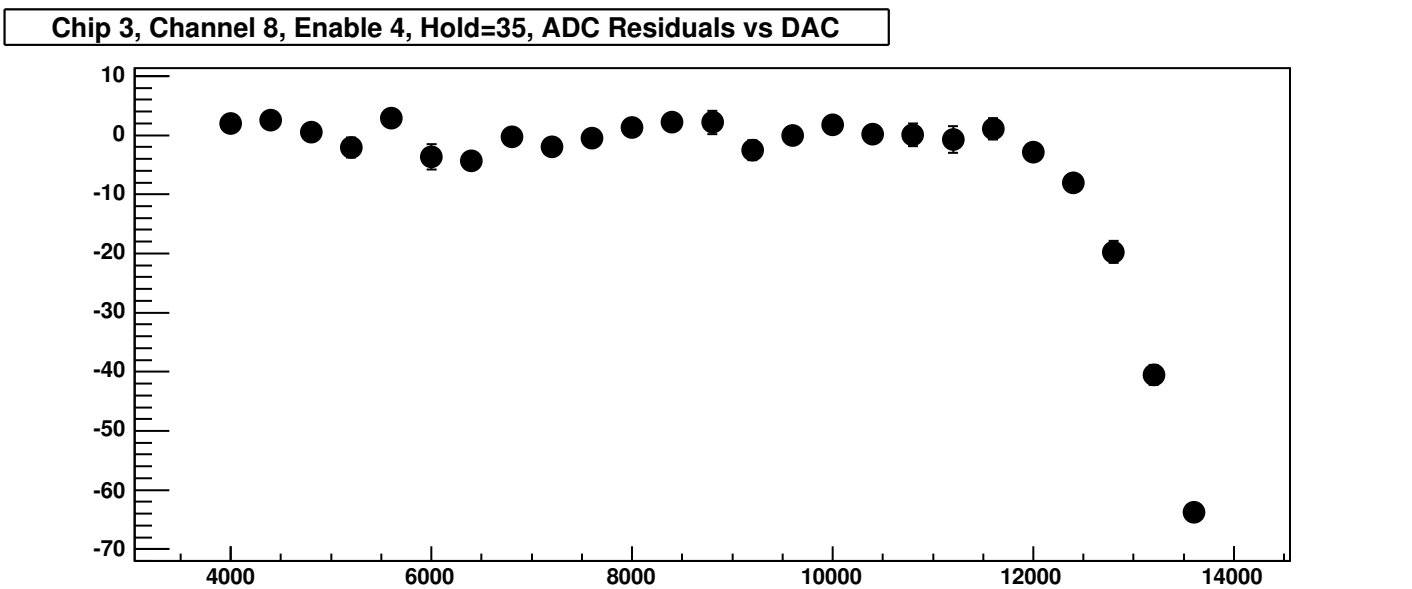


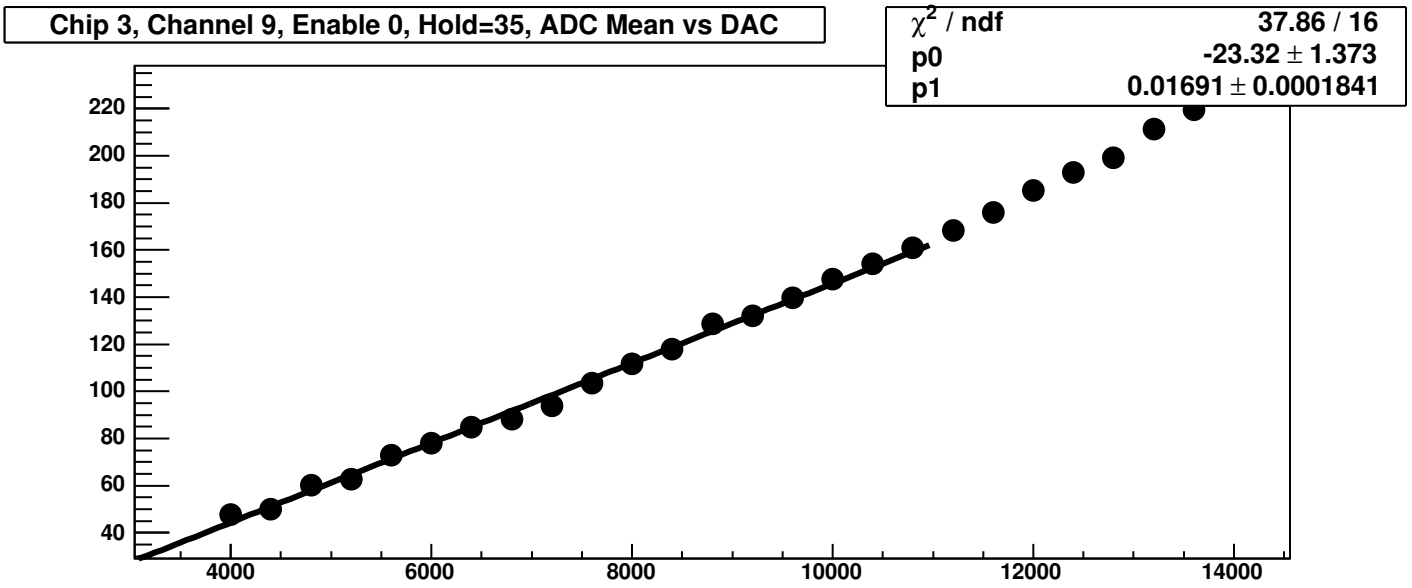
Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

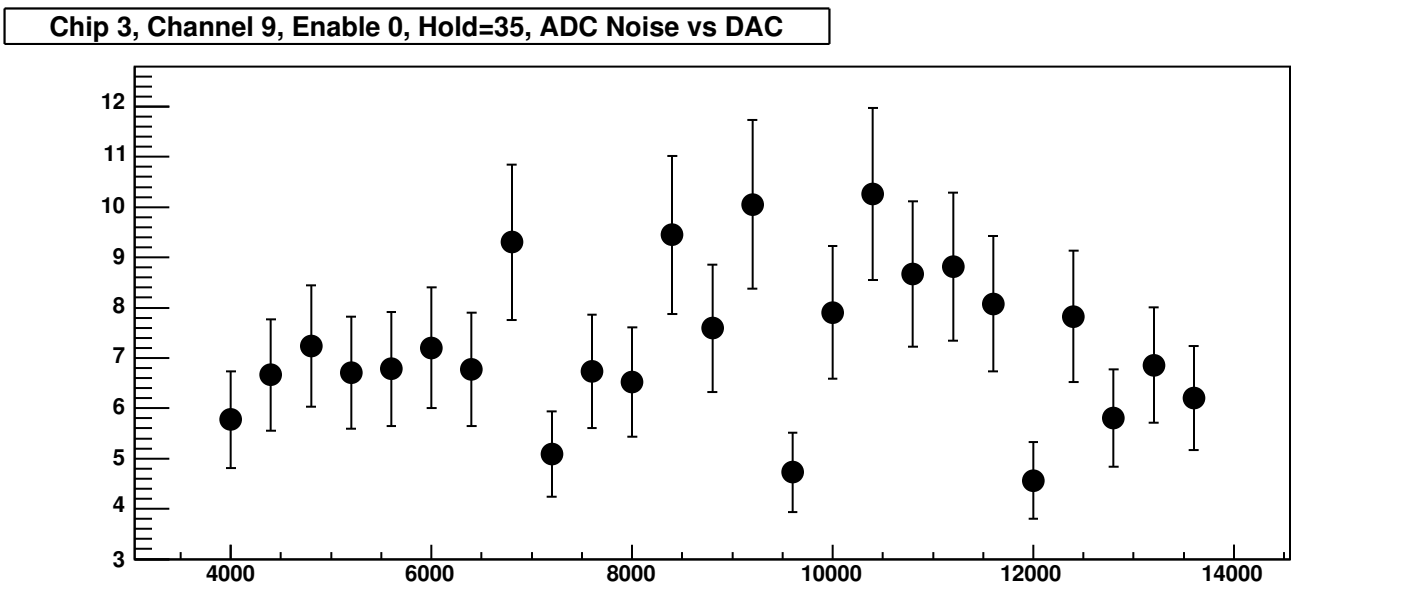


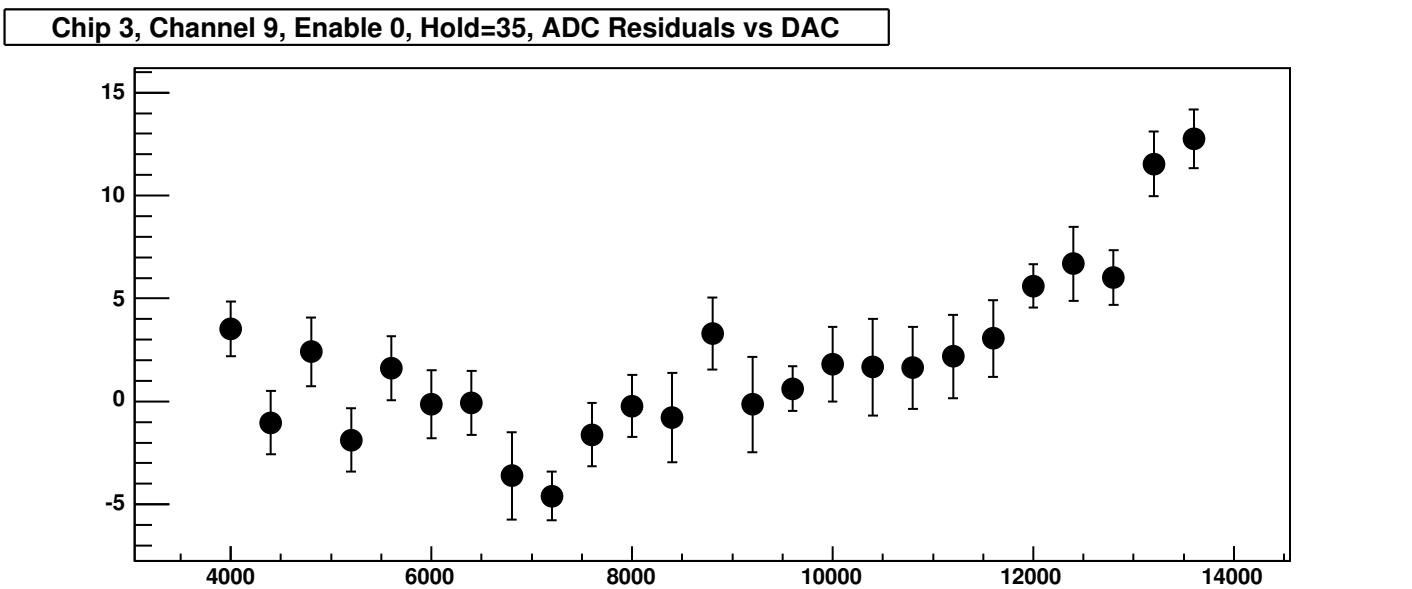

Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.12 / 16
p0 -66.96 ± 1.318
p1 0.01775 ± 0.0001735
160
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

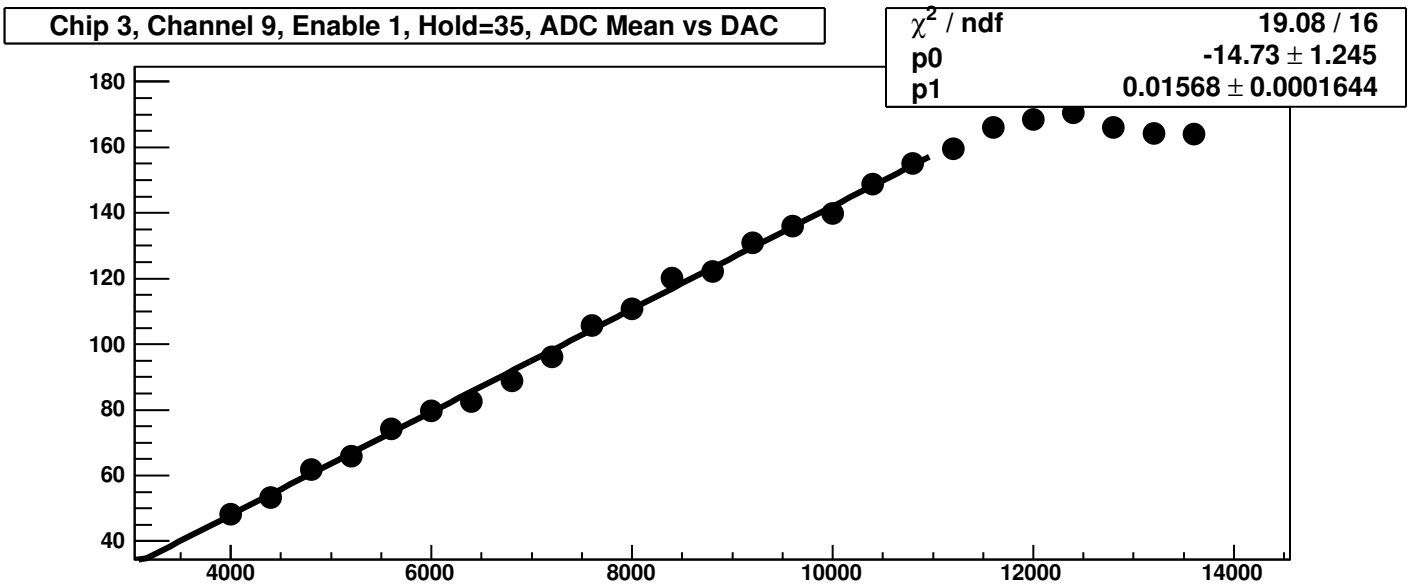

Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

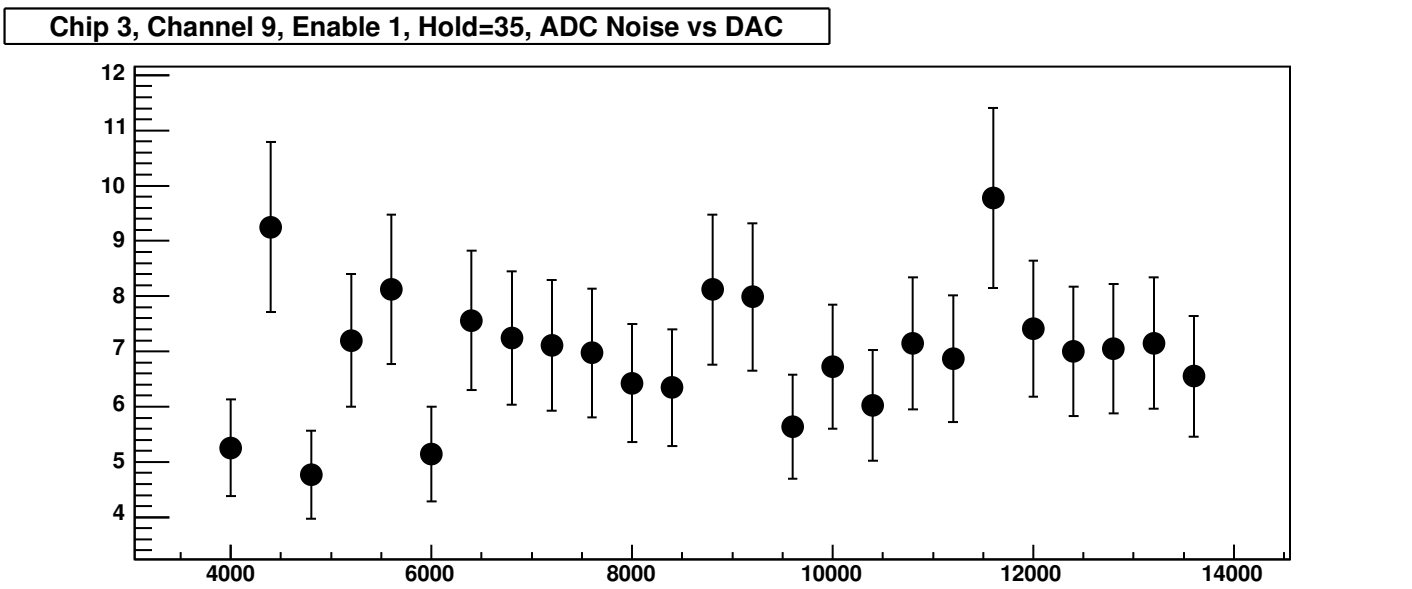

Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
4000
6000
8000
10000
12000
14000

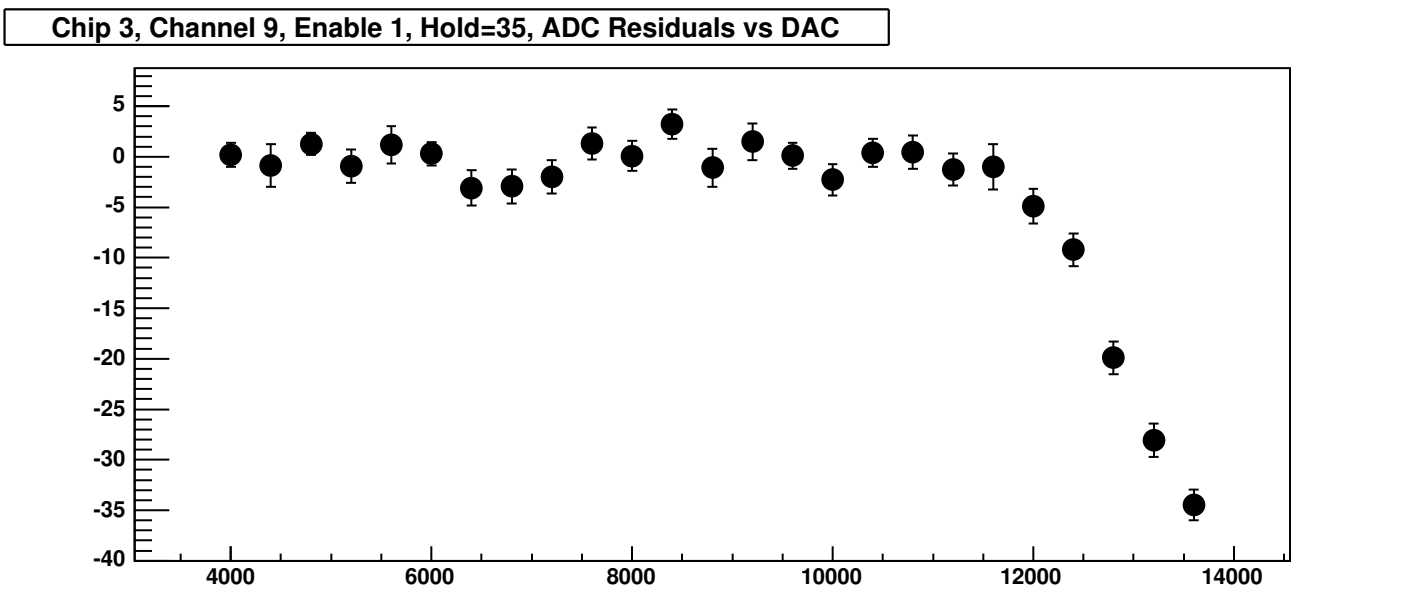

Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.86 / 16
p0 -23.32 ± 1.373
p1 0.01691 ± 0.0001841
220
200
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

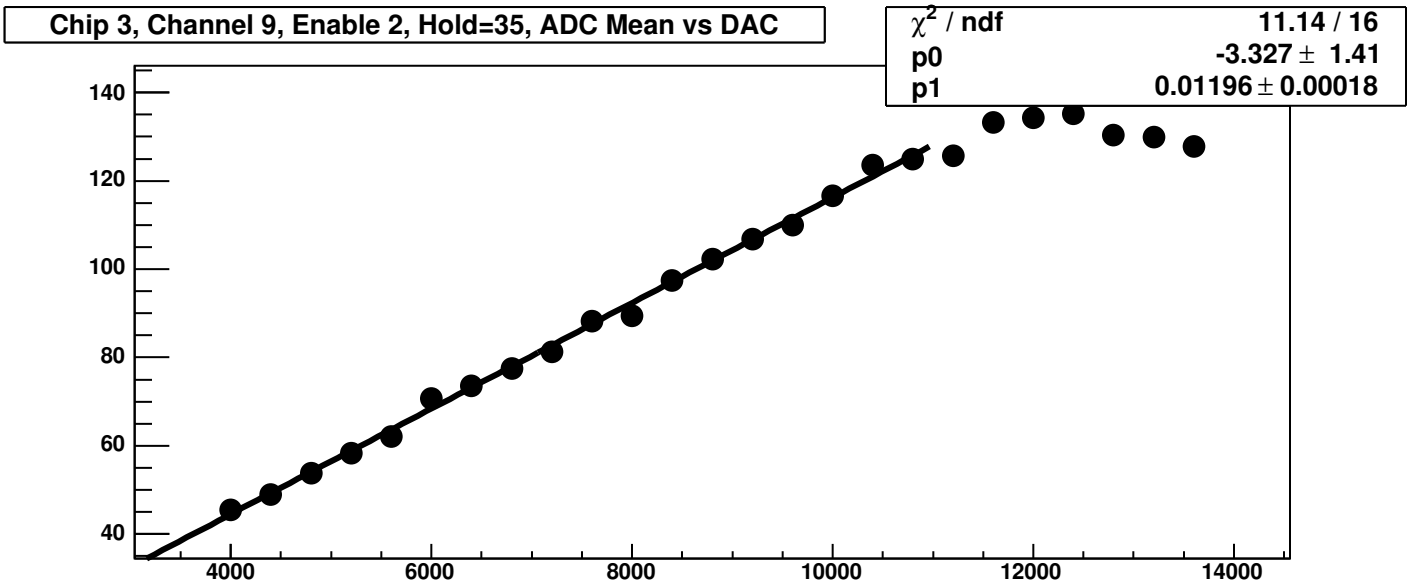

Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

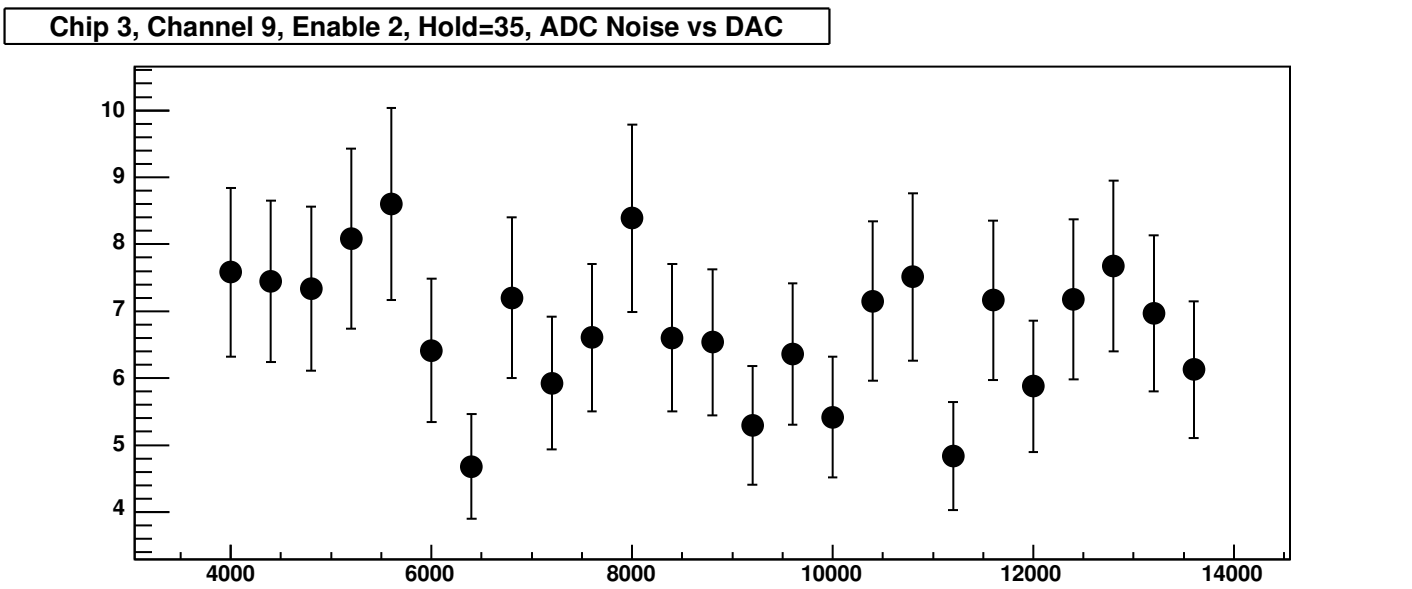

Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

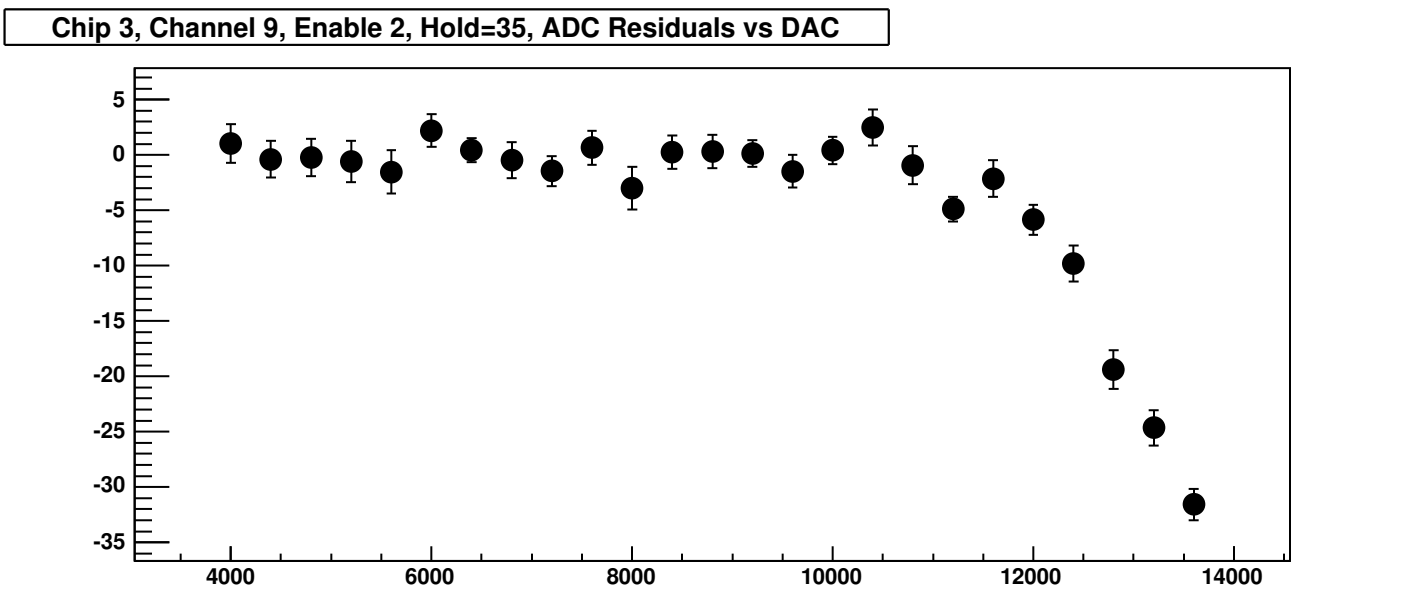

Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.08 / 16
p0 -14.73 ± 1.245
p1 0.01568 ± 0.0001644
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

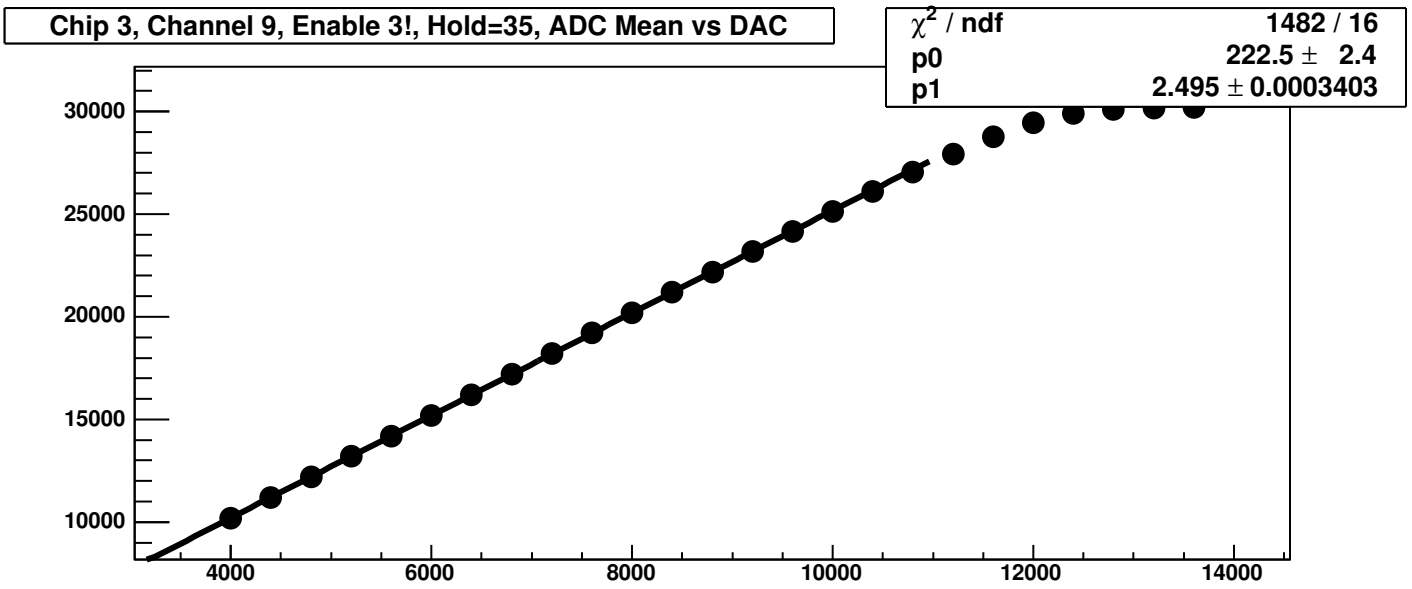

Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

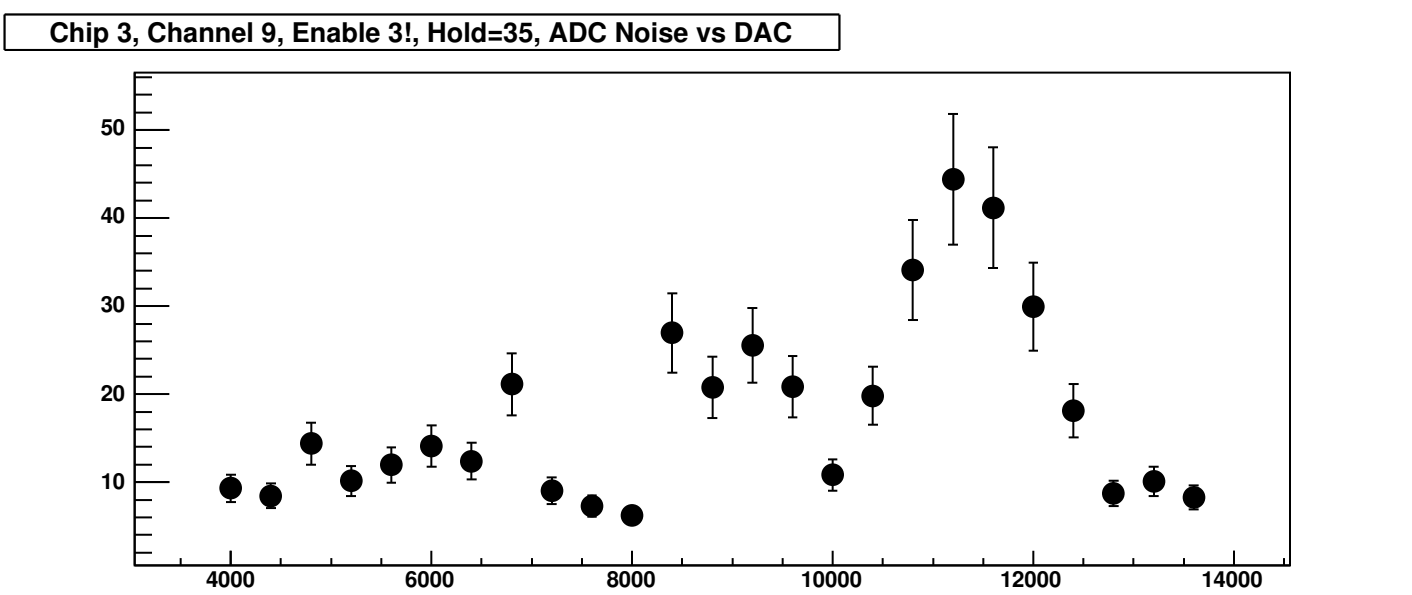

Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
-40
4000
6000
8000
10000
12000
14000

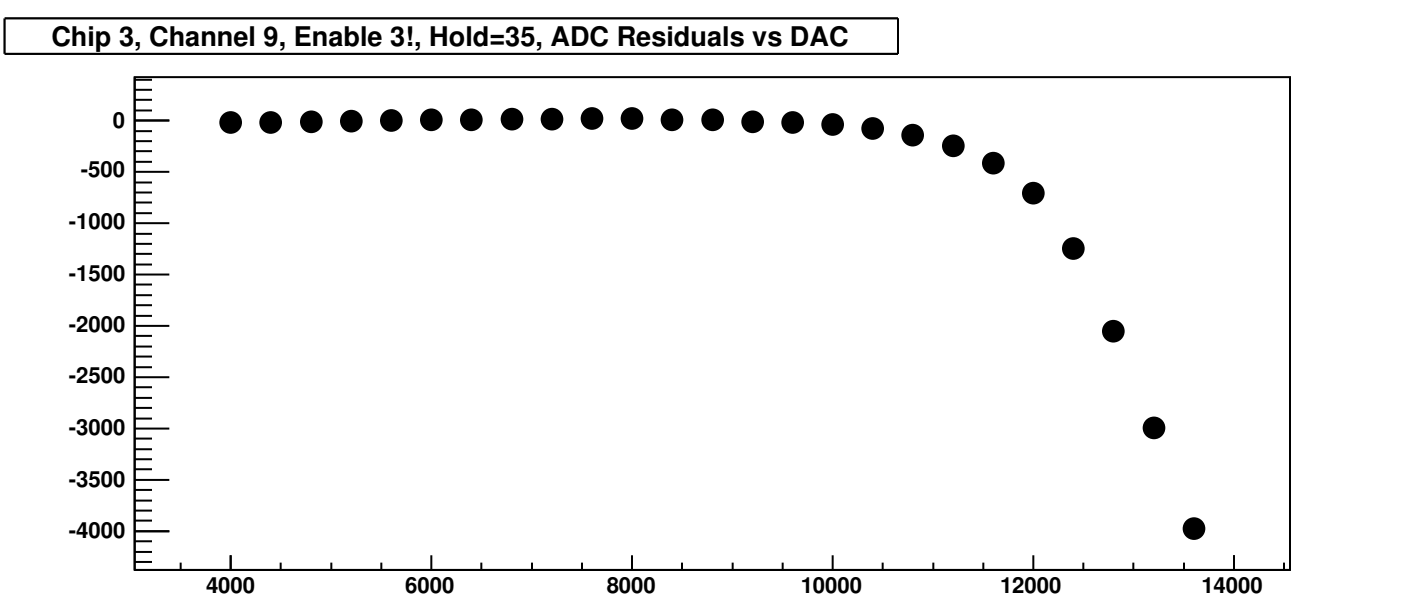

Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 11.14 / 16
p0 -3.327 ± 1.41
p1 0.01196 ± 0.00018
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

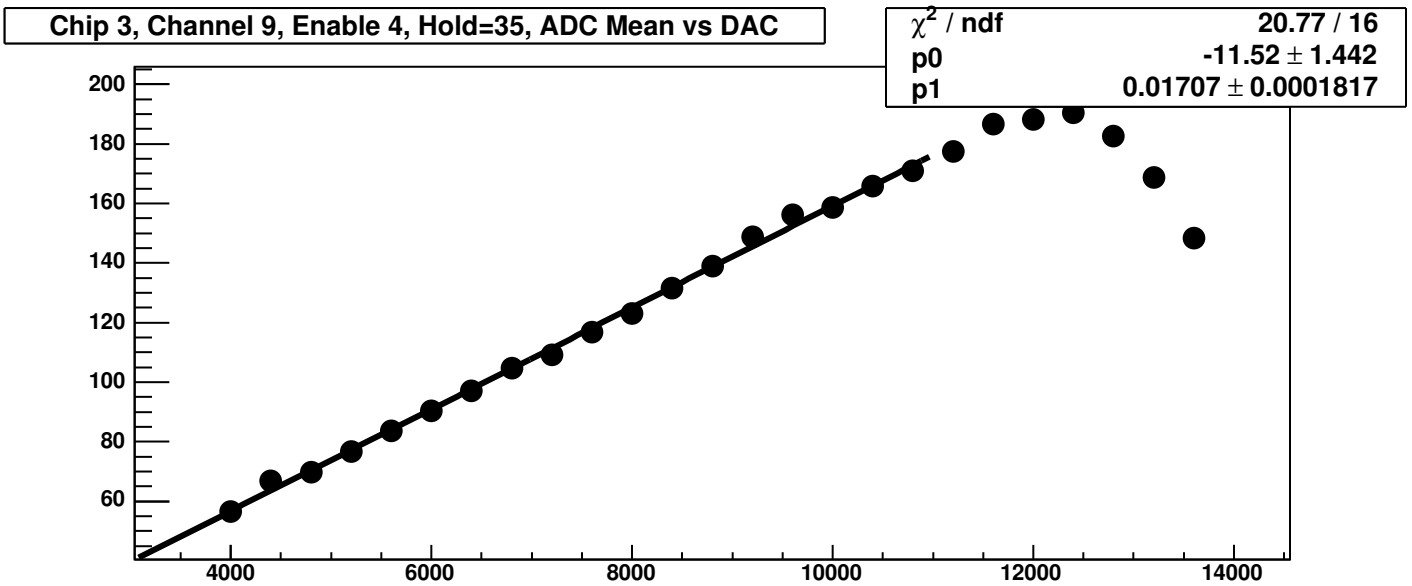

Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

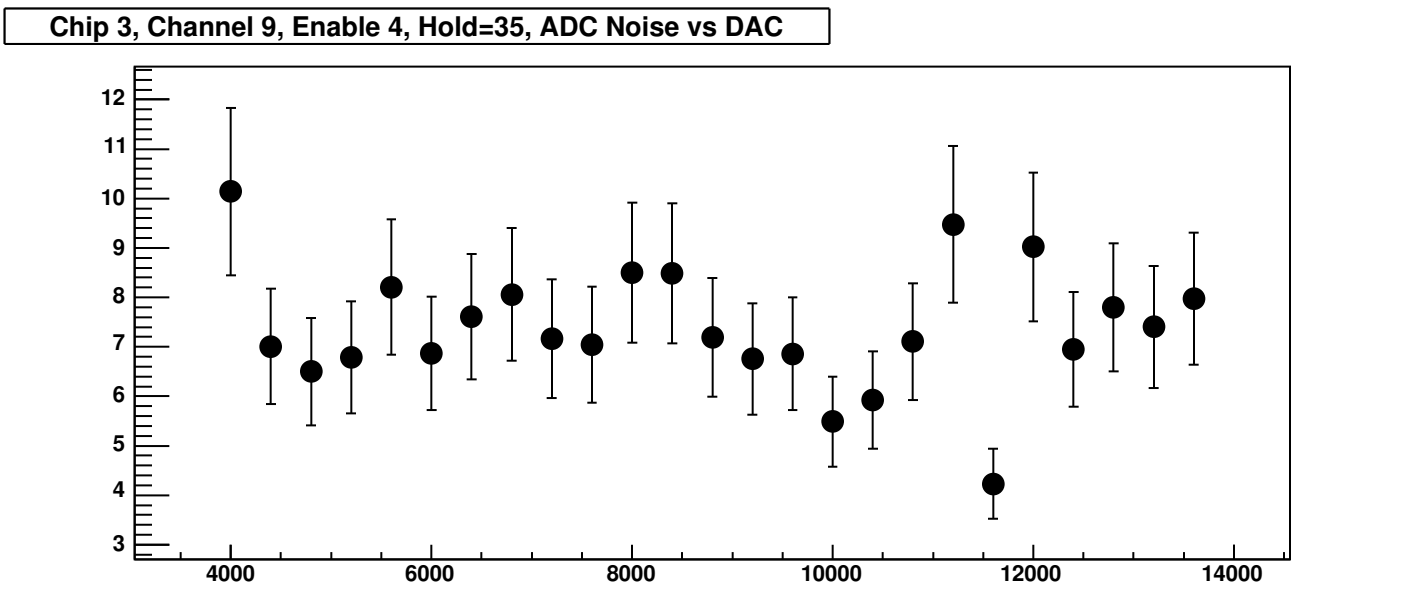

Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

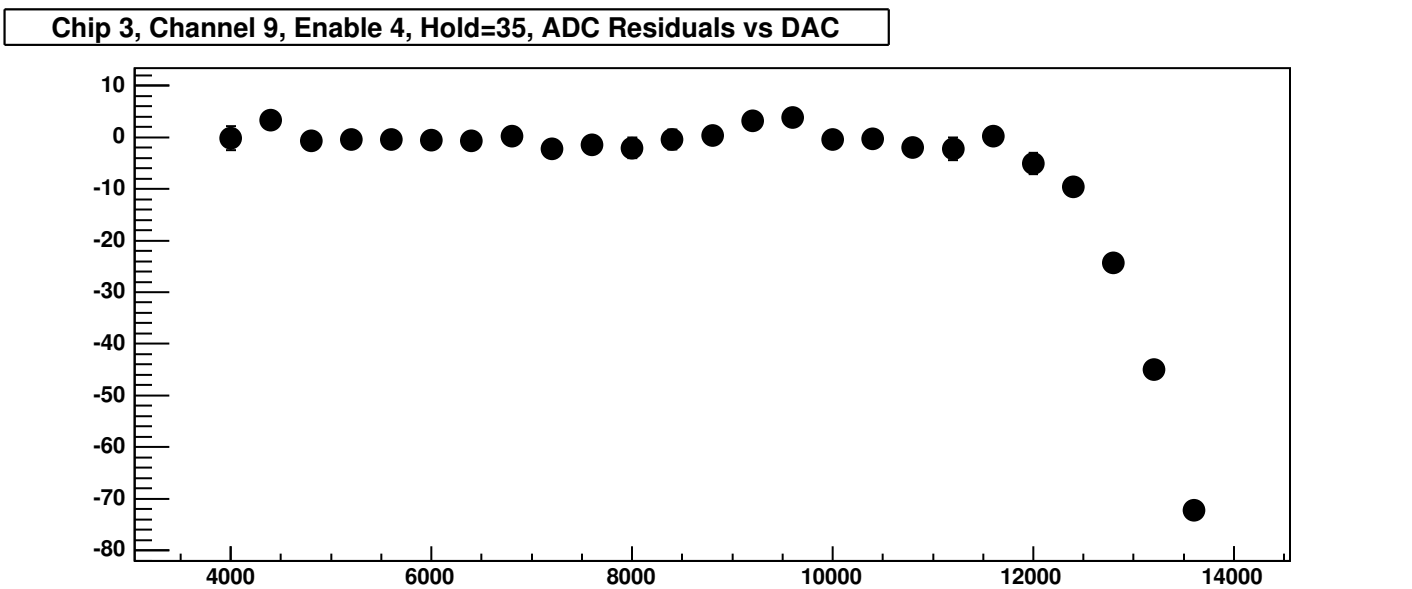

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1482 / 16
p0 222.5 ± 2.4
p1 2.495 ± 0.0003403
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

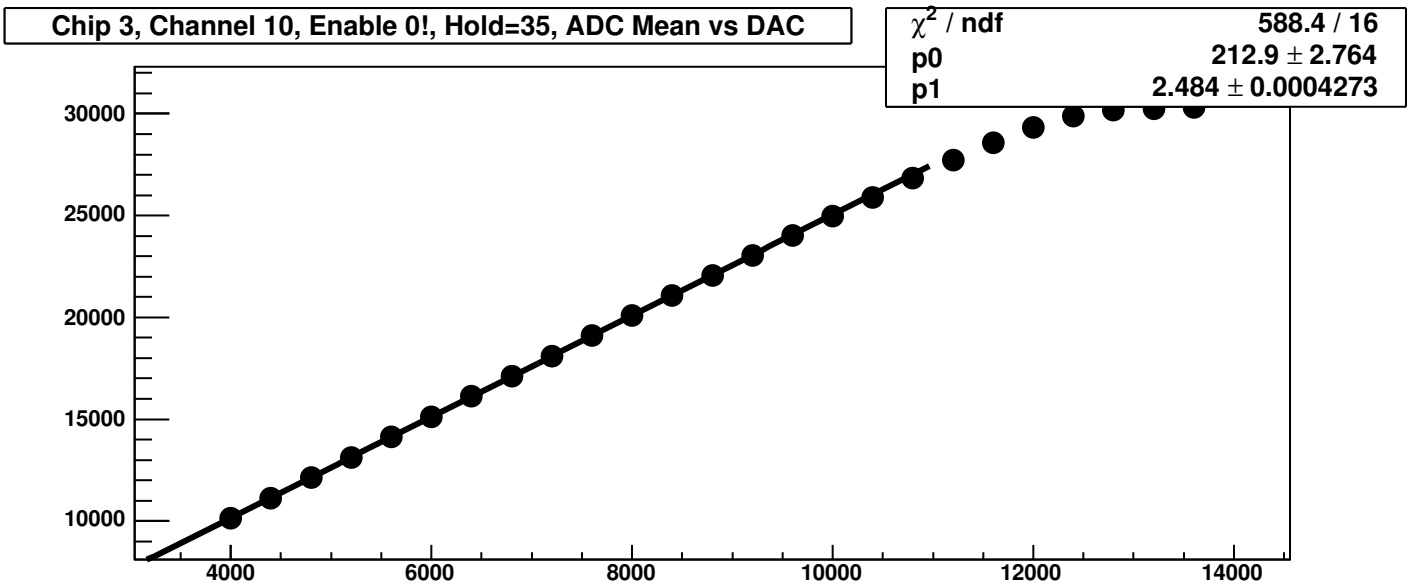

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

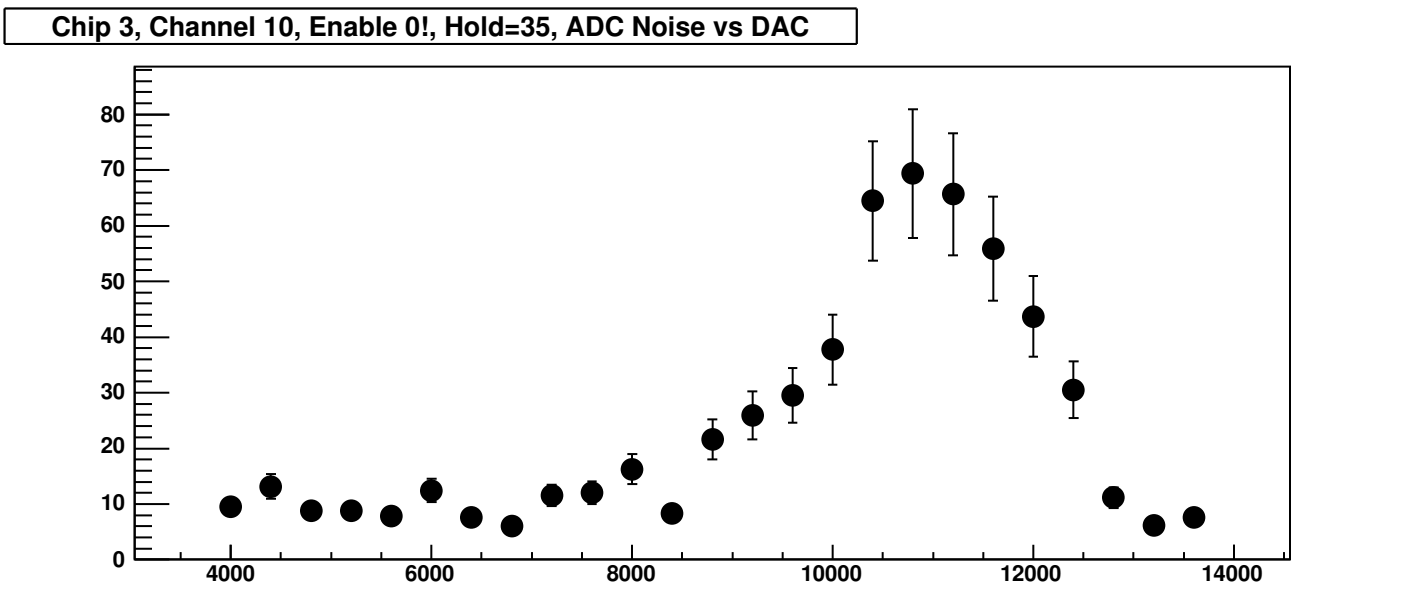

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

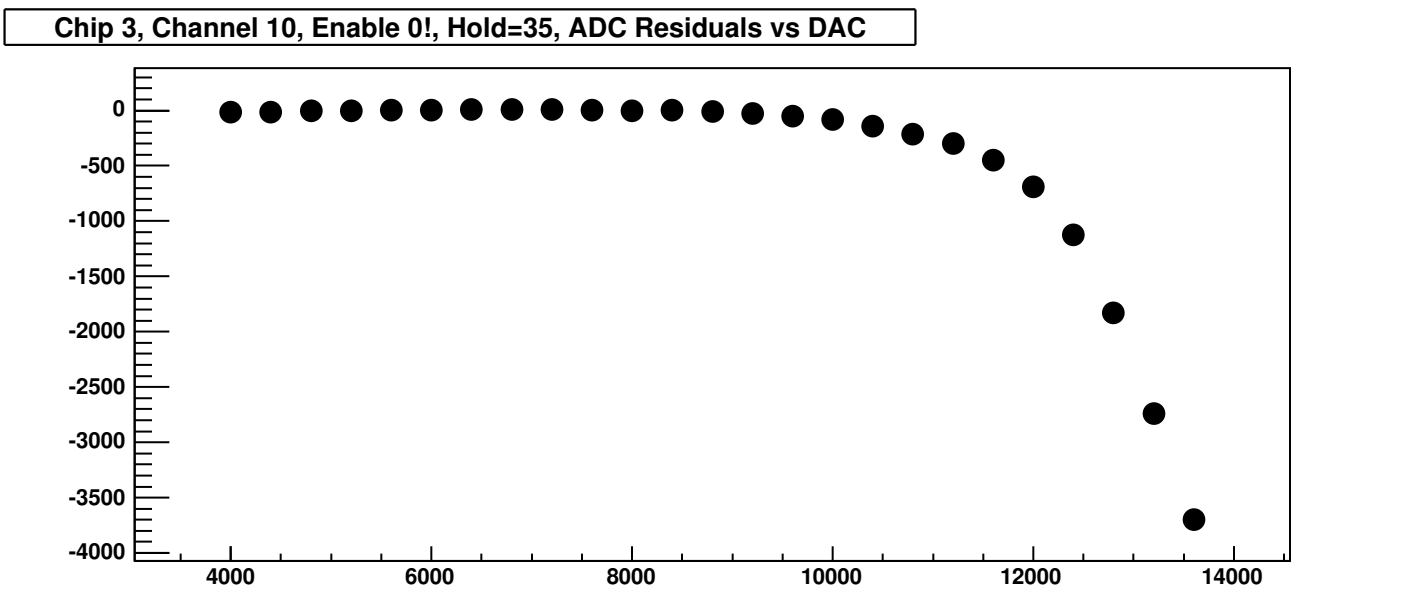

Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.77 / 16
p0 -11.52 ± 1.442
p1 0.01707 ± 0.0001817
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

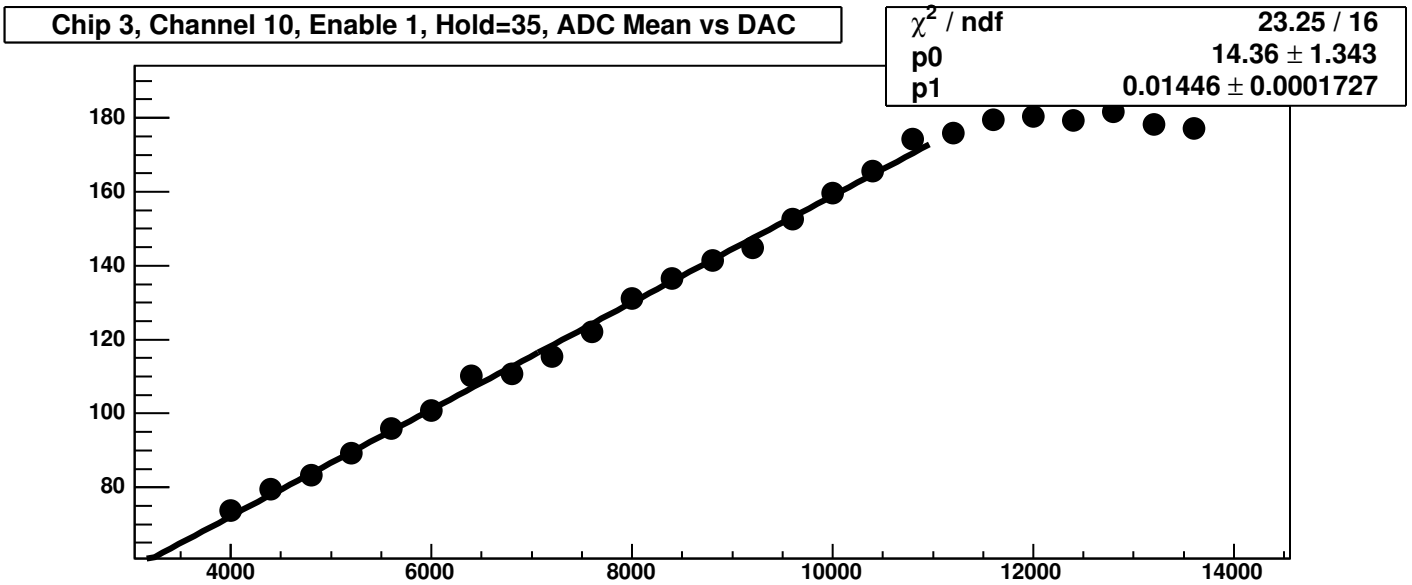

Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

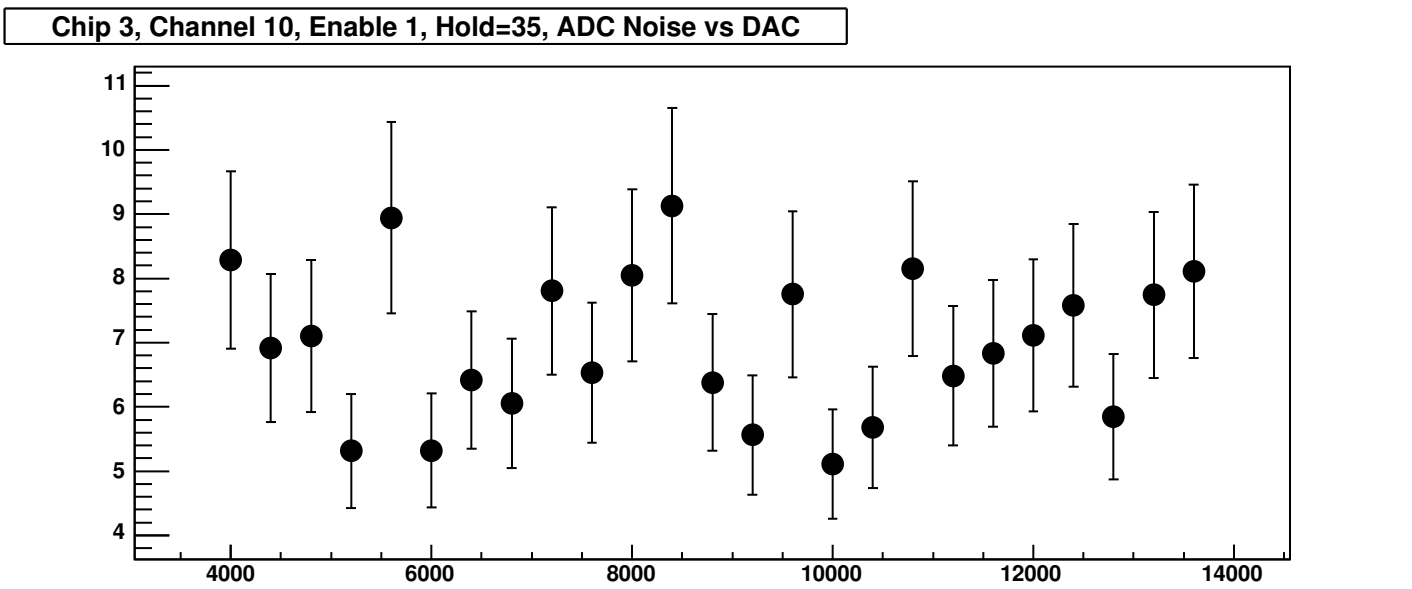

Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
-80
4000
6000
8000
10000
12000
14000

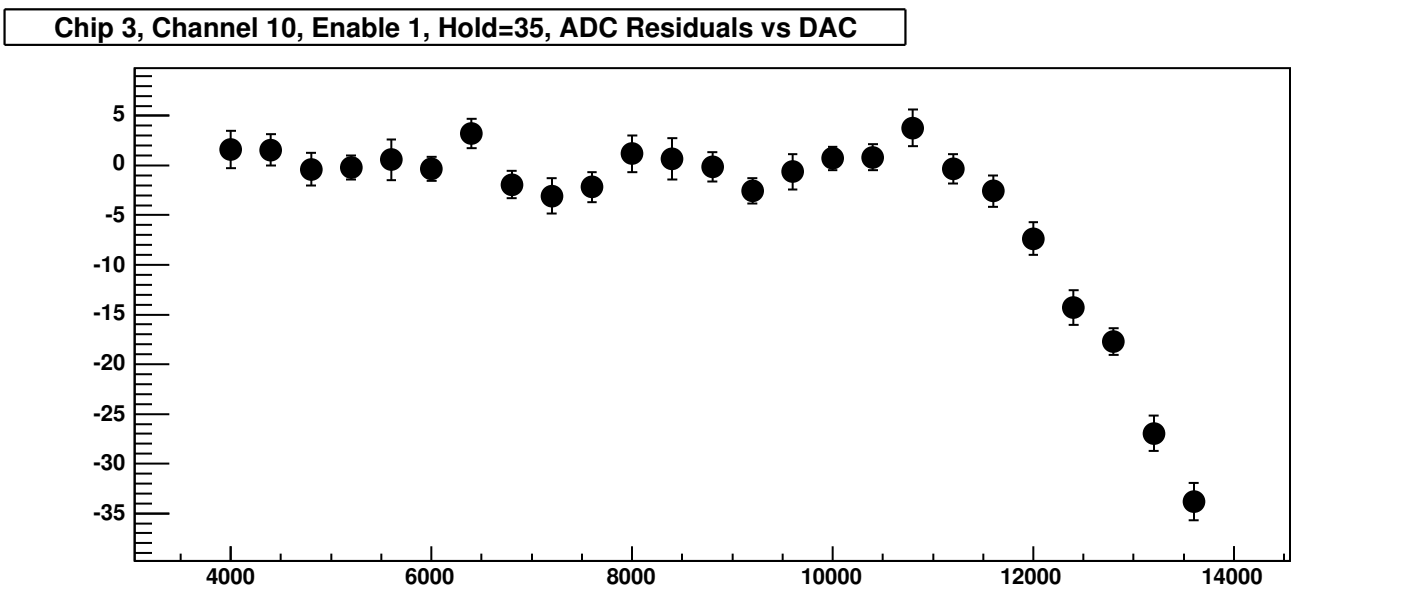

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 588.4 / 16
p0 212.9 ± 2.764
p1 2.484 ± 0.0004273
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

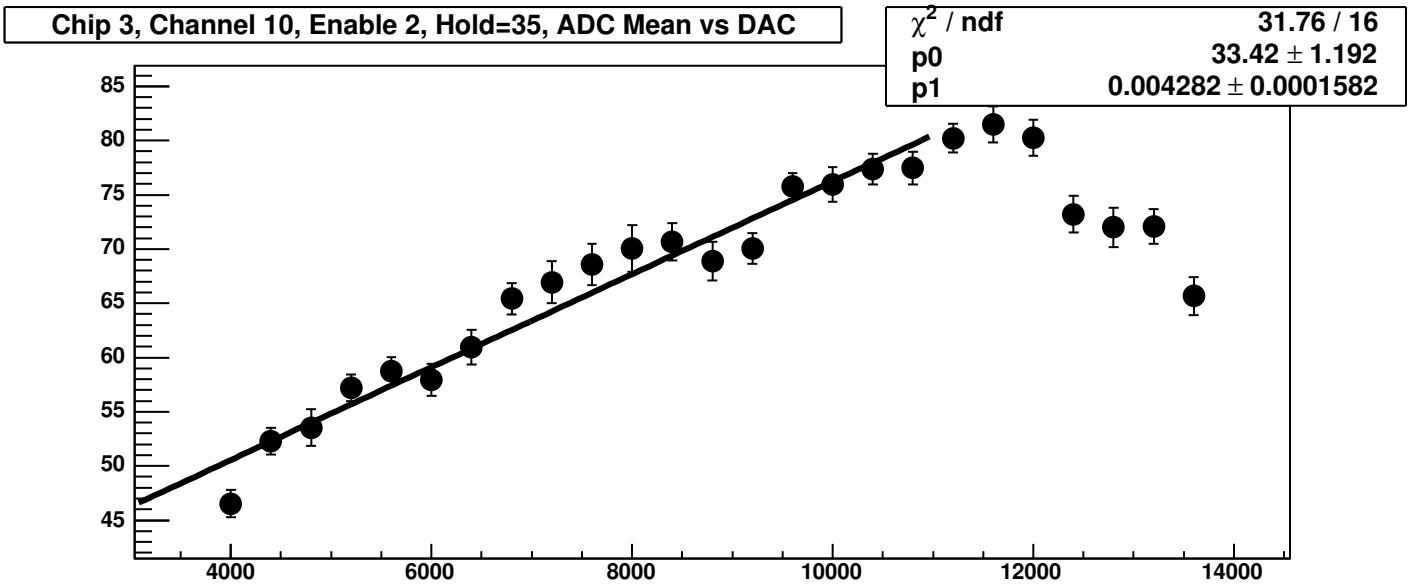

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

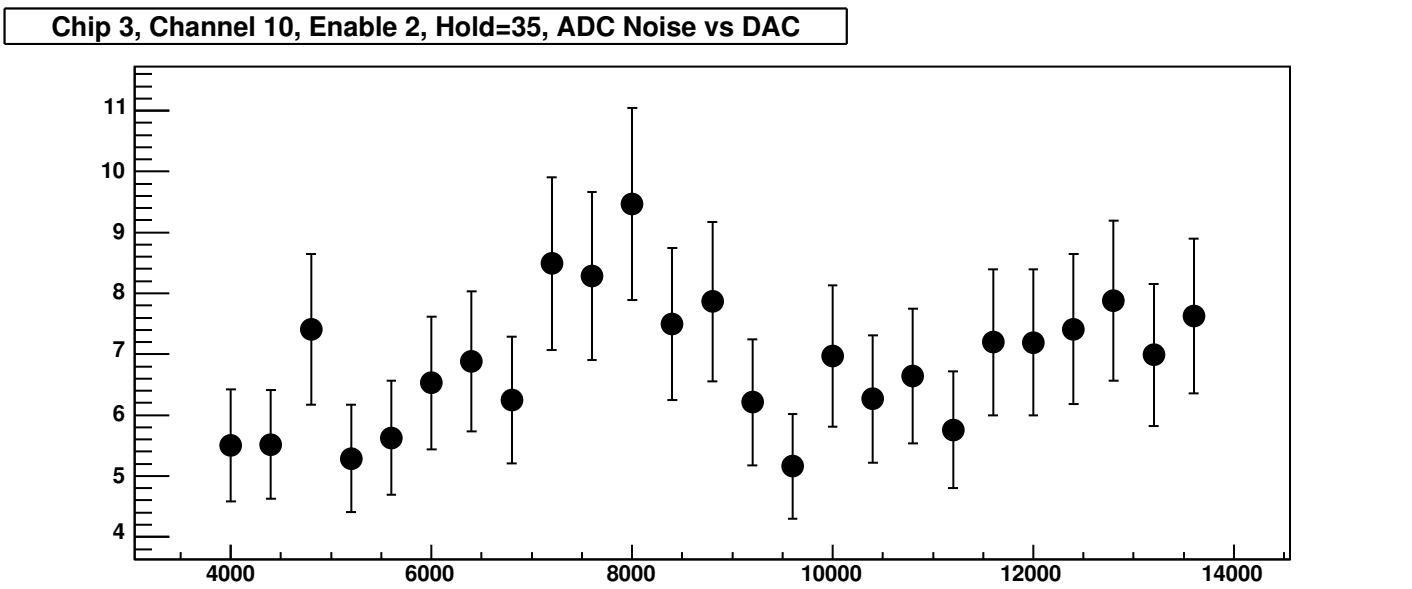

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

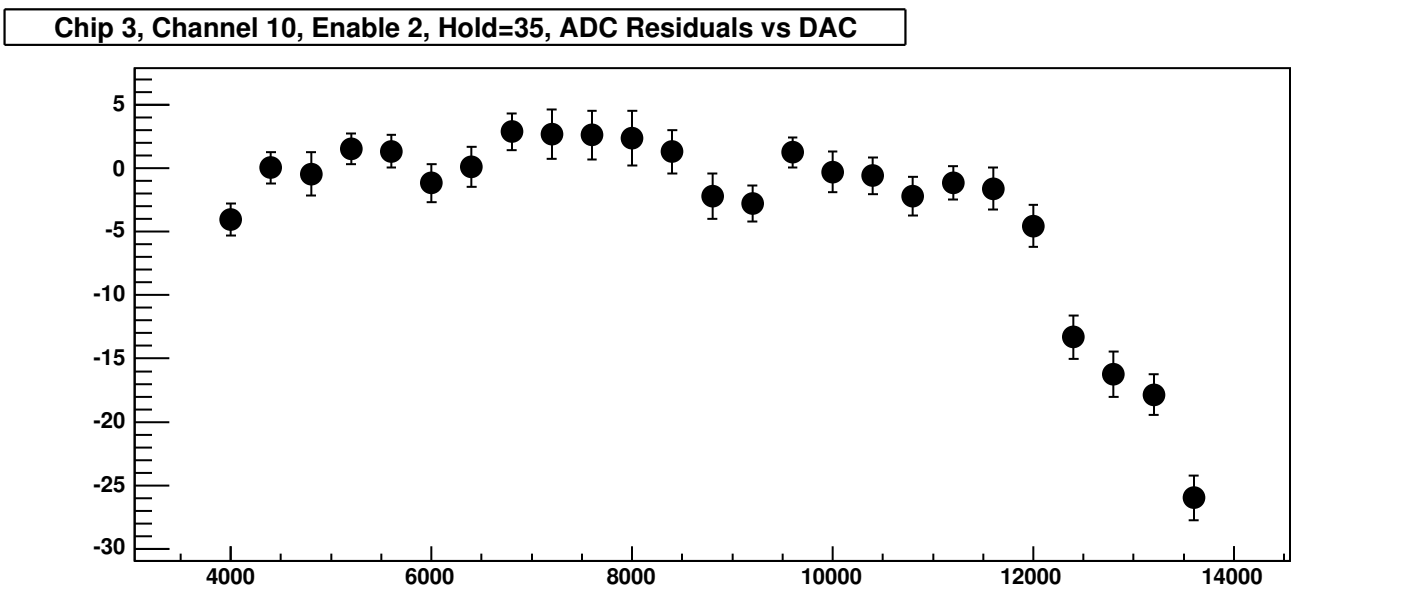

Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.25 / 16
p0 14.36 ± 1.343
p1 0.01446 ± 0.0001727
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

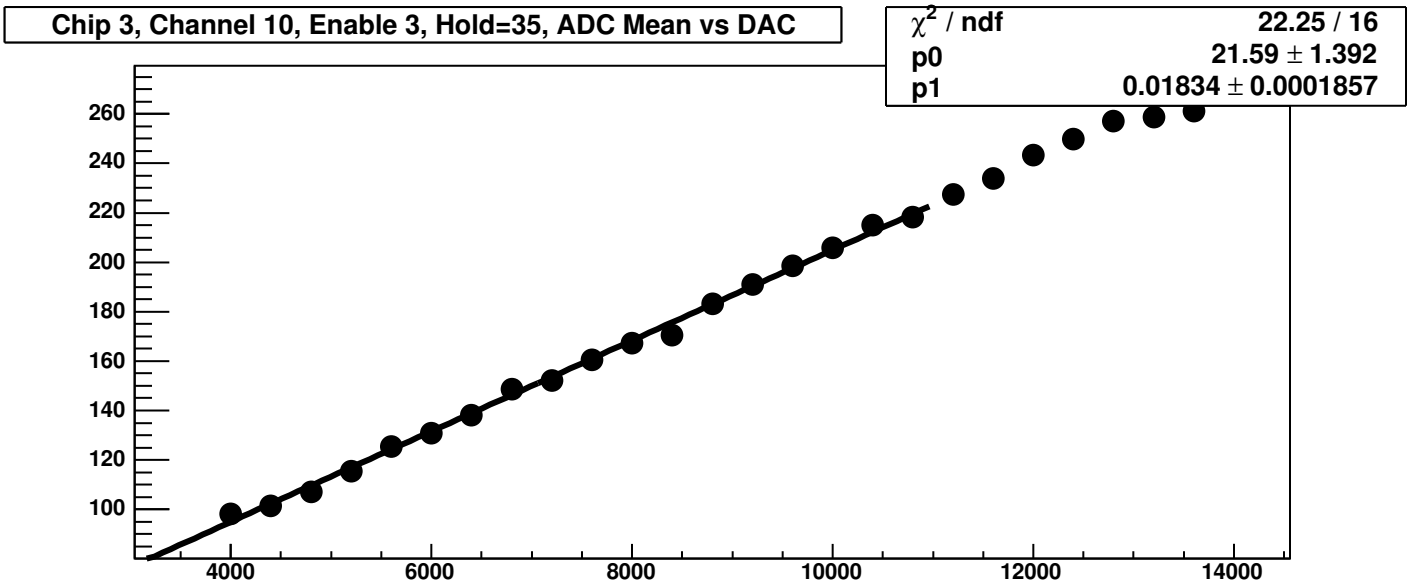

Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

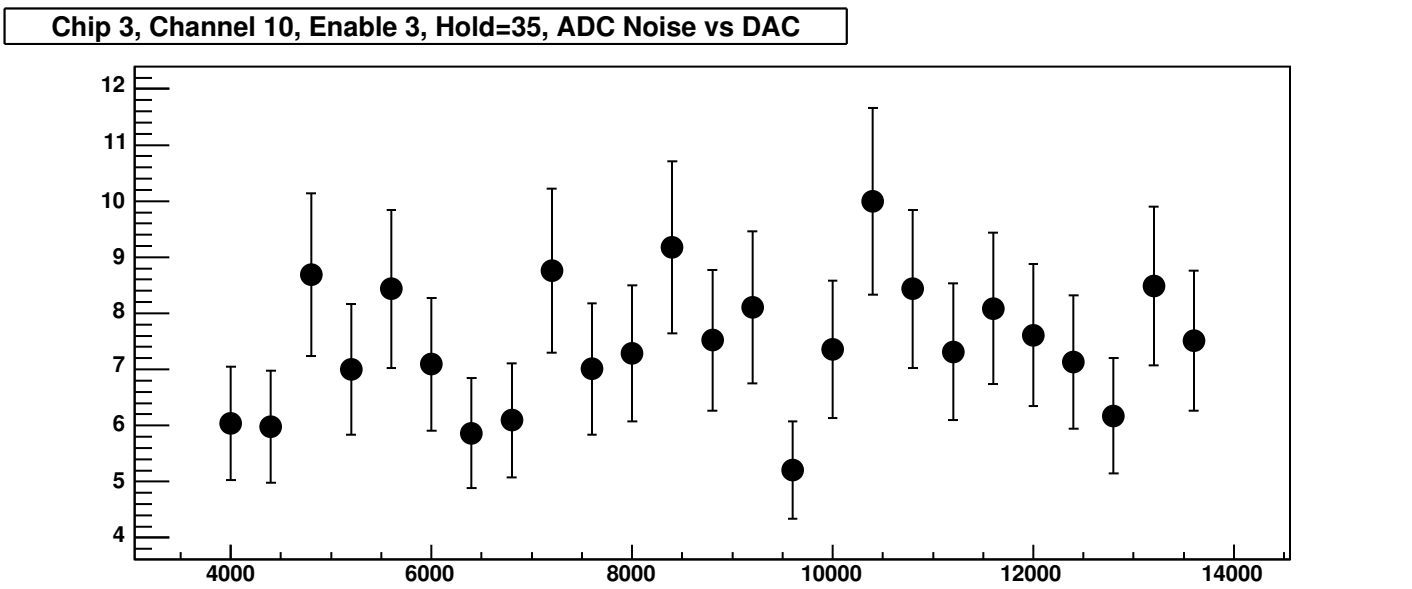

Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

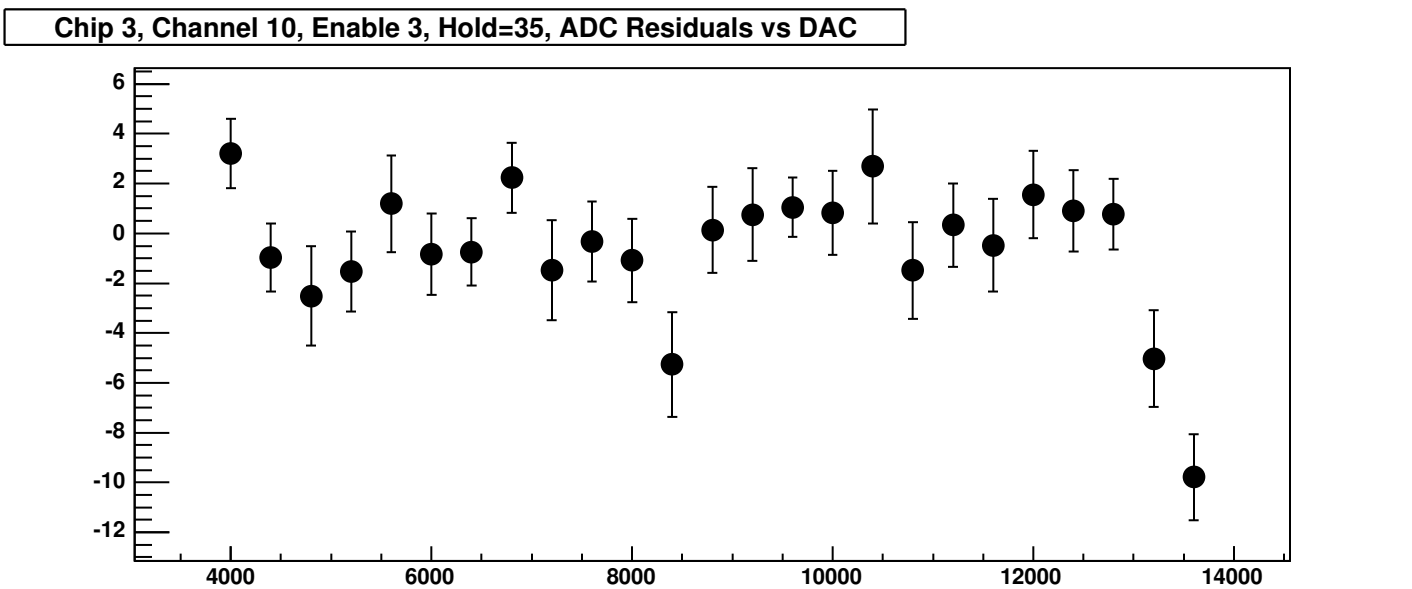

Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.76 / 16
p0 33.42 ± 1.192
p1 0.004282 ± 0.0001582
85
80
75
70
65
60
55
50
45
4000
6000
8000
10000
12000
14000

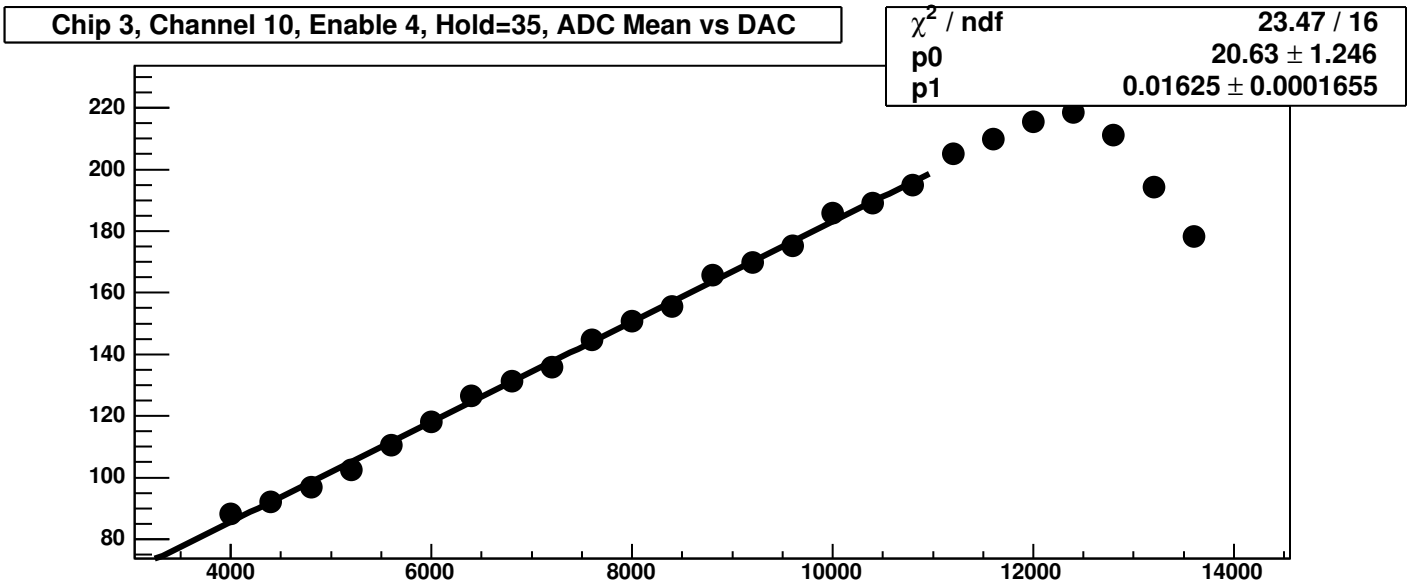

Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

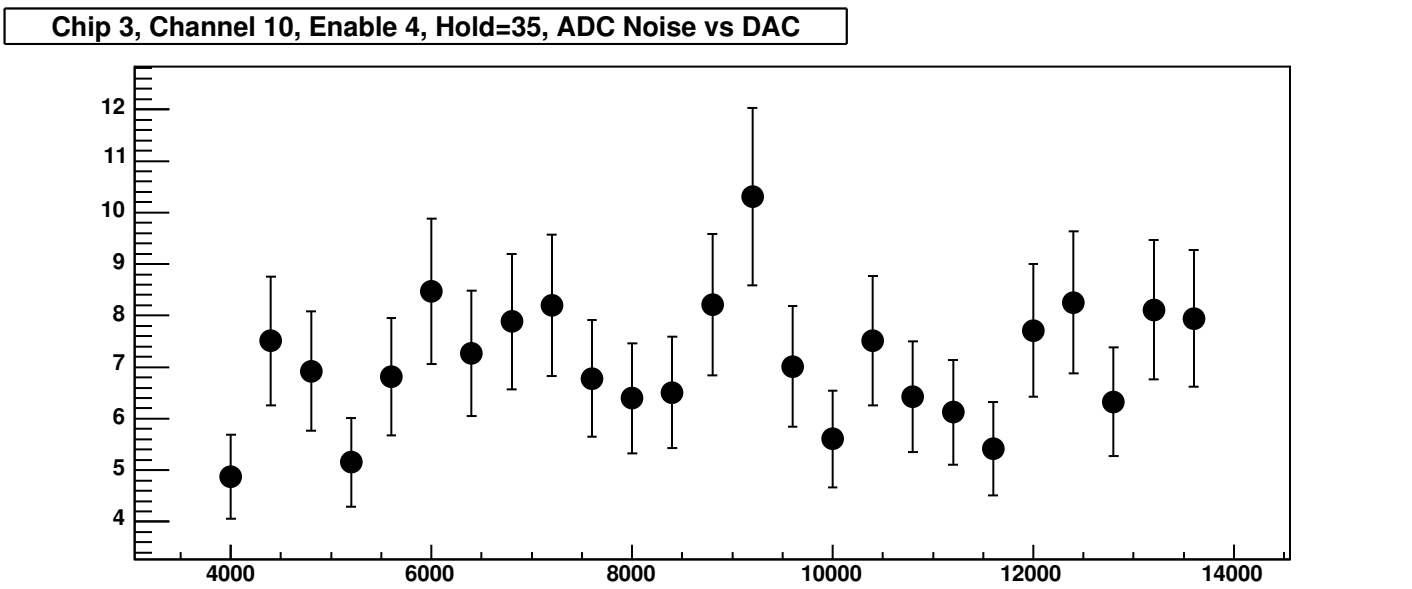

Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

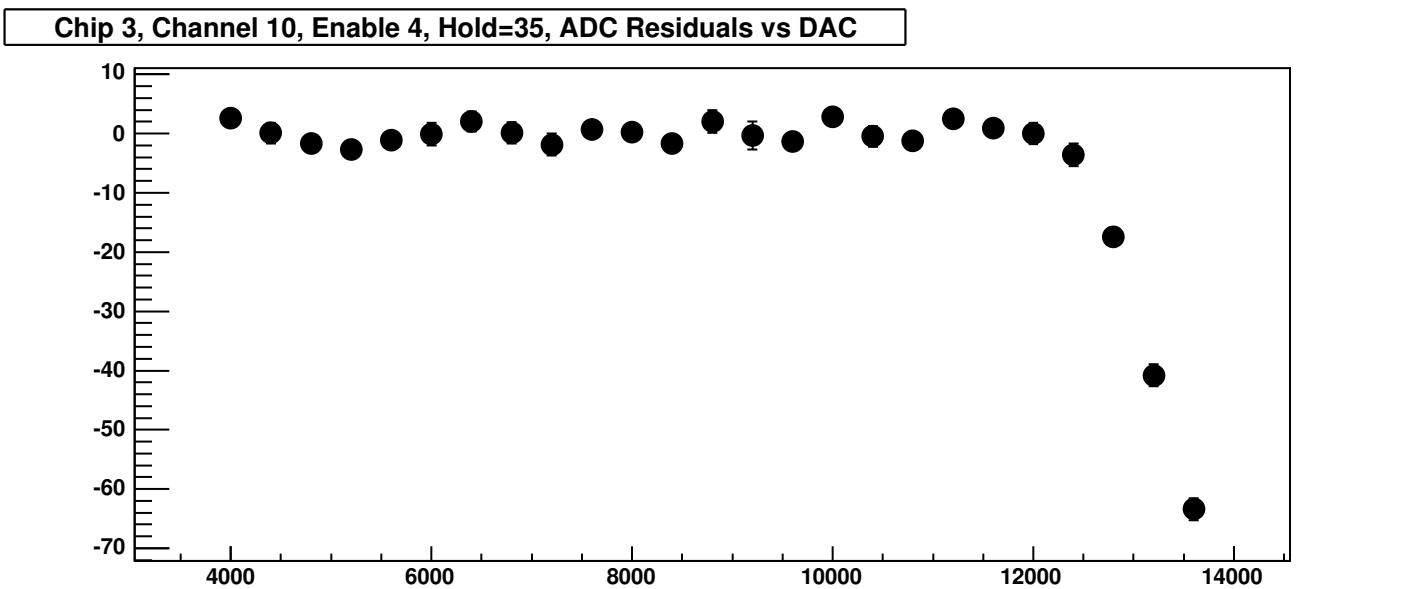

Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.25 / 16
p0 21.59 ± 1.392
p1 0.01834 ± 0.0001857
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000

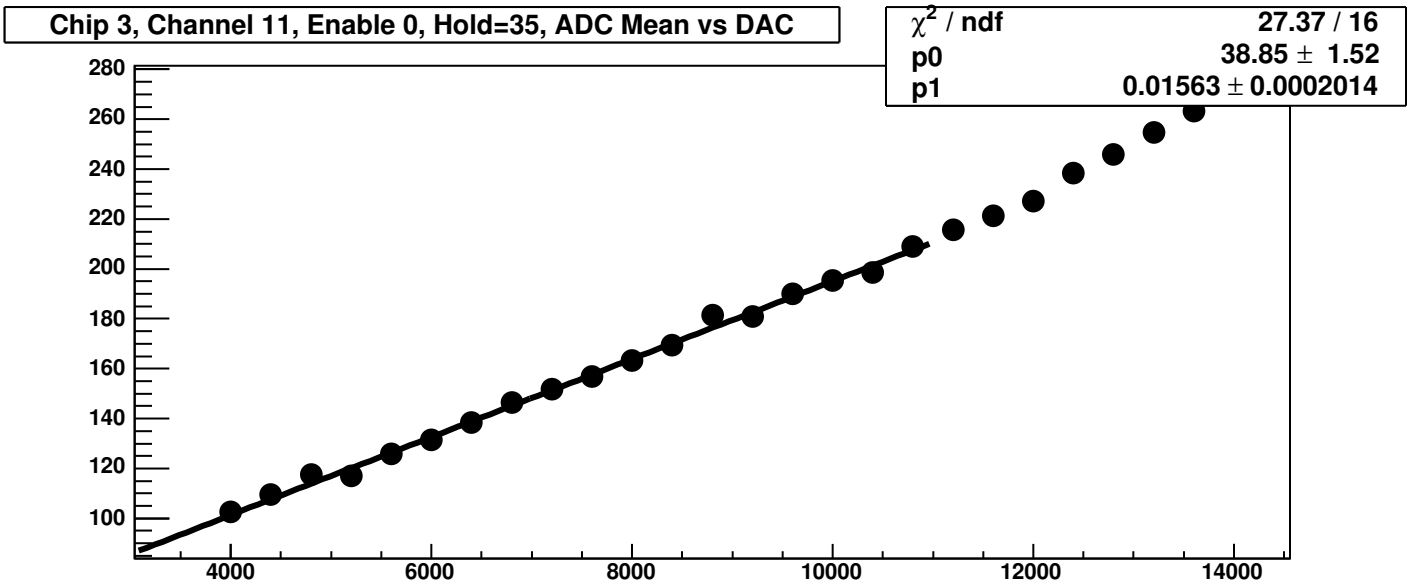

Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

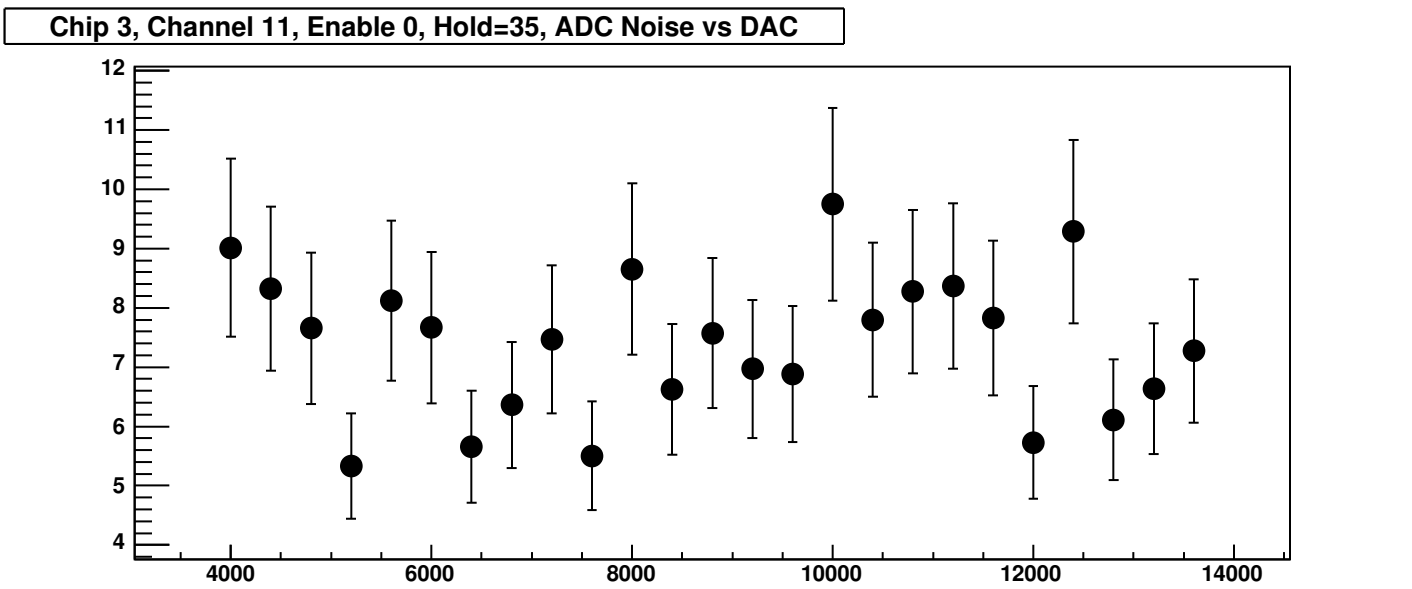

Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000

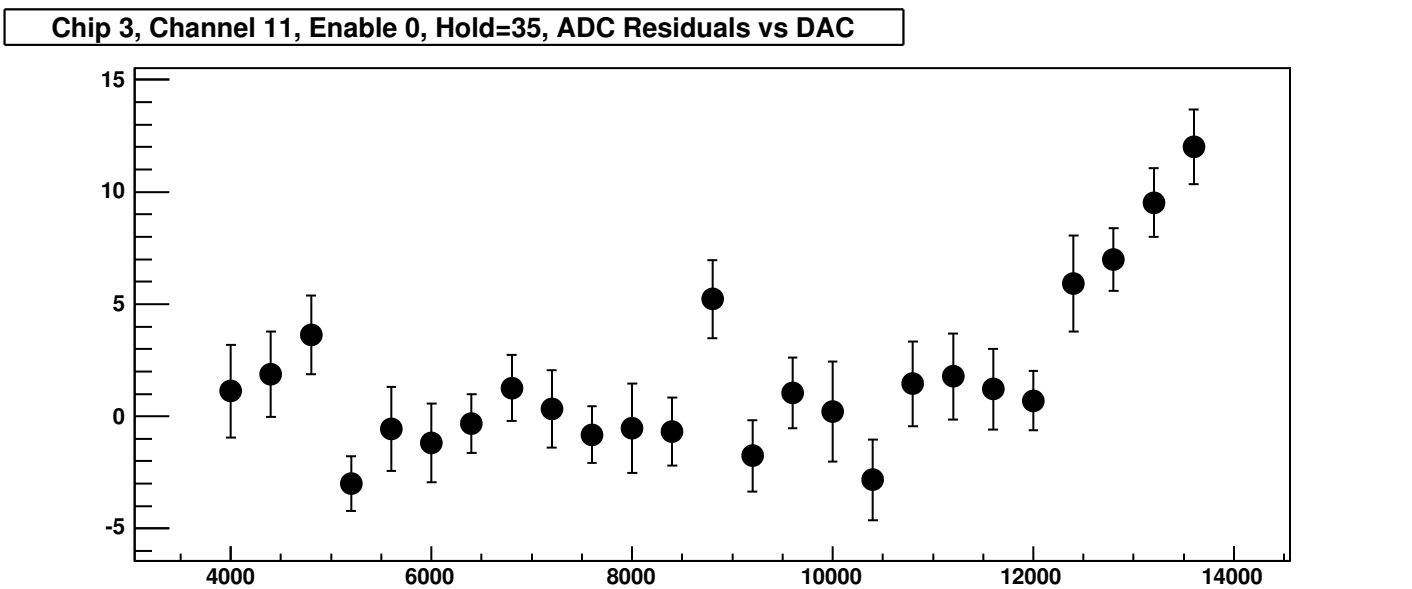

Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.47 / 16
p0 20.63 ± 1.246
p1 0.01625 ± 0.0001655
220
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

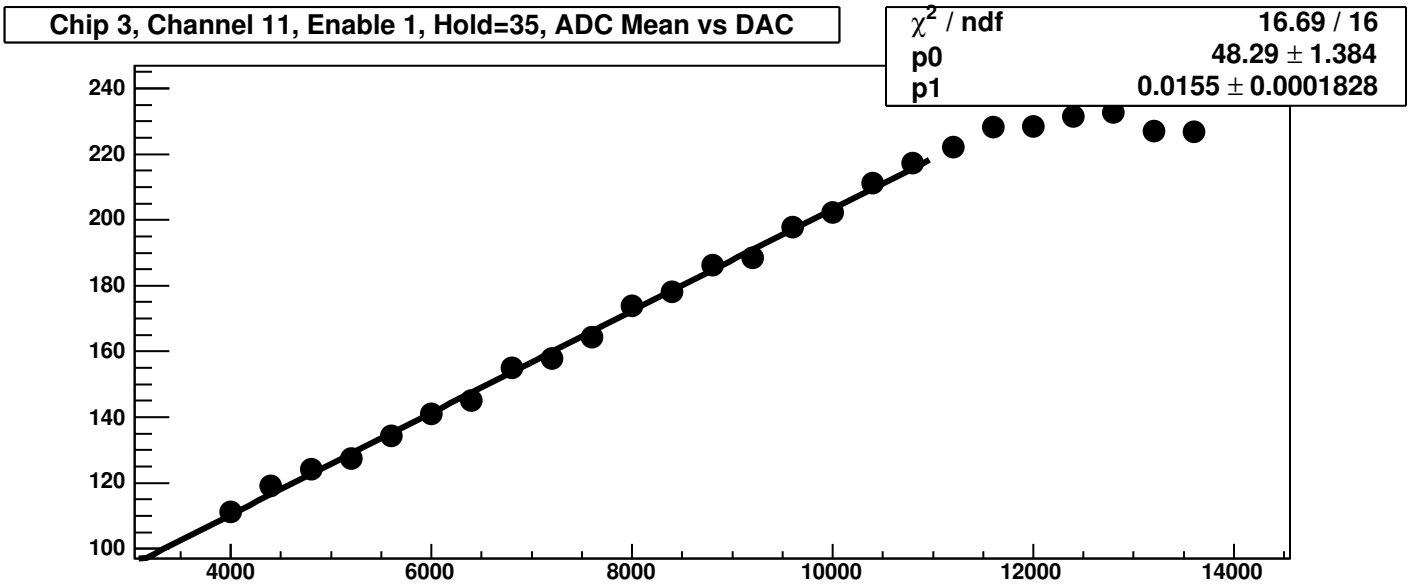

Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

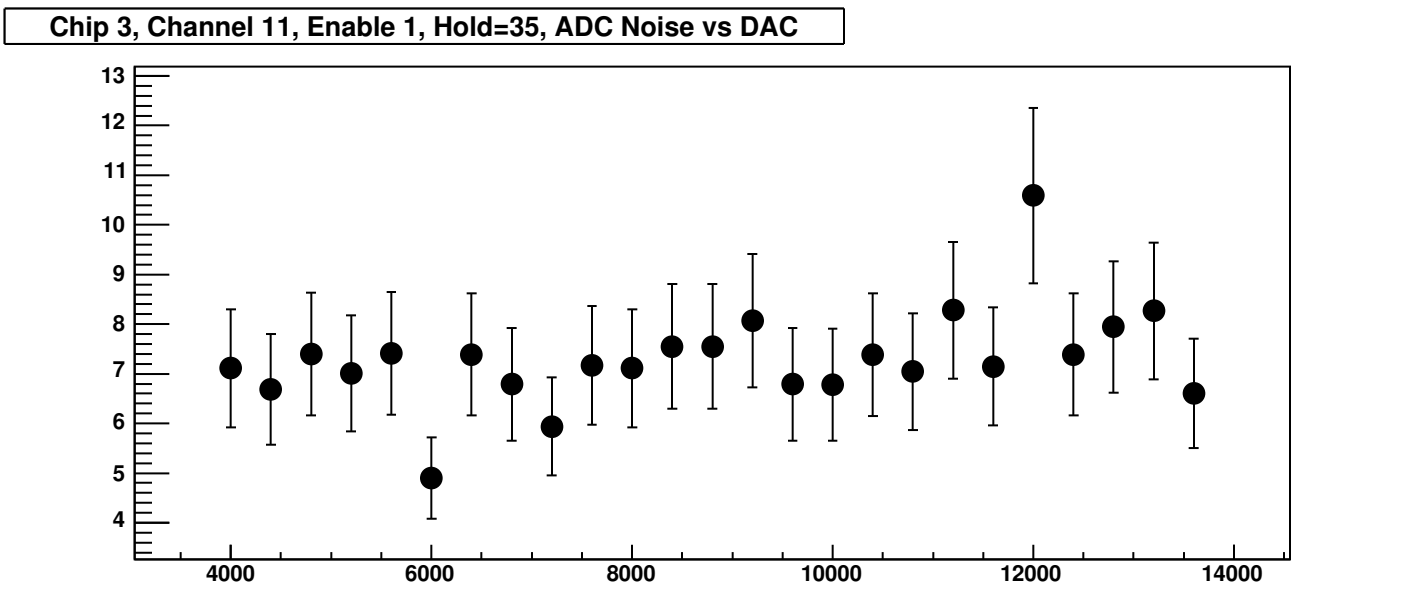

Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
4000
6000
8000
10000
12000
14000

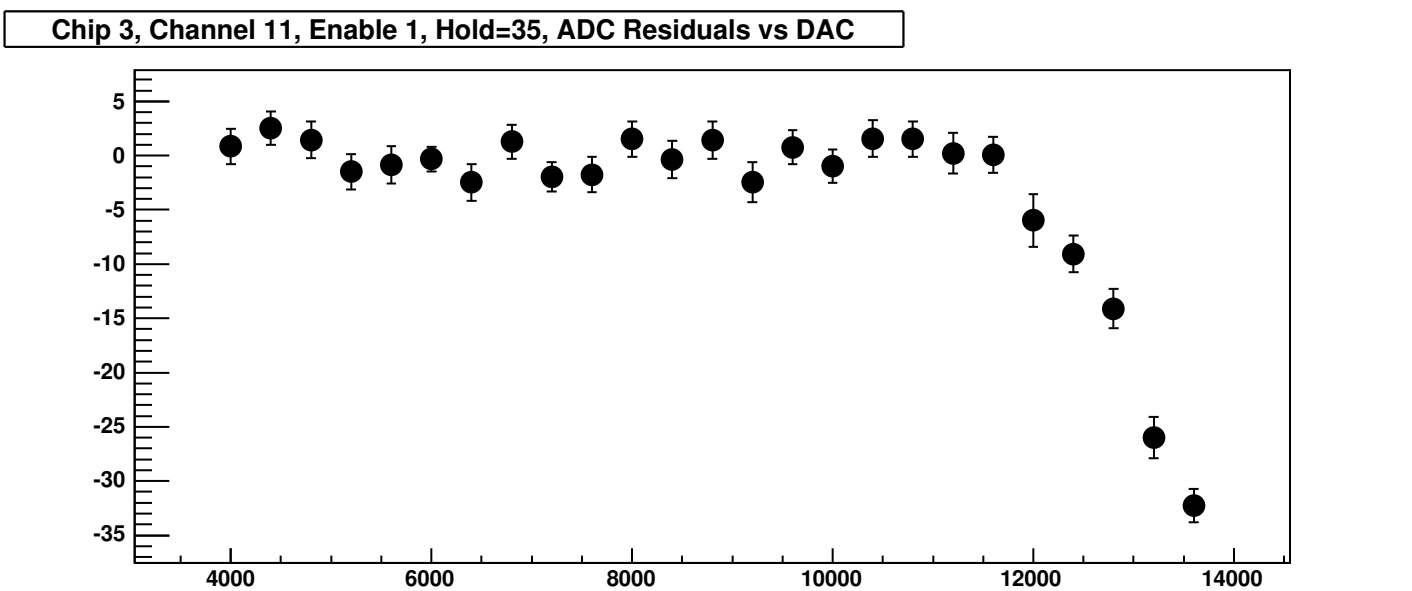

Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.37 / 16
p0 38.85 ± 1.52
p1 0.01563 ± 0.0002014
280
260
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

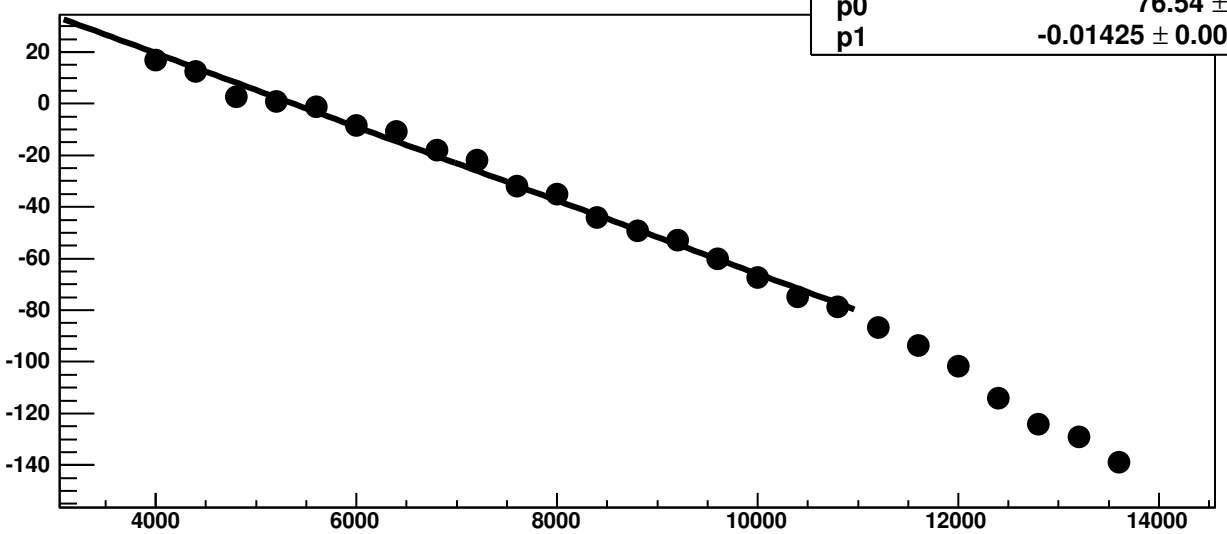

Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.69 / 16
p0 48.29 ± 1.384
p1 0.0155 ± 0.0001828
240
220
200
180
160
140
120
100
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

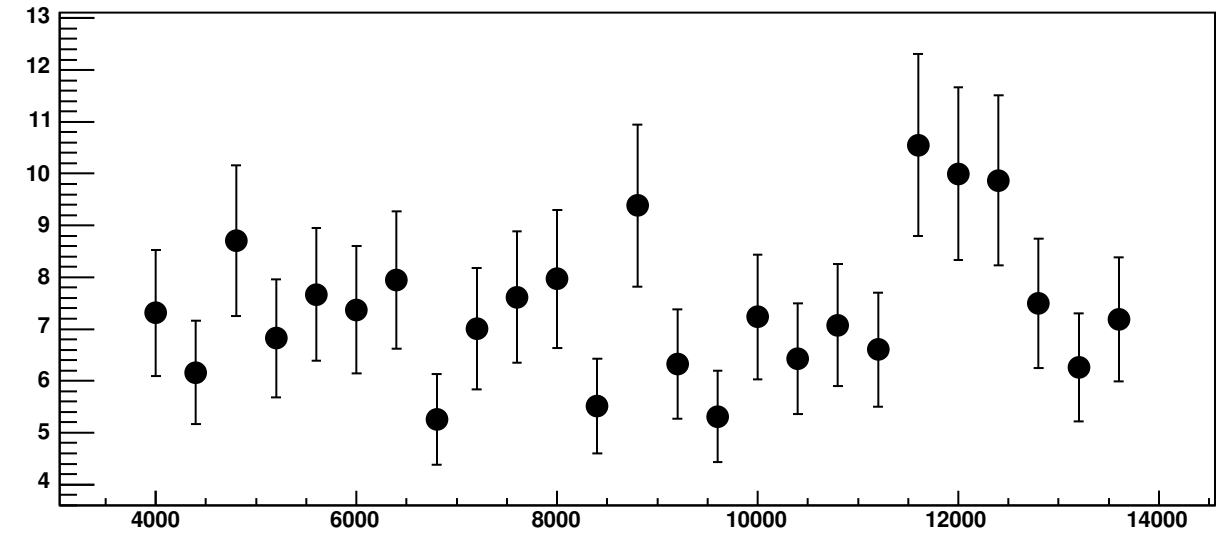

Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

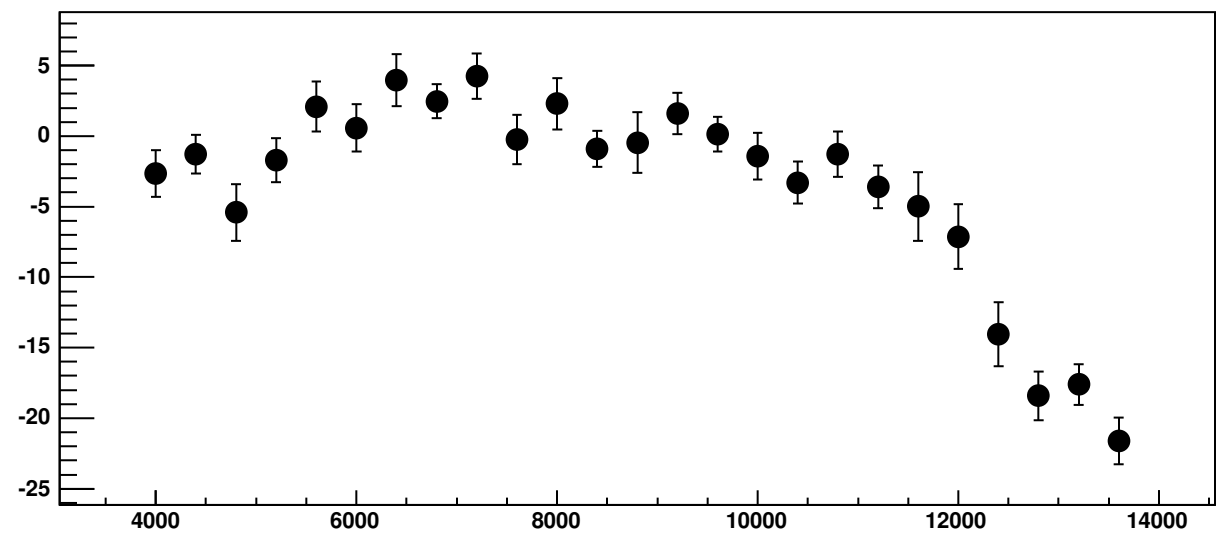
χ^2 / ndf	39.12 / 16
p0	76.54 ± 1.387
p1	-0.01425 ± 0.0001777

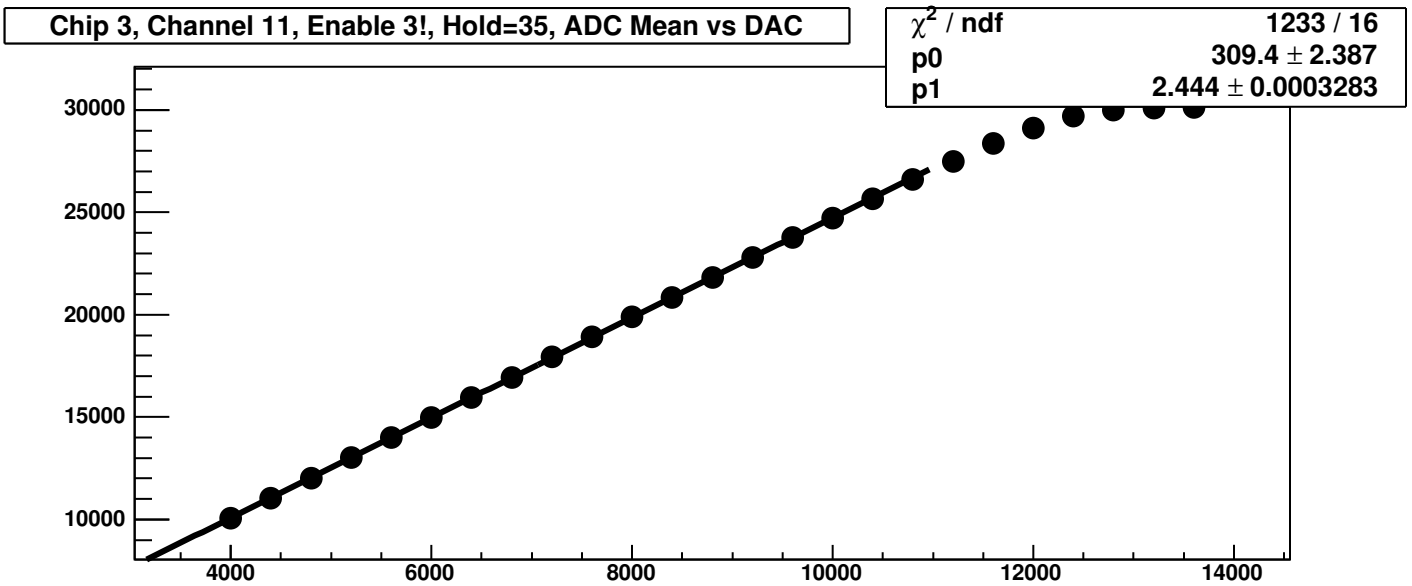


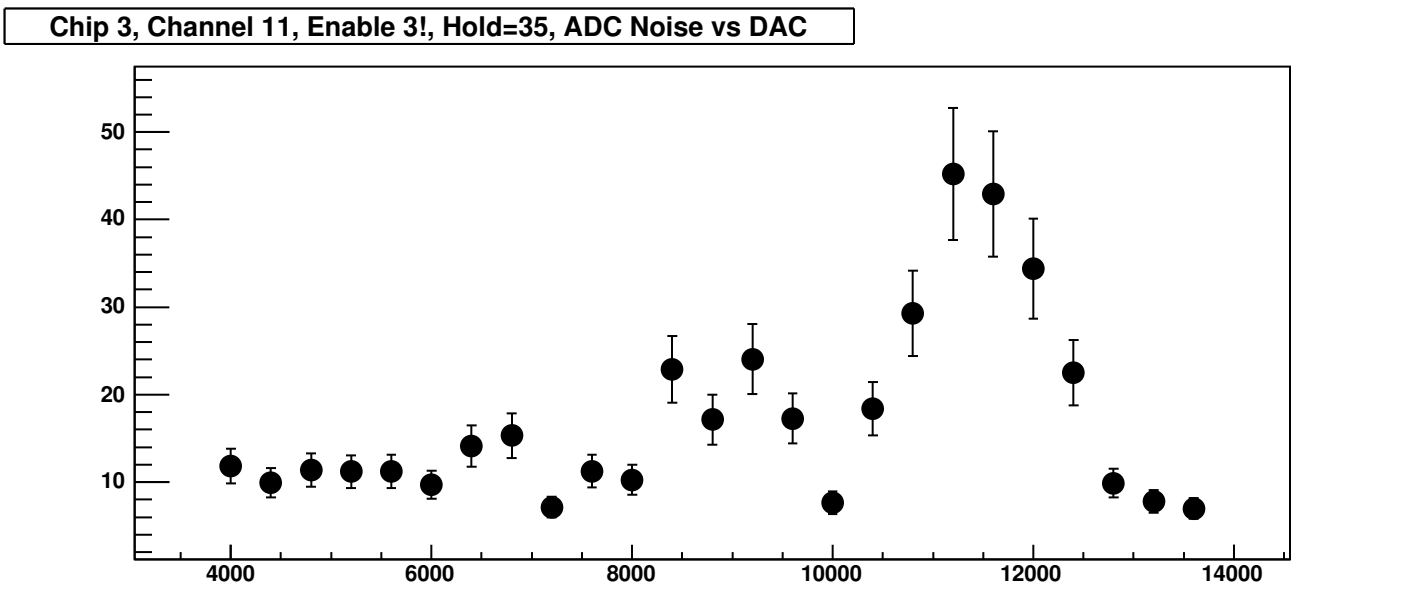
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

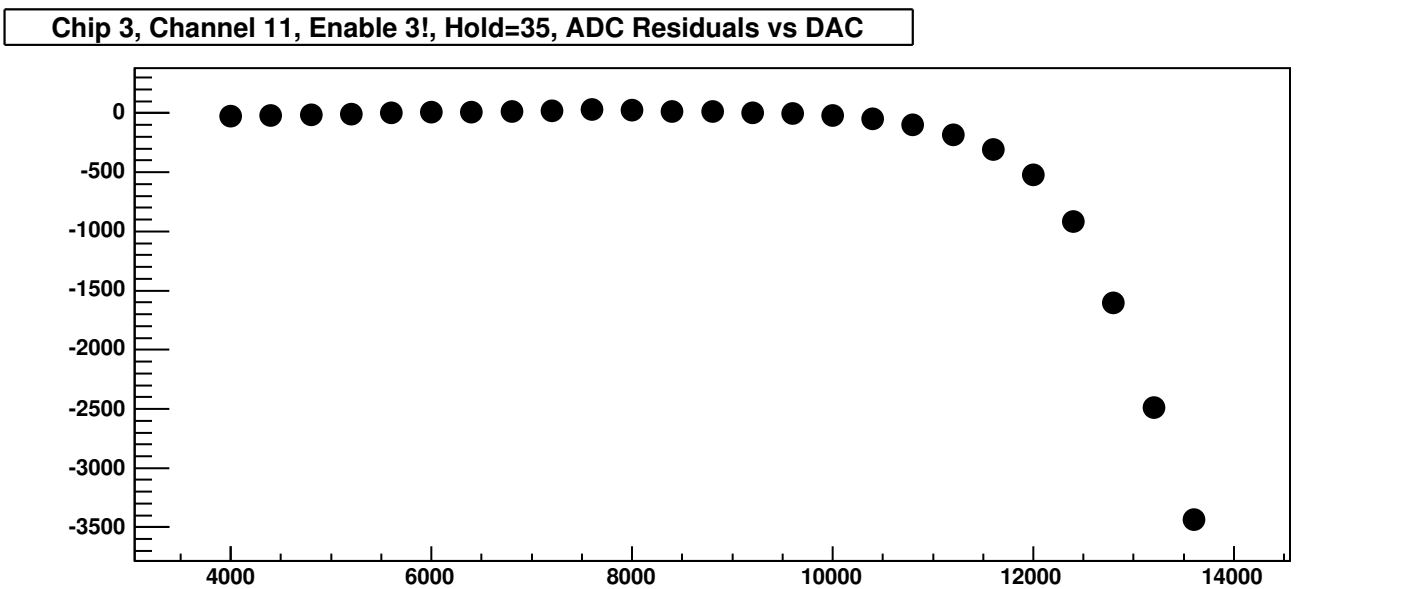


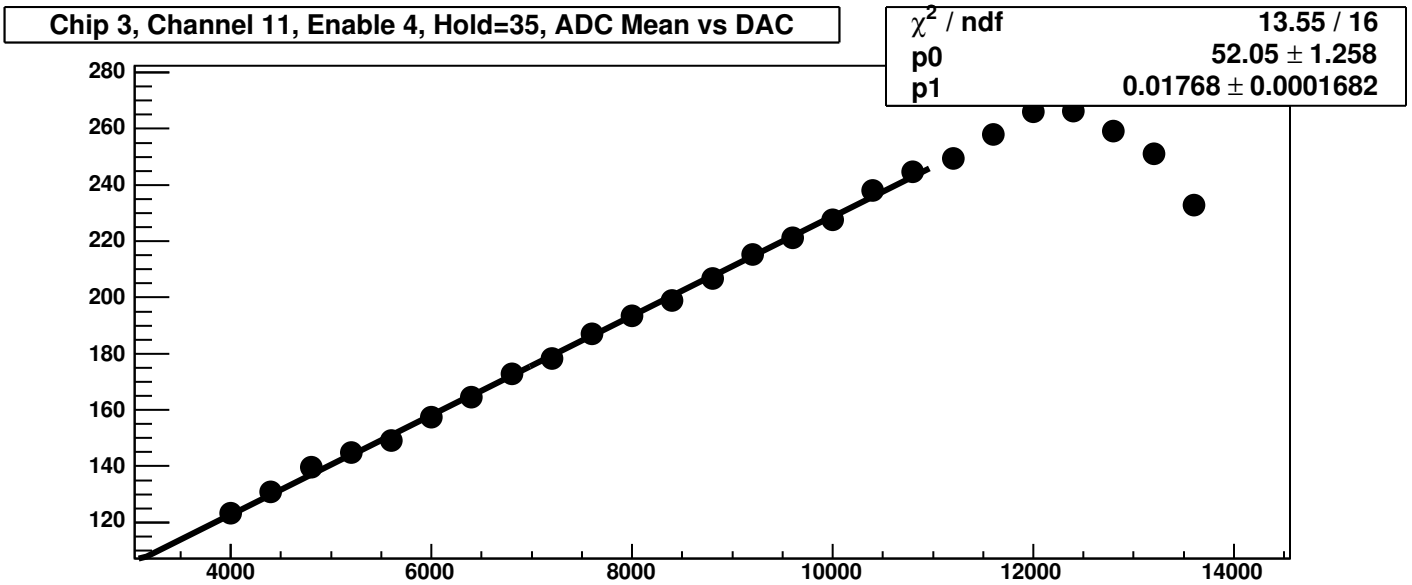
Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

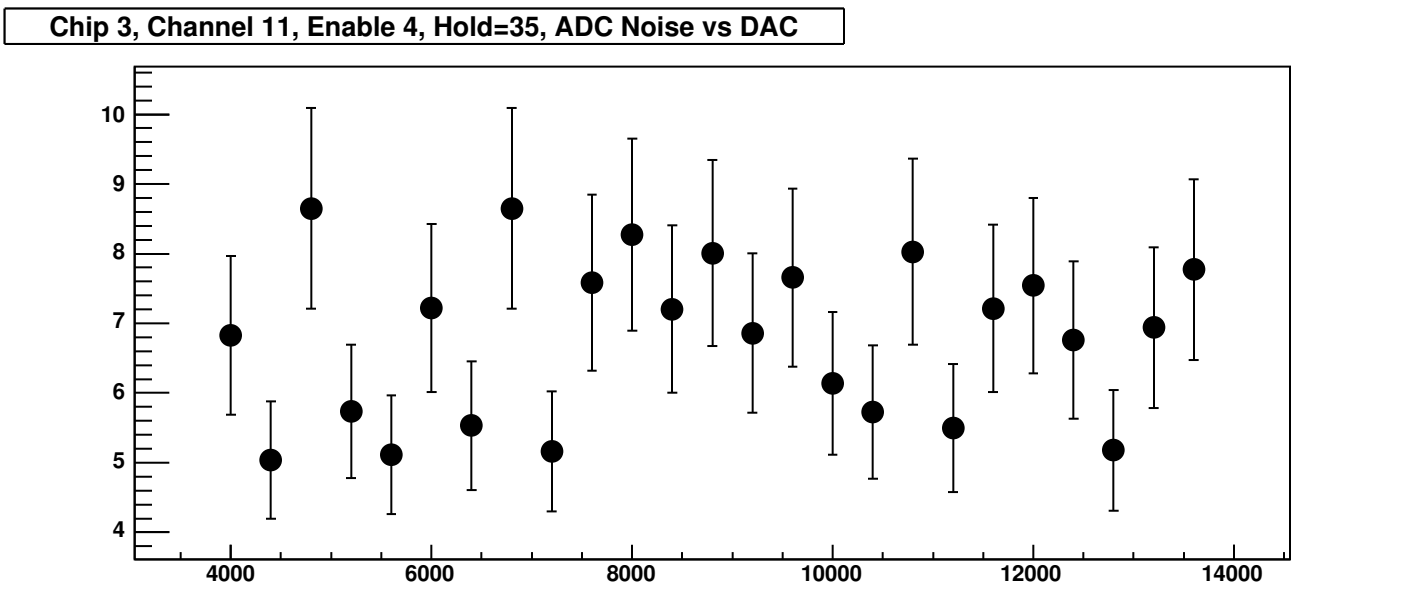


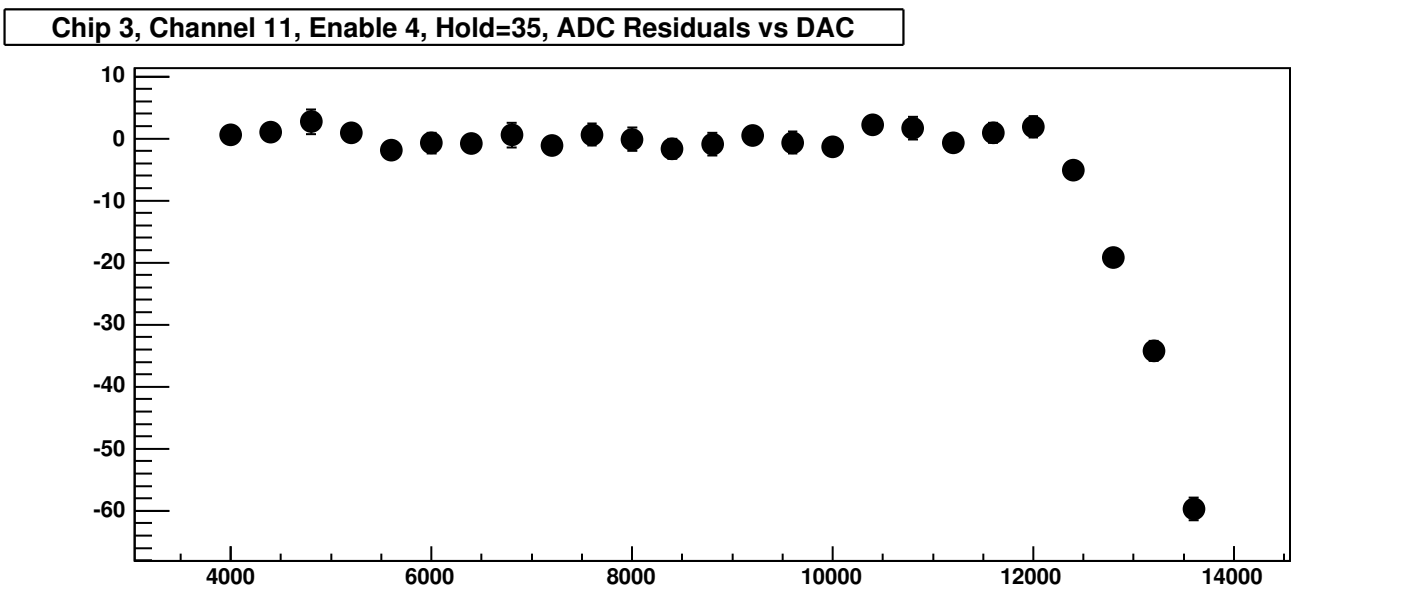

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1233 / 16
p0 309.4 ± 2.387
p1 2.444 ± 0.0003283
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

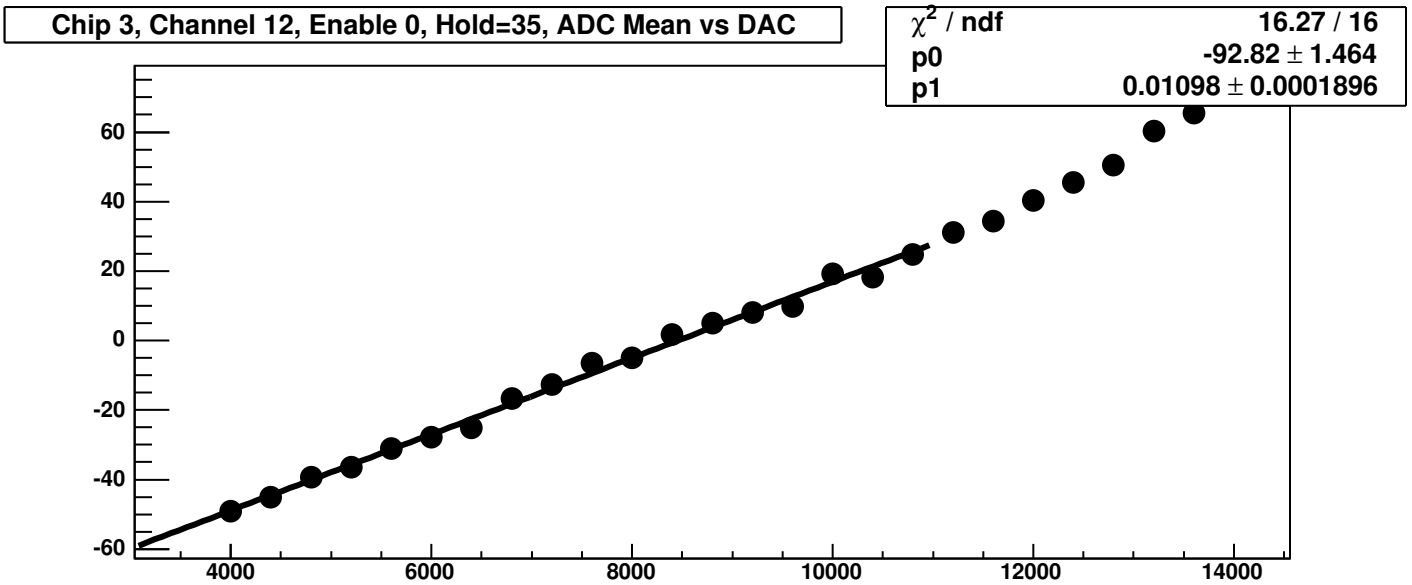

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

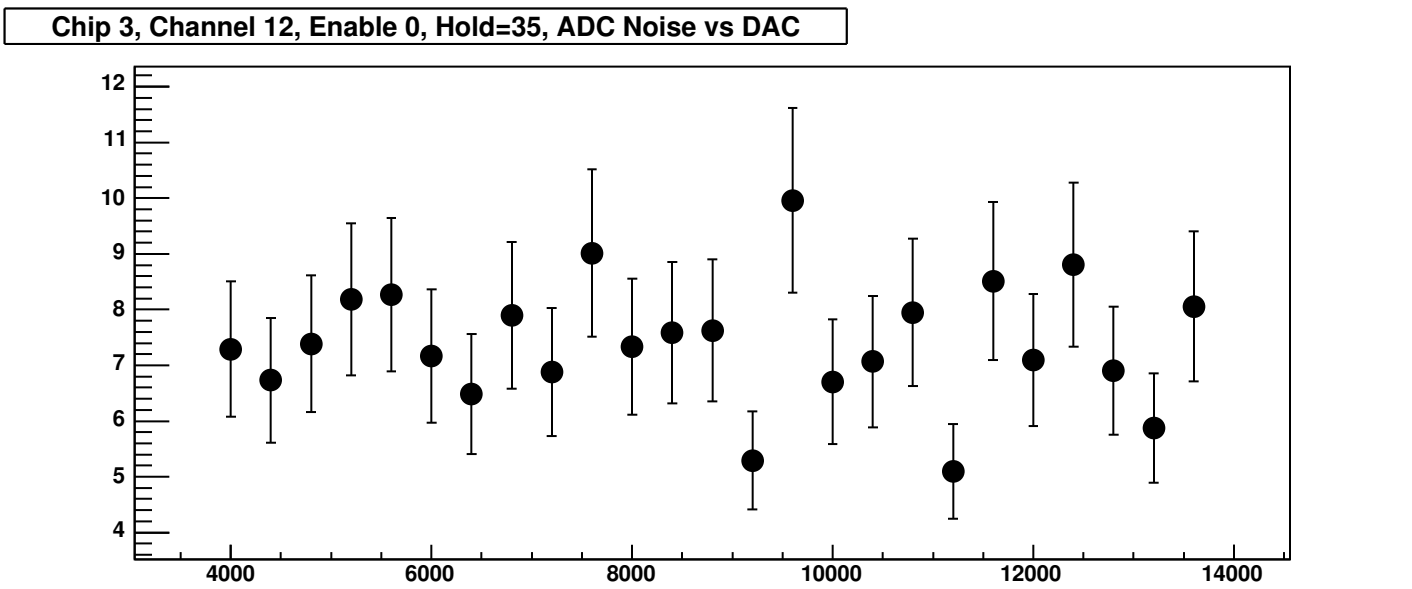

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

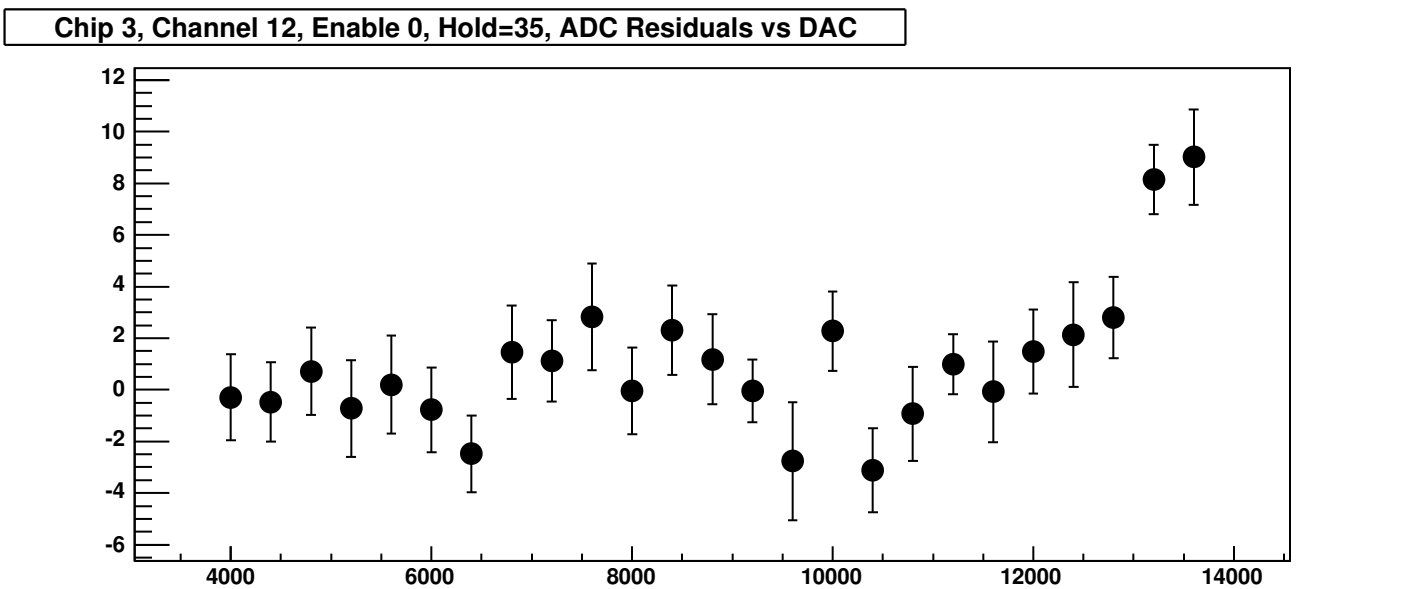

Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.55 / 16
p0 52.05 ± 1.258
p1 0.01768 ± 0.0001682
280
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

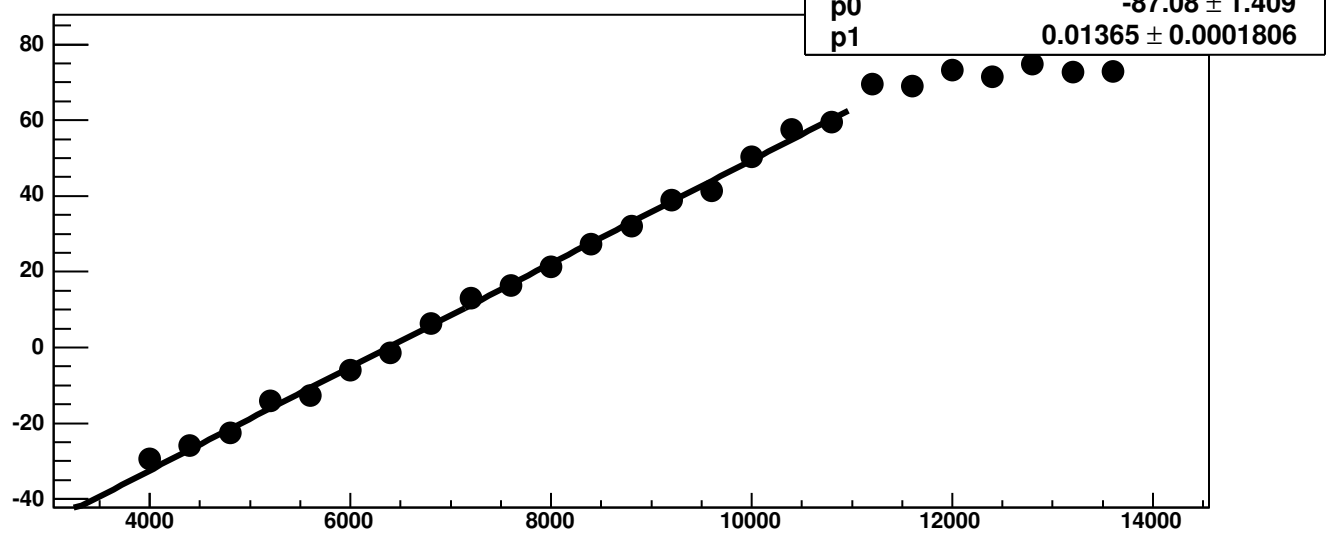

Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 16.27 / 16
p0 -92.82 ± 1.464
p1 0.01098 ± 0.0001896
60
40
20
0
-20
-40
-60
4000
6000
8000
10000
12000
14000

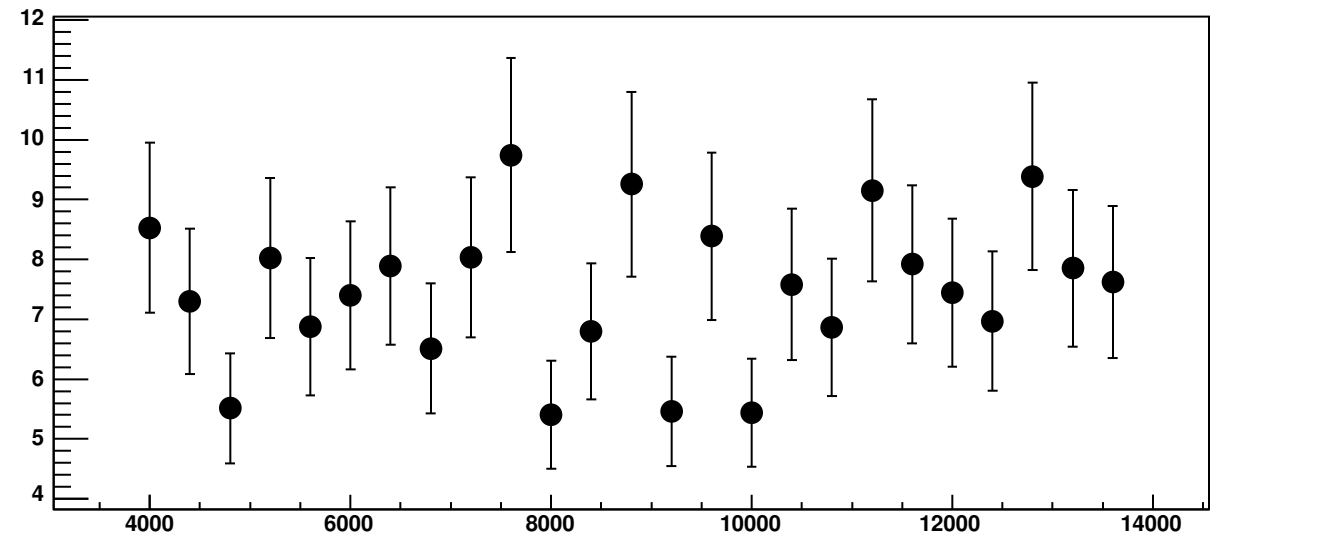

Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

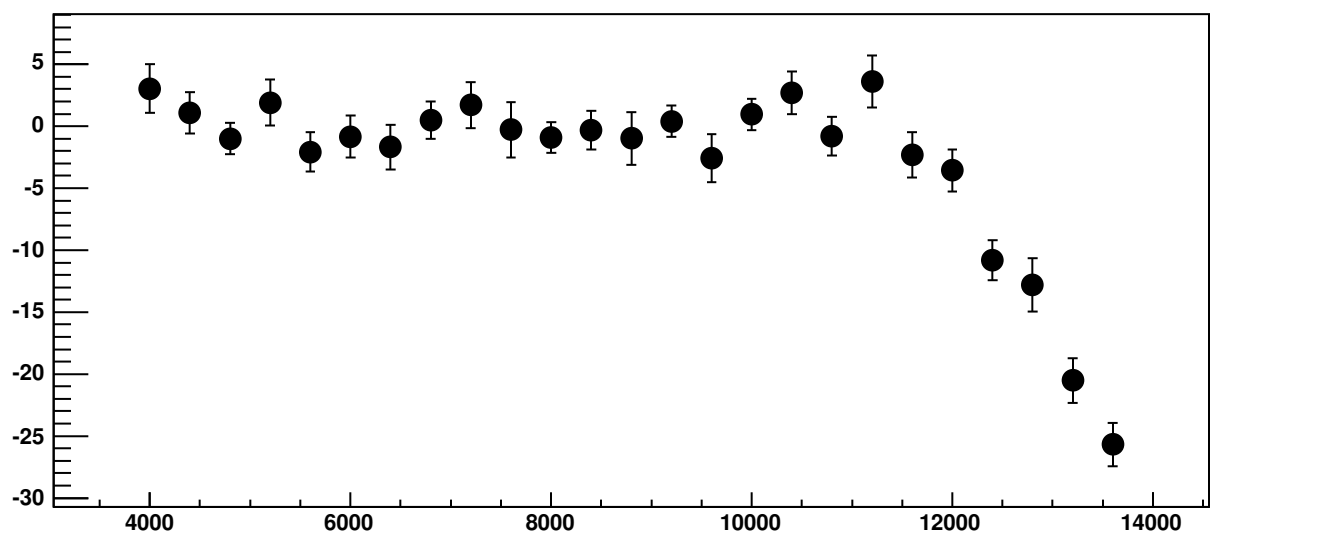
Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

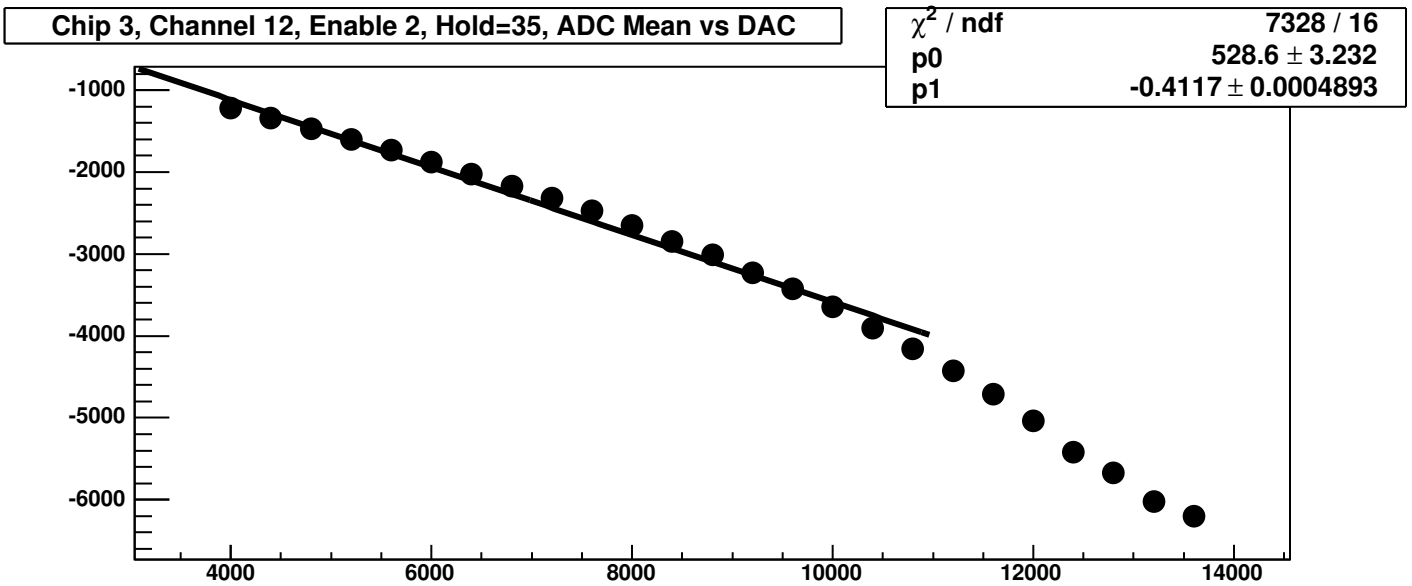


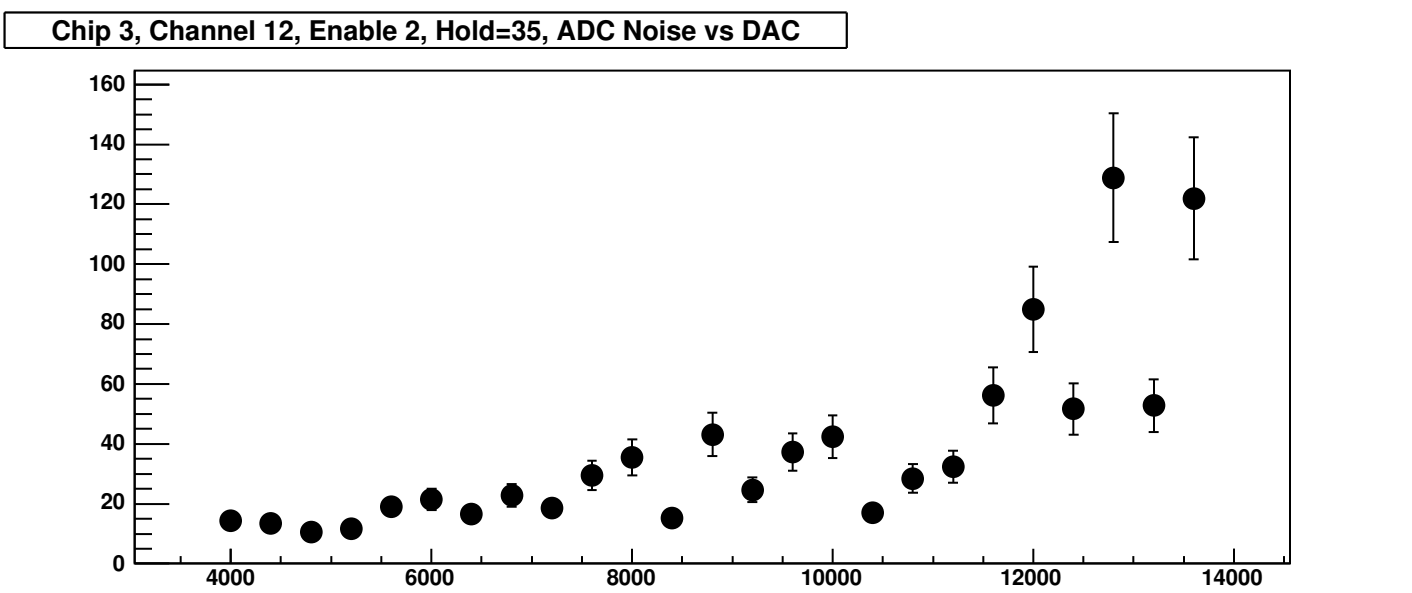
Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC

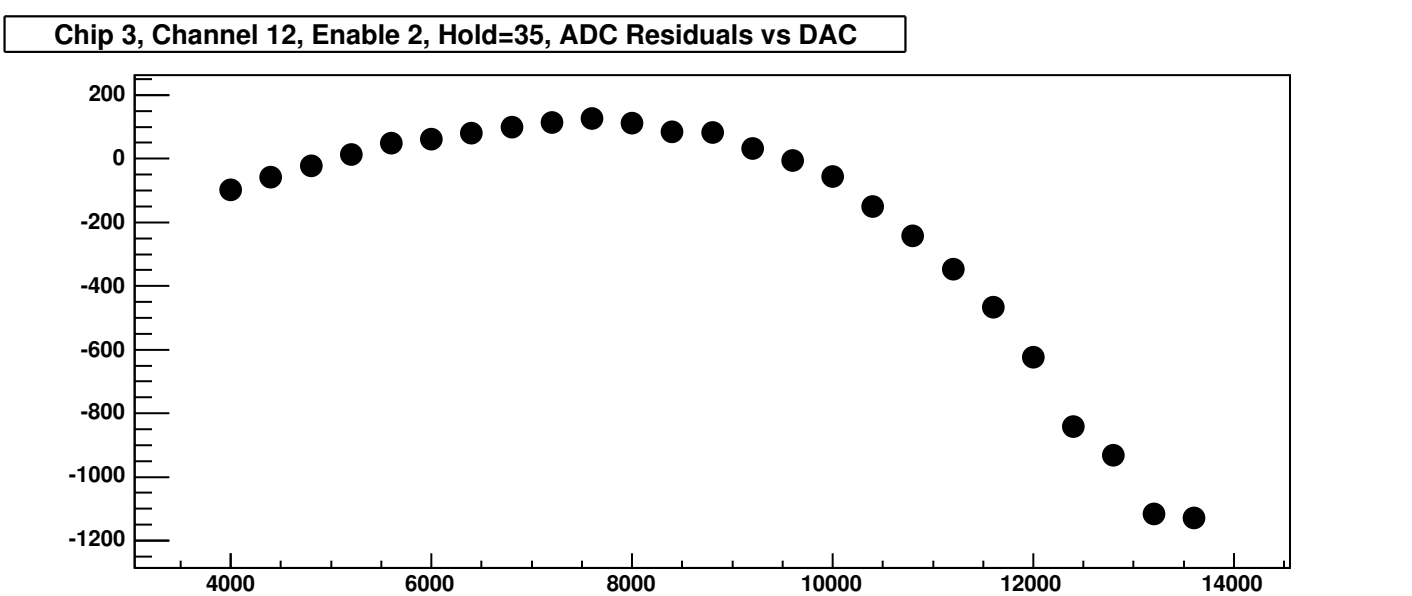


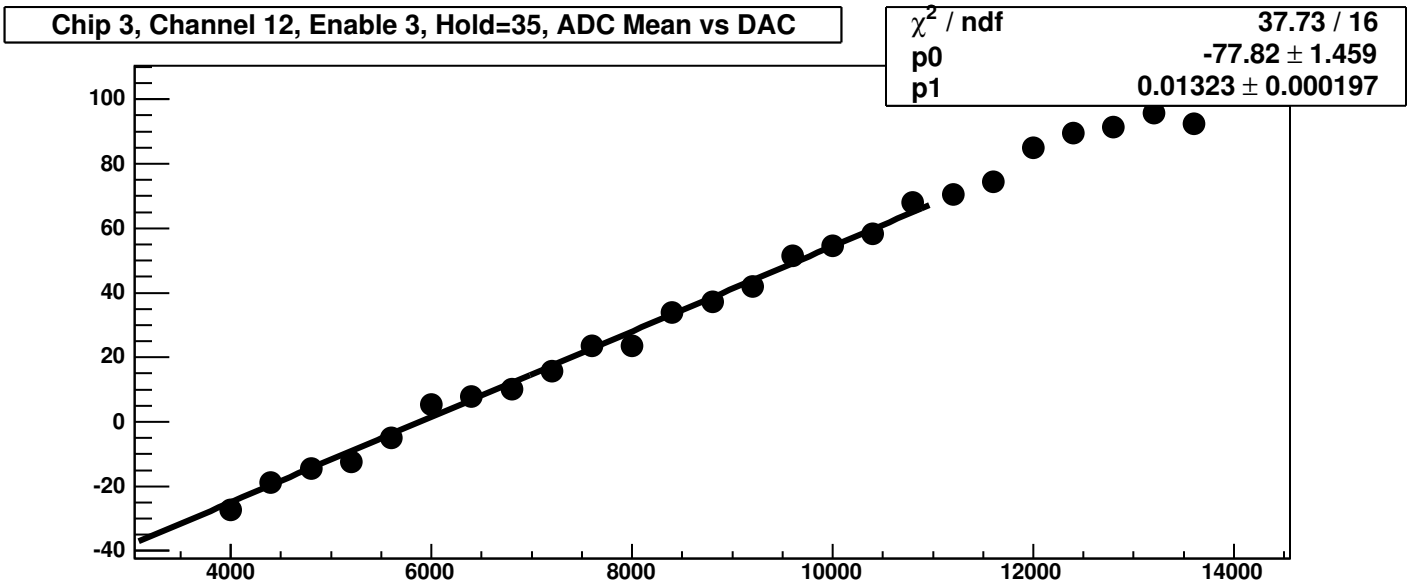
Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

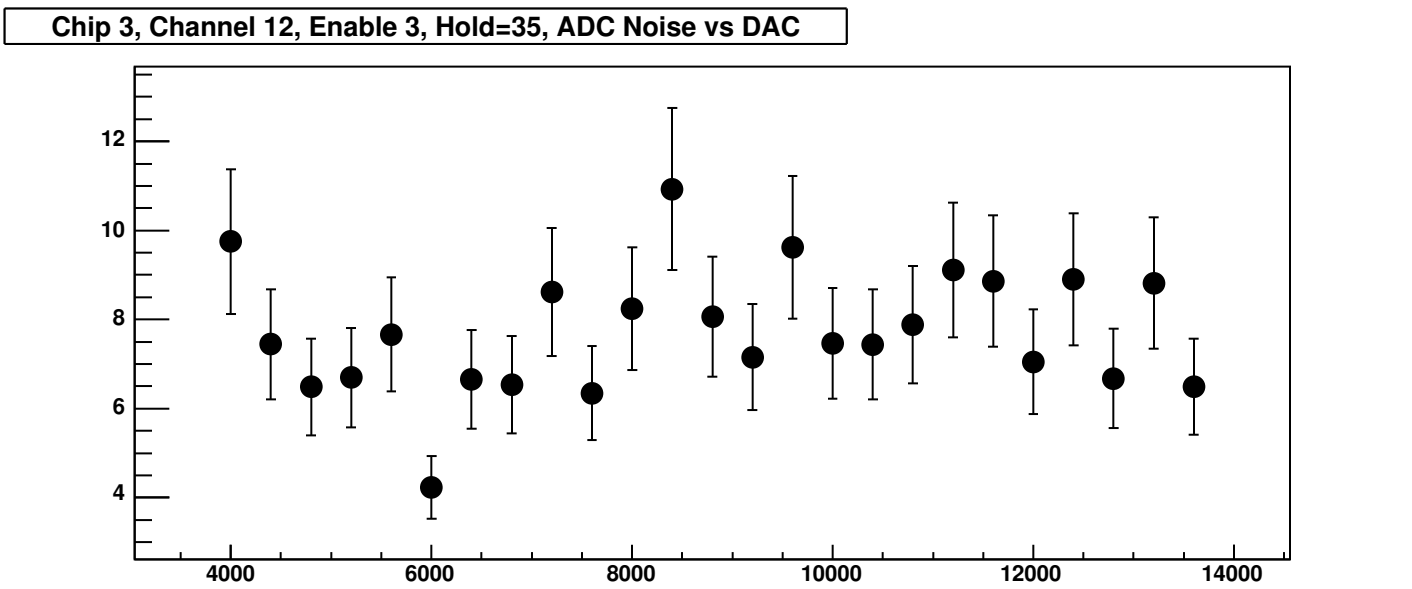


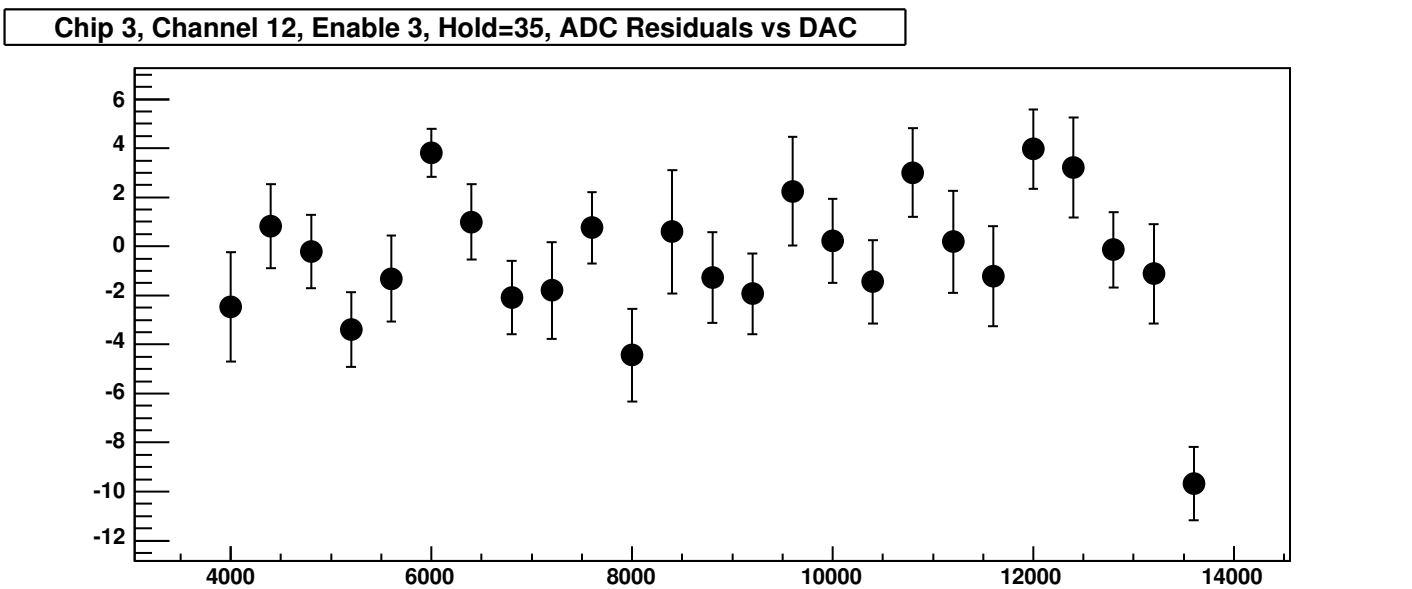

Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 7328 / 16
p0 528.6 ± 3.232
p1 -0.4117 ± 0.0004893
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000

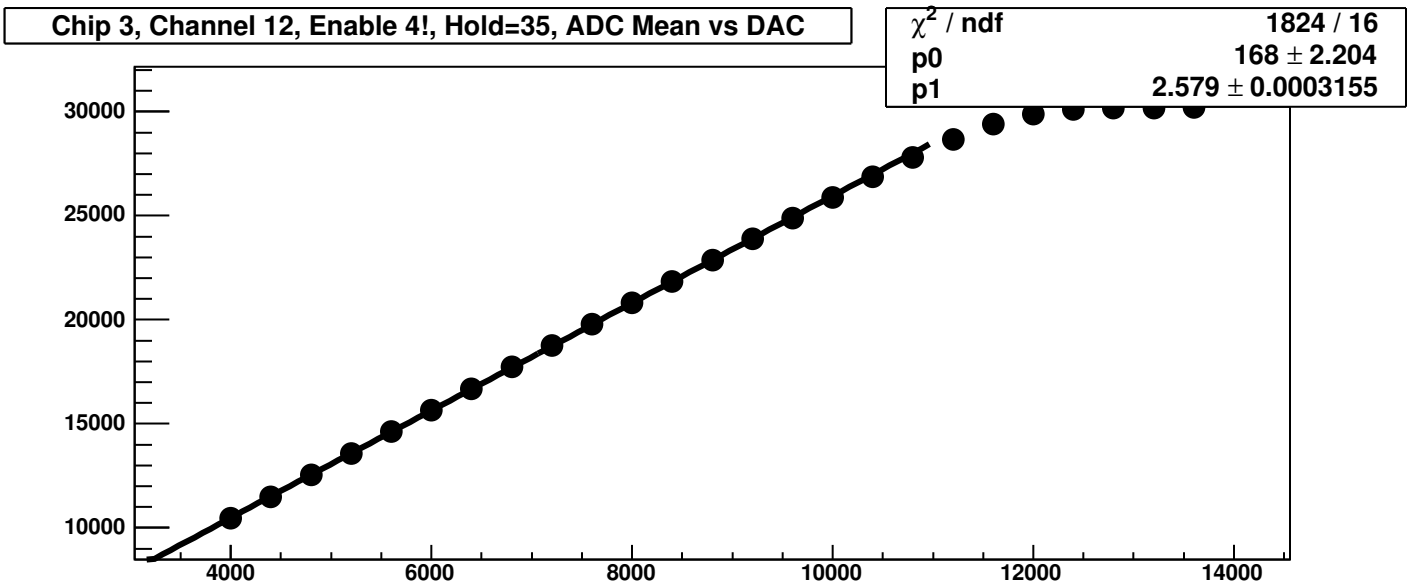

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

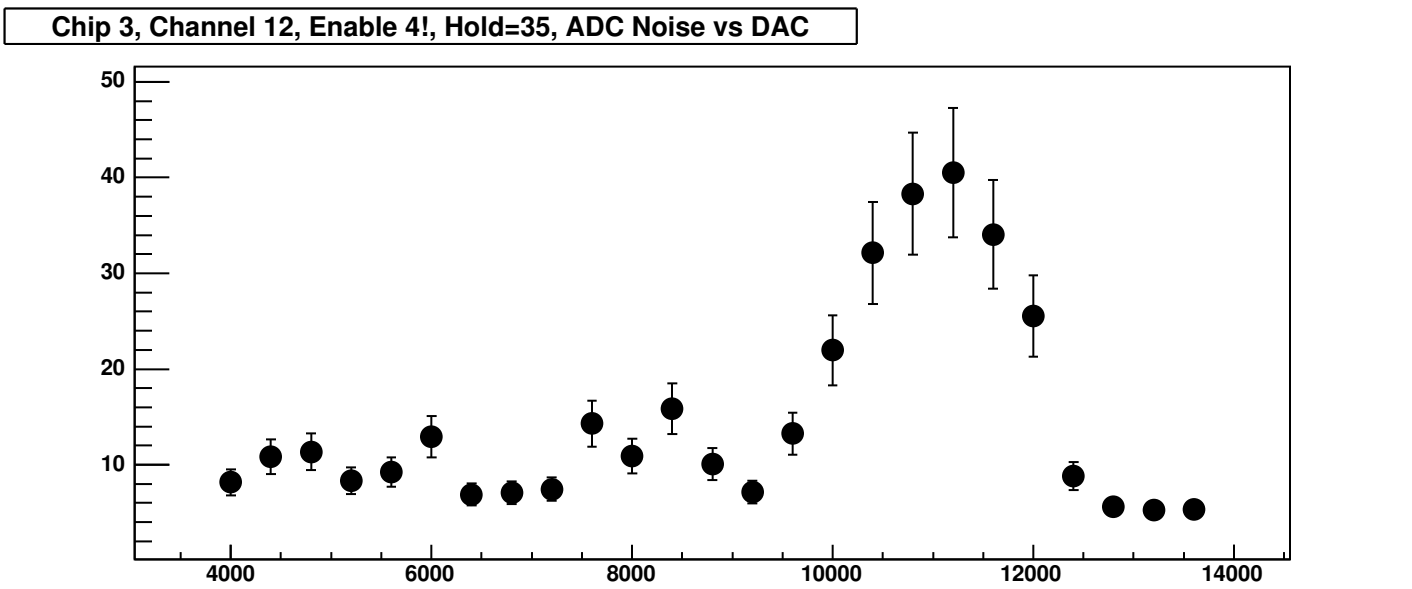

Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
4000
6000
8000
10000
12000
14000

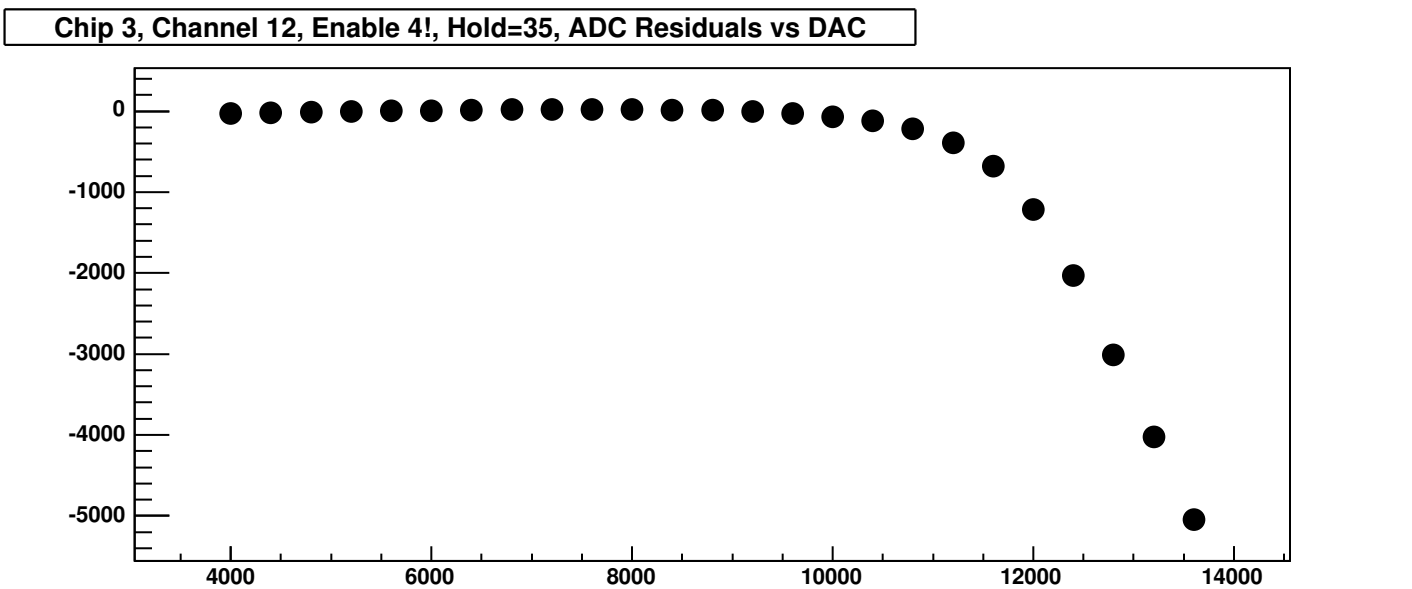

Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.73 / 16
p0 -77.82 ± 1.459
p1 0.01323 ± 0.000197
100
80
60
40
20
0
-20
-40
4000
6000
8000
10000
12000
14000

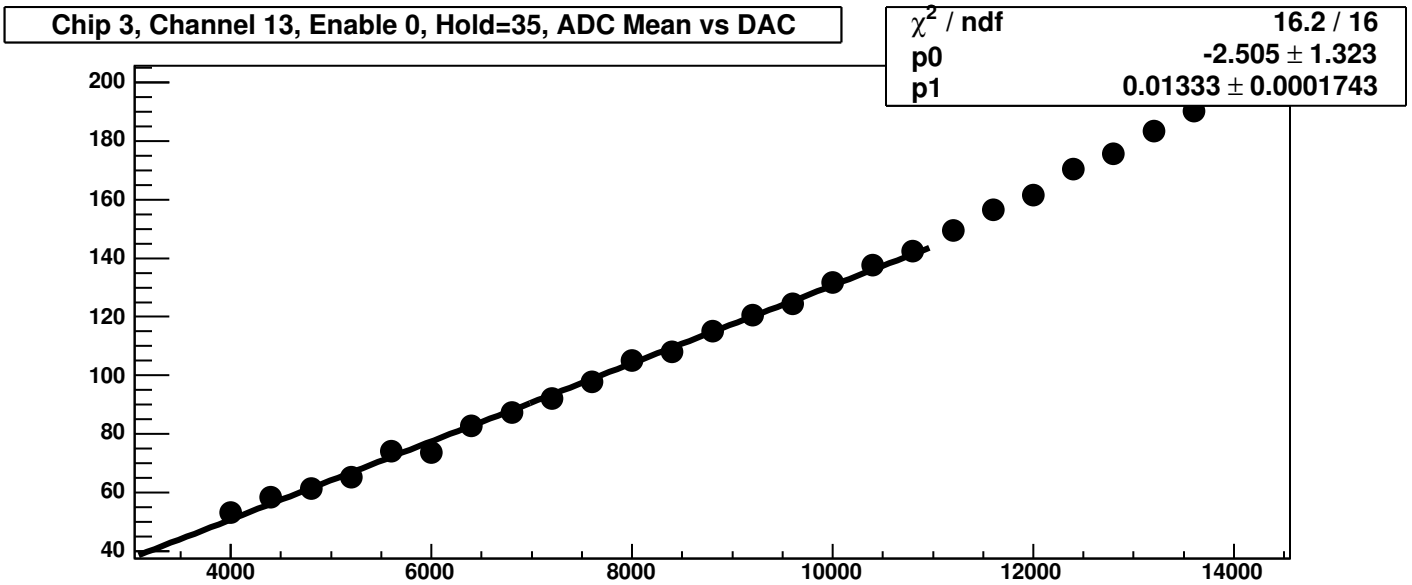

Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

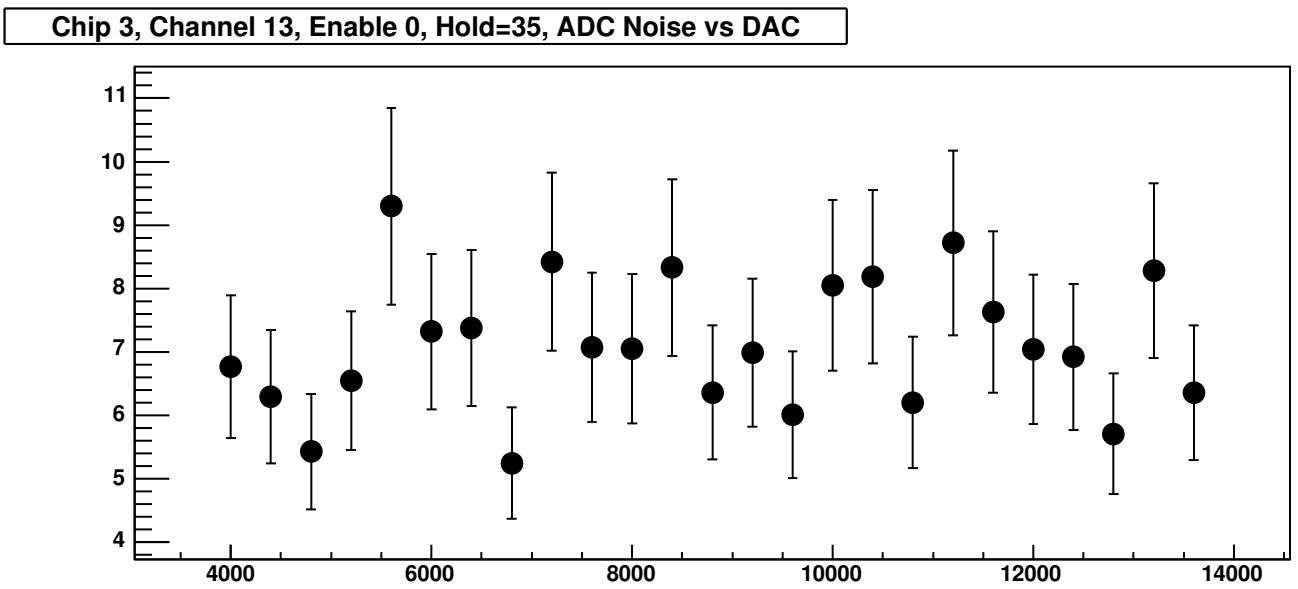

Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000

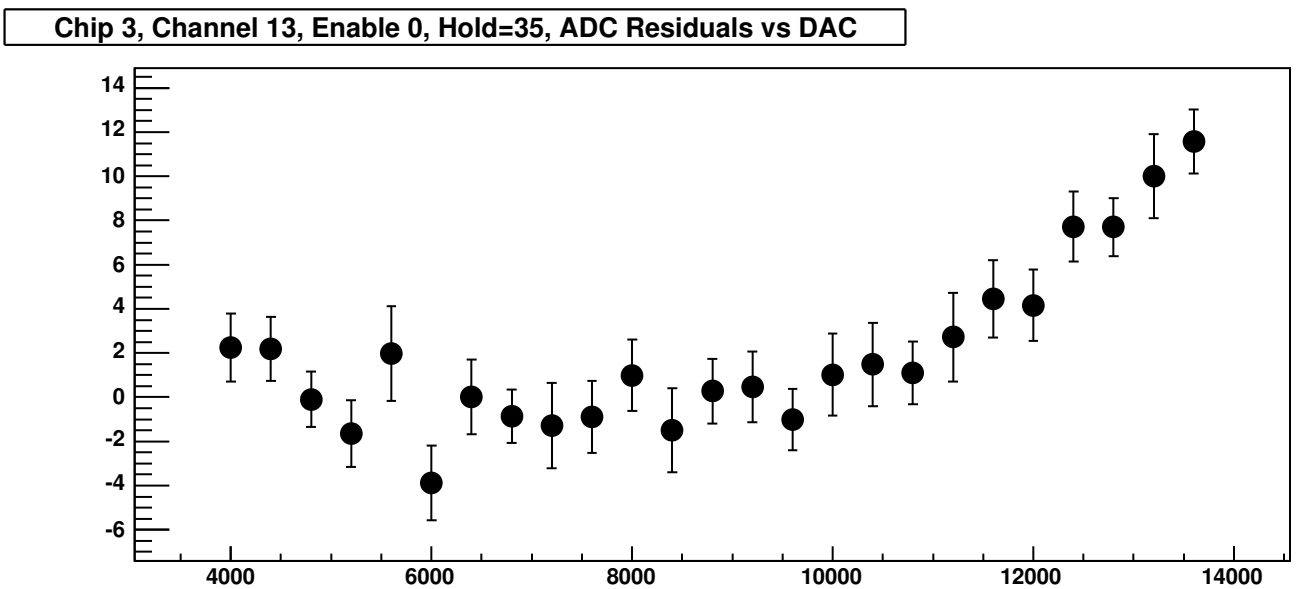

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1824 / 16
p0 168 ± 2.204
p1 2.579 ± 0.0003155
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

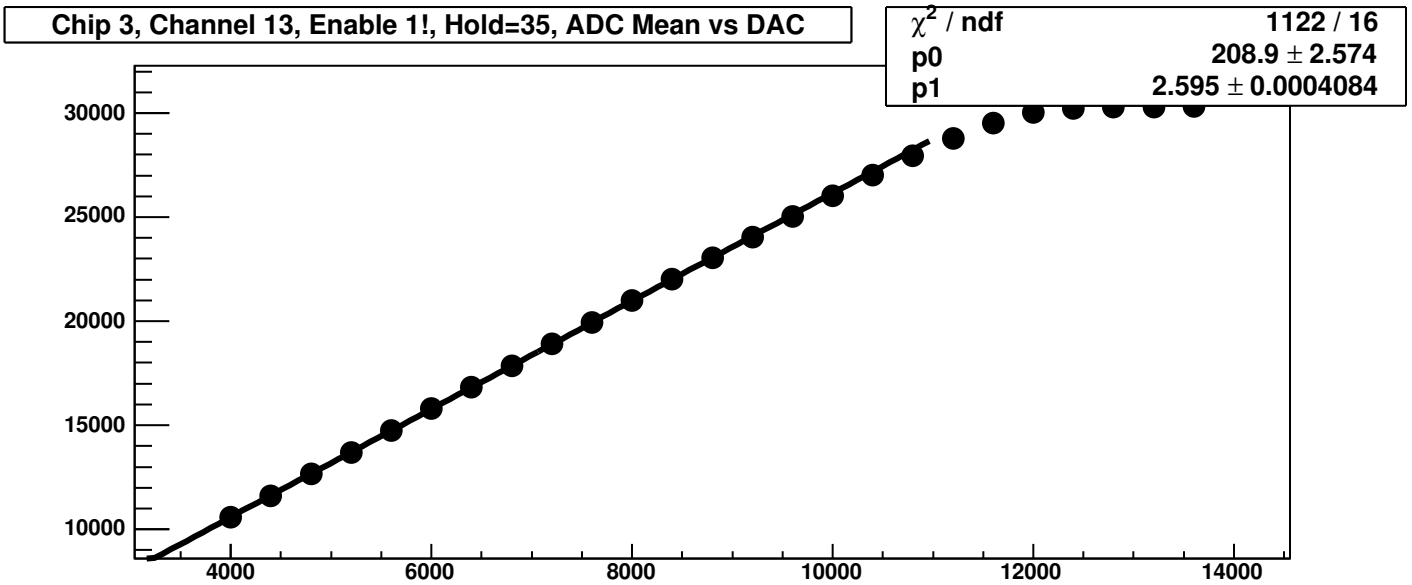

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

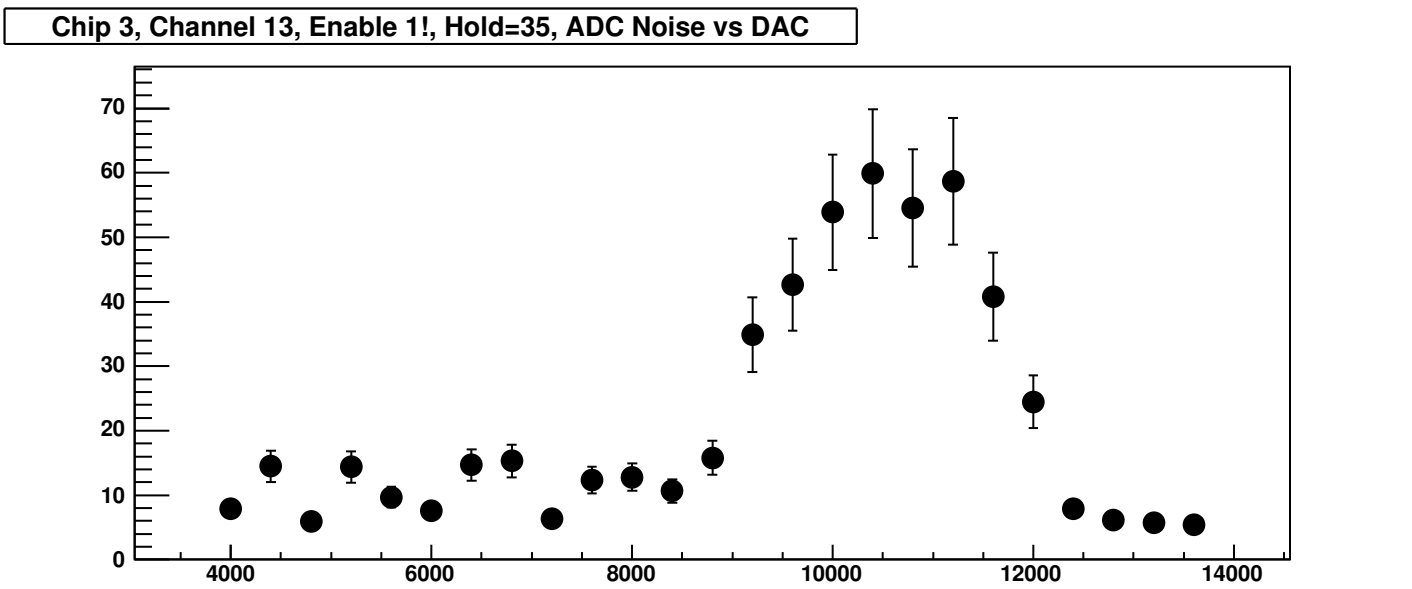

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

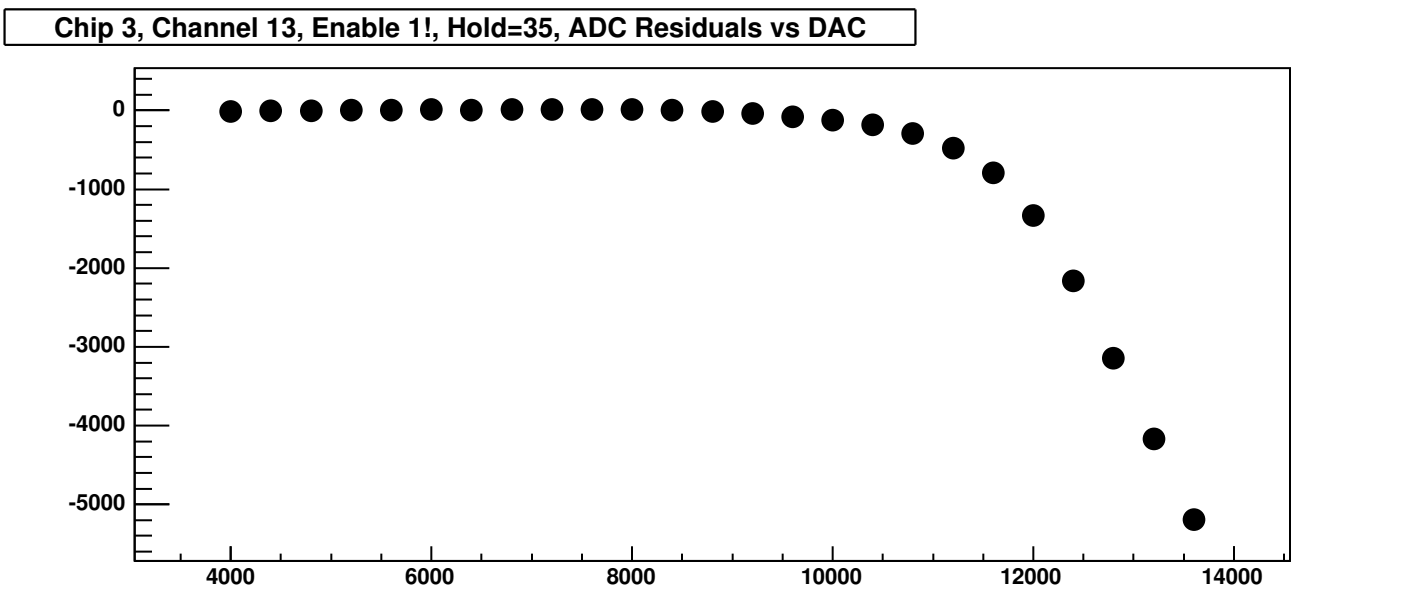

Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 16.2 / 16
p0 -2.505 ± 1.323
p1 0.01333 ± 0.0001743
200
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

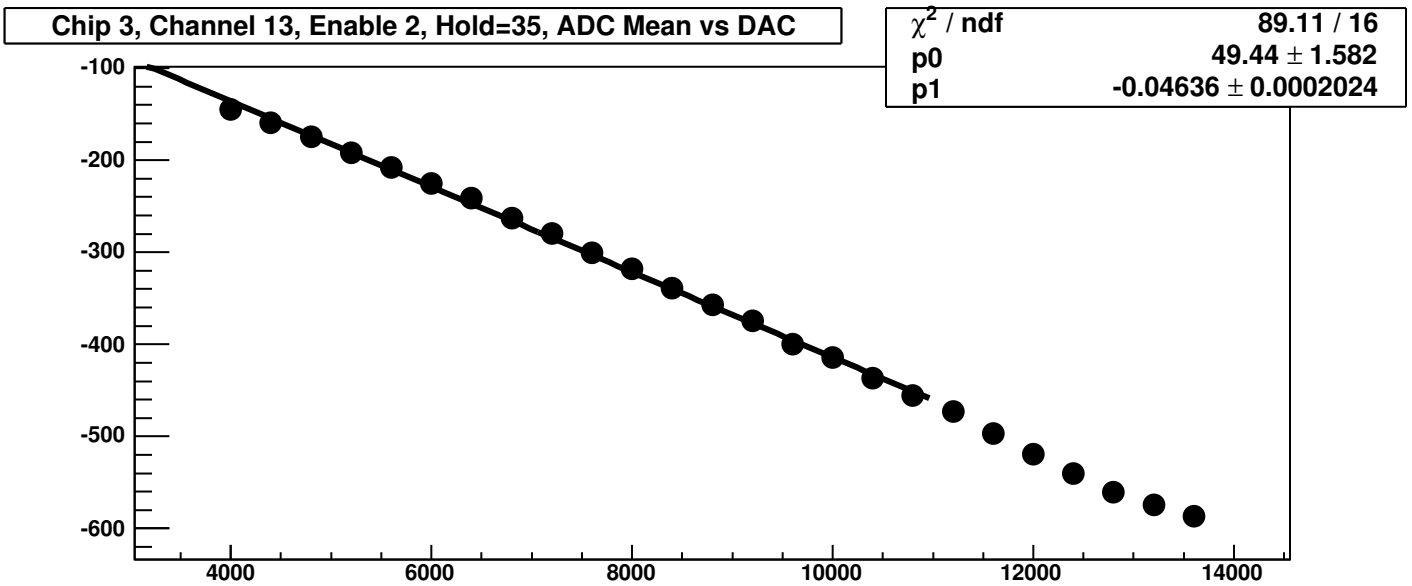

Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

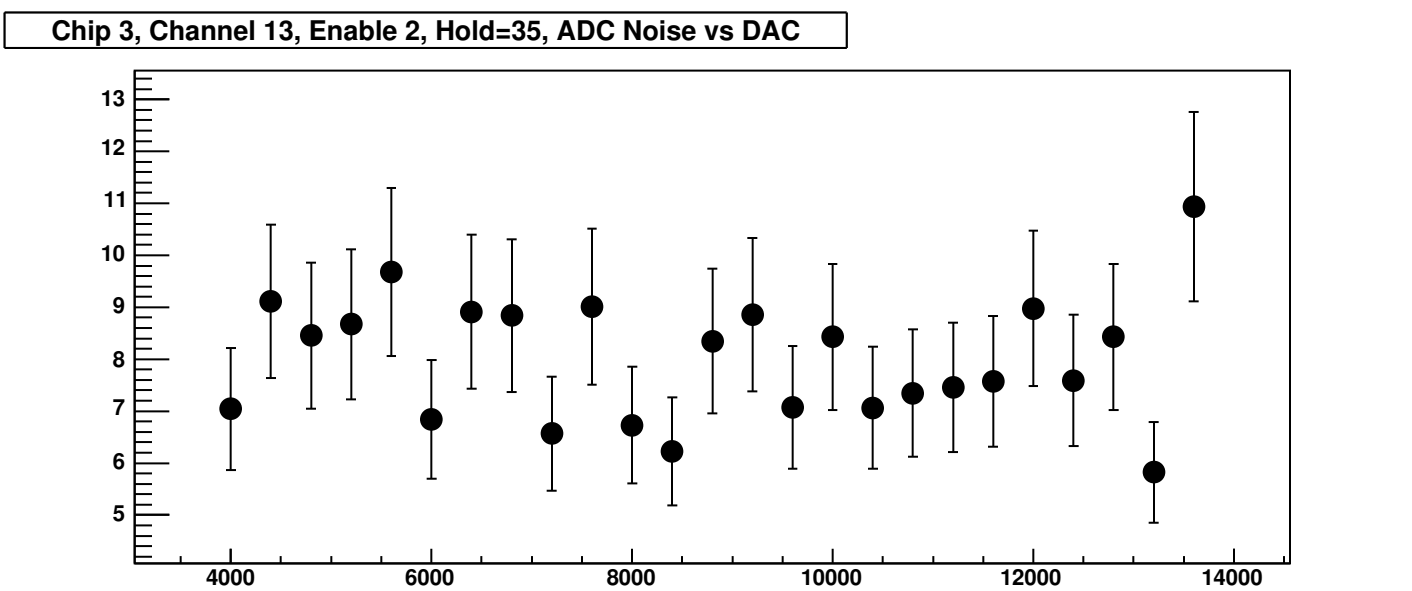

Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

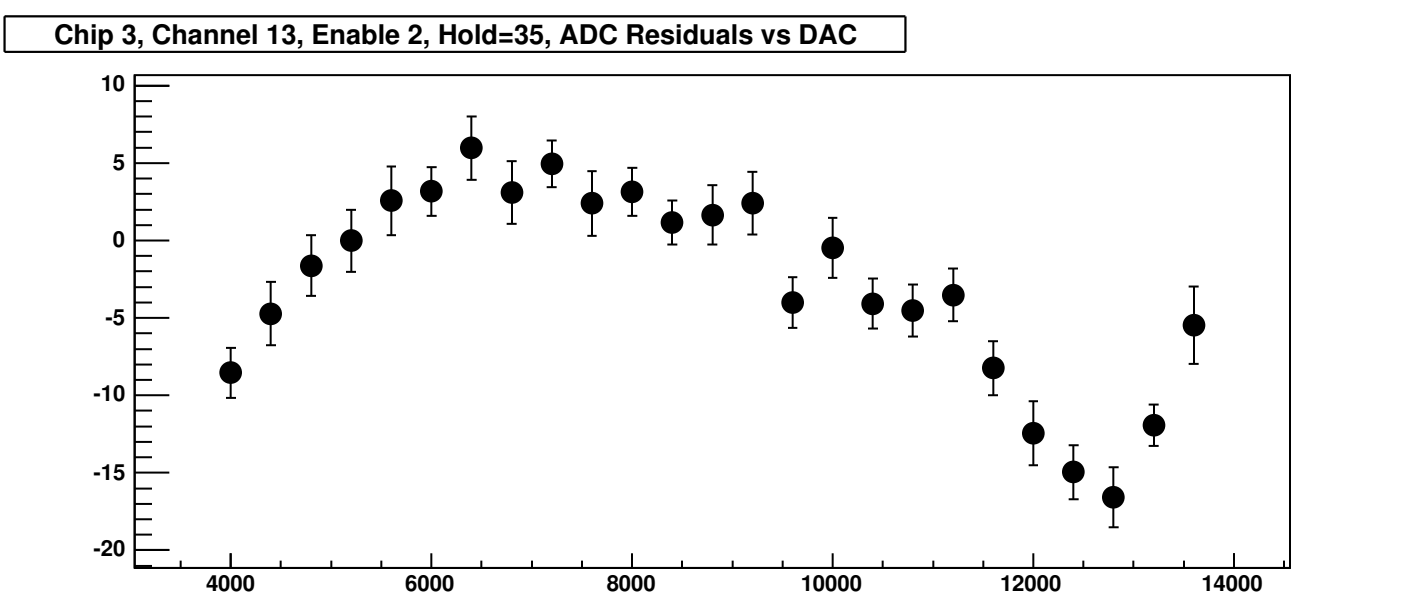

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1122 / 16
p0 208.9 ± 2.574
p1 2.595 ± 0.0004084
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

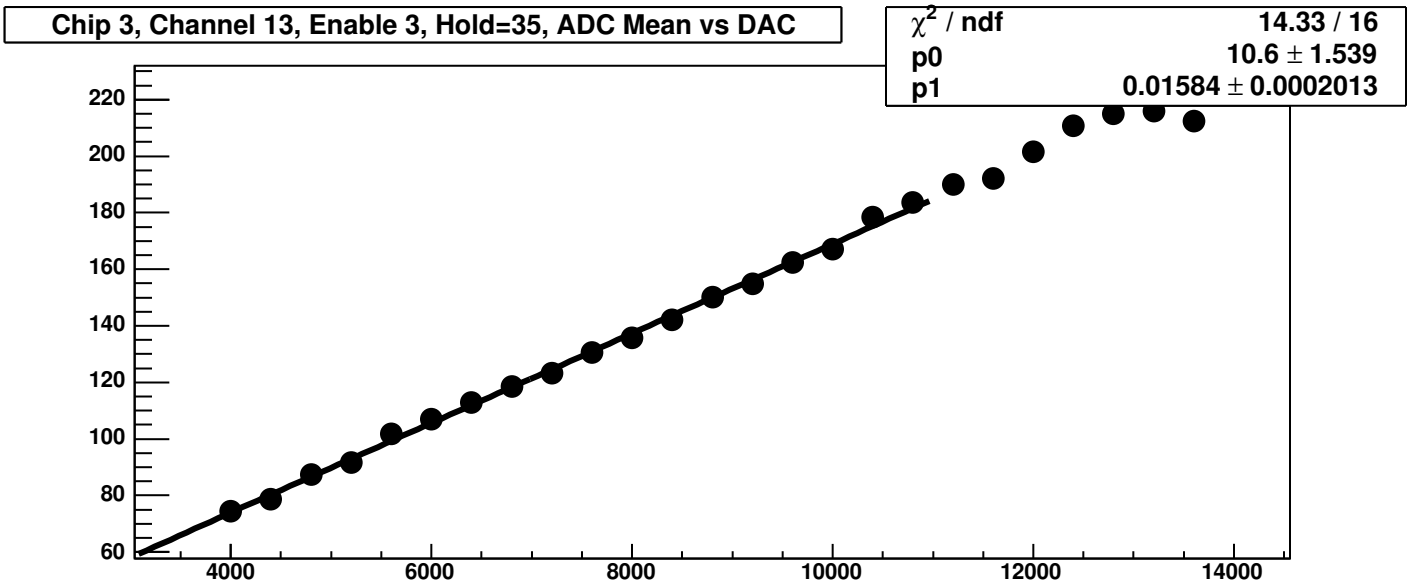

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

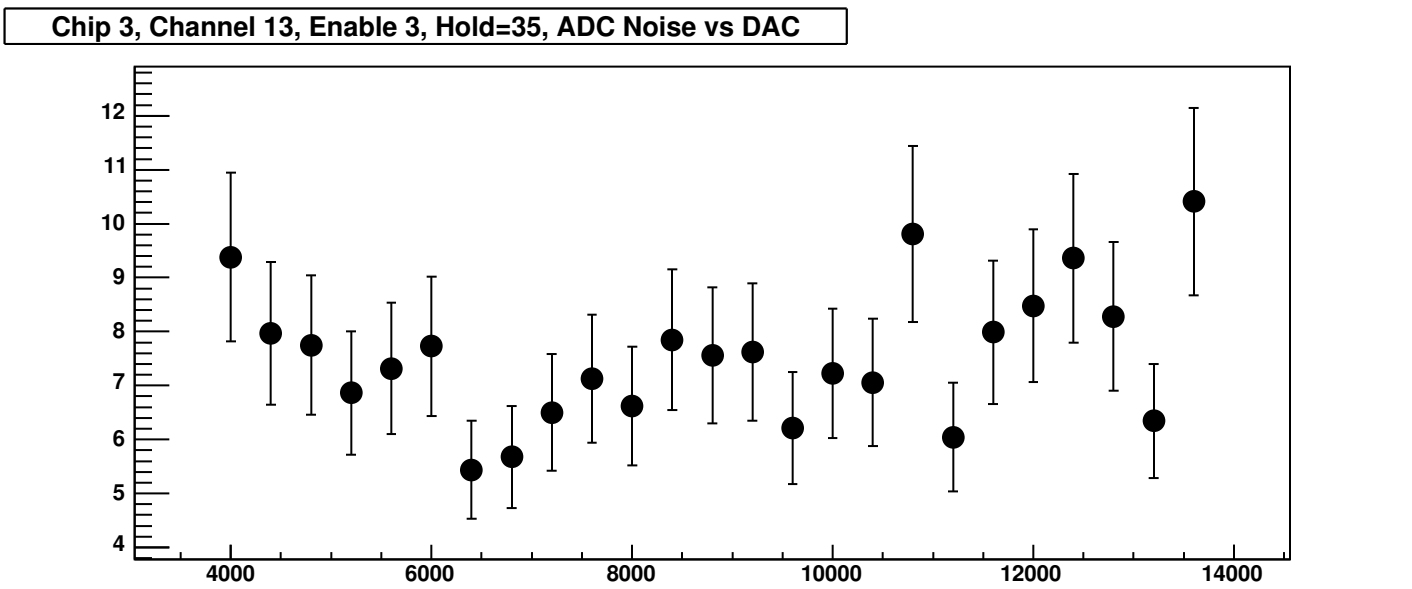

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

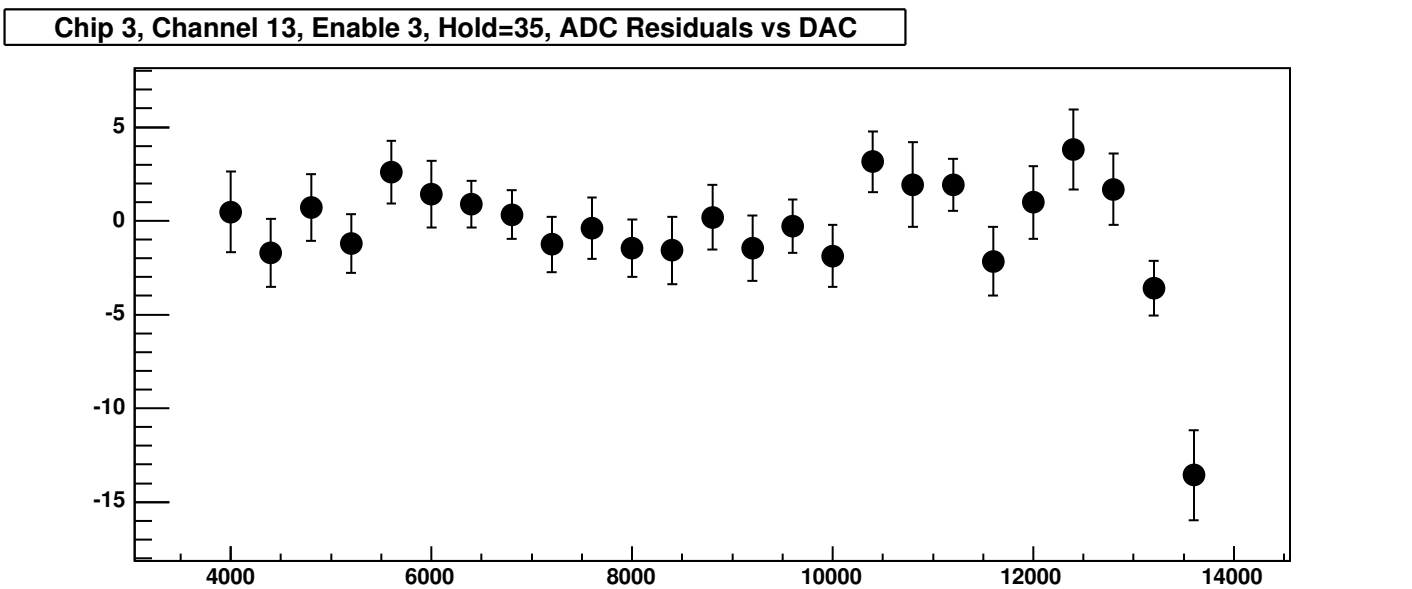

Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 89.11 / 16
p0 49.44 ± 1.582
p1 -0.04636 ± 0.0002024
-100
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

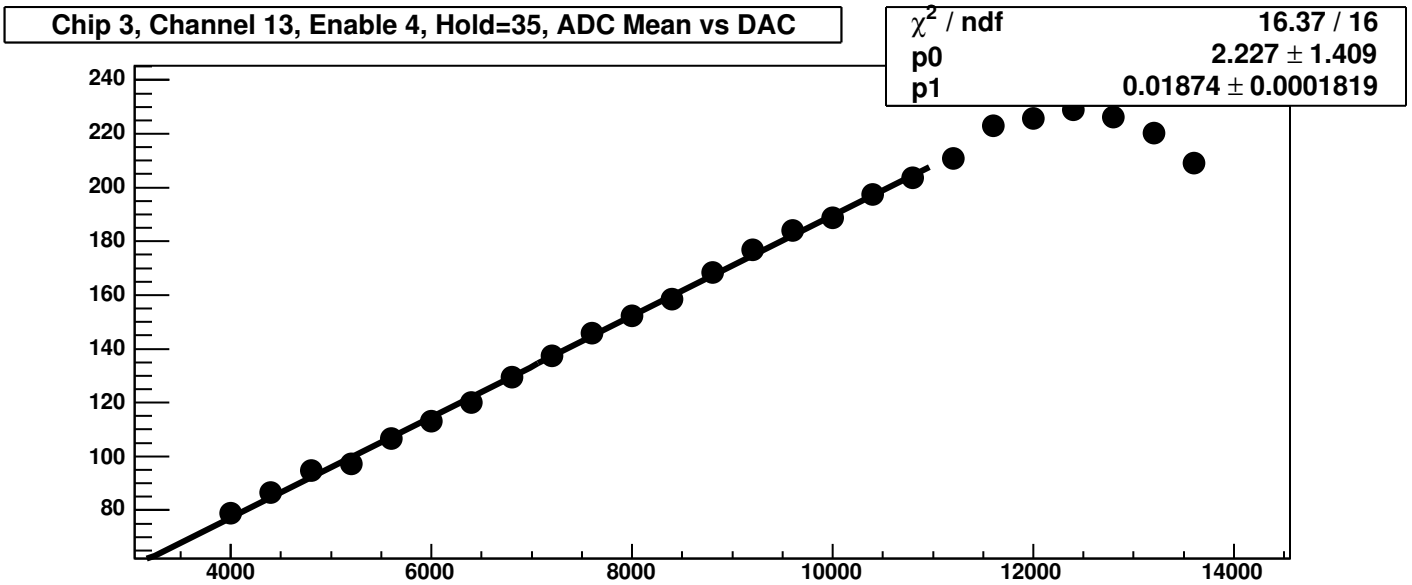

Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

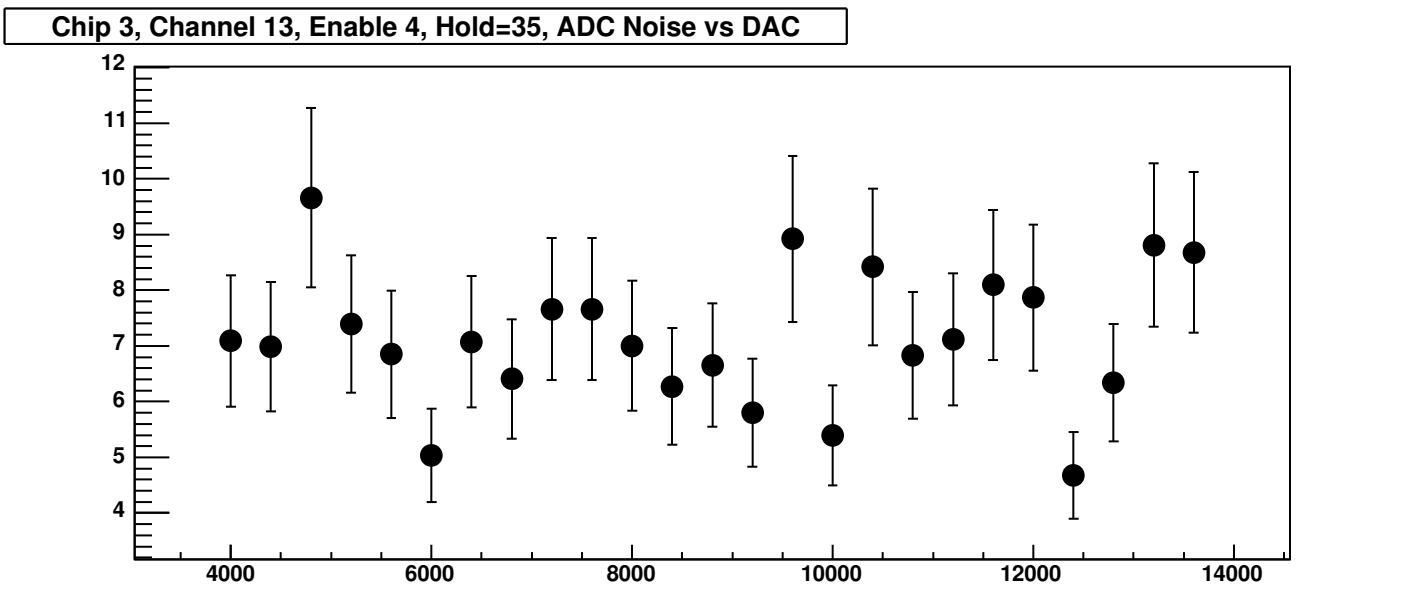

Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

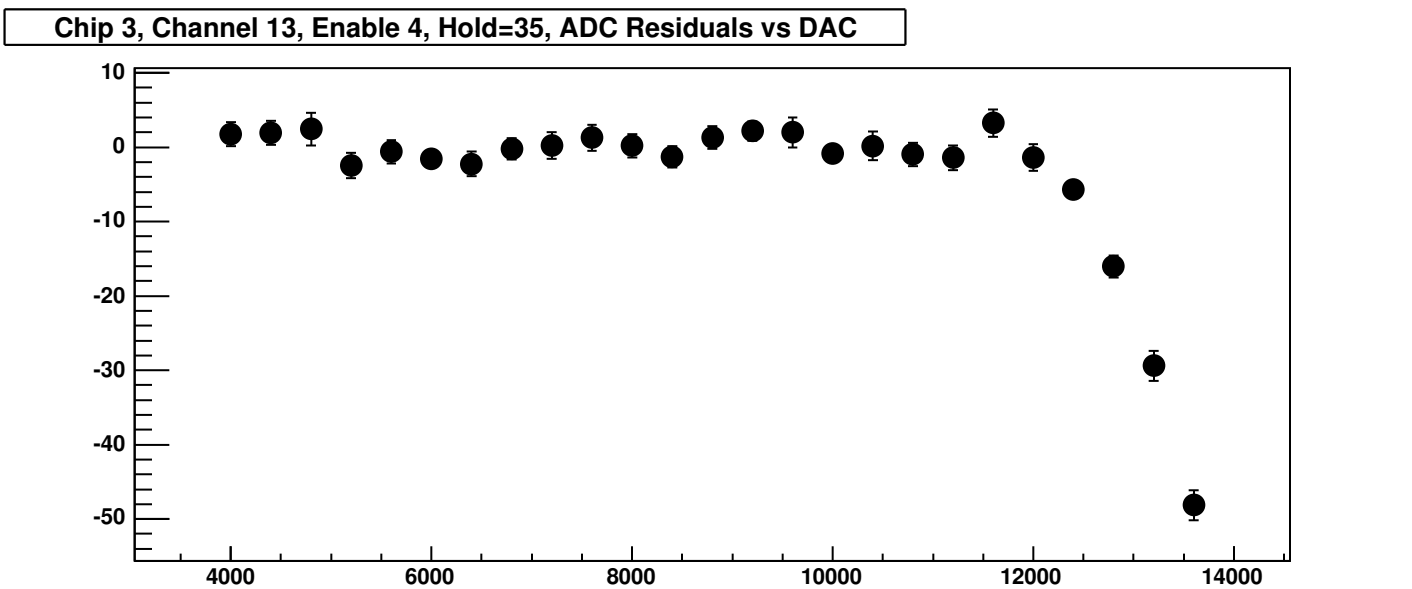

Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.33 / 16
p0 10.6 ± 1.539
p1 0.01584 ± 0.0002013
220
200
180
160
140
120
100
80
60
4000
6000
8000
10000
12000
14000

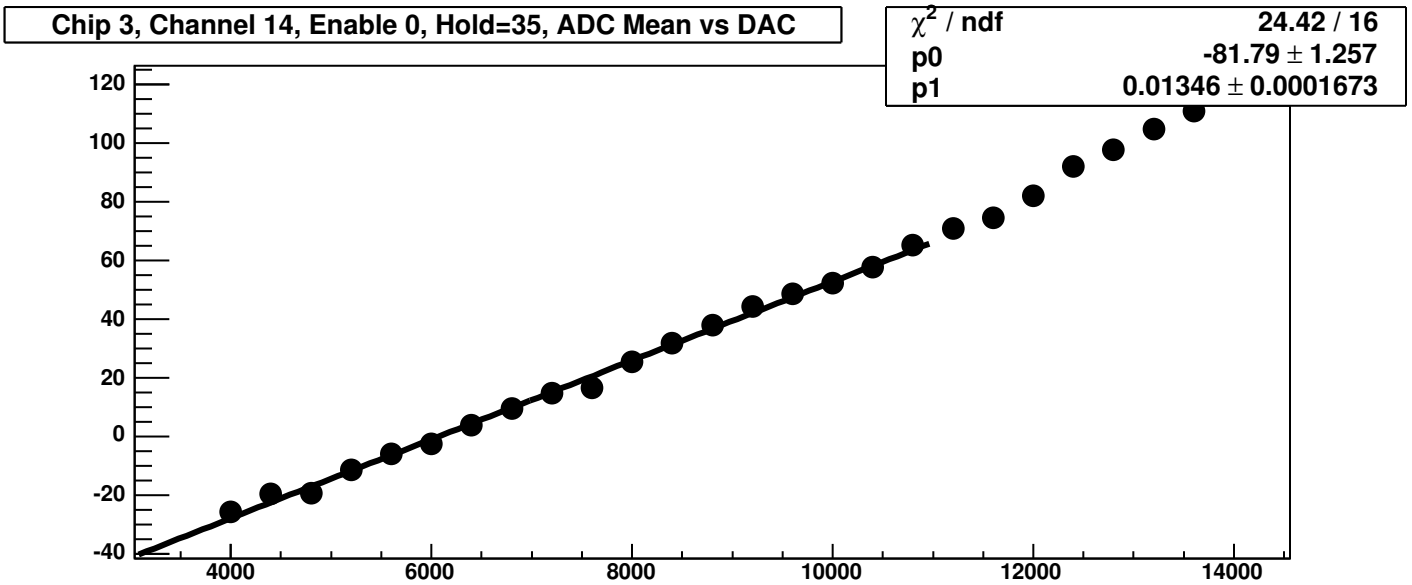

Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

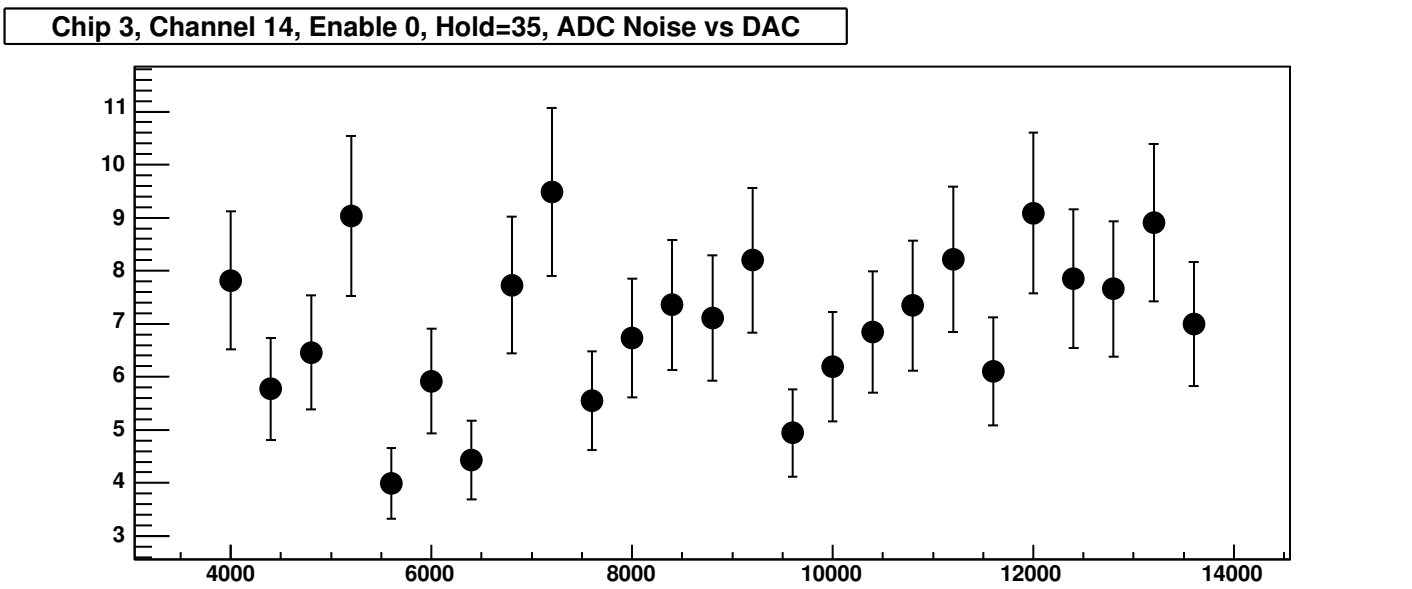

Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

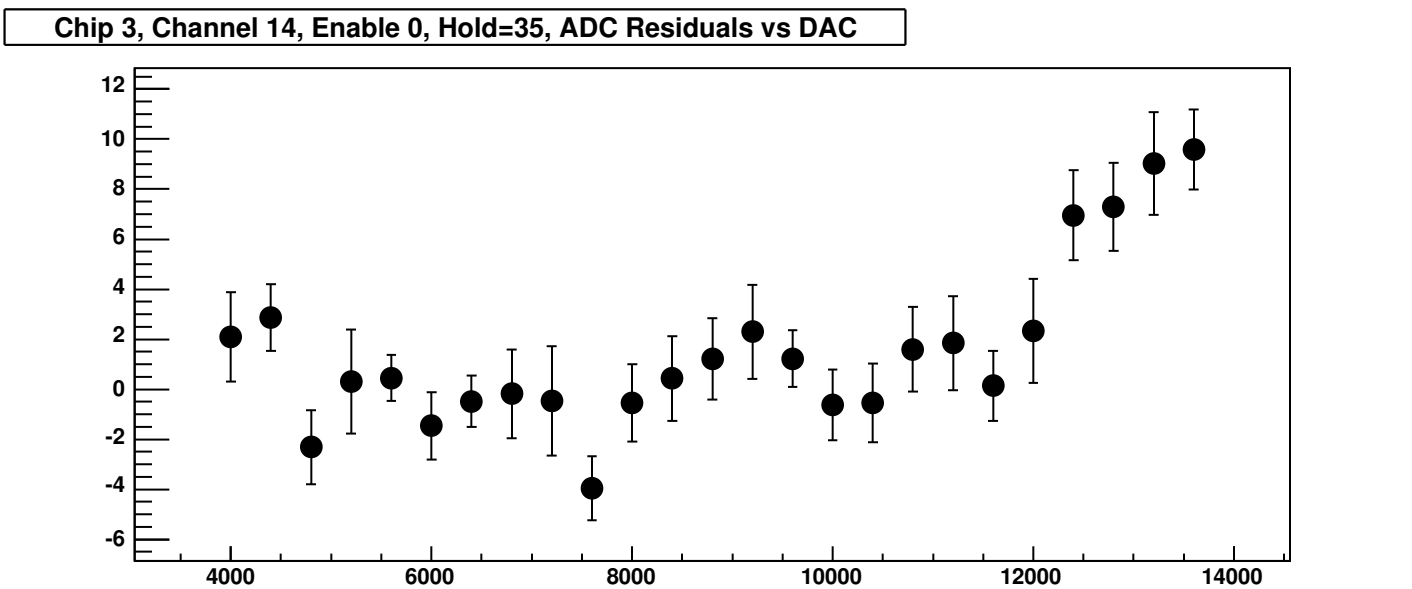

Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.37 / 16
p0 2.227 ± 1.409
p1 0.01874 ± 0.0001819
240
220
200
180
160
140
120
100
80
4000
6000
8000
10000
12000
14000

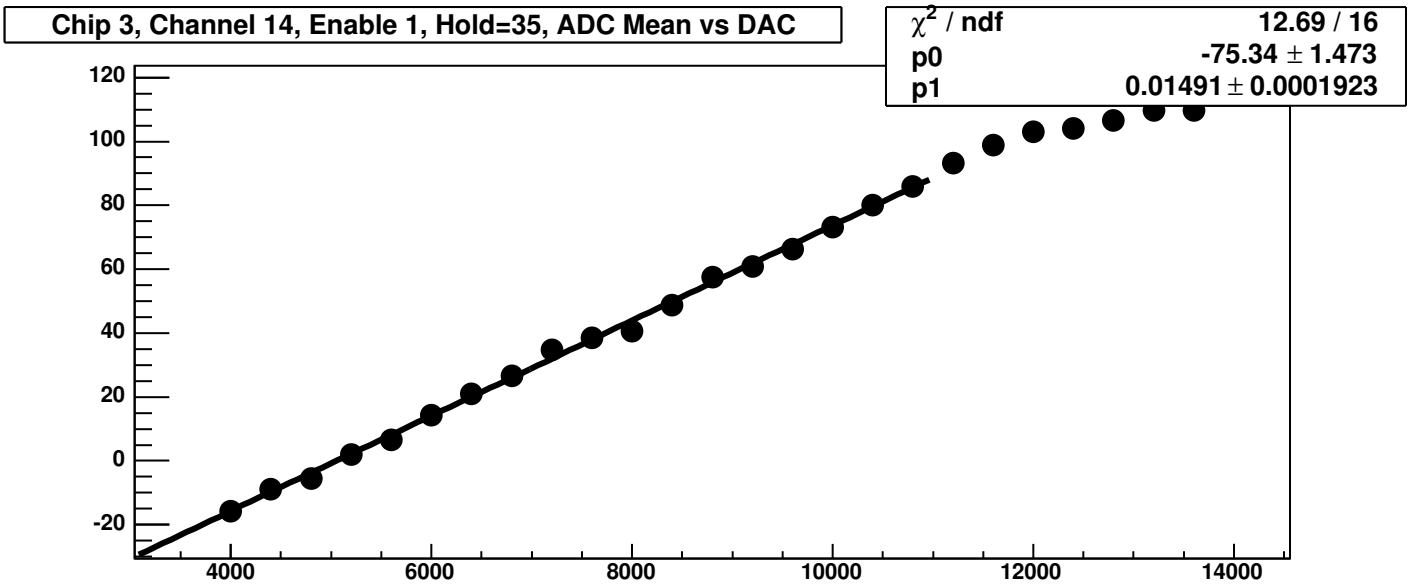

Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

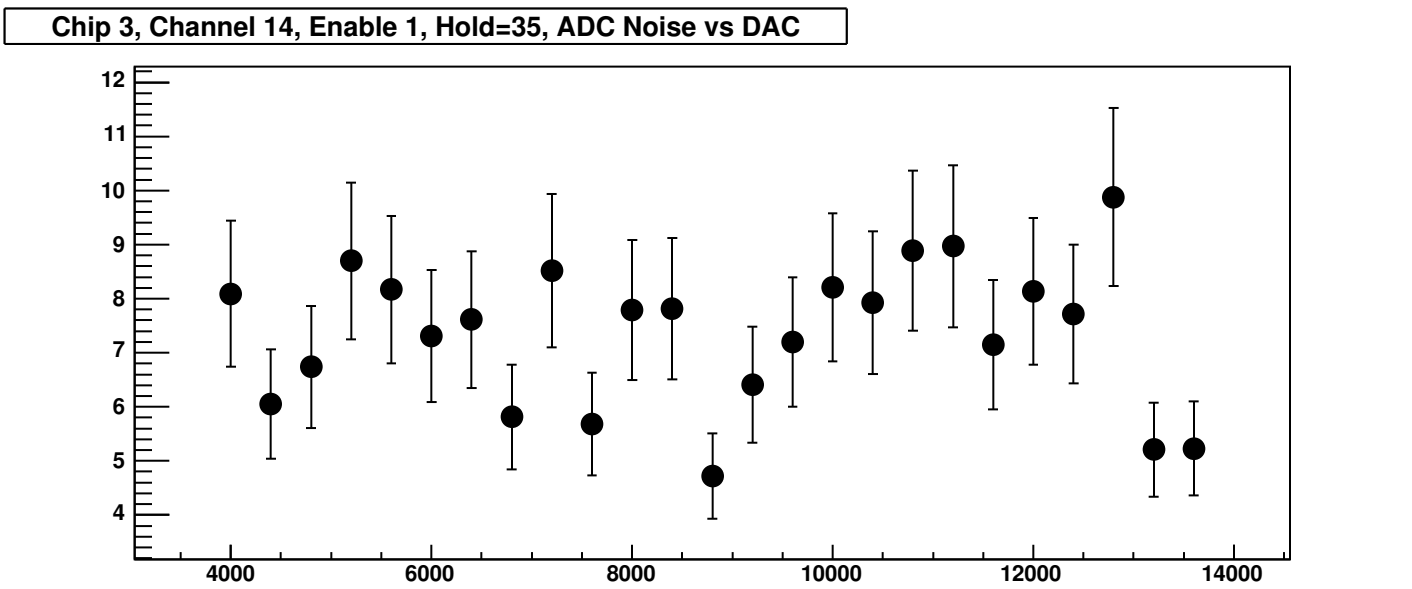

Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

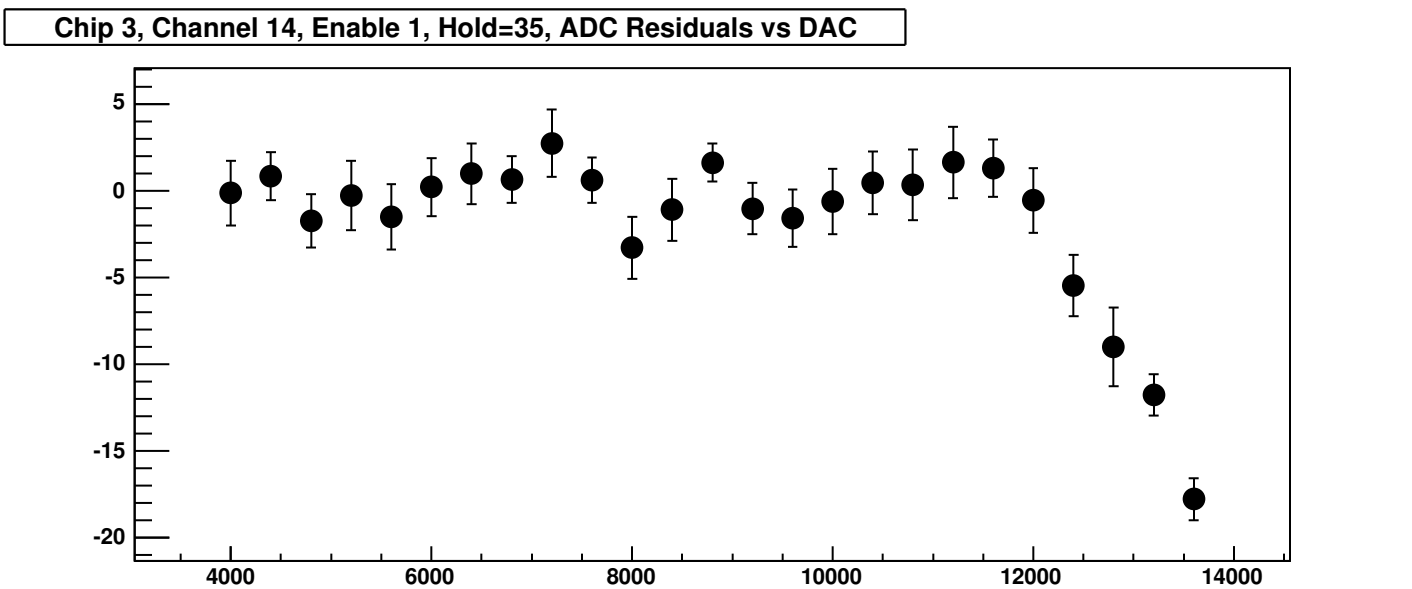

Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 24.42 / 16
p0 -81.79 ± 1.257
p1 0.01346 ± 0.0001673
120
100
80
60
40
20
0
-20
-40
4000
6000
8000
10000
12000
14000

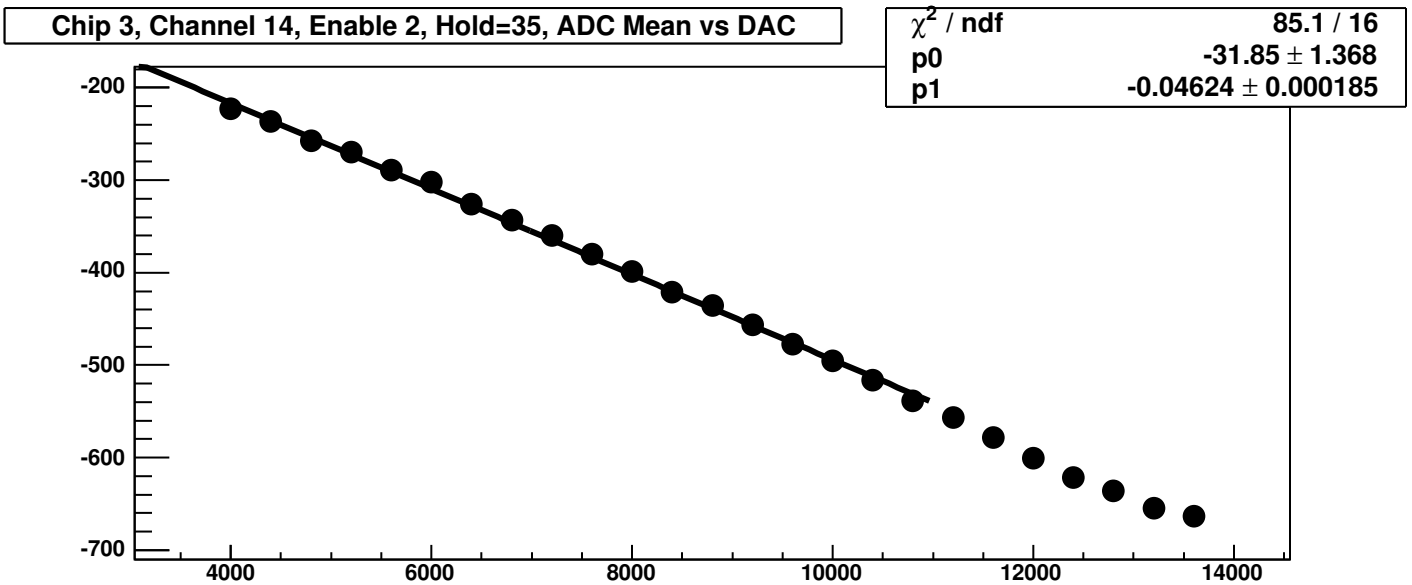

Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

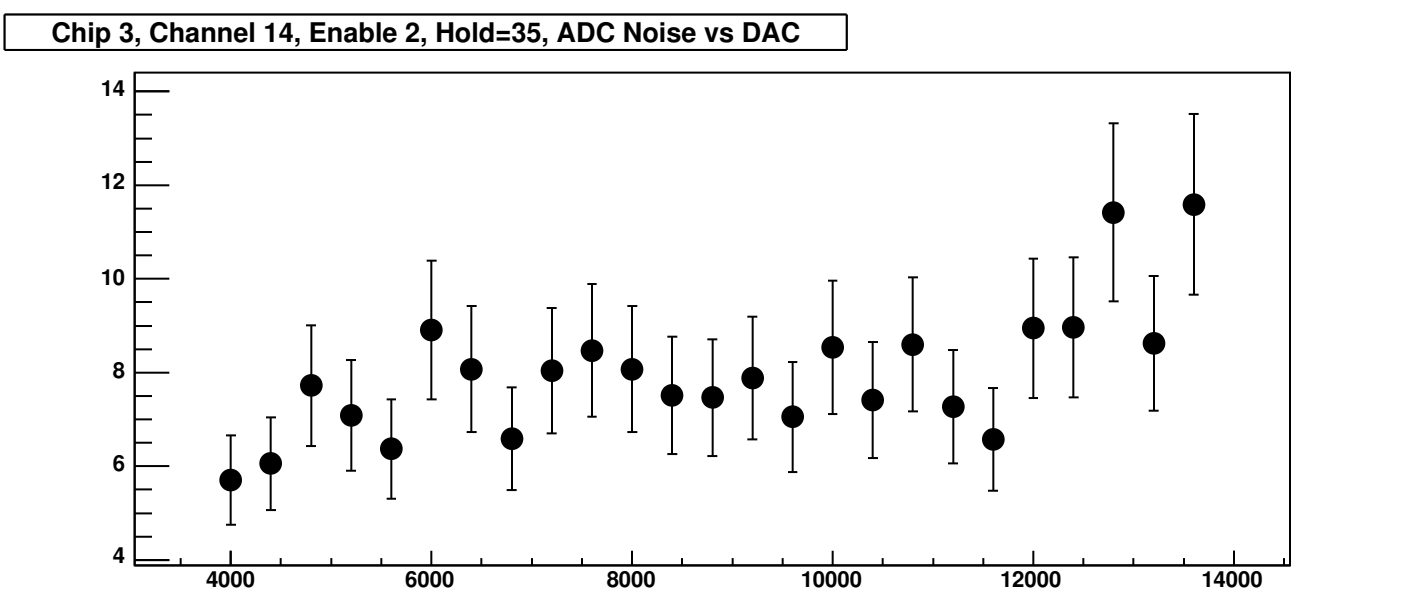

Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

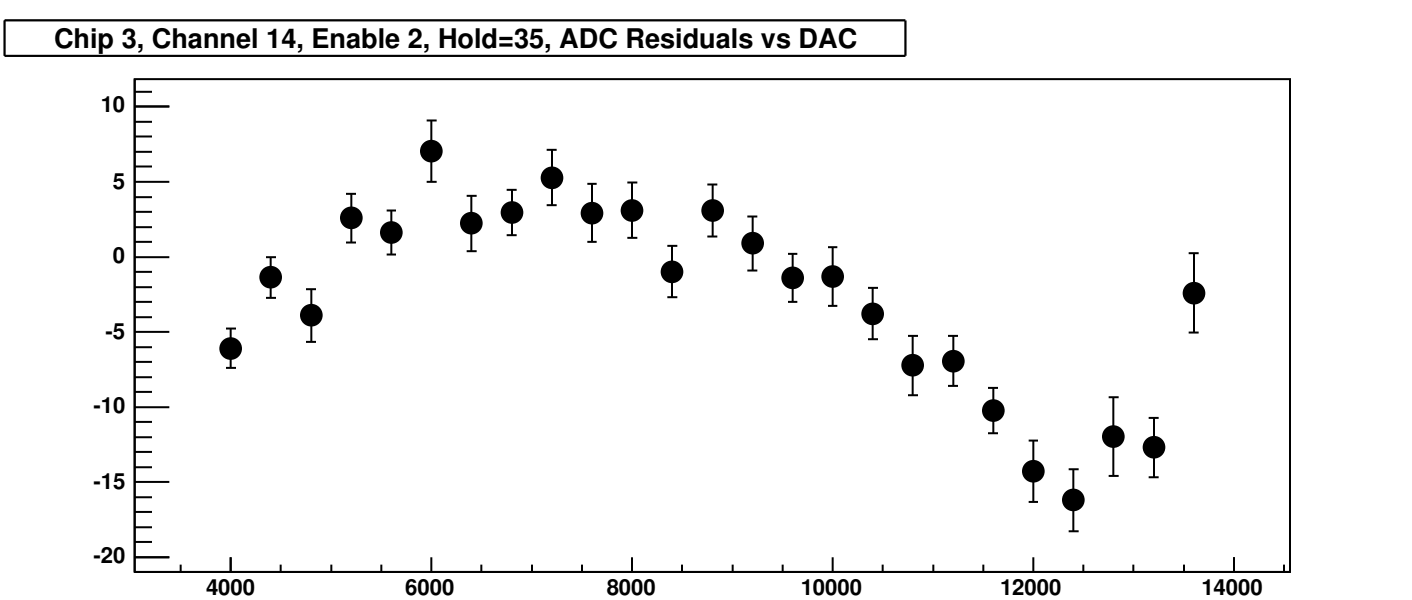

Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 12.69 / 16
p0 -75.34 ± 1.473
p1 0.01491 ± 0.0001923
120
100
80
60
40
20
0
-20
4000
6000
8000
10000
12000
14000

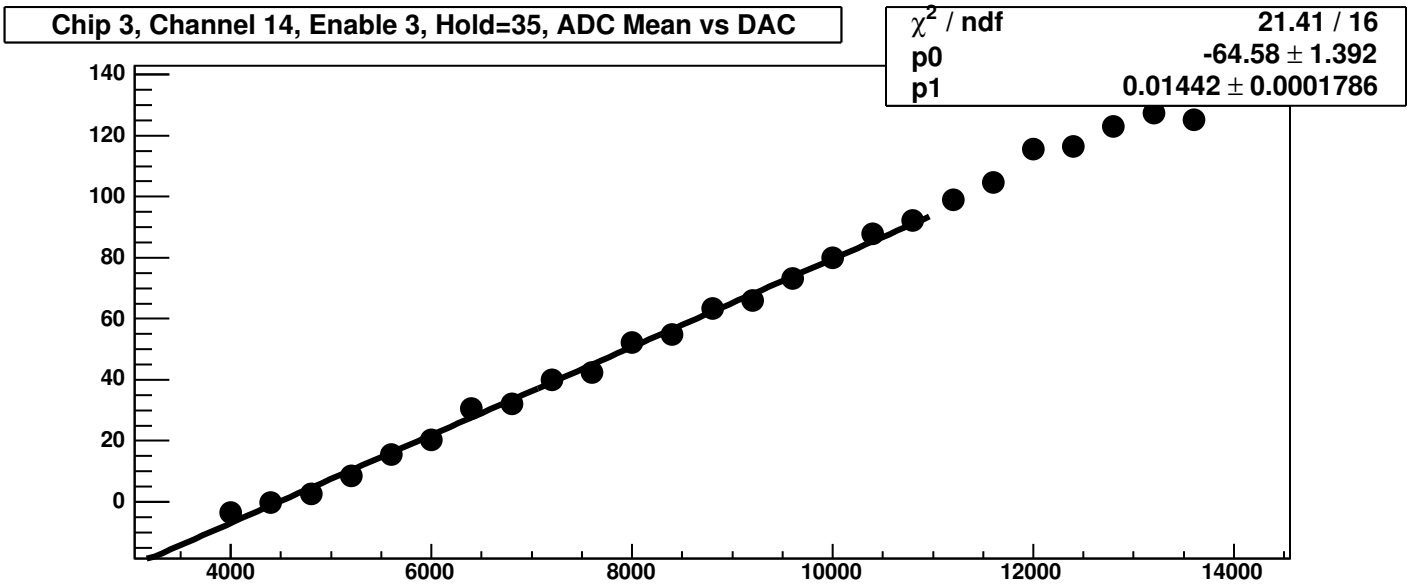

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

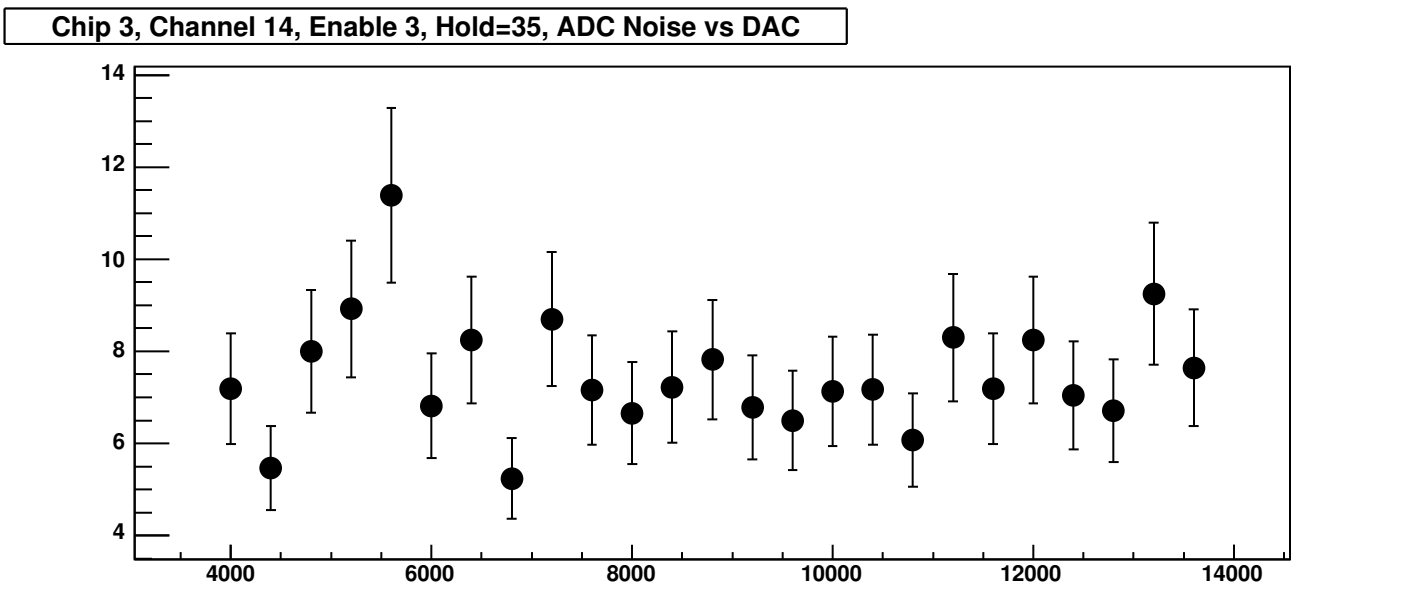

Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

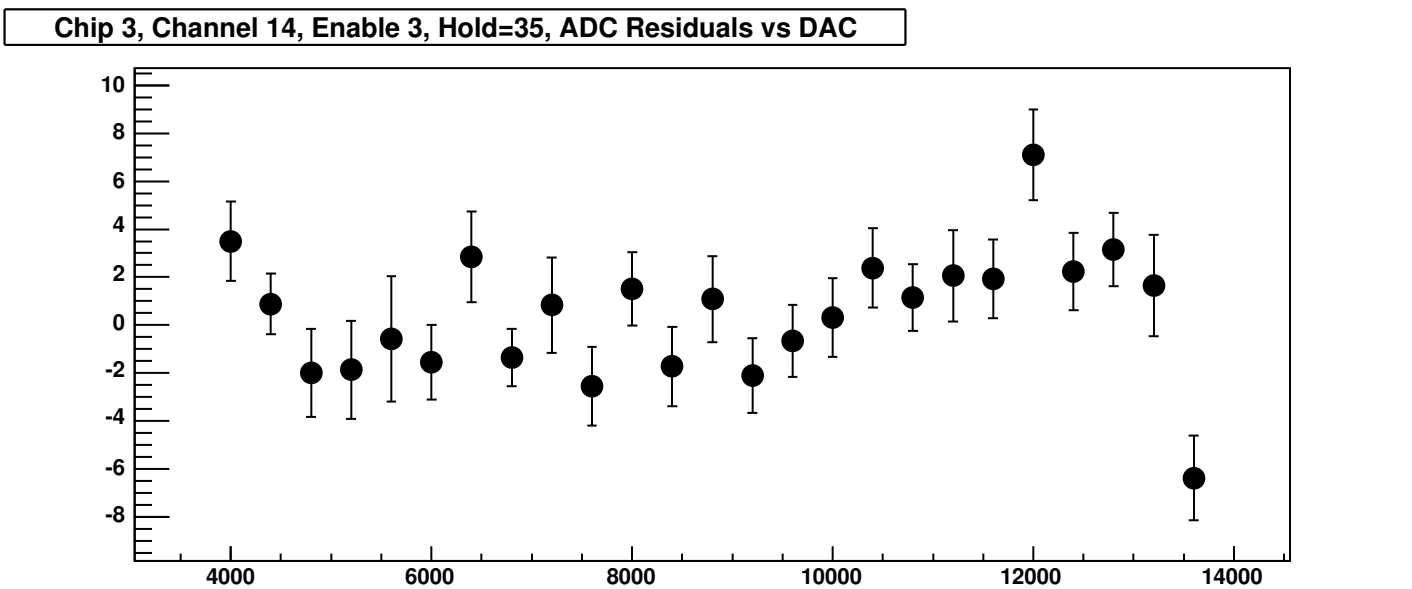

Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 85.1 / 16
p0 -31.85 ± 1.368
p1 -0.04624 ± 0.000185
-200
-300
-400
-500
-600
-700
4000
6000
8000
10000
12000
14000

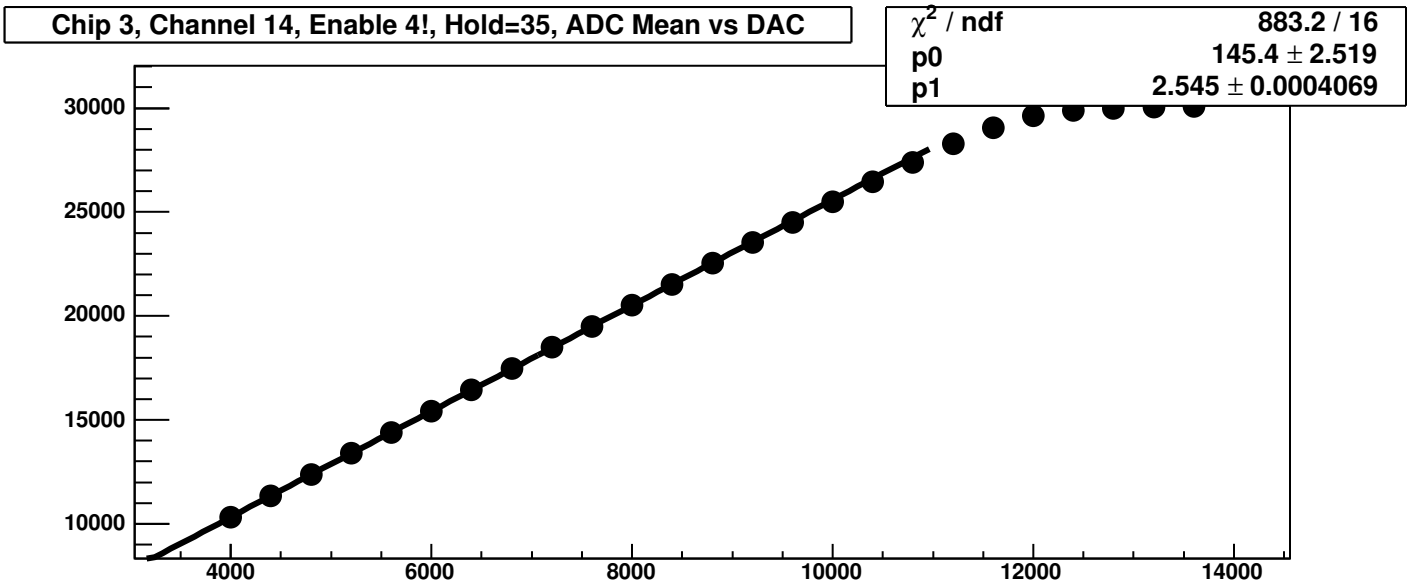

Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

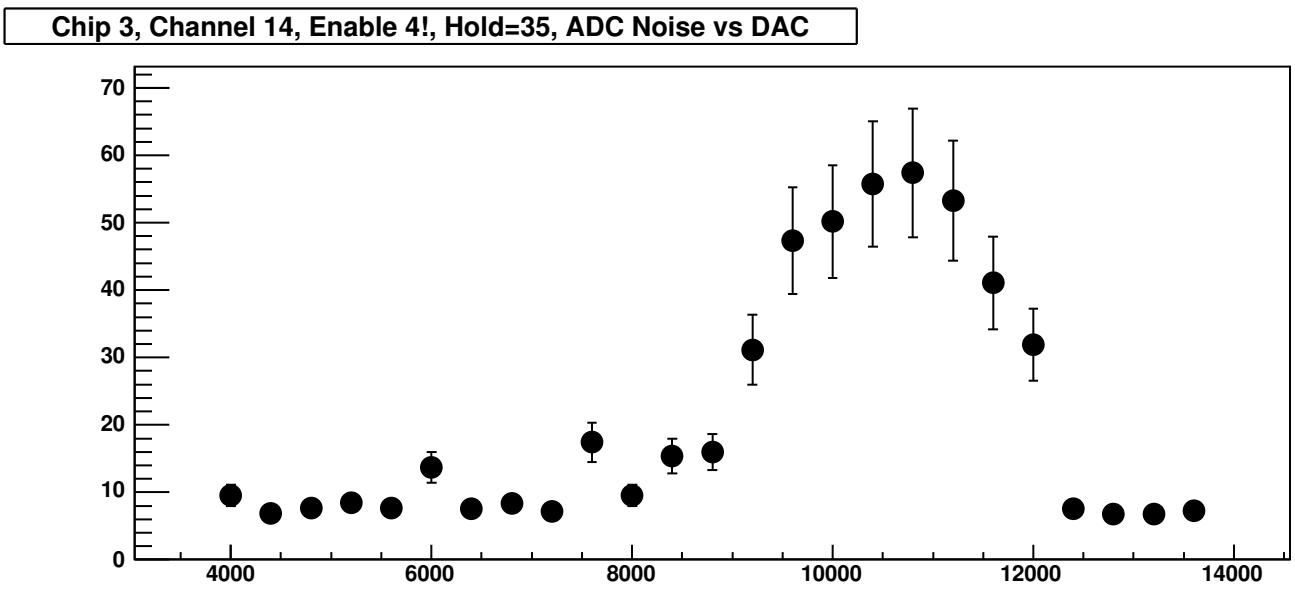

Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

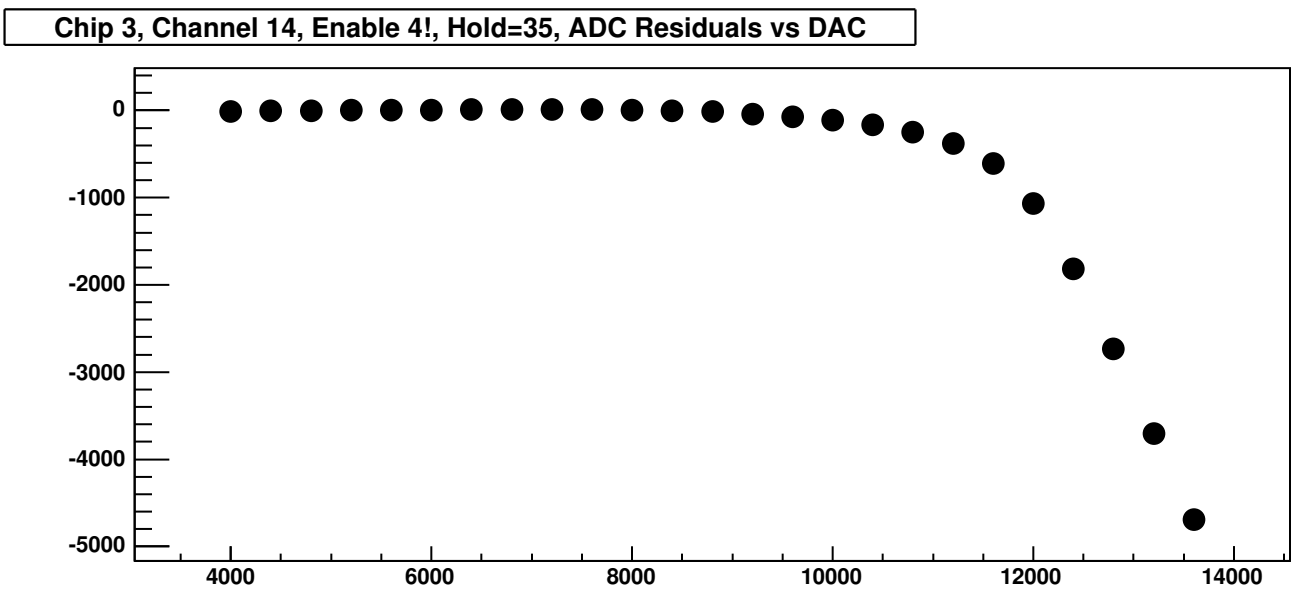

Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.41 / 16
p0 -64.58 ± 1.392
p1 0.01442 ± 0.0001786
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

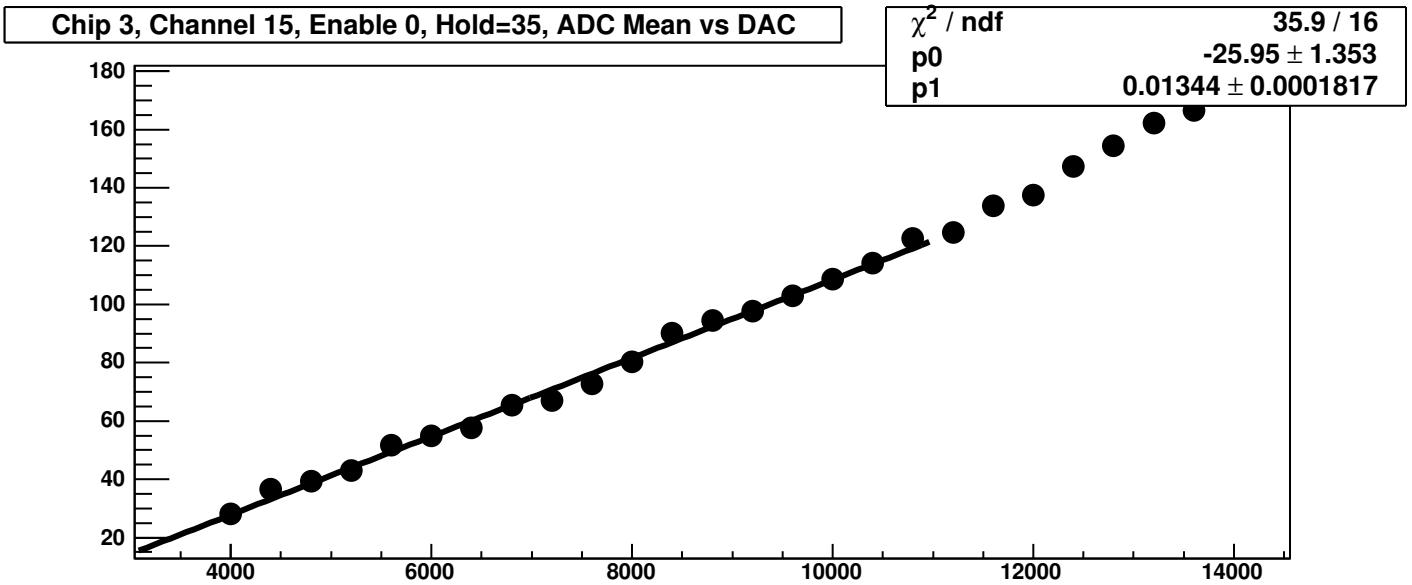

Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

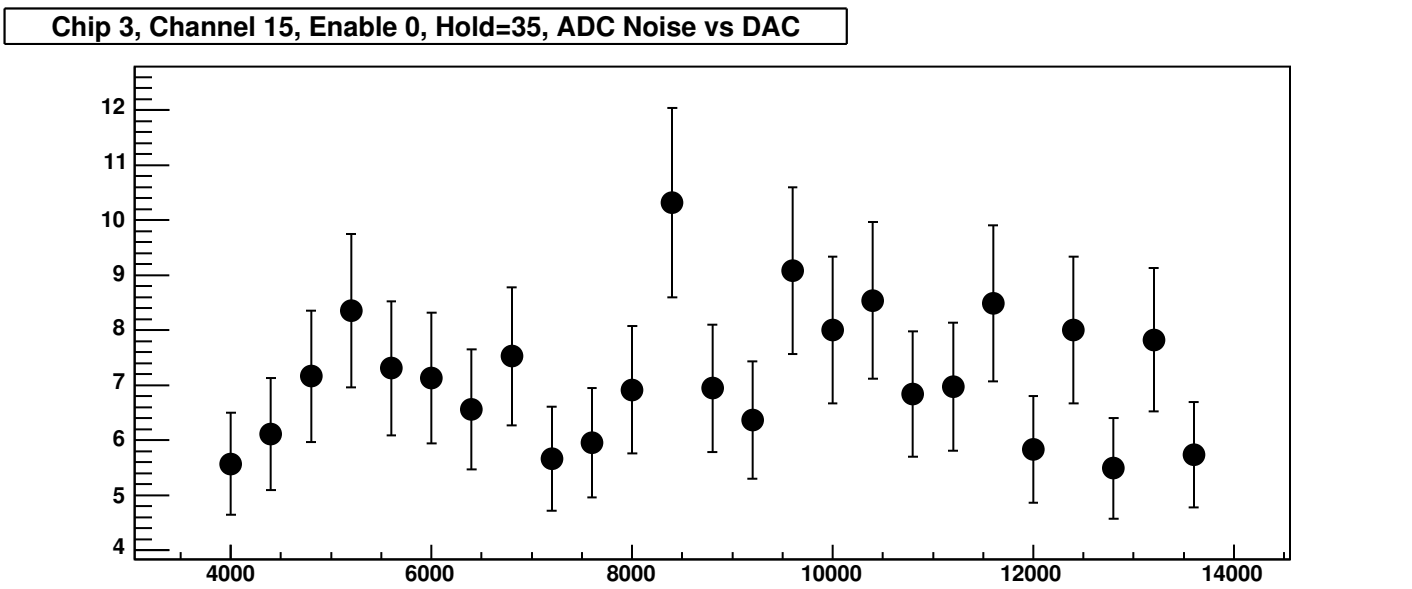

Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

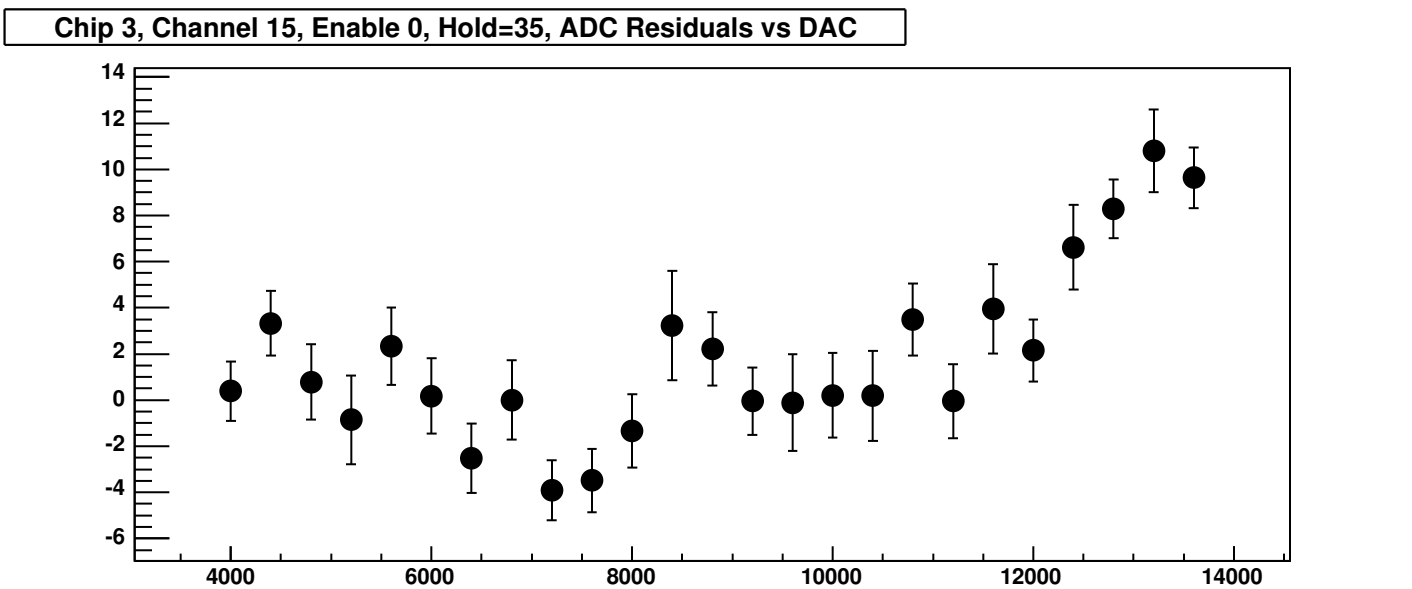

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 883.2 / 16
p0 145.4 ± 2.519
p1 2.545 ± 0.0004069
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

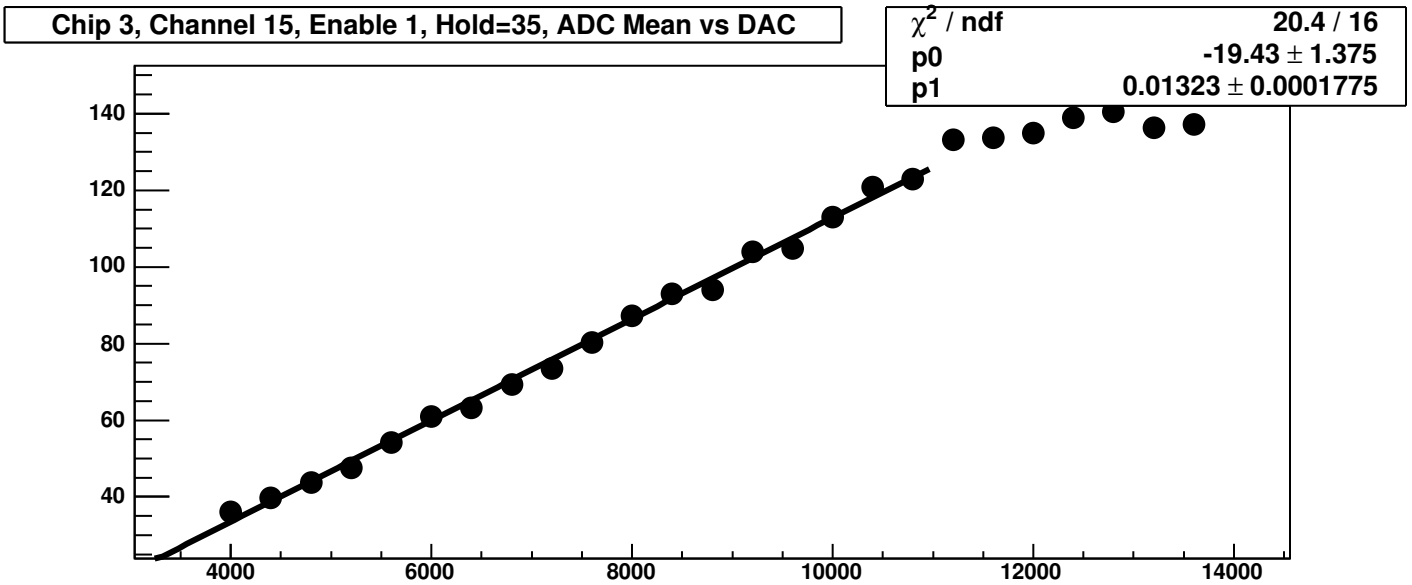

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

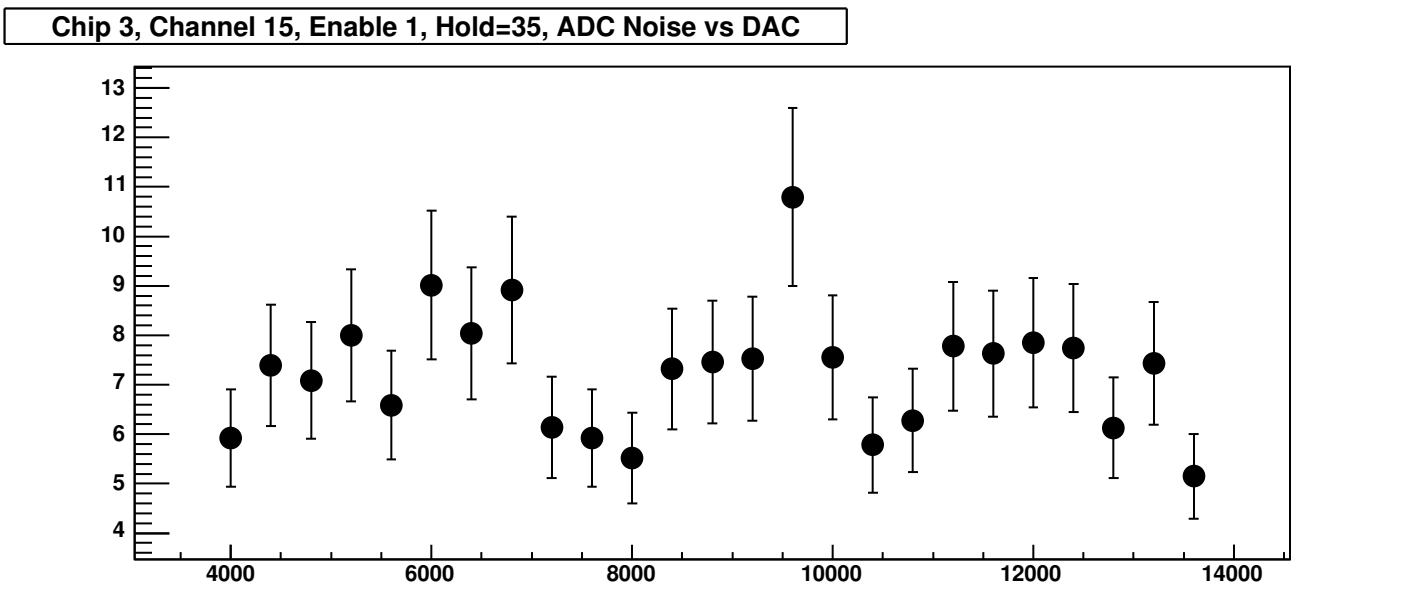

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

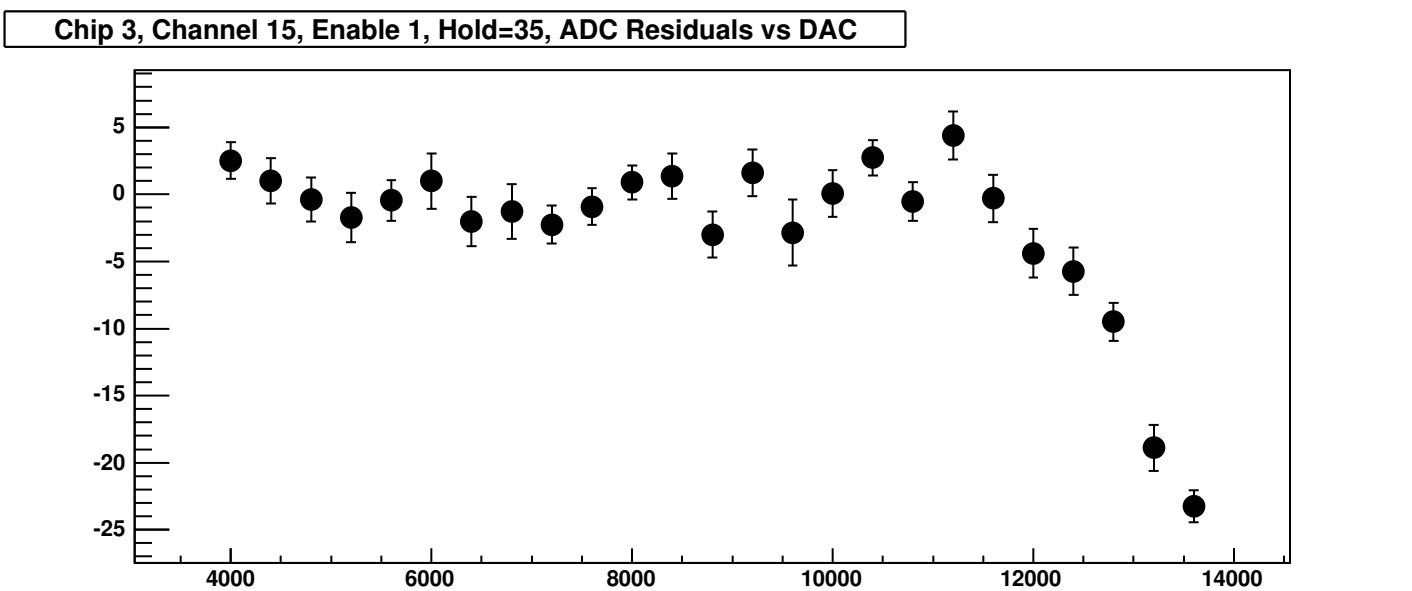

Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 35.9 / 16
p0 -25.95 ± 1.353
p1 0.01344 ± 0.0001817
180
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000

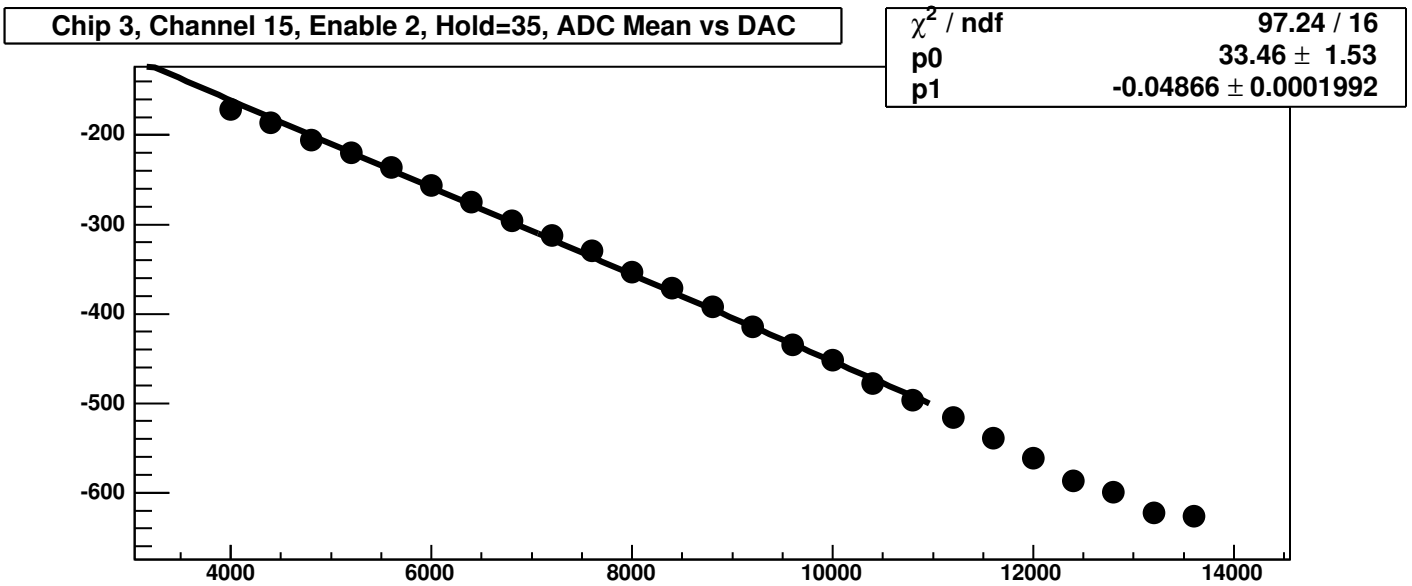

Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

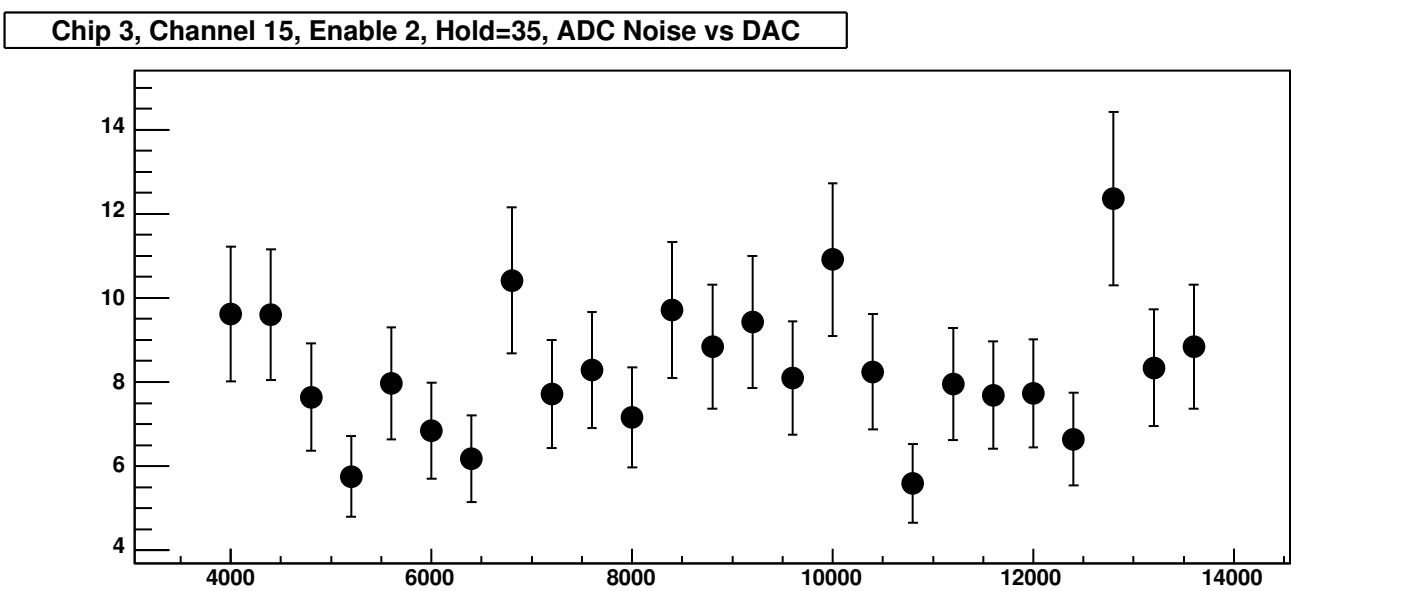

Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

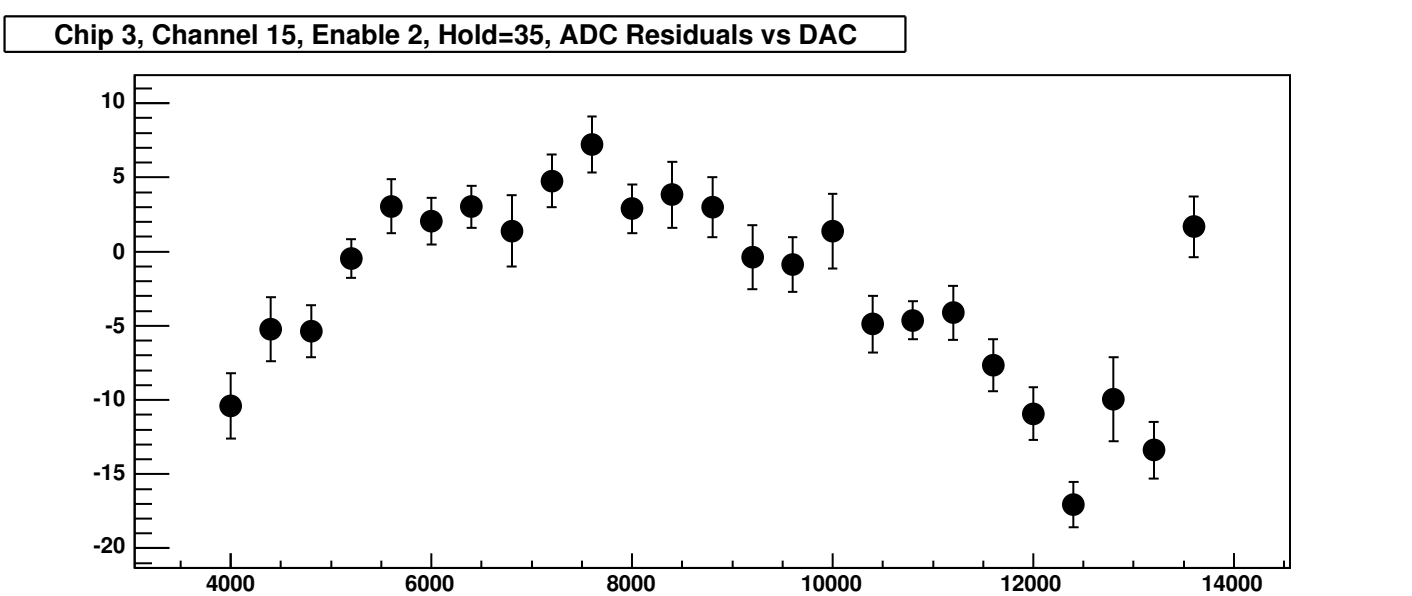

Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.4 / 16
p0 -19.43 ± 1.375
p1 0.01323 ± 0.0001775
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

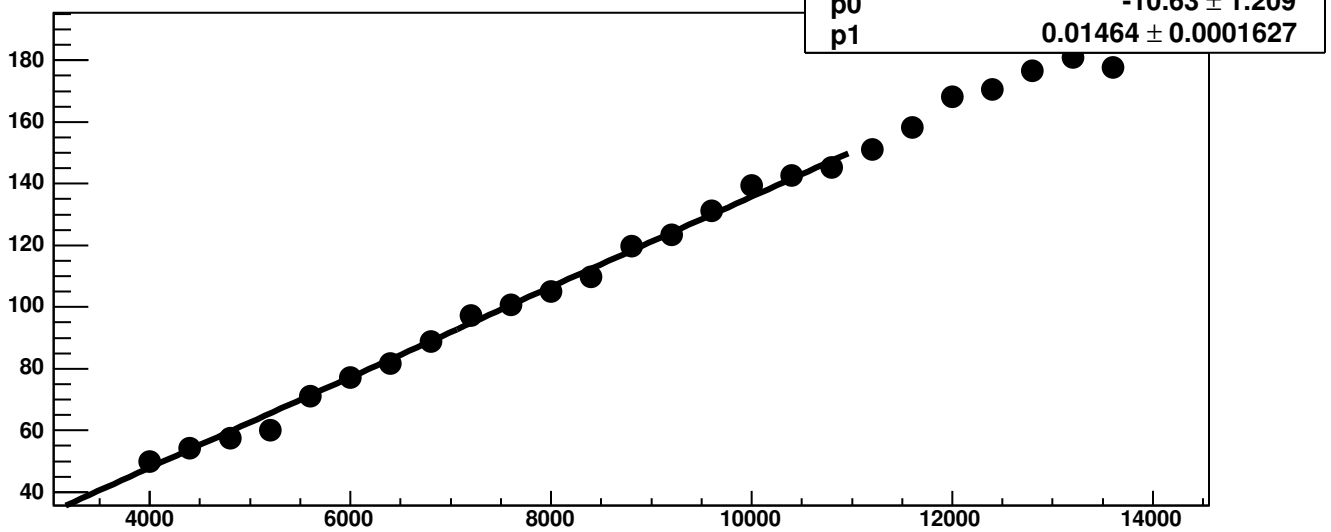

Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 97.24 / 16
p0 33.46 ± 1.53
p1 -0.04866 ± 0.0001992
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

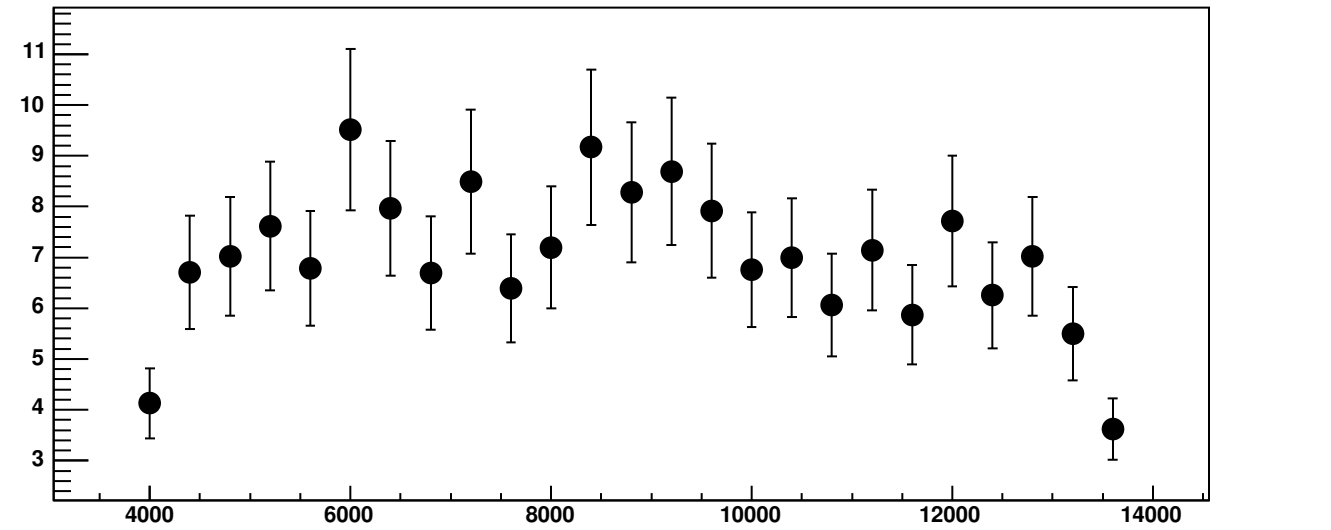

Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

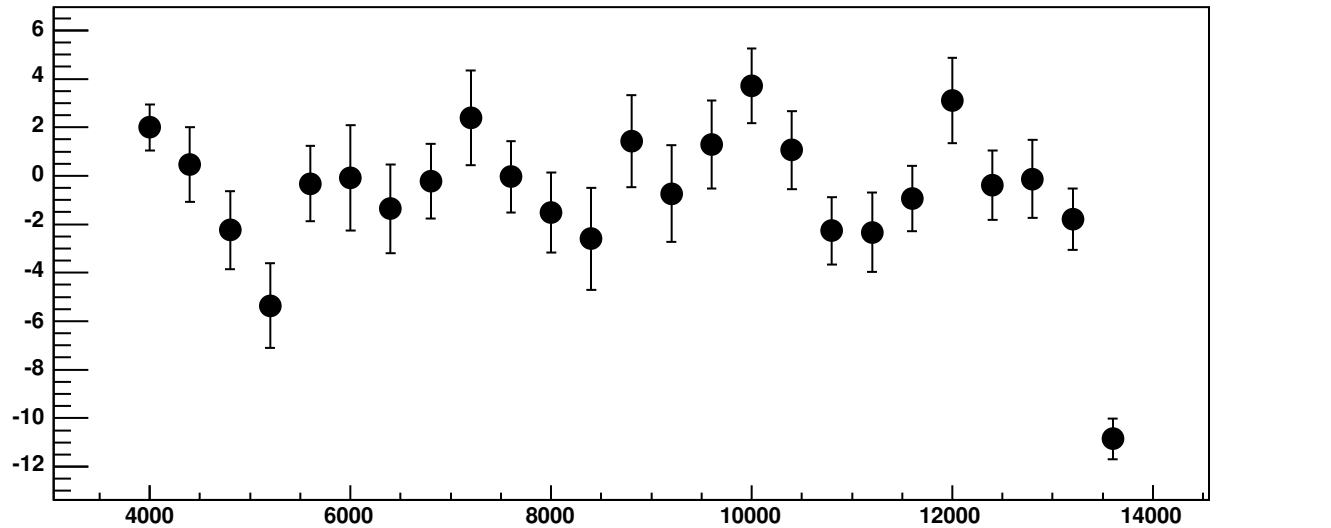
Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

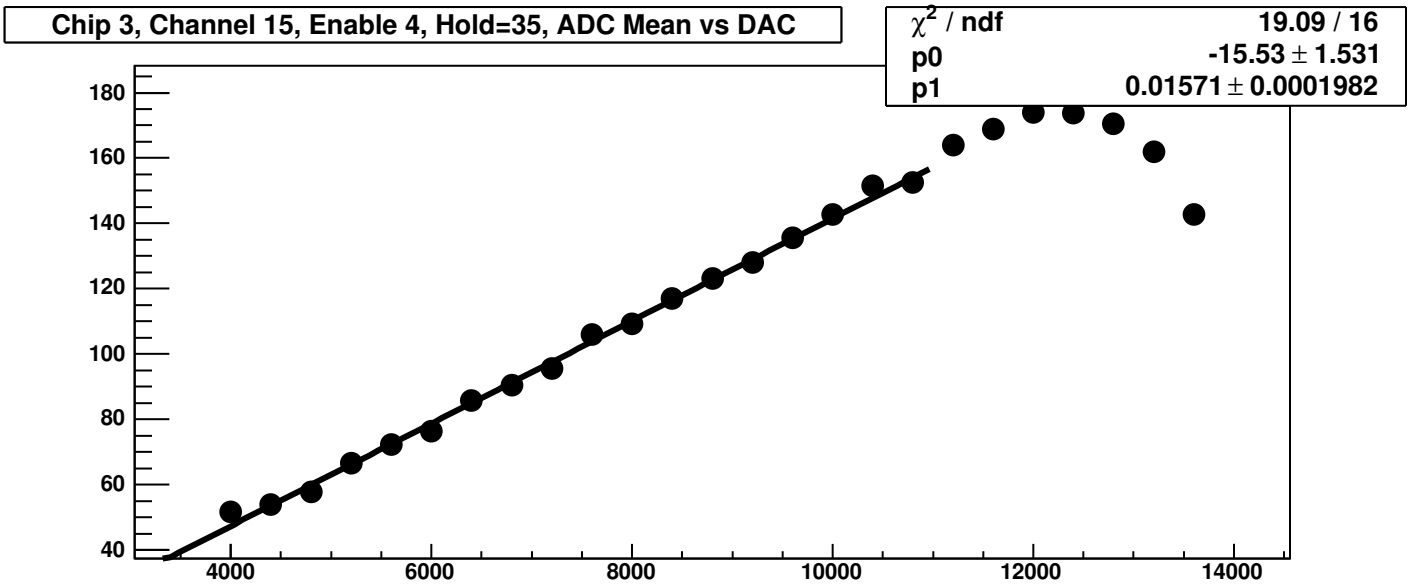


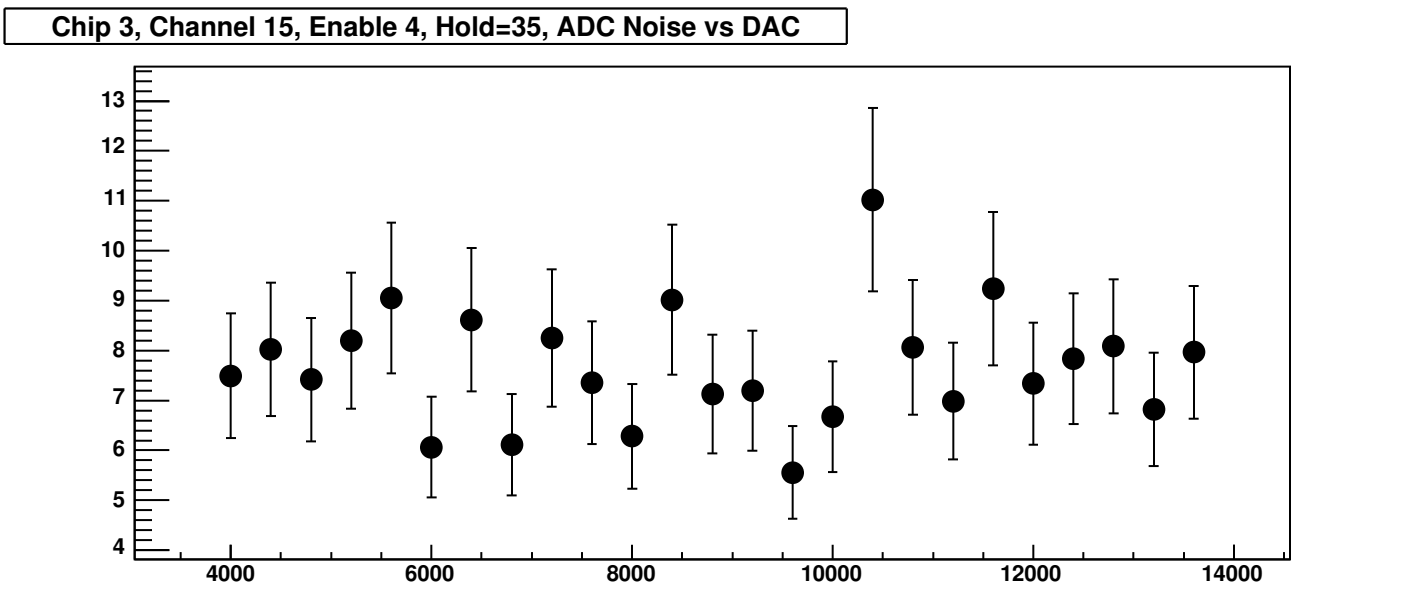
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC

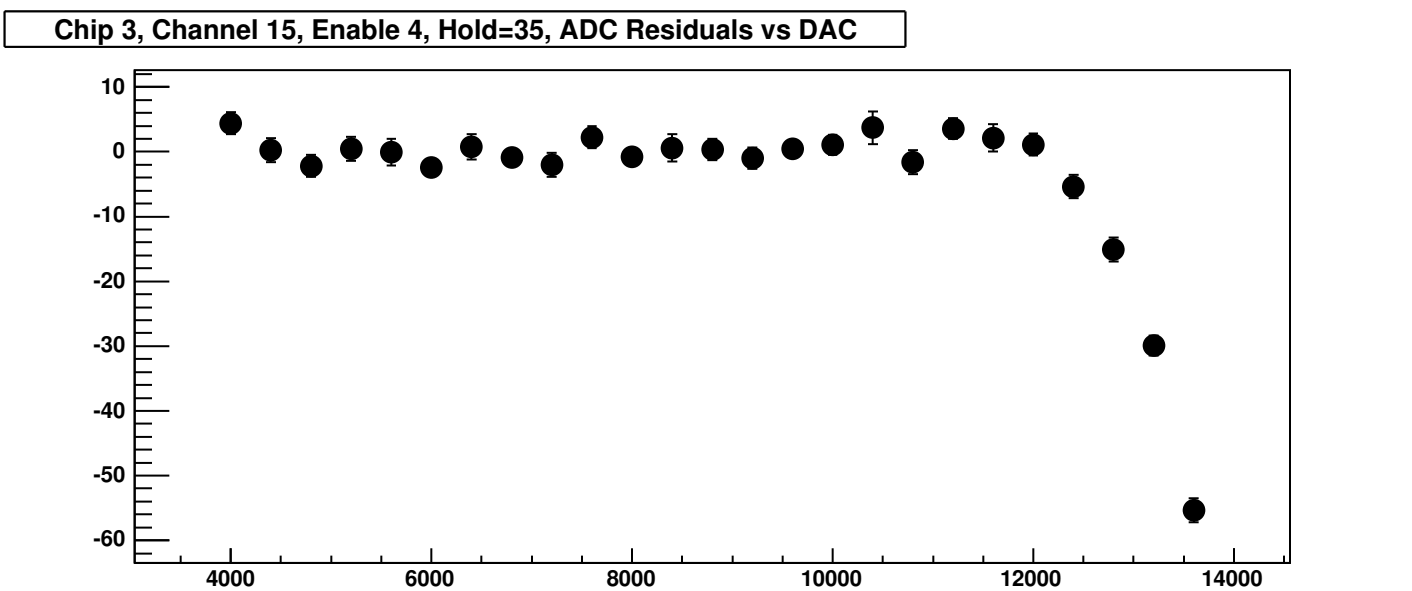


Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

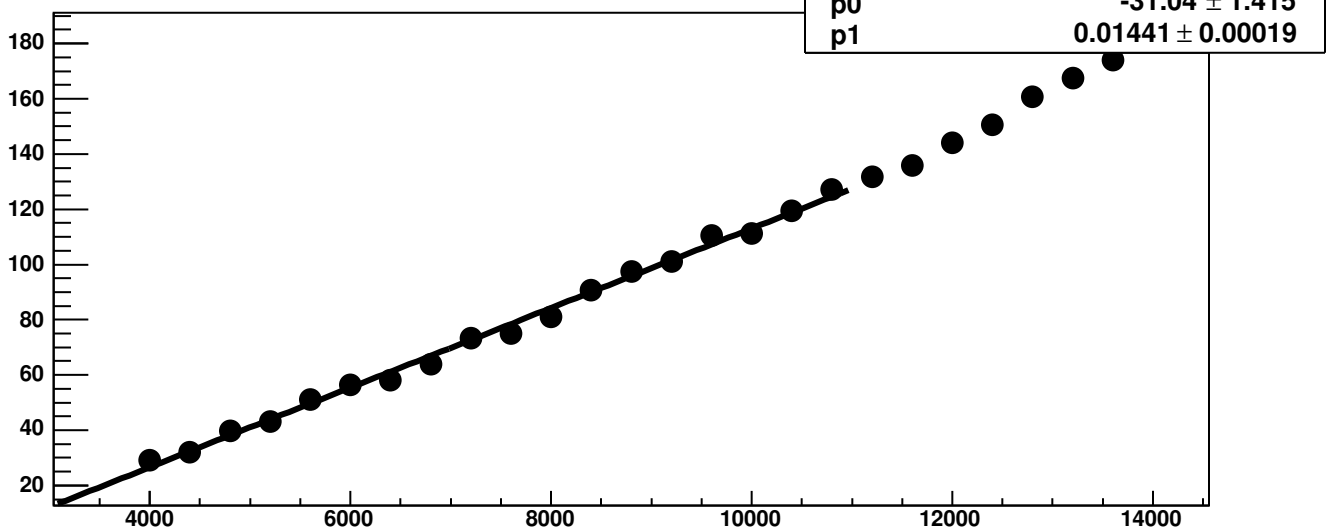



Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.09 / 16
p0 -15.53 ± 1.531
p1 0.01571 ± 0.0001982
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

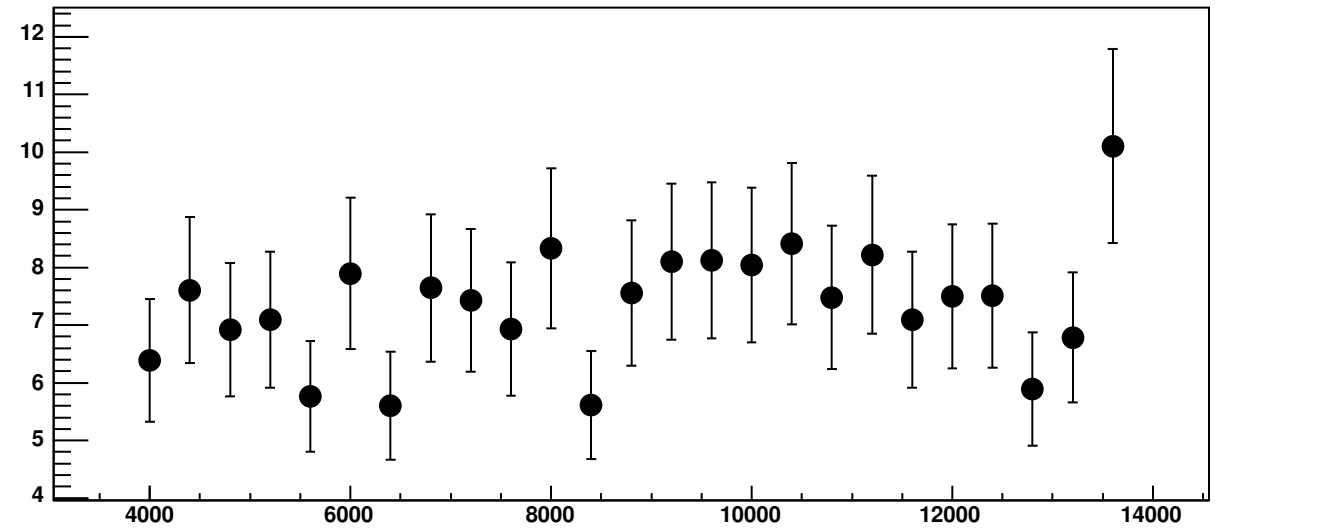

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
4000
6000
8000
10000
12000
14000

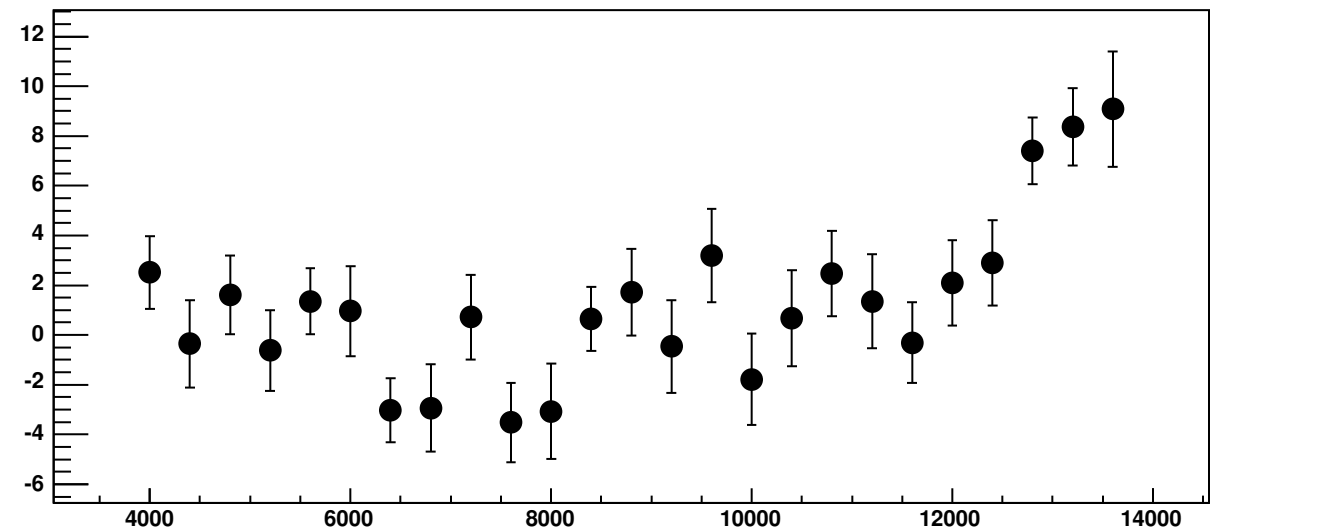
Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

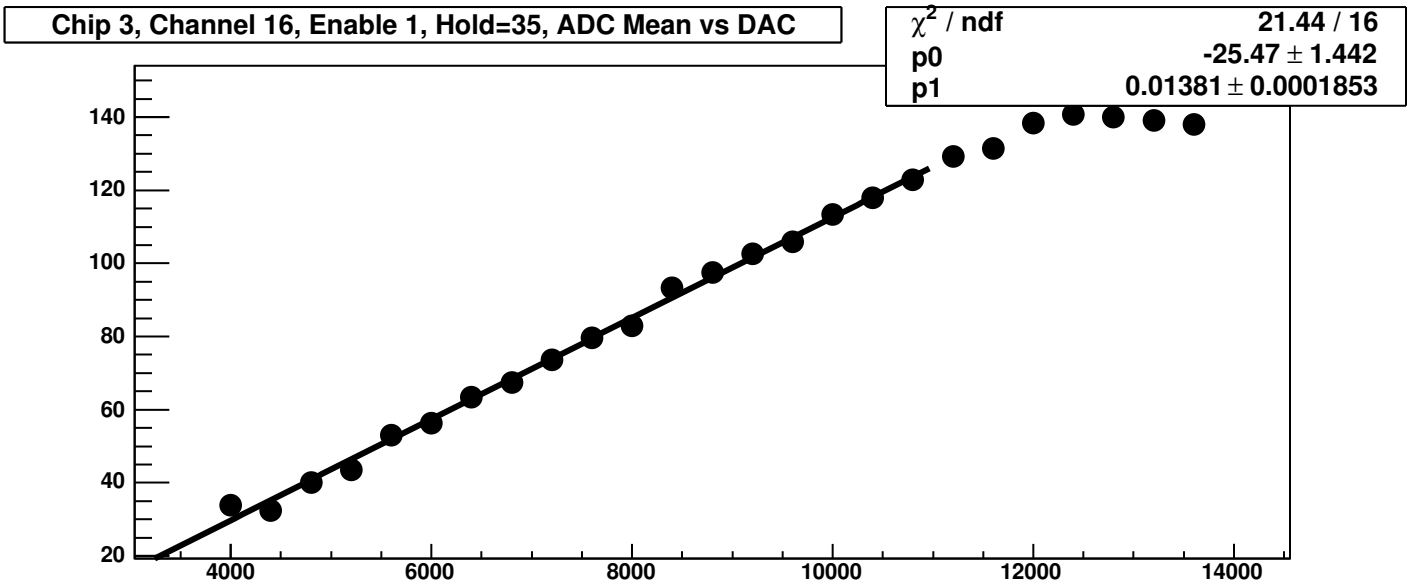


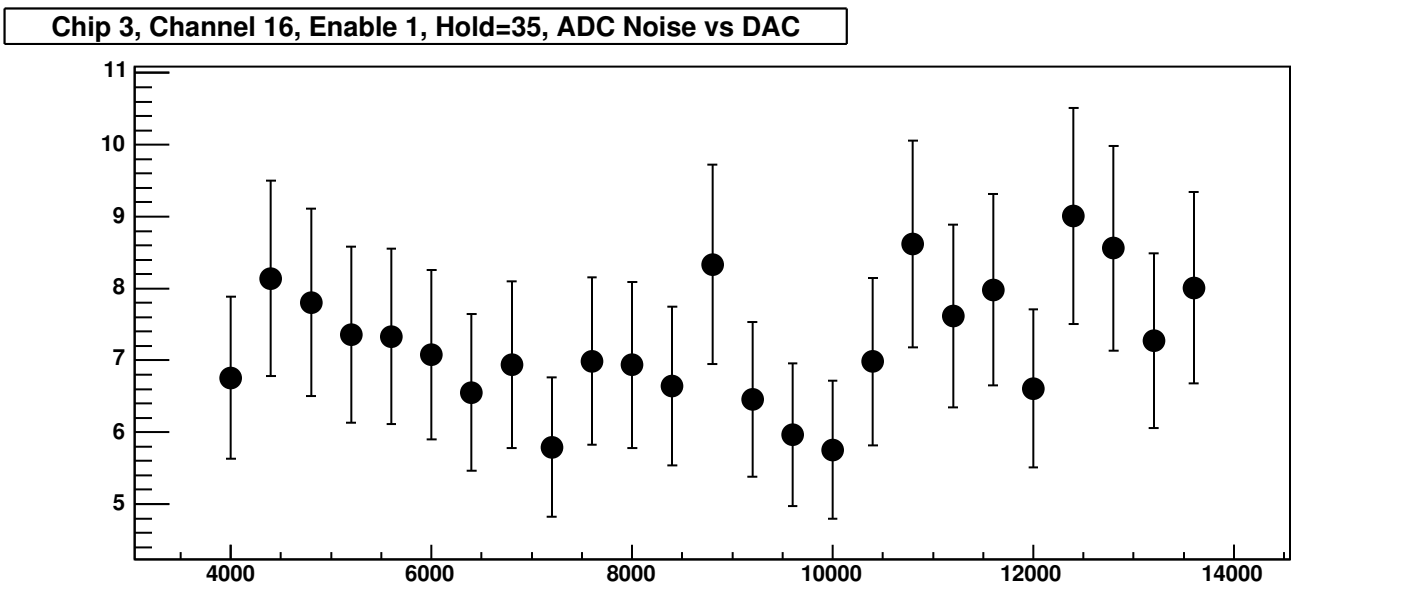
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

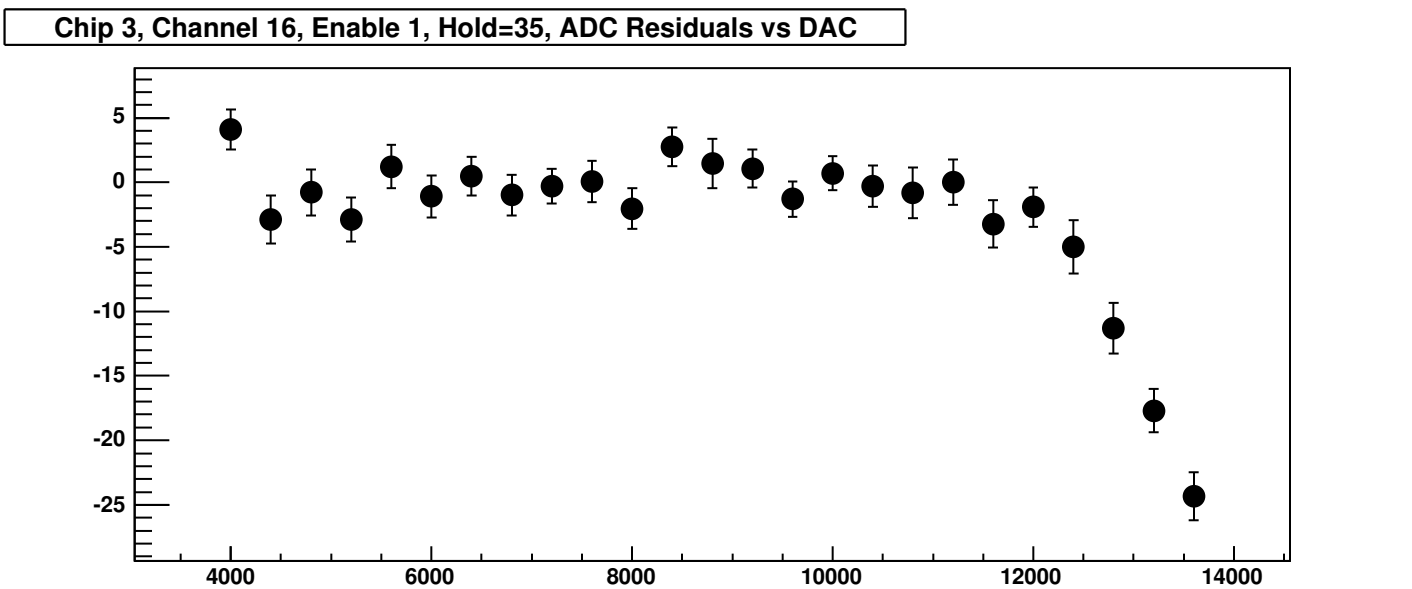


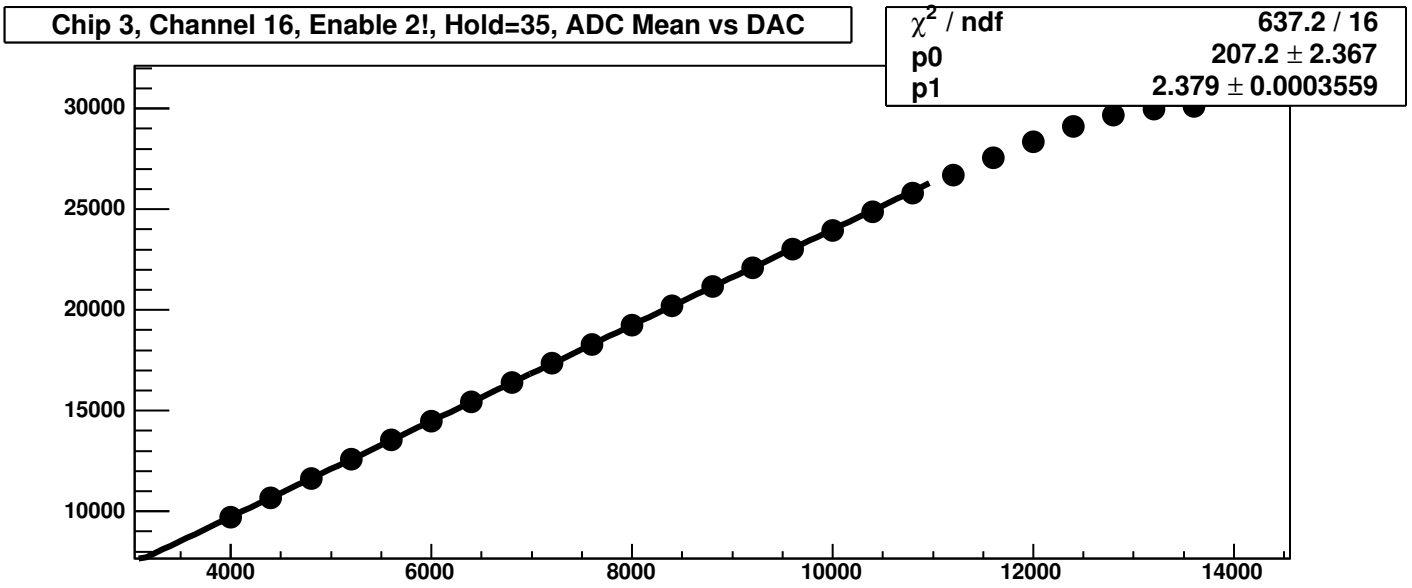
Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

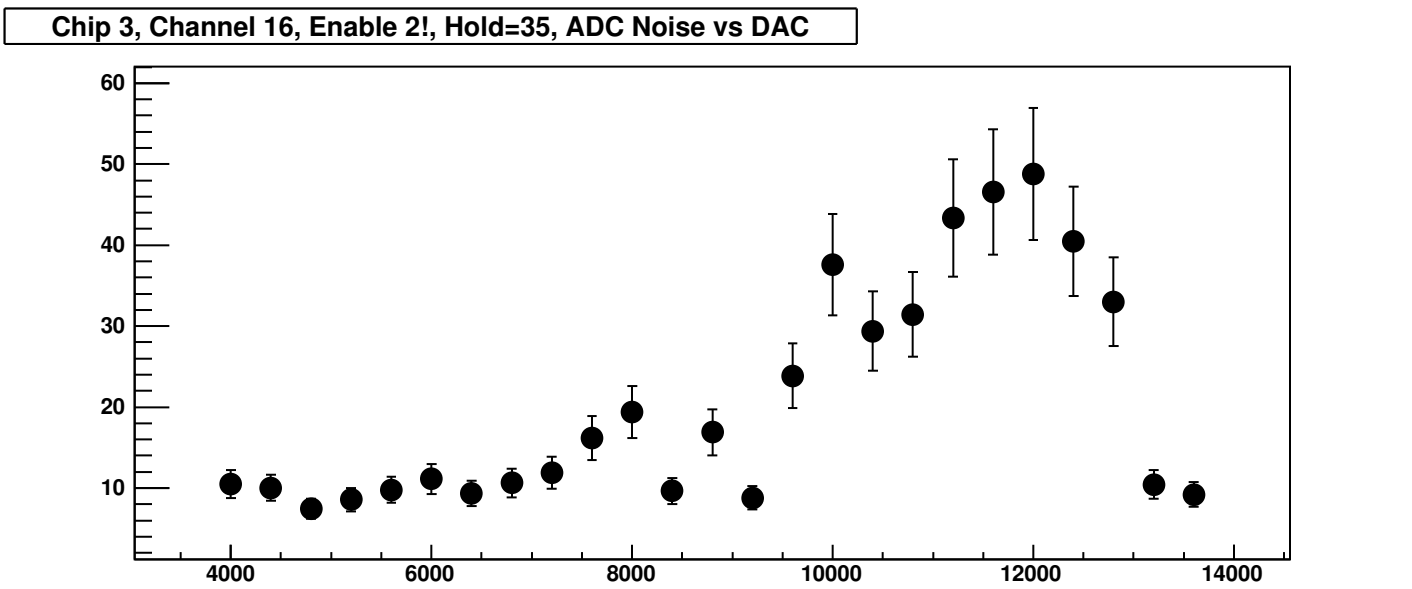


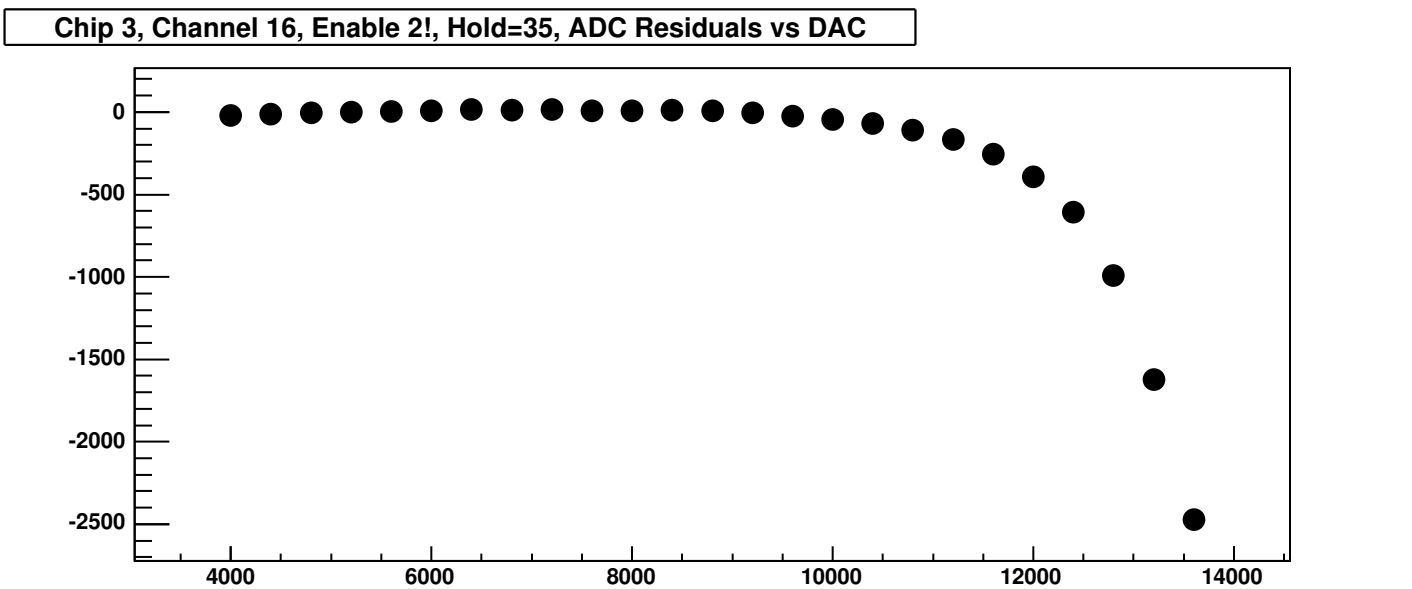

Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.44 / 16
p0 -25.47 ± 1.442
p1 0.01381 ± 0.0001853
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000

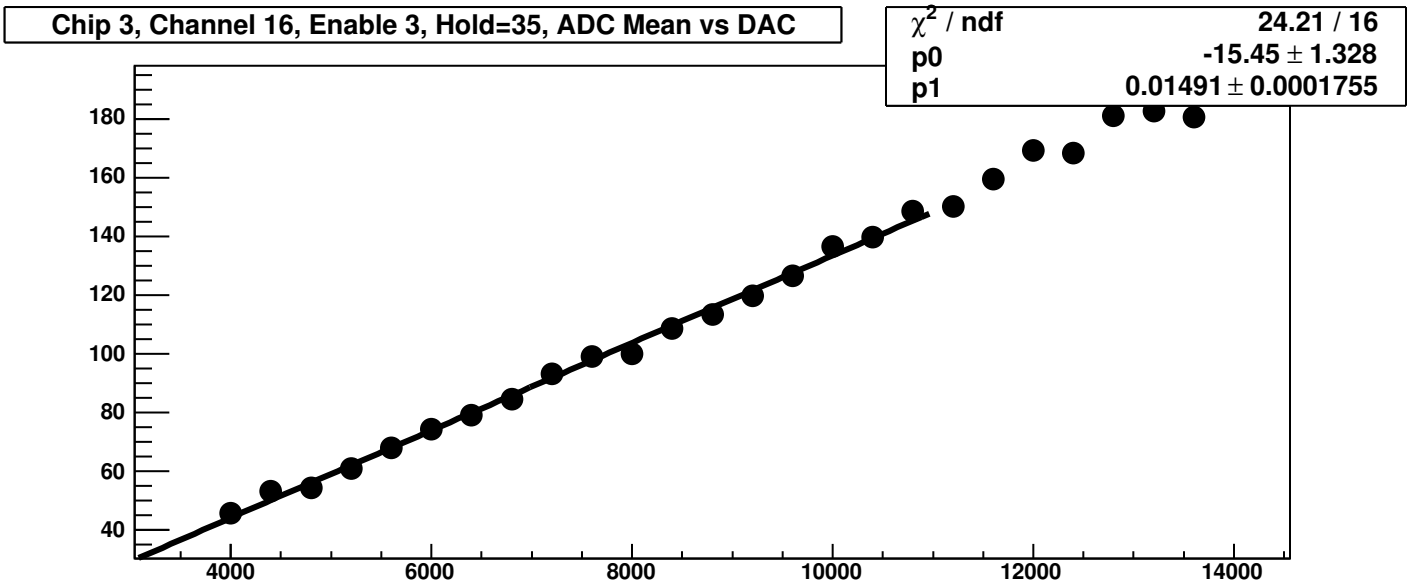

Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

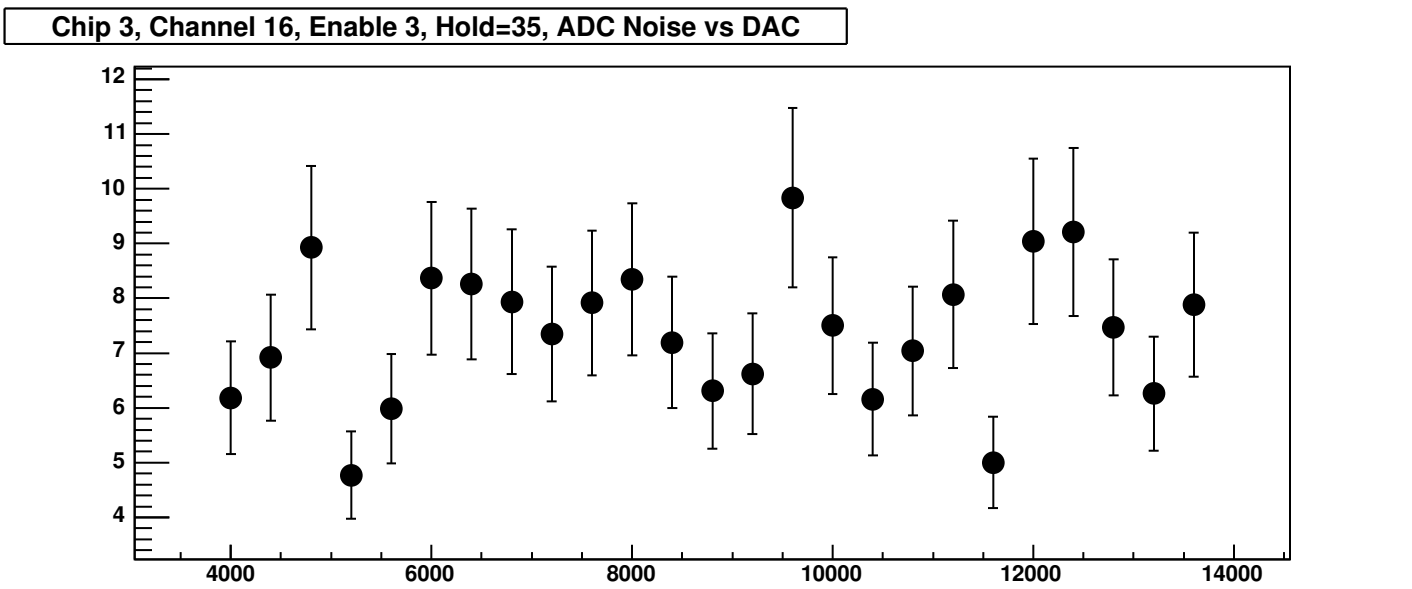

Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

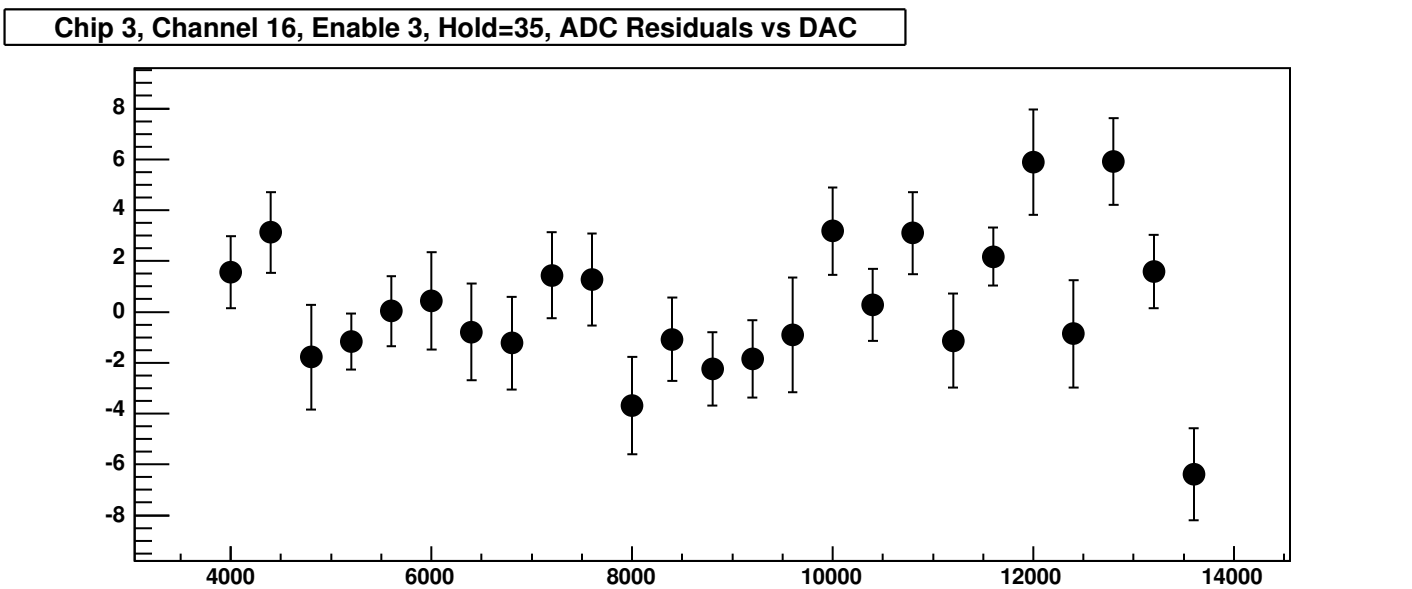

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 637.2 / 16
p0 207.2 ± 2.367
p1 2.379 ± 0.0003559
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

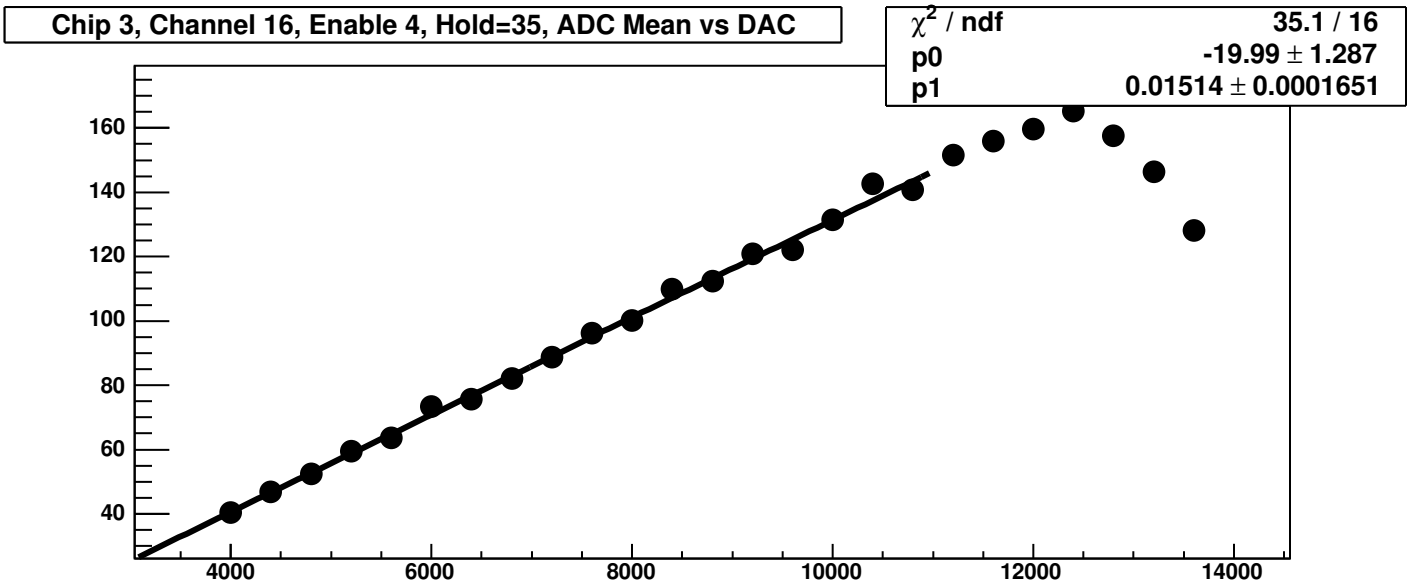

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
4000
6000
8000
10000
12000
14000

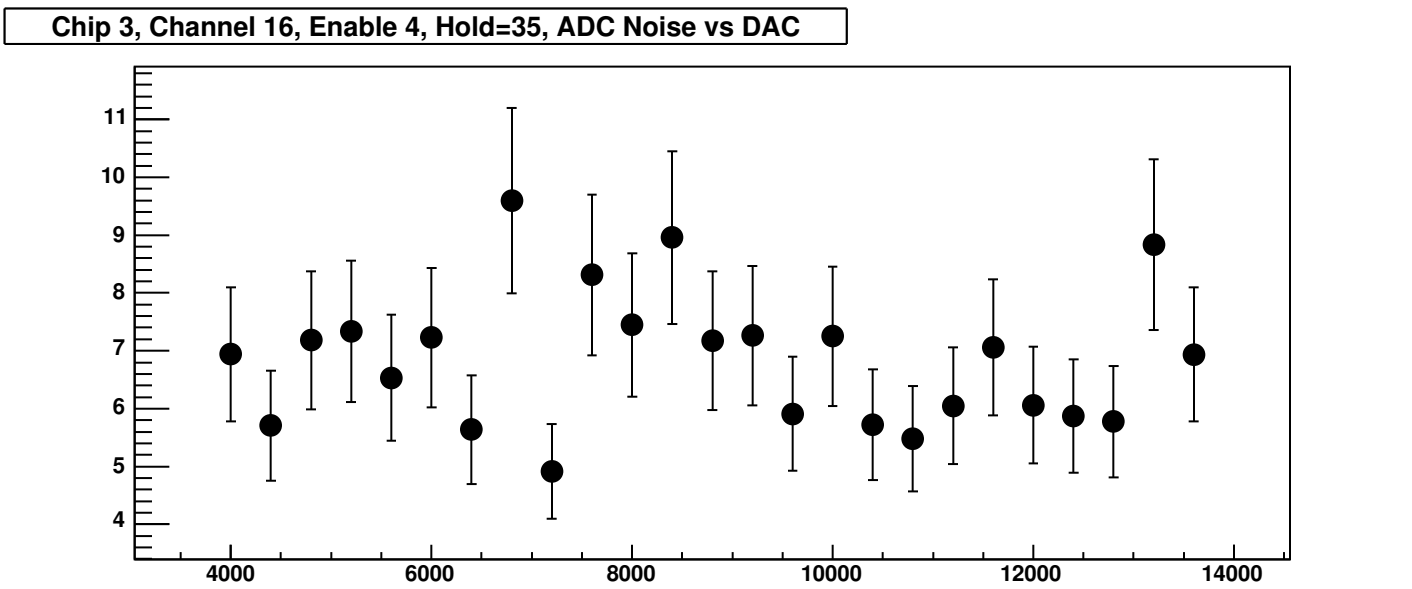

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
4000
6000
8000
10000
12000
14000

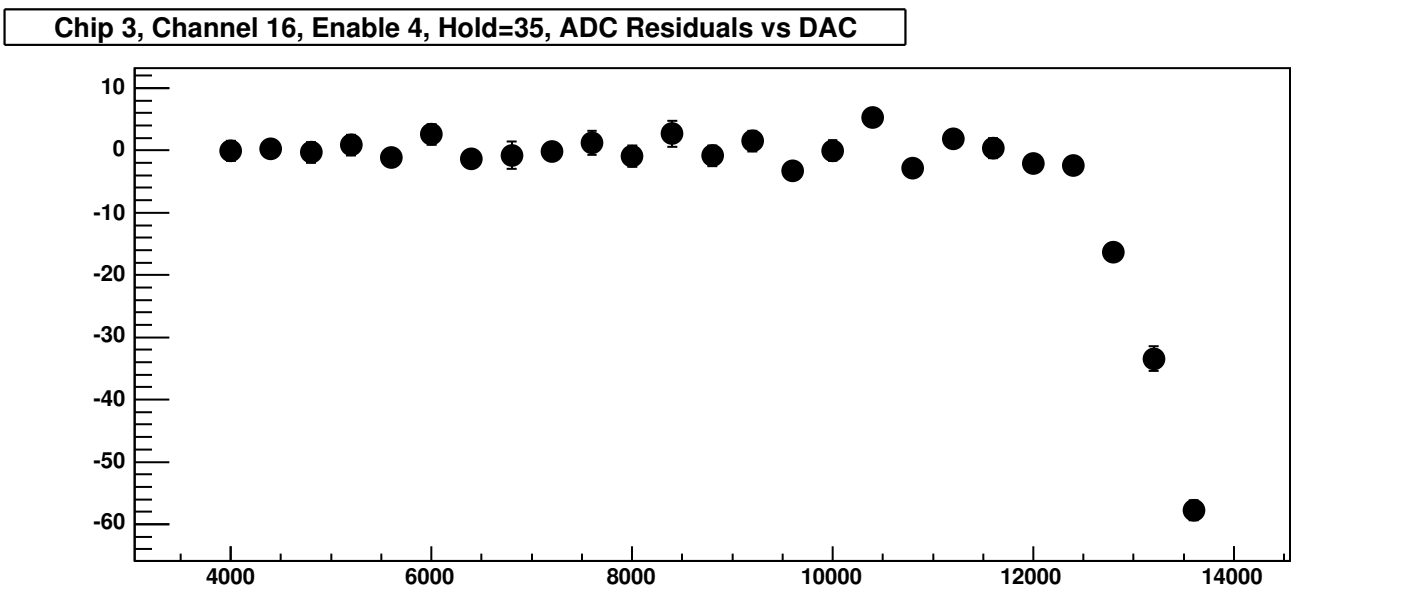

Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.21 / 16
p0 -15.45 ± 1.328
p1 0.01491 ± 0.0001755
180
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

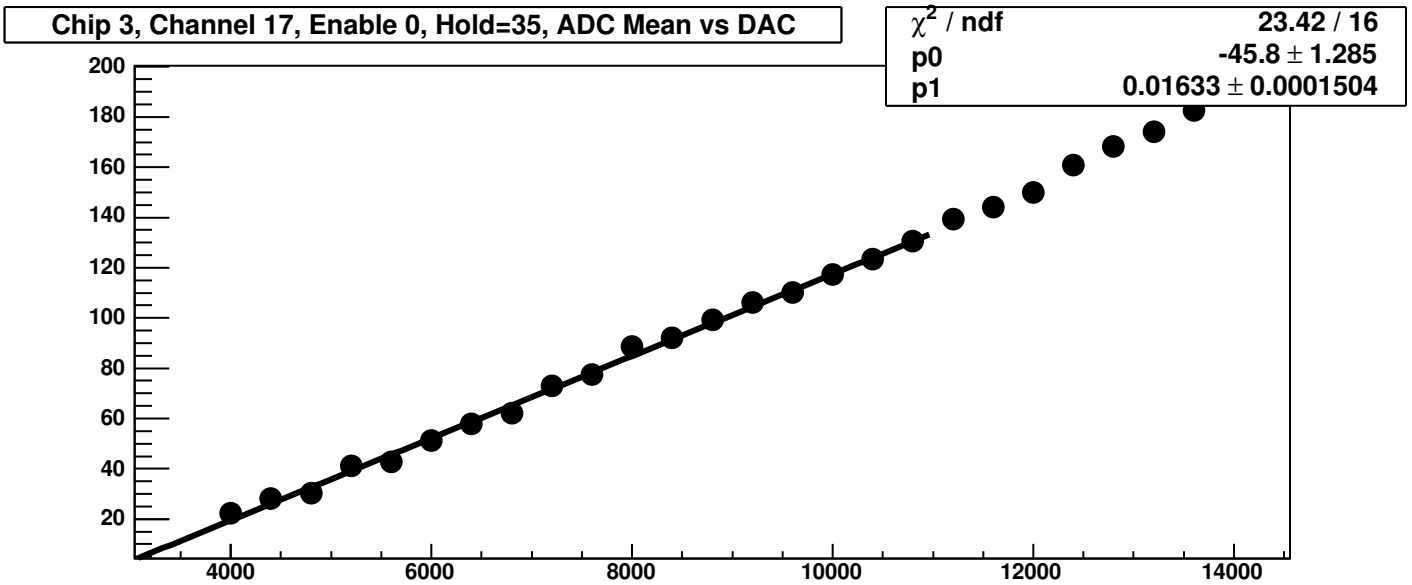

Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

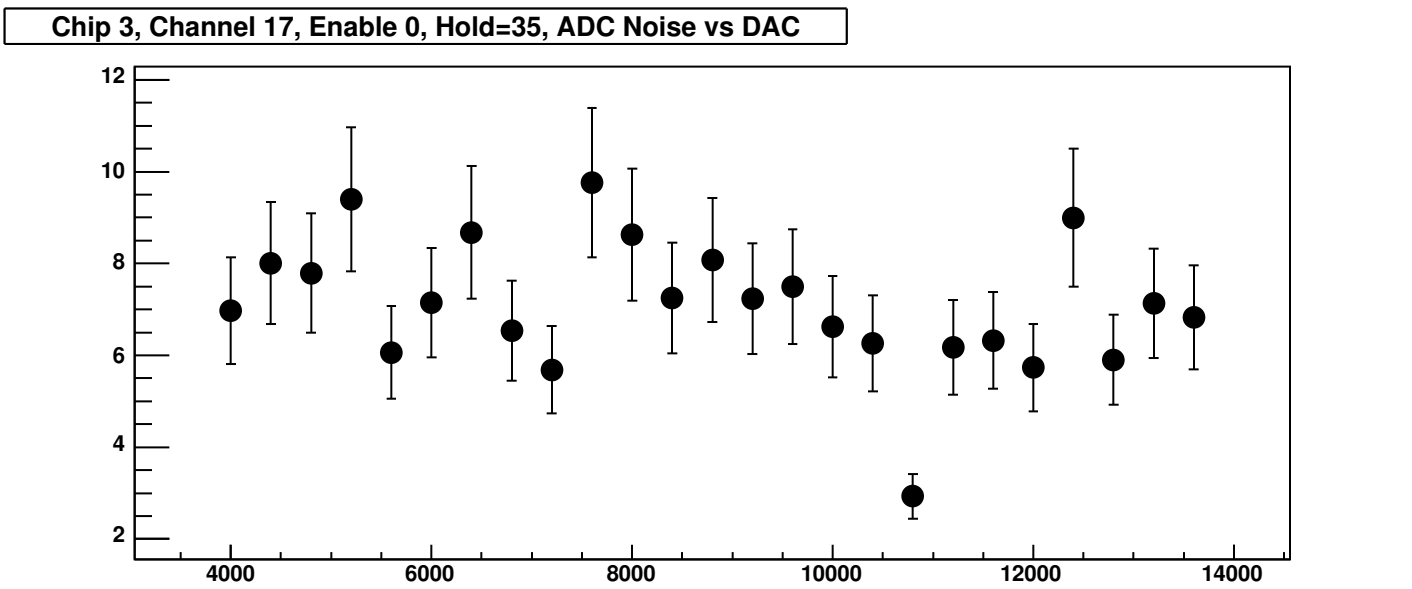

Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

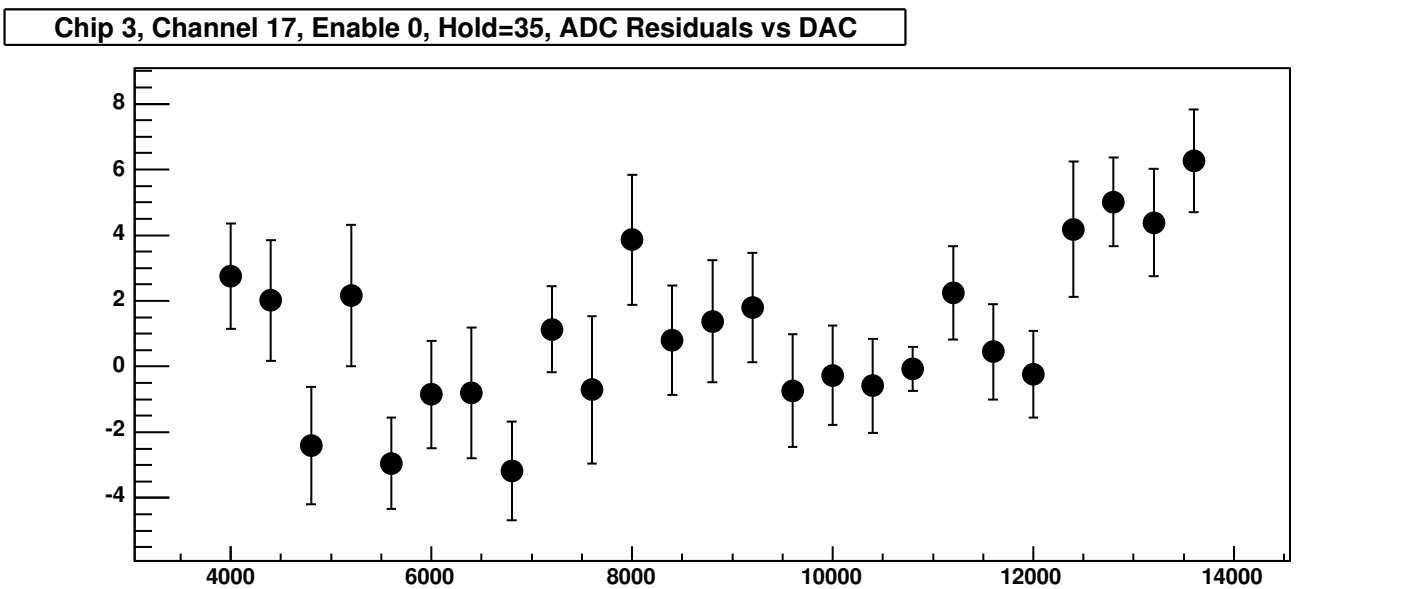

Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.1 / 16
p0 -19.99 ± 1.287
p1 0.01514 ± 0.0001651
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000


Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

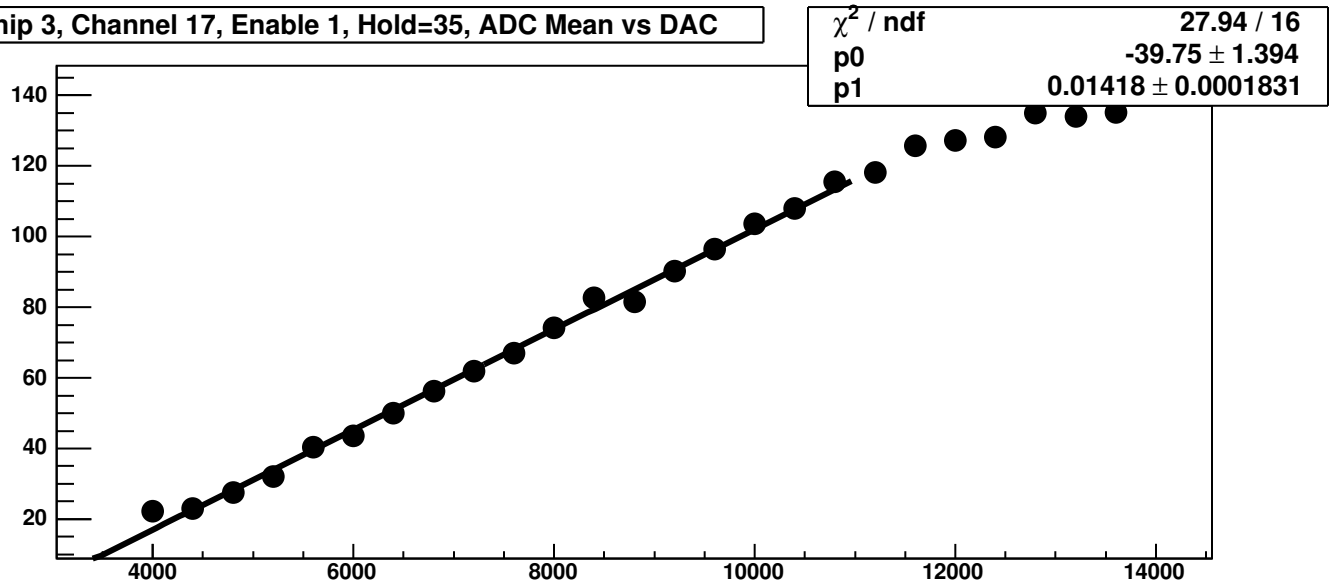

Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 23.42 / 16
p0 -45.8 ± 1.285
p1 0.01633 ± 0.0001504
200
180
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000

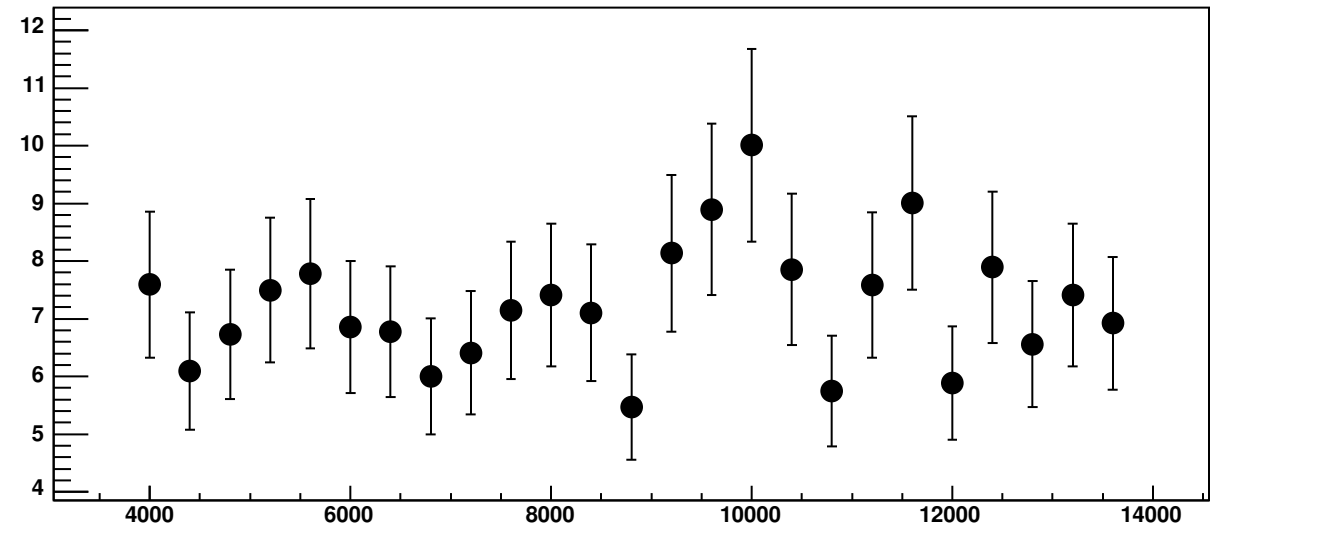

Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

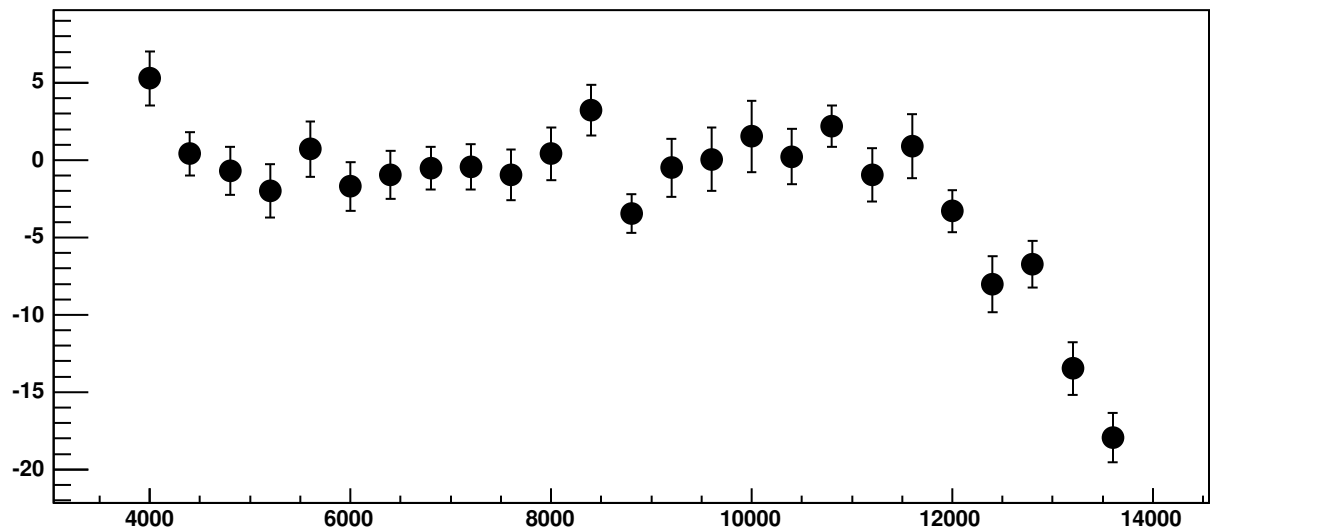
Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

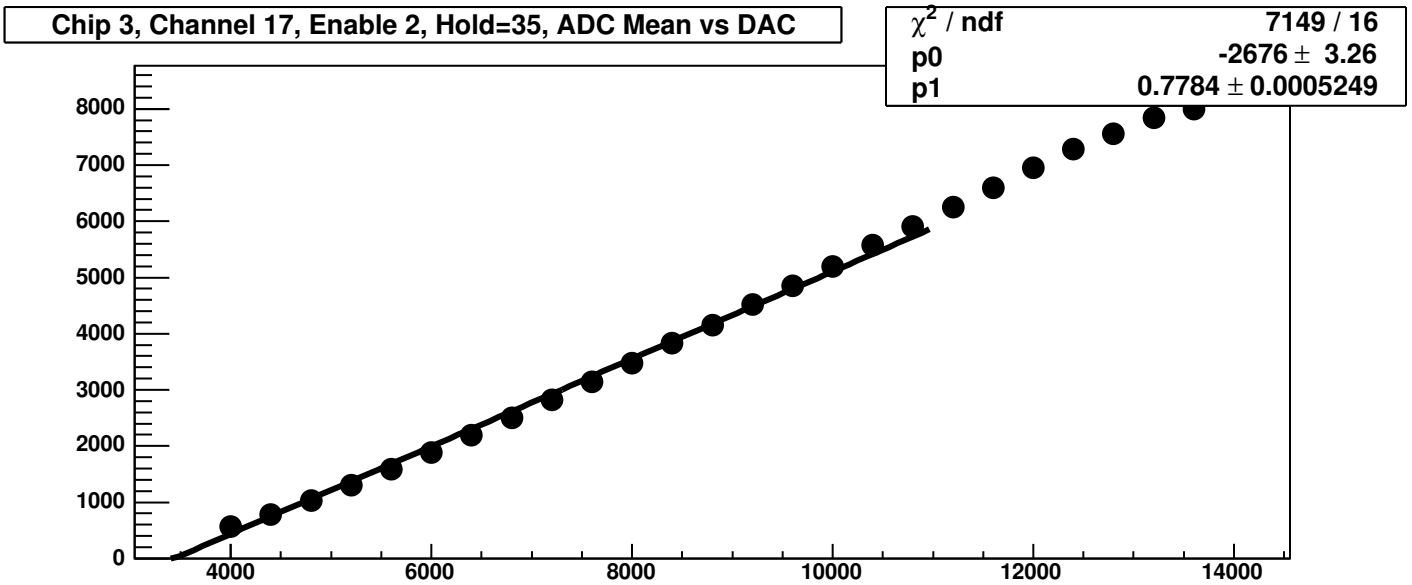


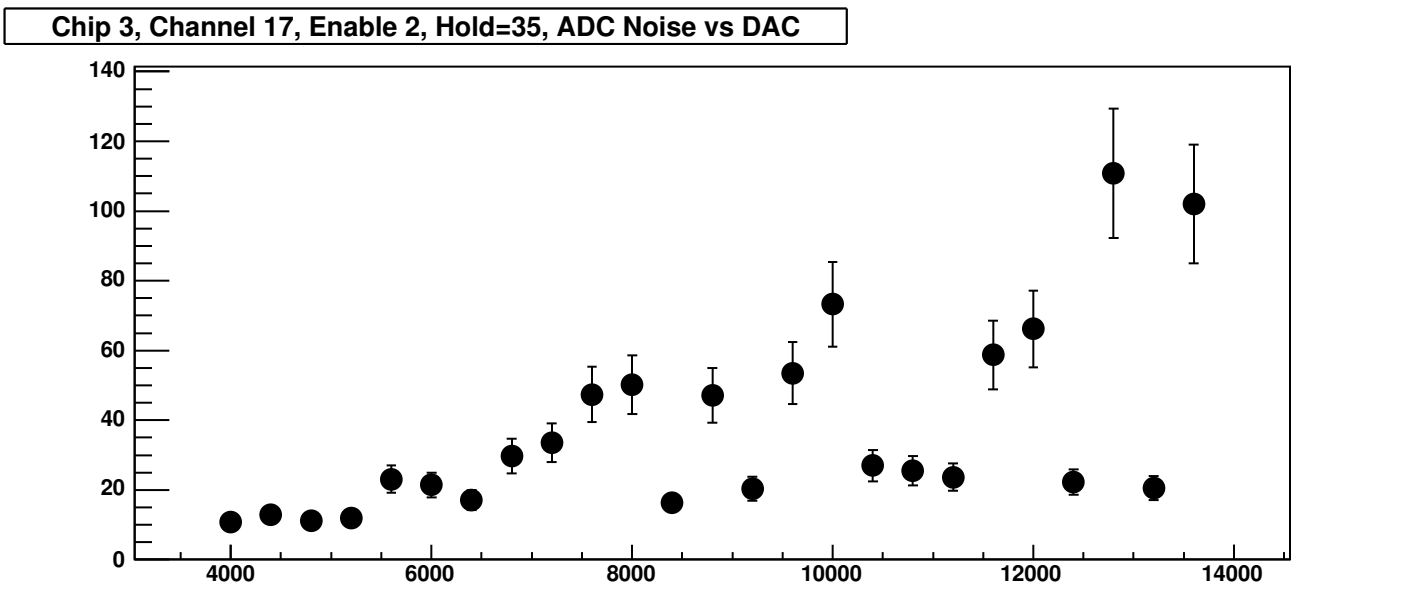
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

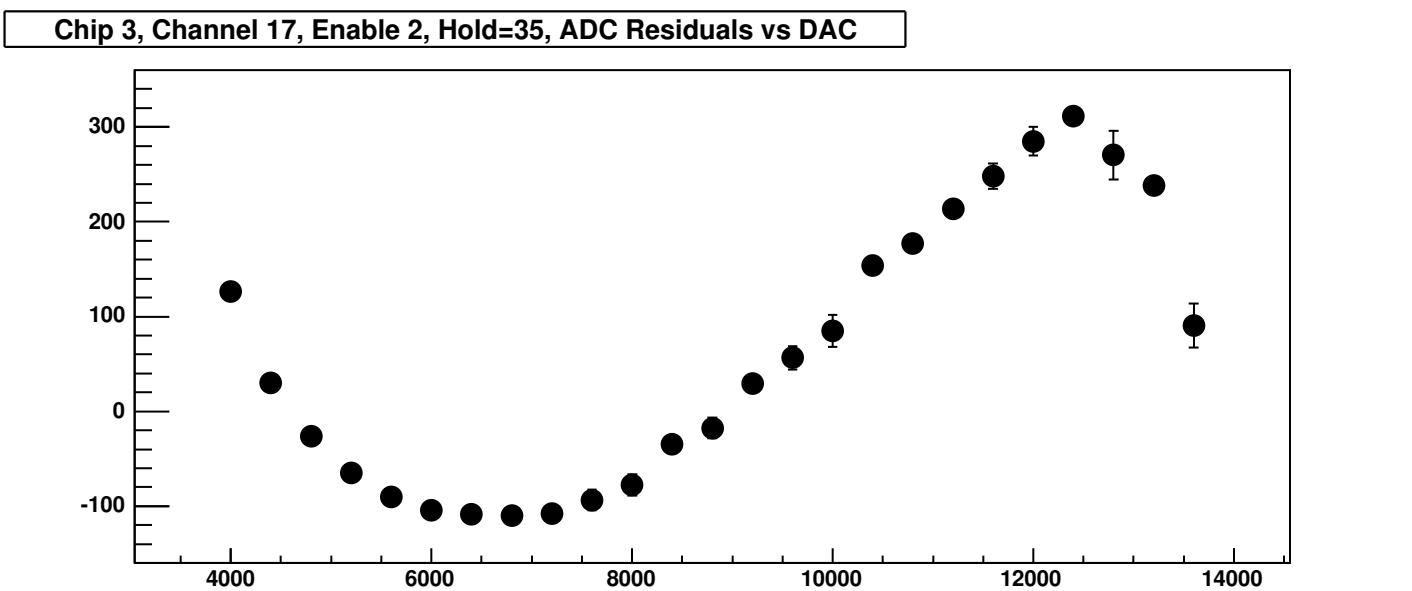


Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

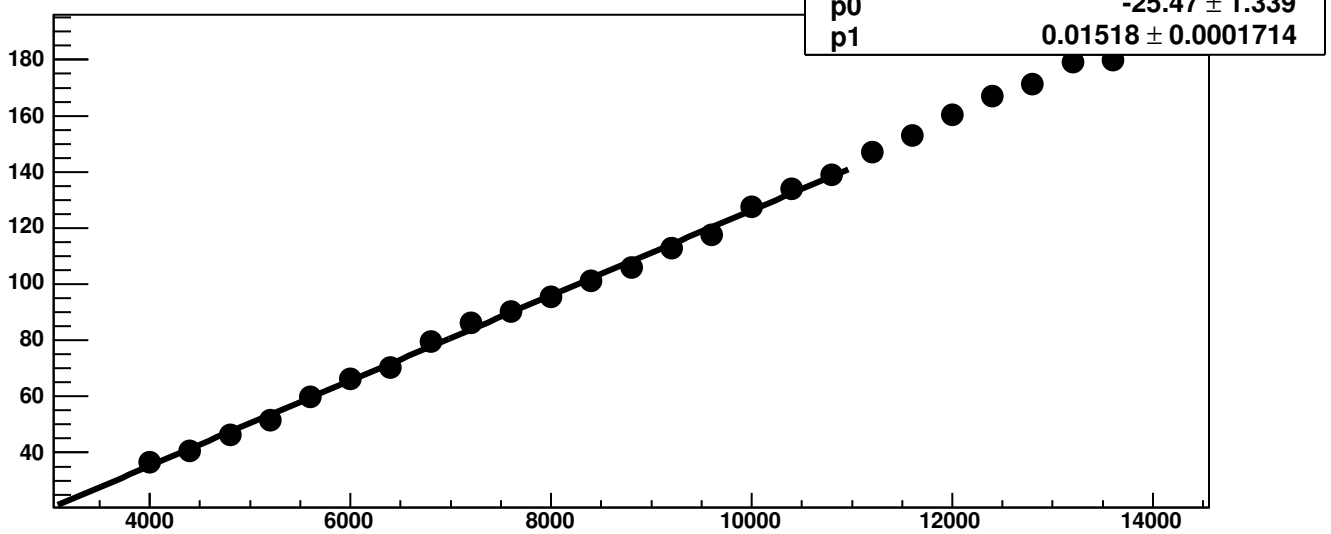



Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 7149 / 16
p0 -2676 ± 3.26
p1 0.7784 ± 0.0005249
8000
7000
6000
5000
4000
3000
2000
1000
0
4000
6000
8000
10000
12000
14000

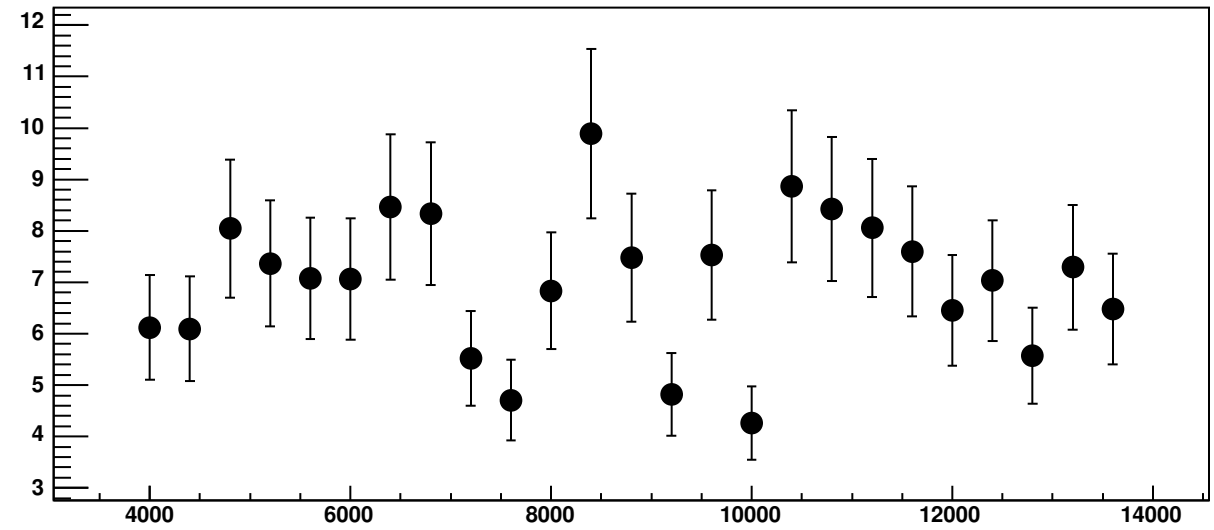

Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
4000
6000
8000
10000
12000
14000

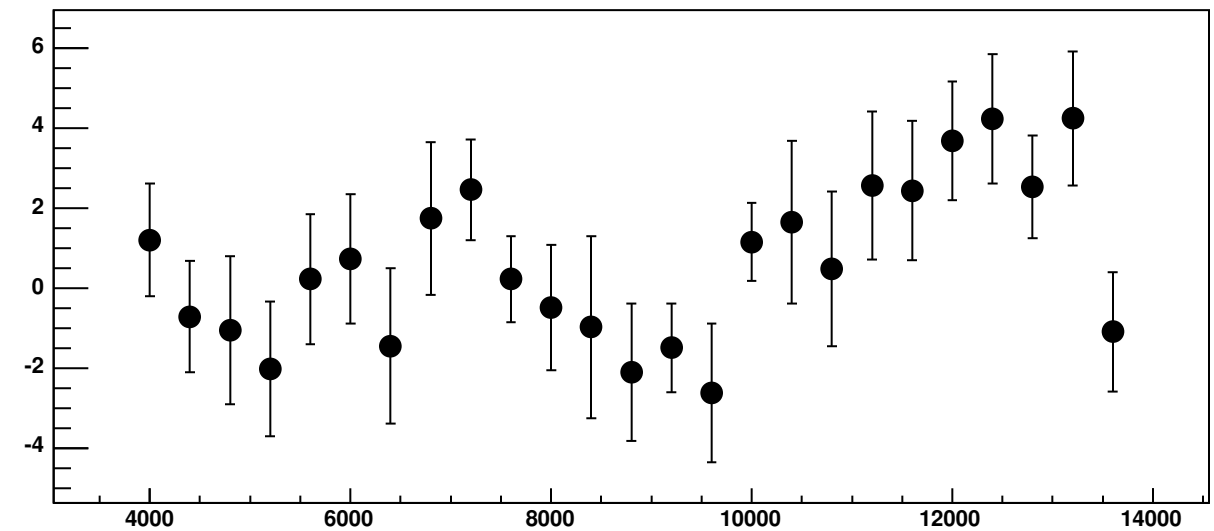
Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

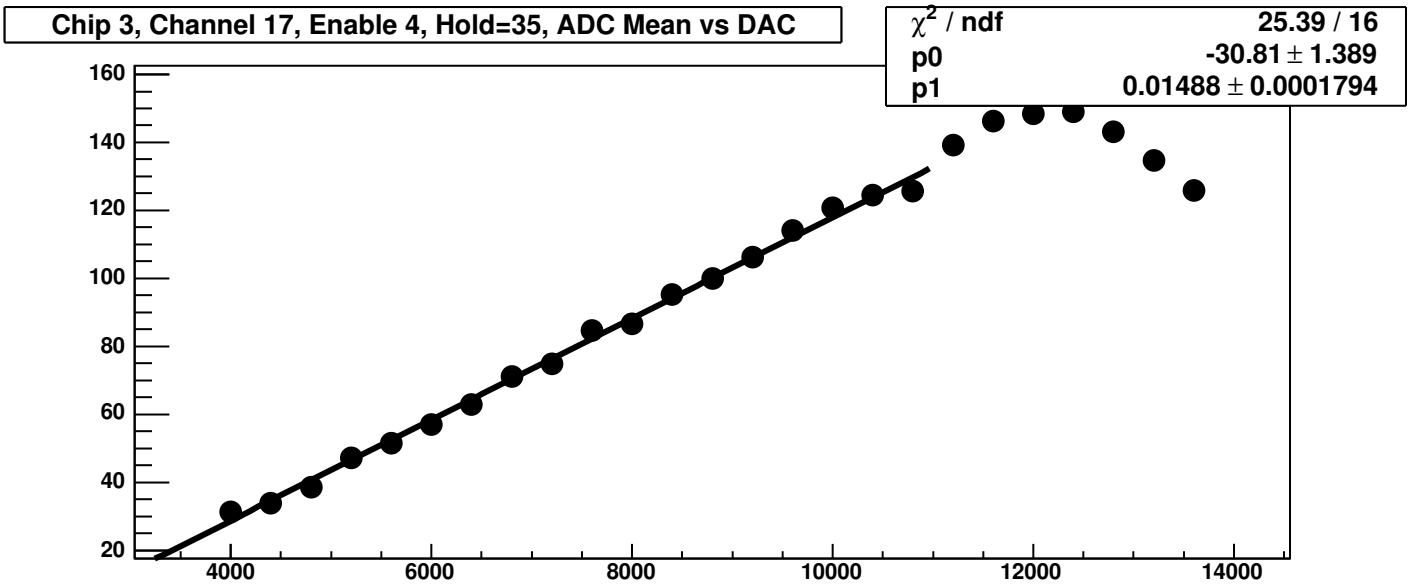


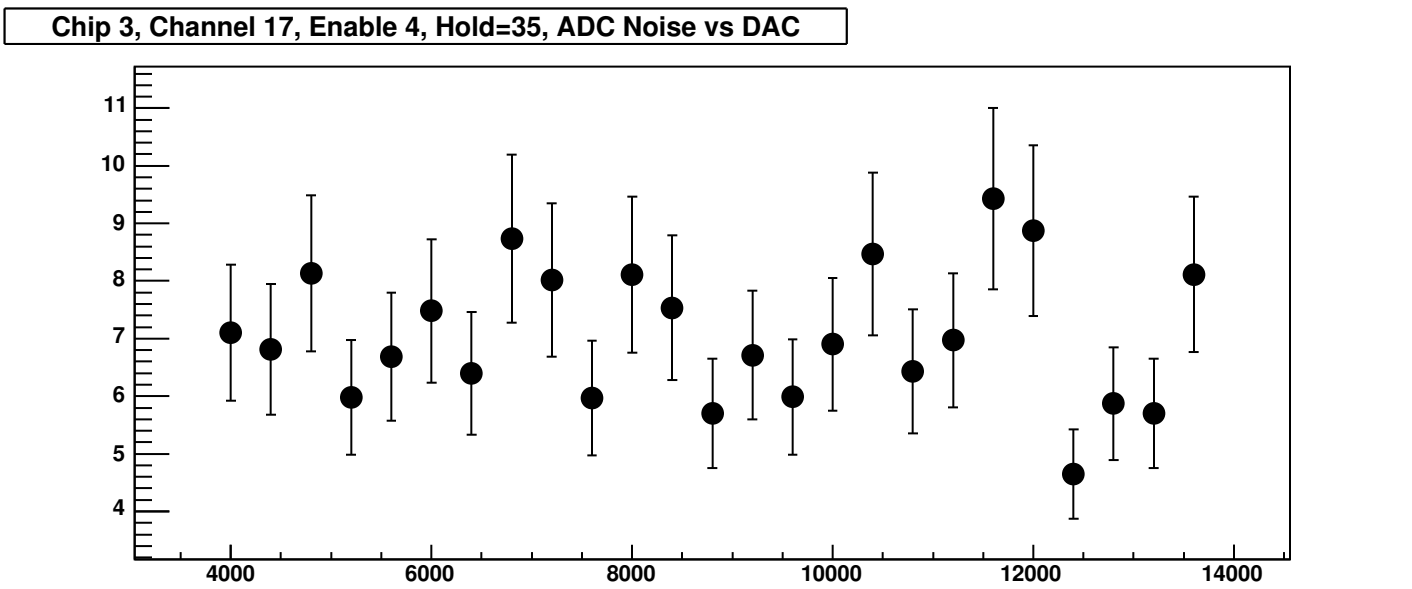
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

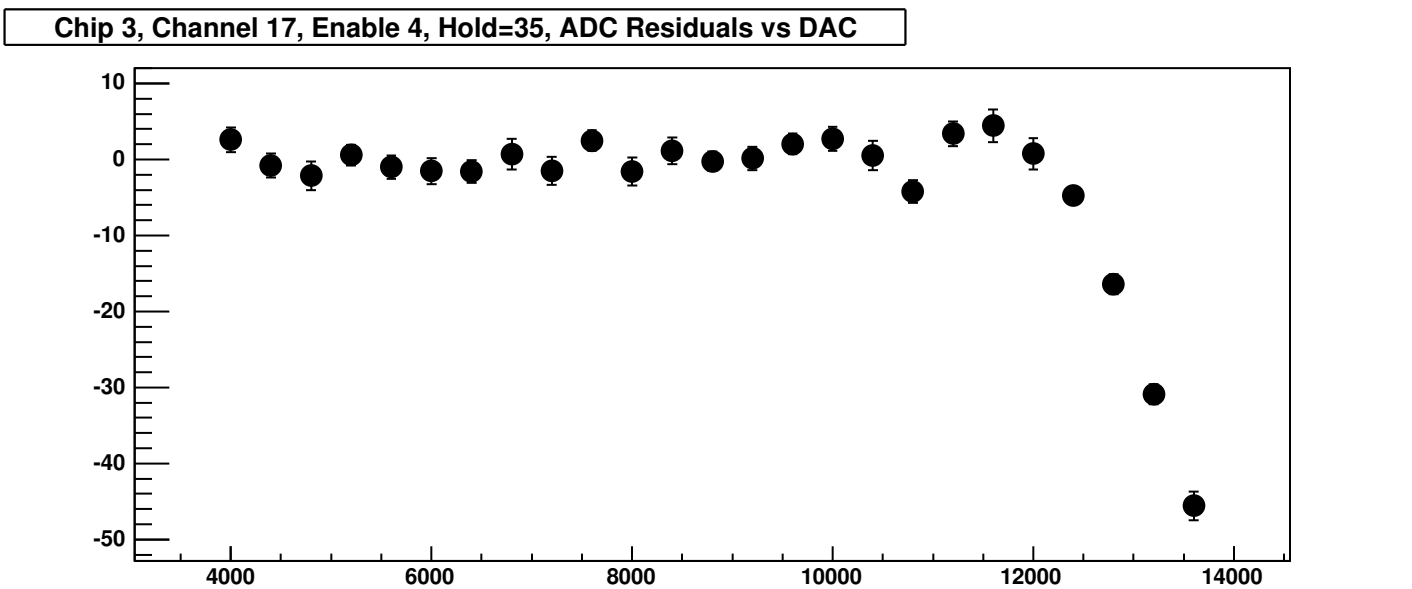


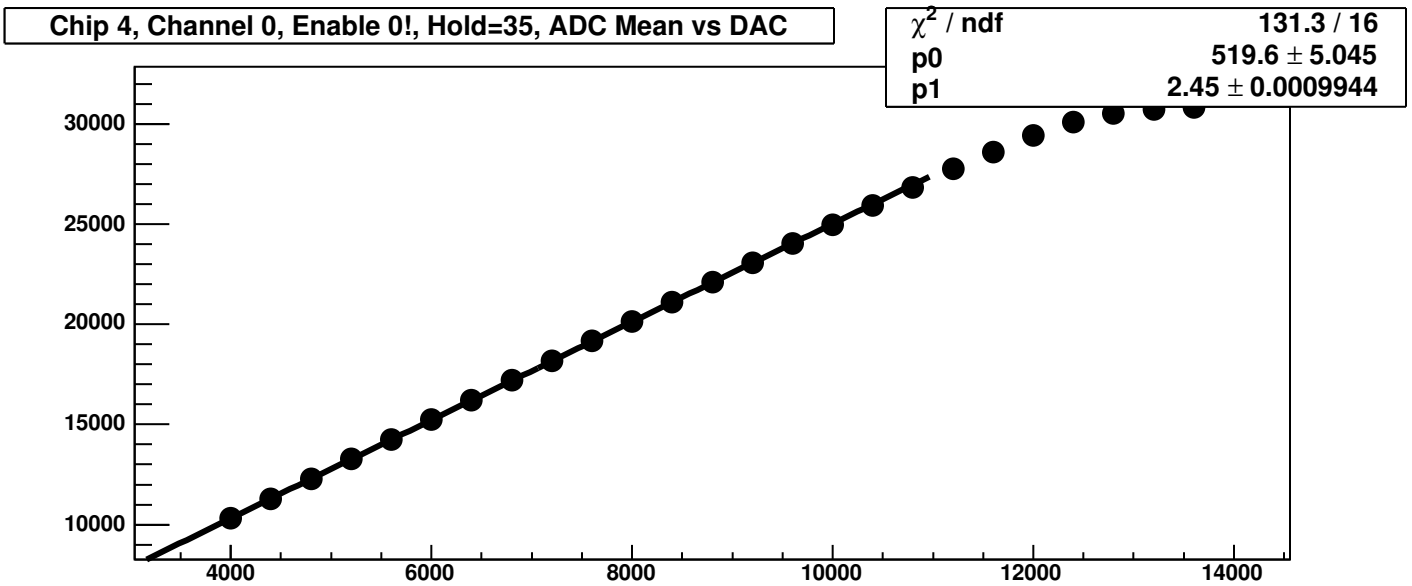
Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

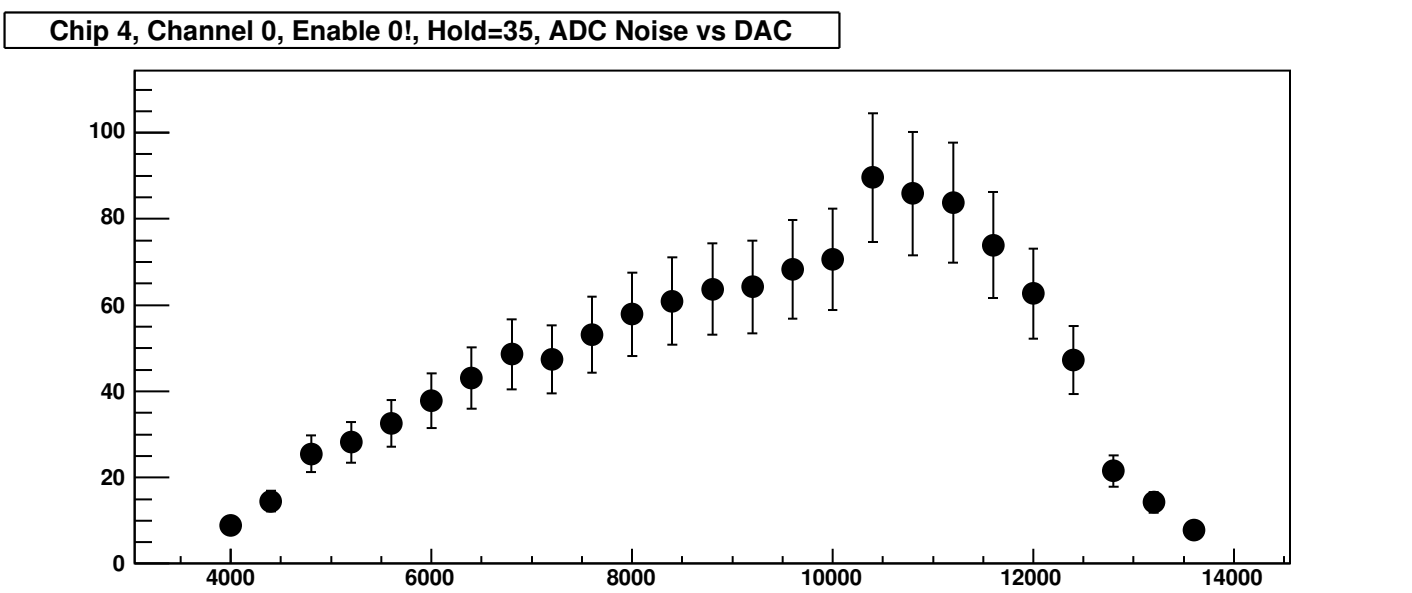


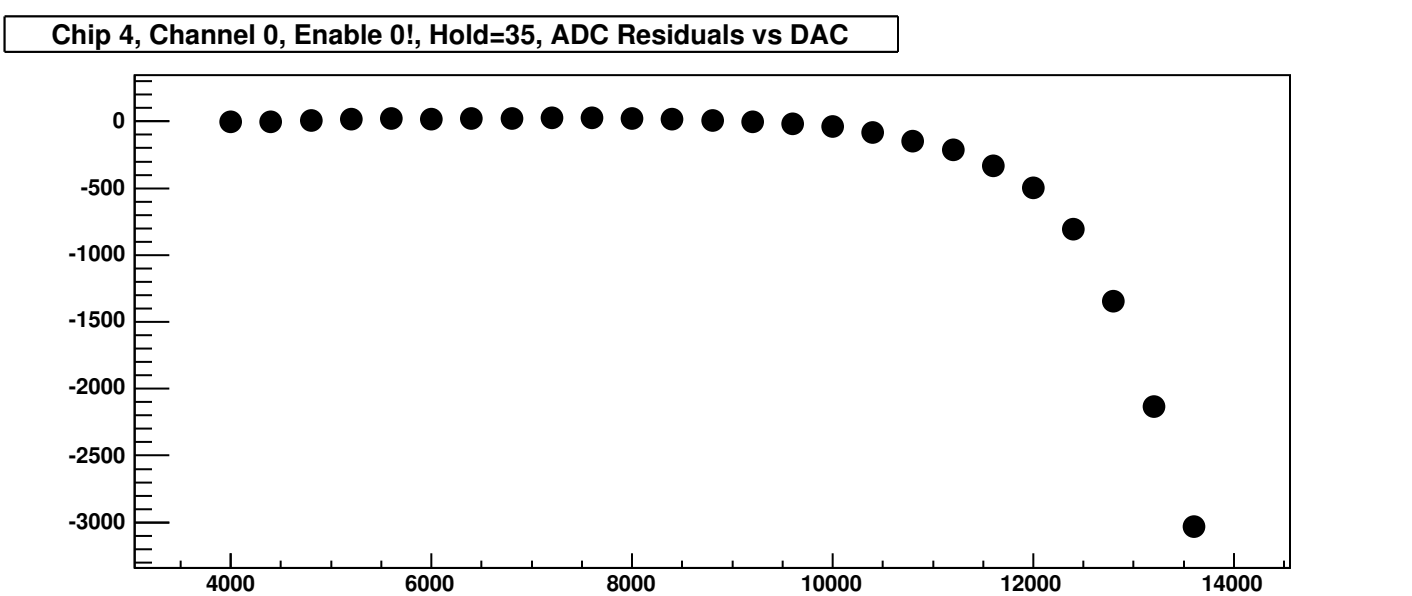

Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.39 / 16
p0 -30.81 ± 1.389
p1 0.01488 ± 0.0001794
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

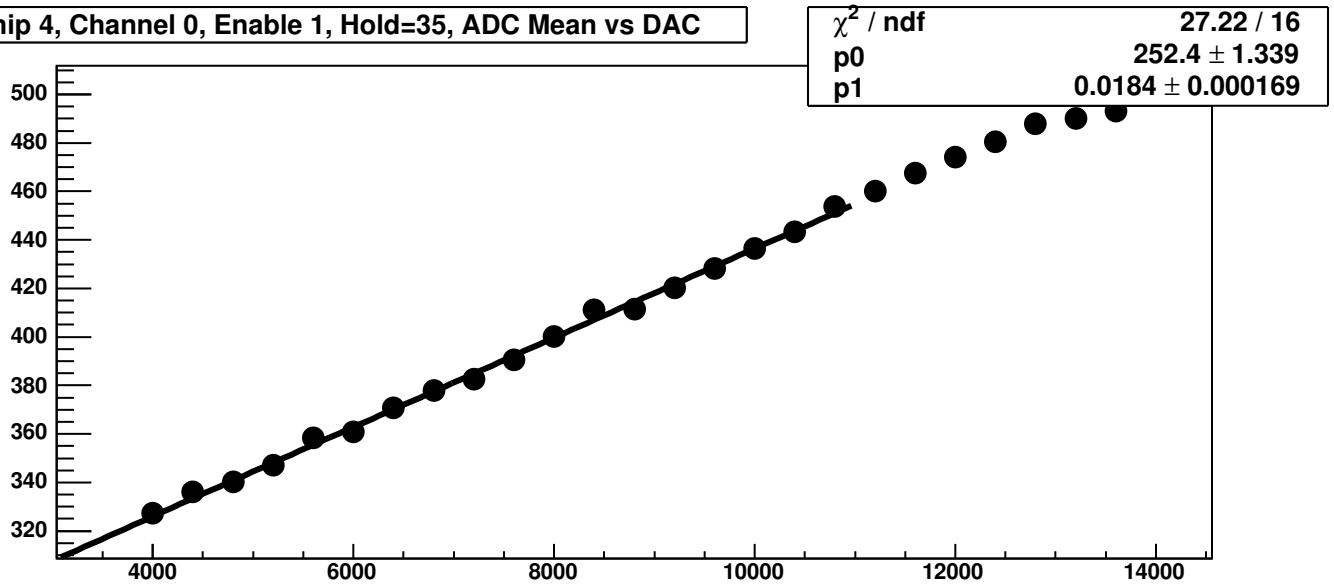

Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 131.3 / 16
p0 519.6 ± 5.045
p1 2.45 ± 0.0009944
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

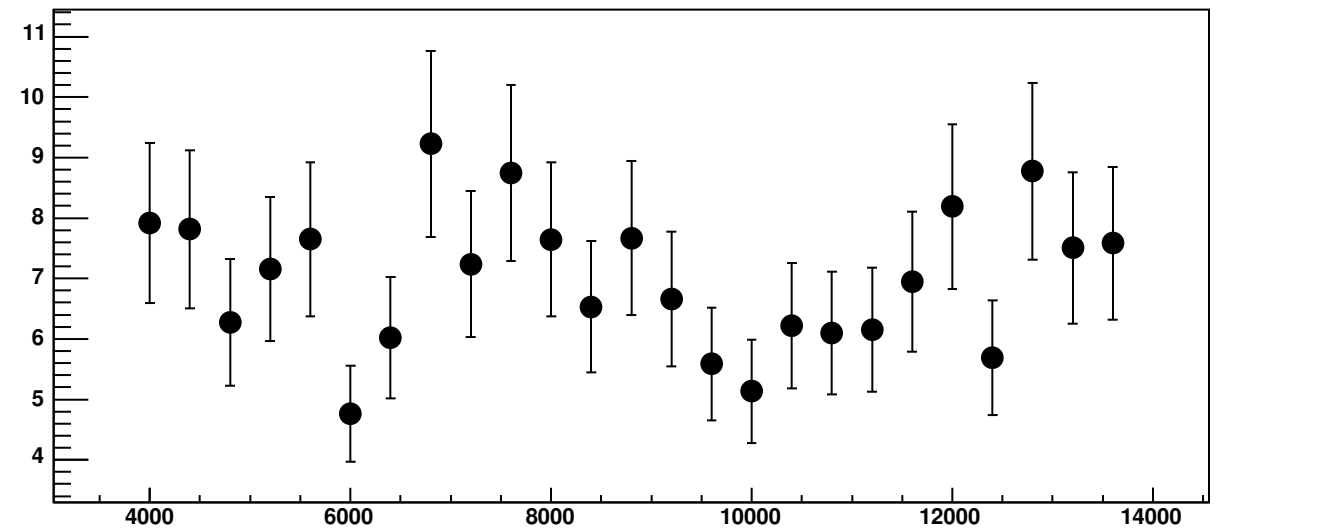

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
4000
6000
8000
10000
12000
14000

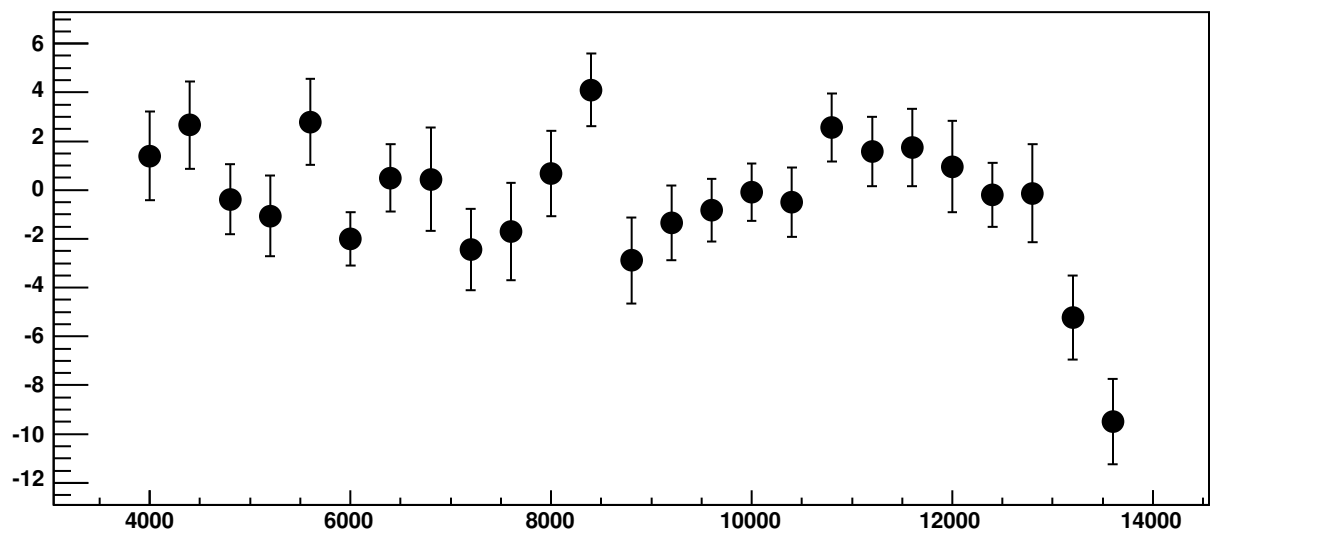
Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

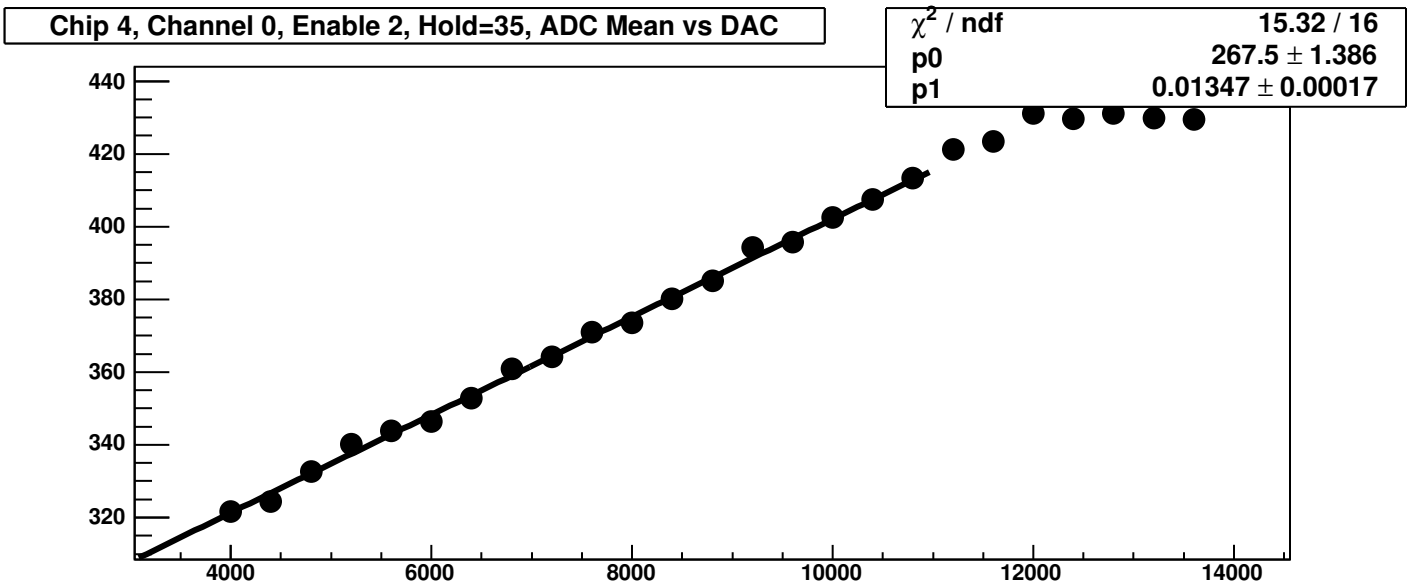


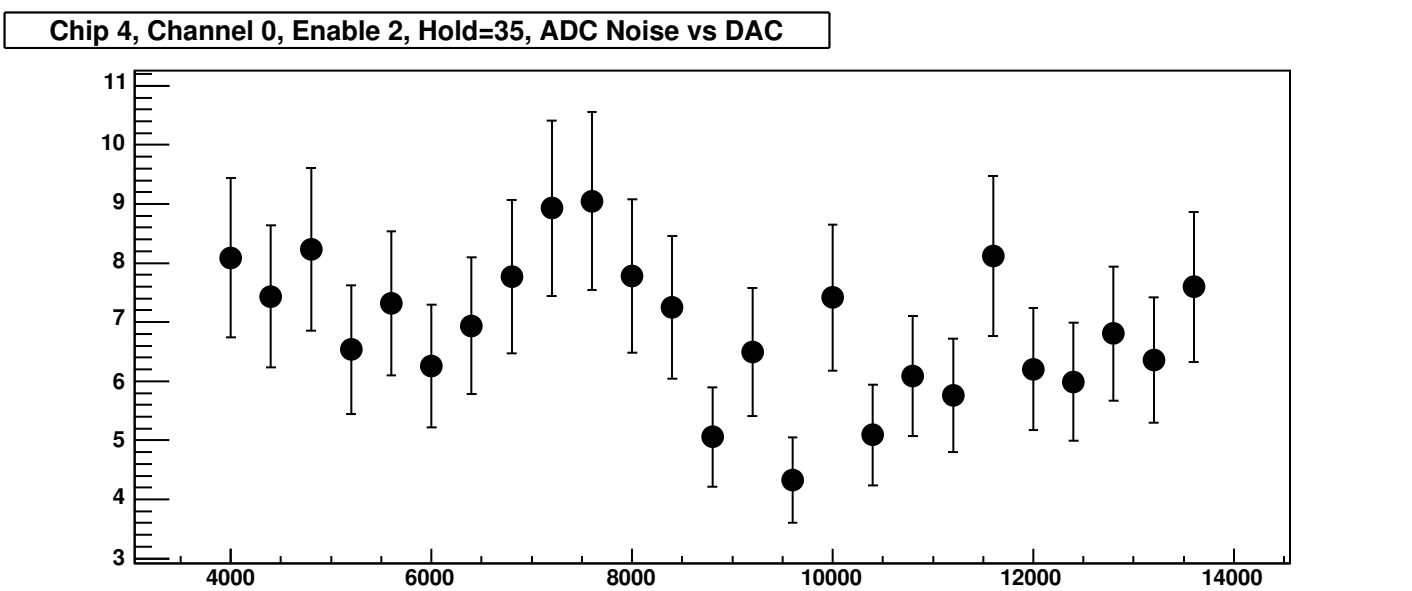
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

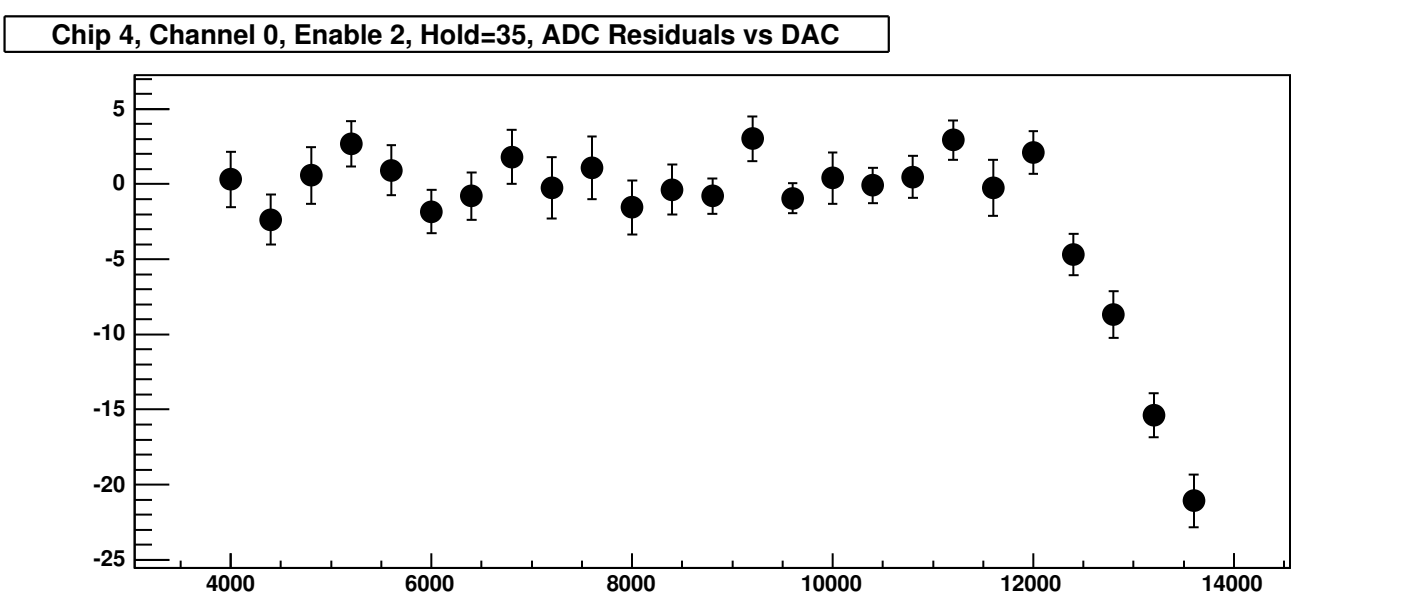


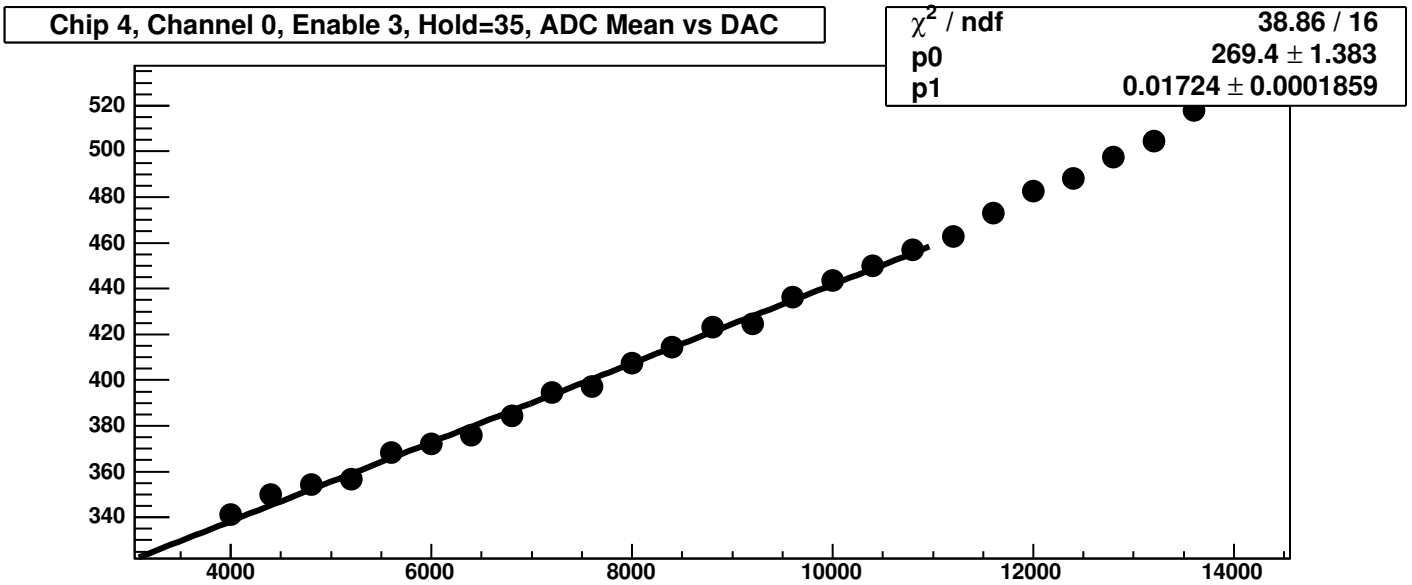
Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

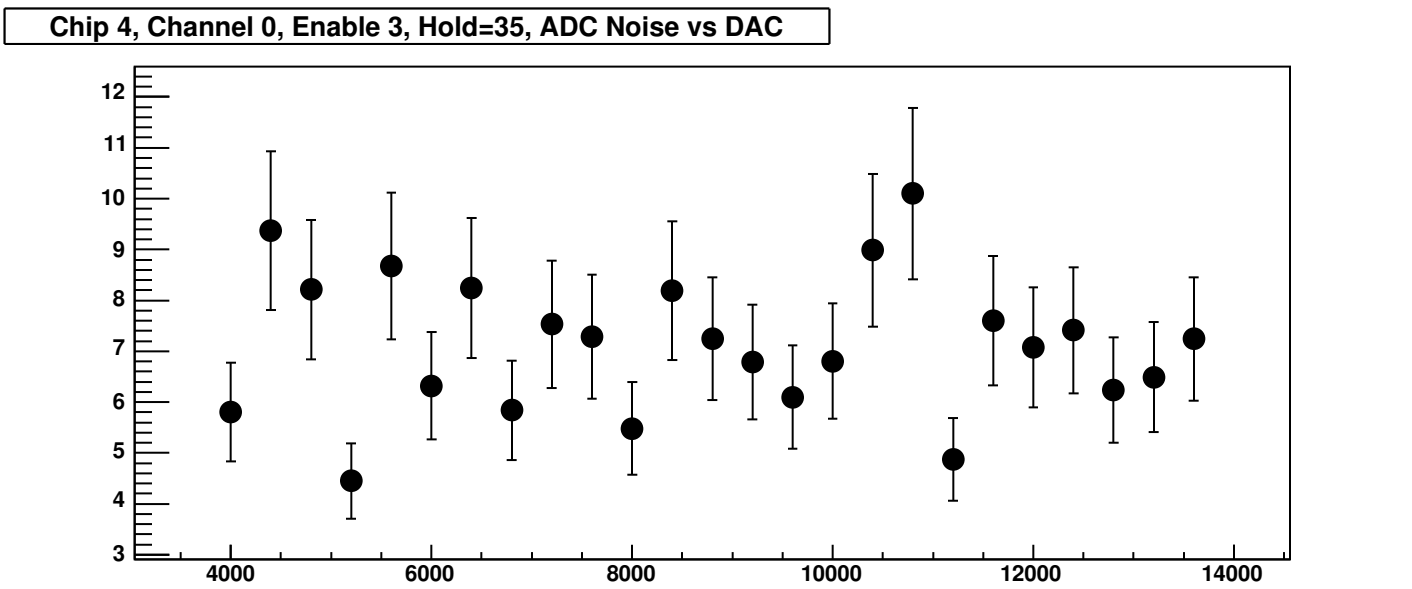


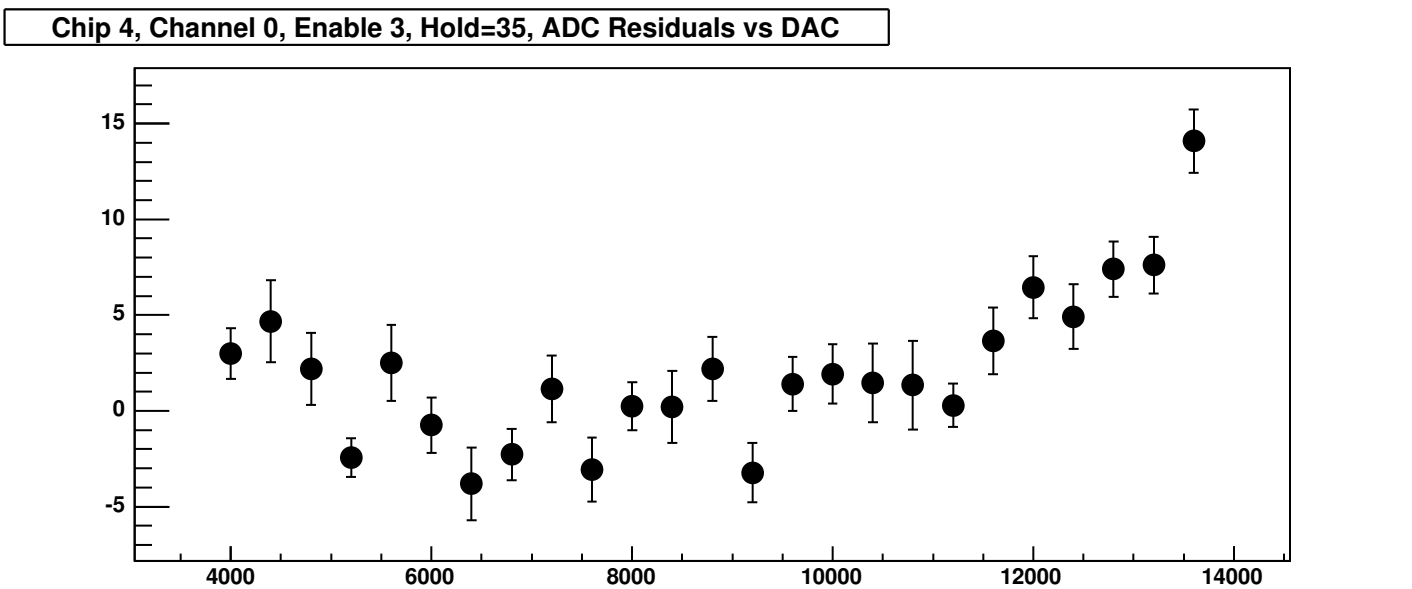

Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.32 / 16
p0 267.5 ± 1.386
p1 0.01347 ± 0.00017
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

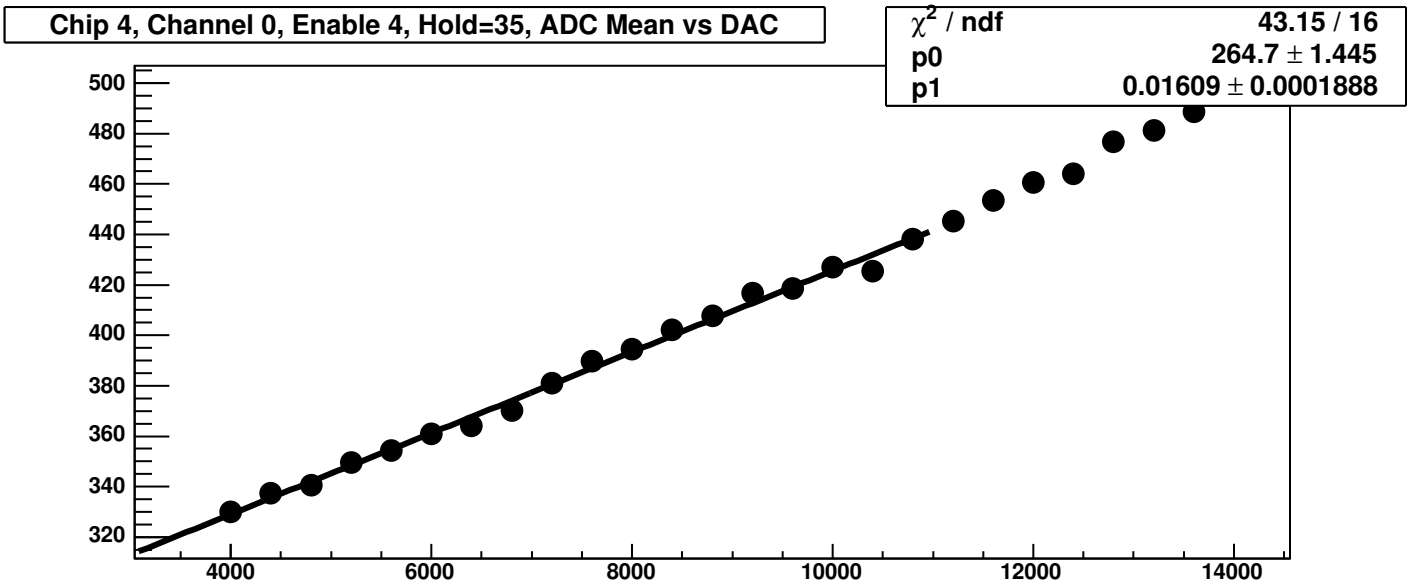

Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

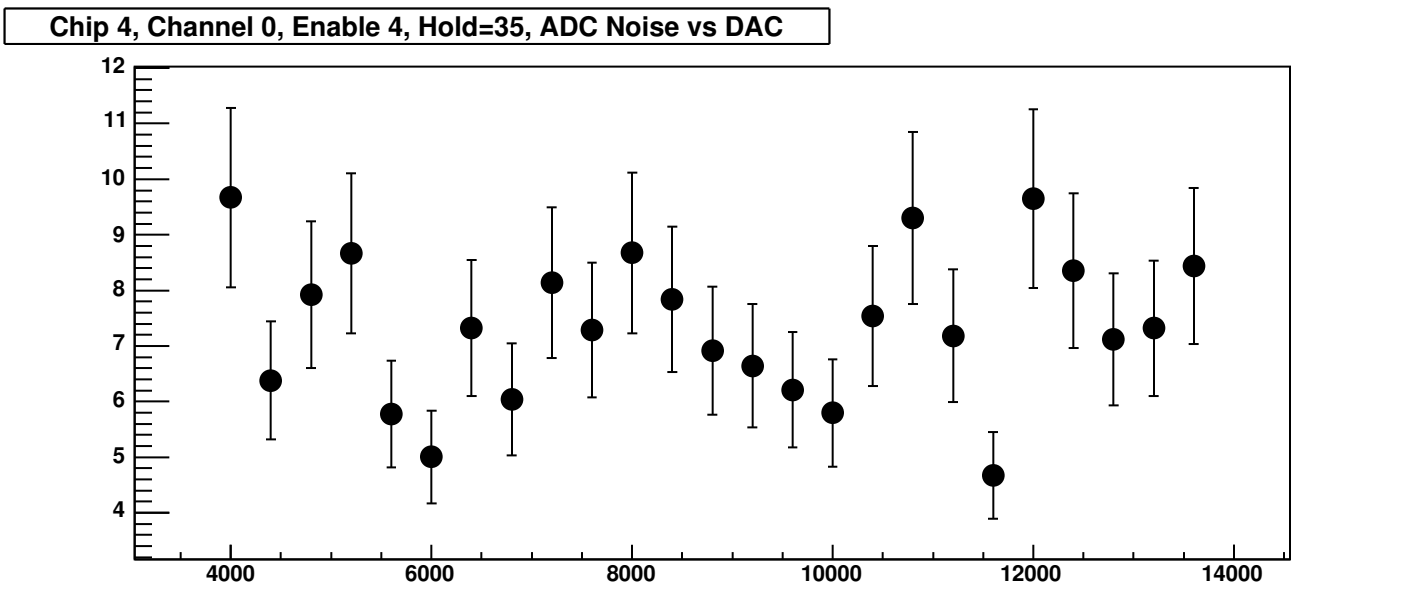

Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
4000
6000
8000
10000
12000
14000

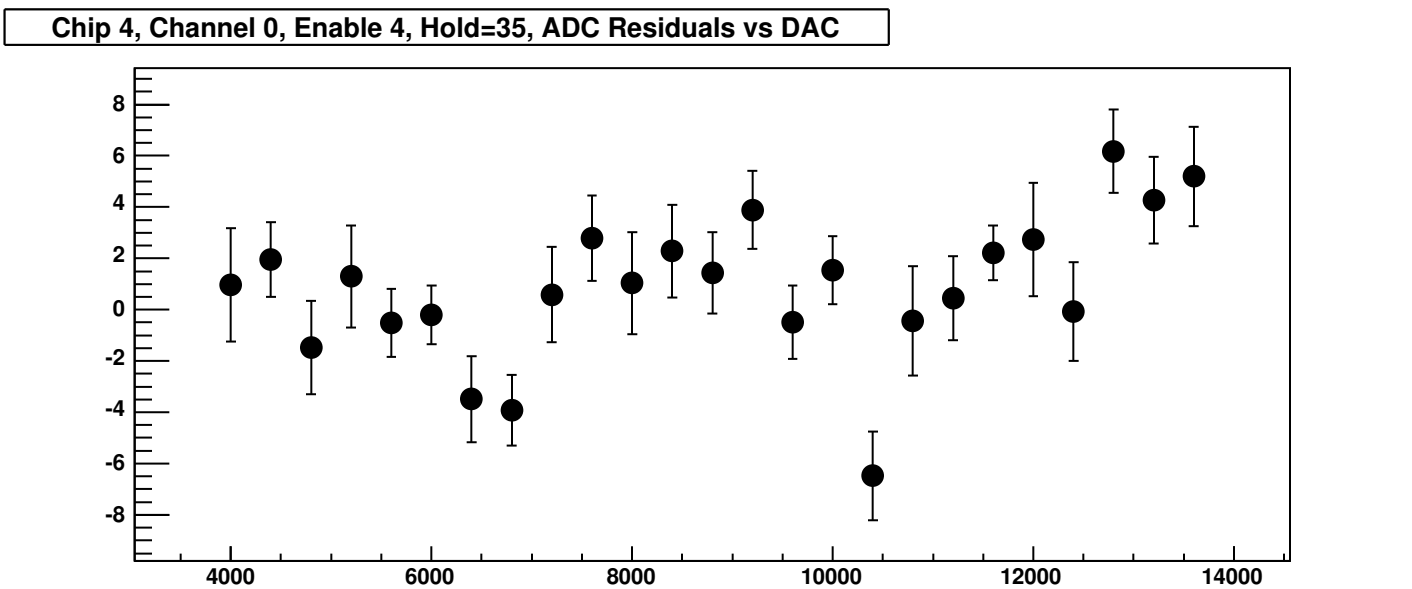

Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.86 / 16
p0 269.4 ± 1.383
p1 0.01724 ± 0.0001859
520
500
480
460
440
420
400
380
360
340
4000
6000
8000
10000
12000
14000

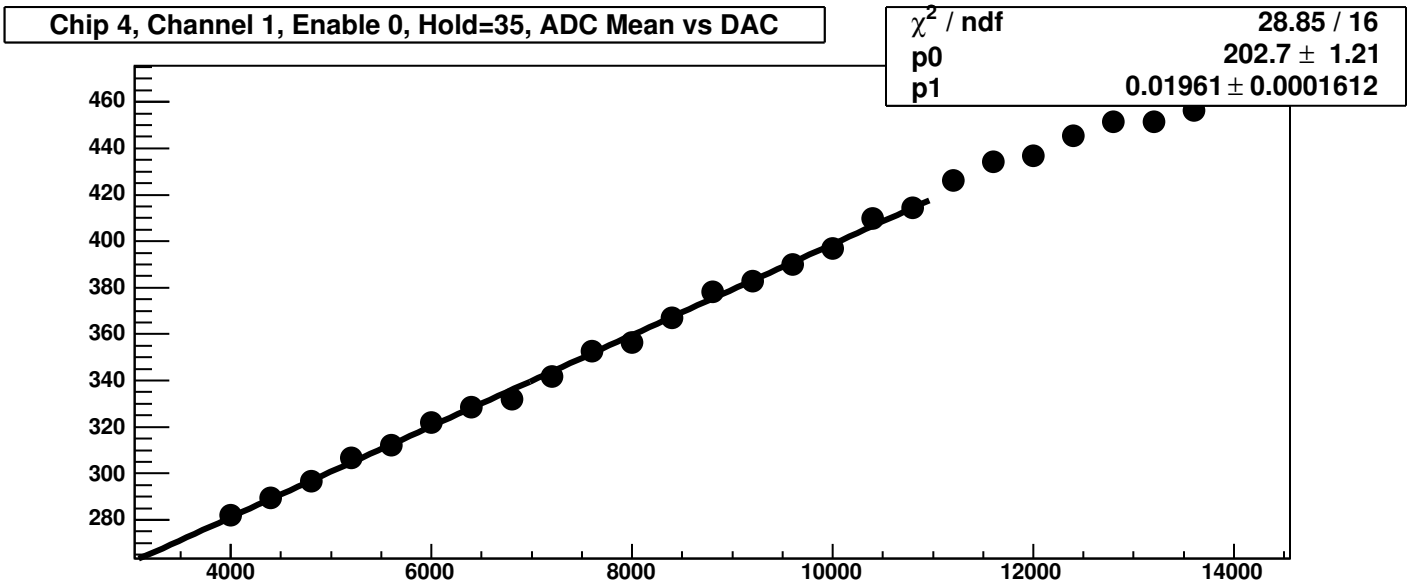

Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

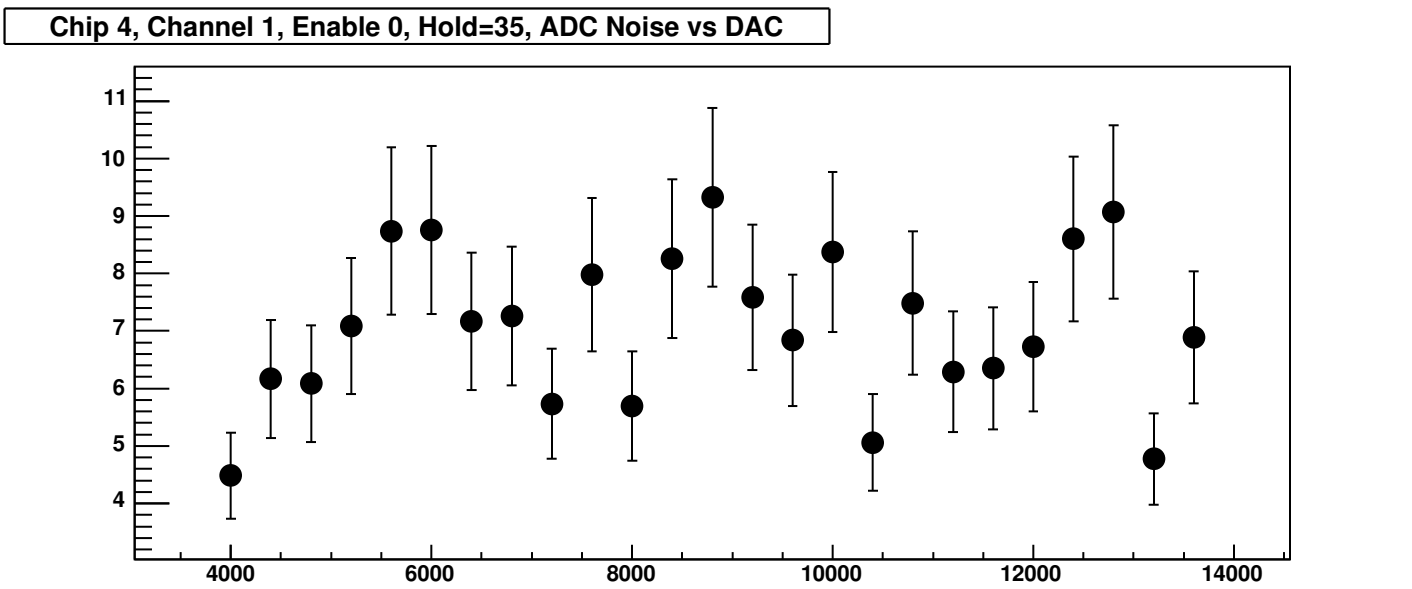

Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

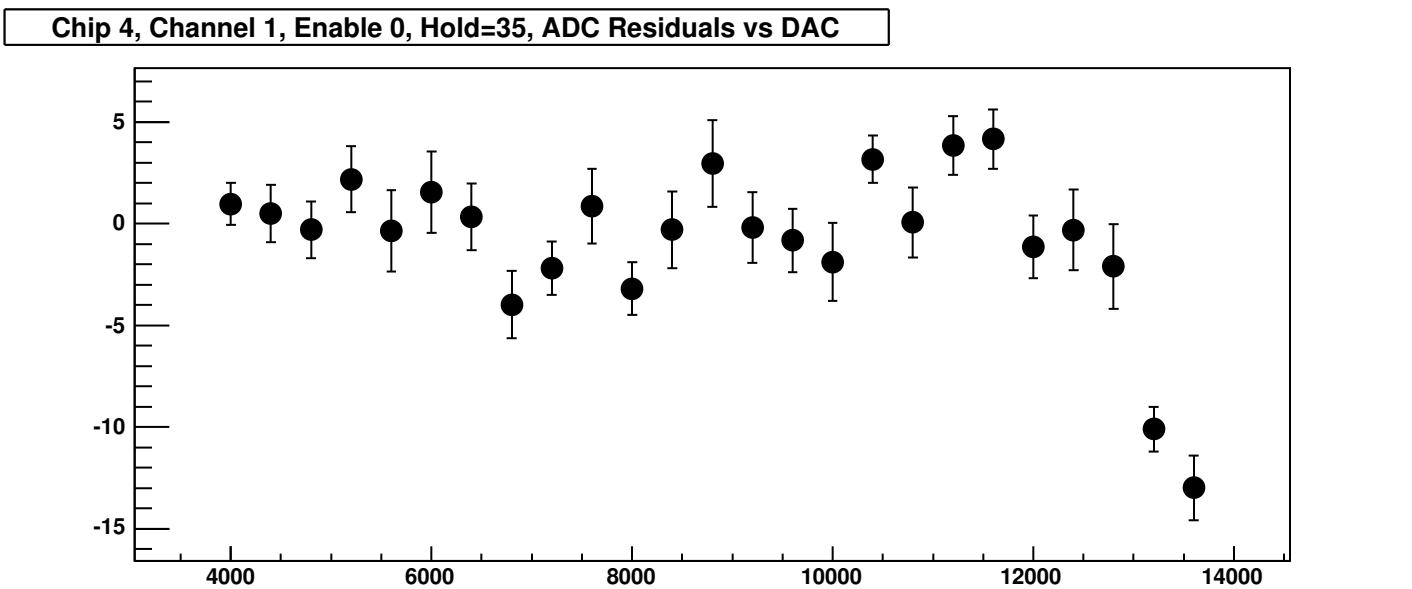

Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.15 / 16
p0 264.7 ± 1.445
p1 0.01609 ± 0.0001888
500
480
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

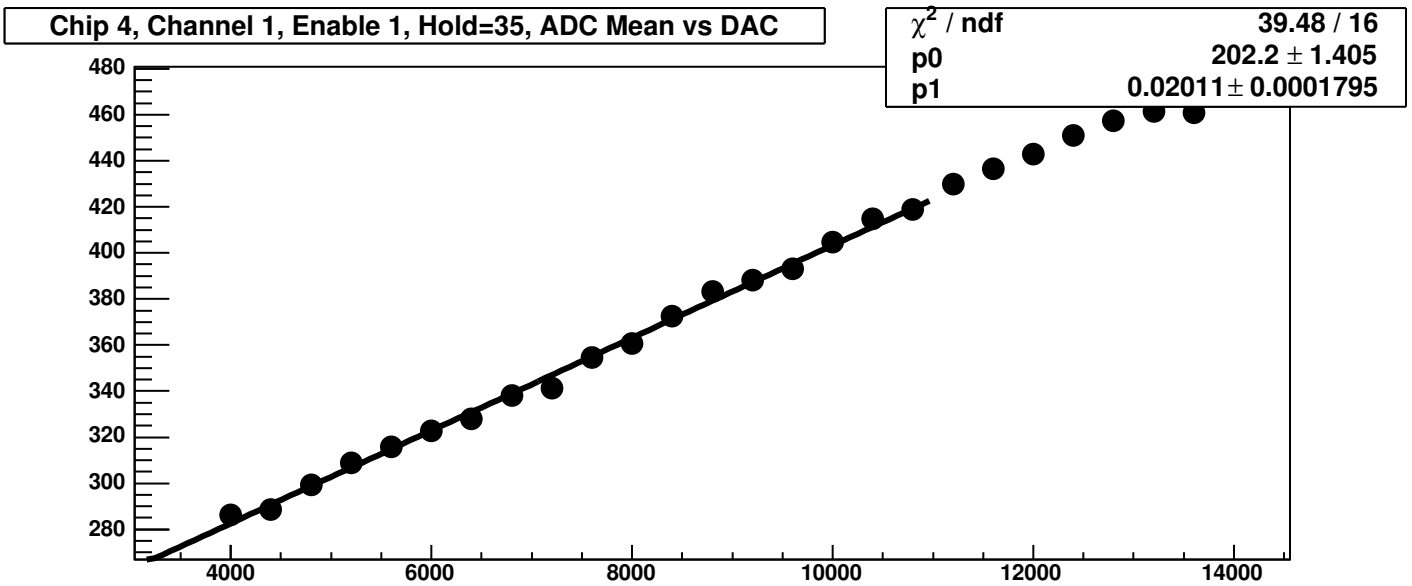

Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

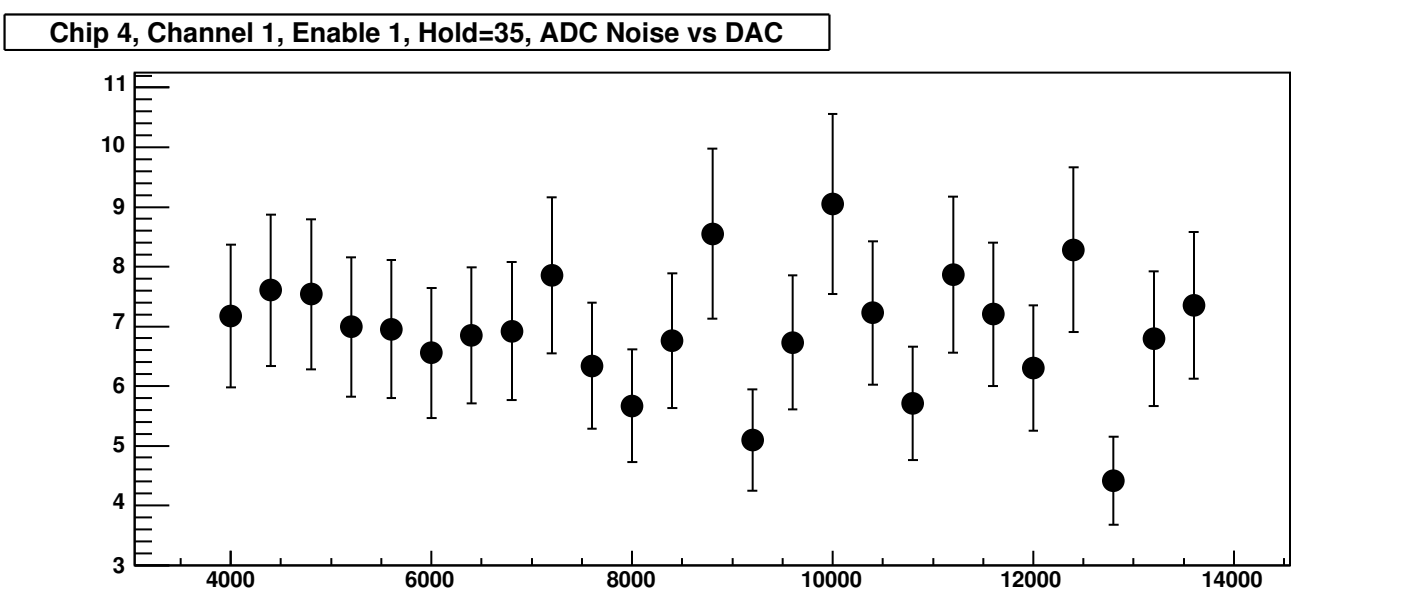

Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

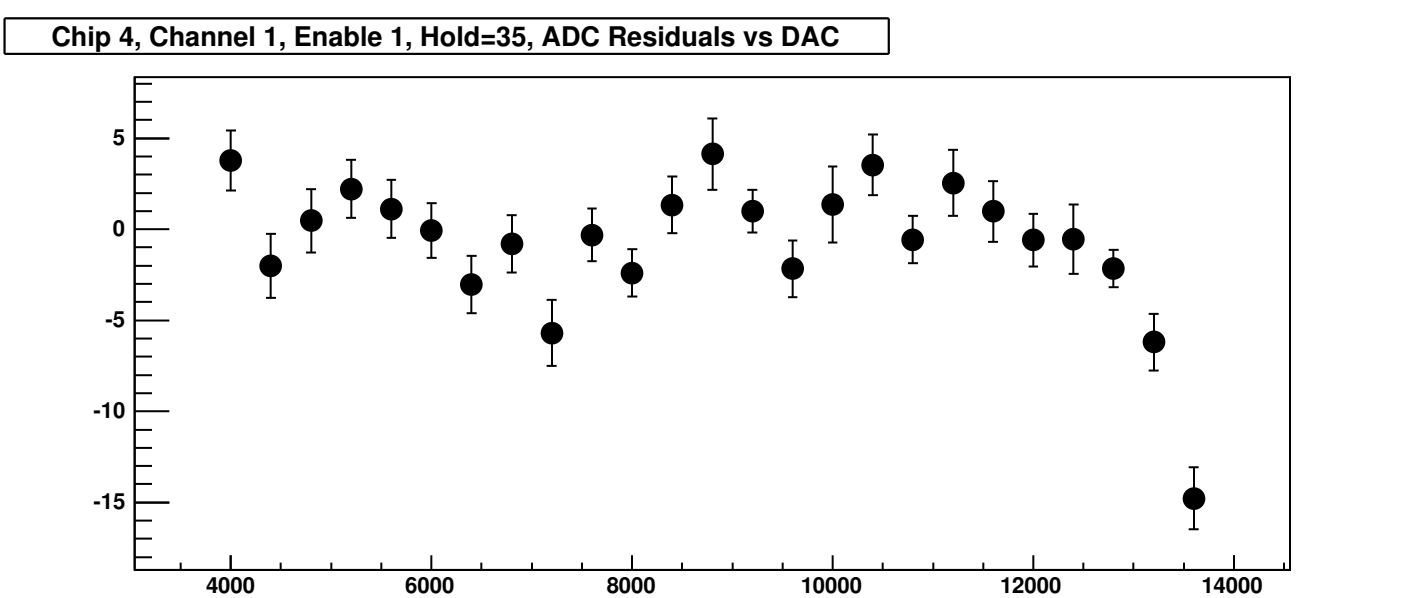

Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 28.85 / 16
p0 202.7 ± 1.21
p1 0.01961 ± 0.0001612
460
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

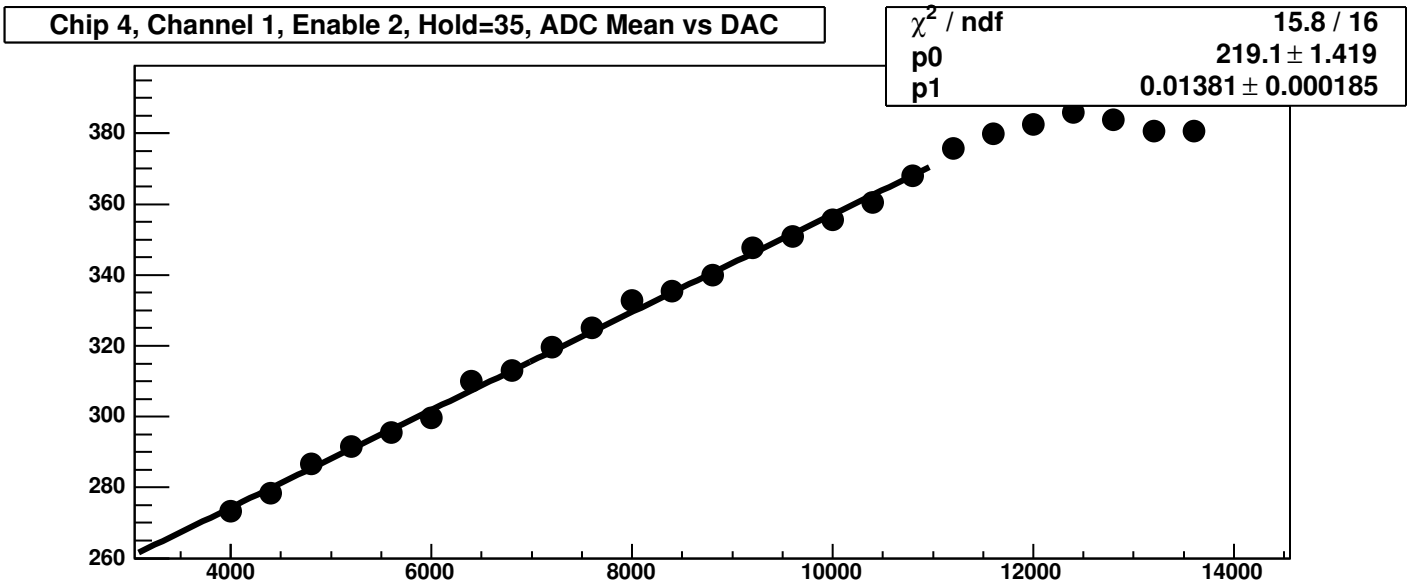

Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

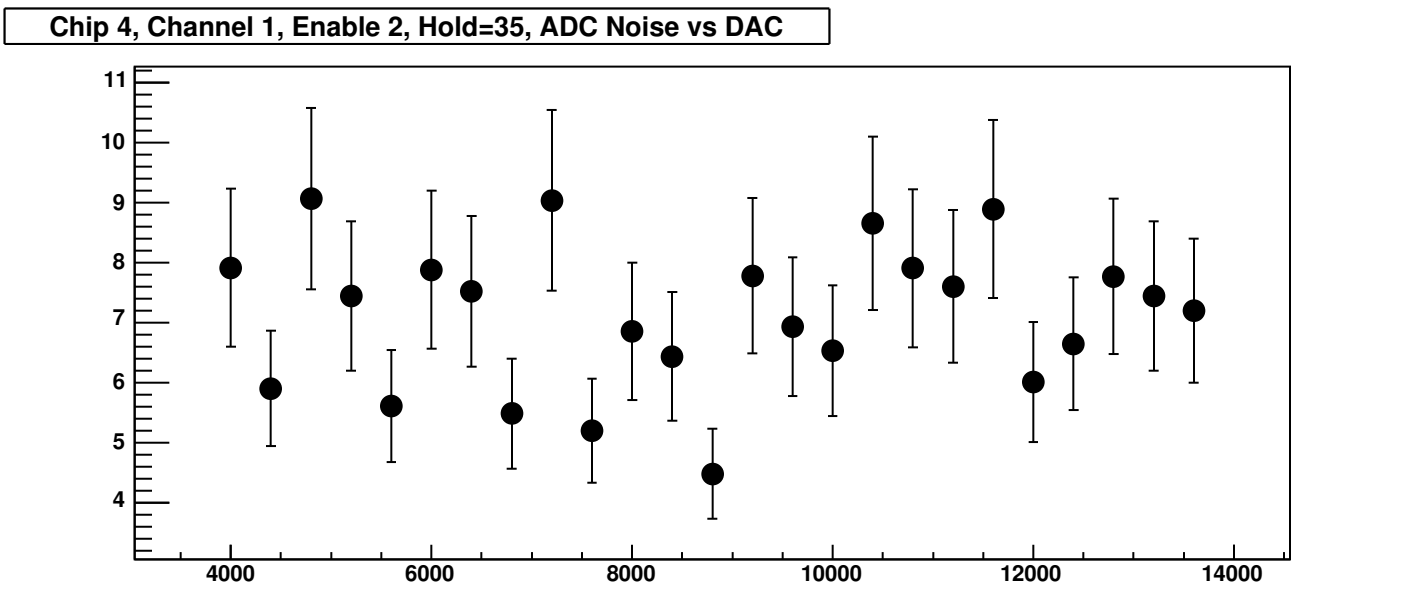

Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

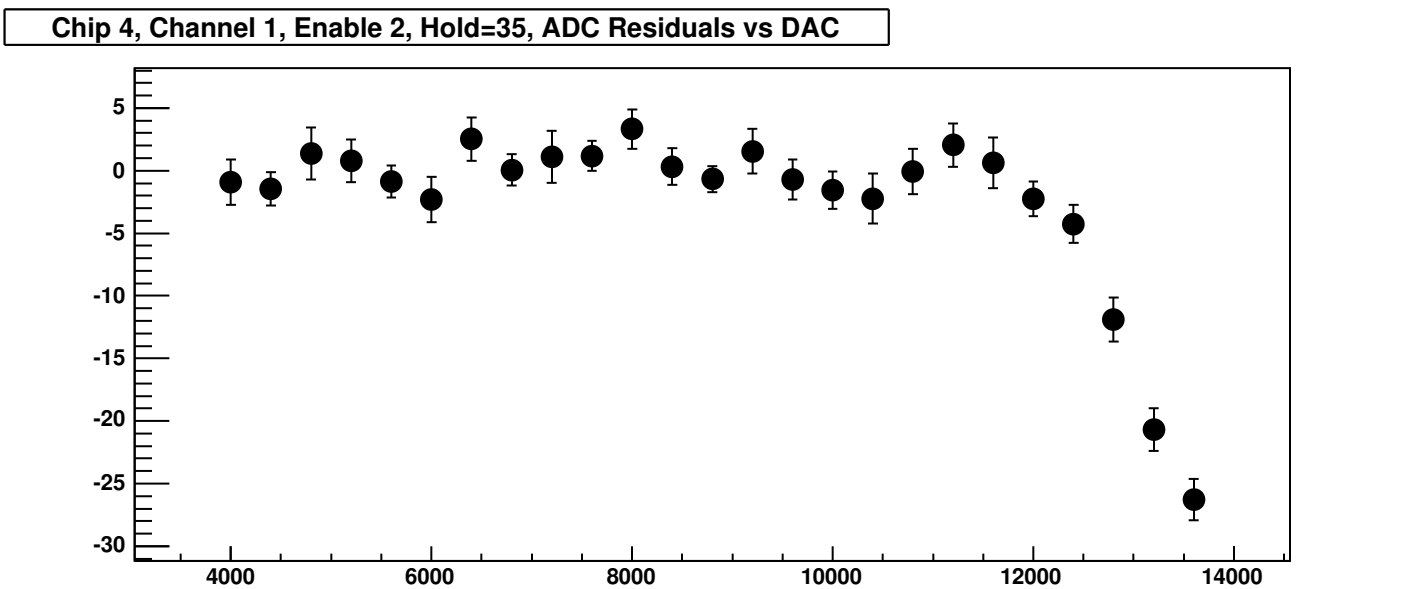

Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.48 / 16
p0 202.2 ± 1.405
p1 0.02011 ± 0.0001795
480
460
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

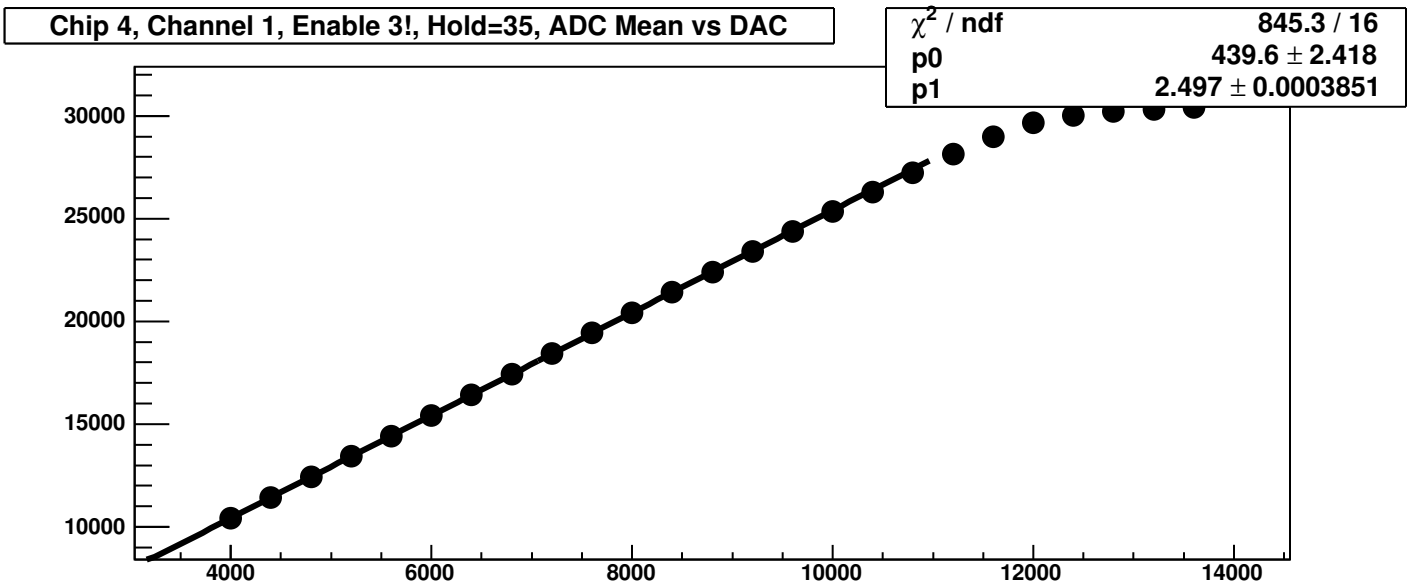

Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

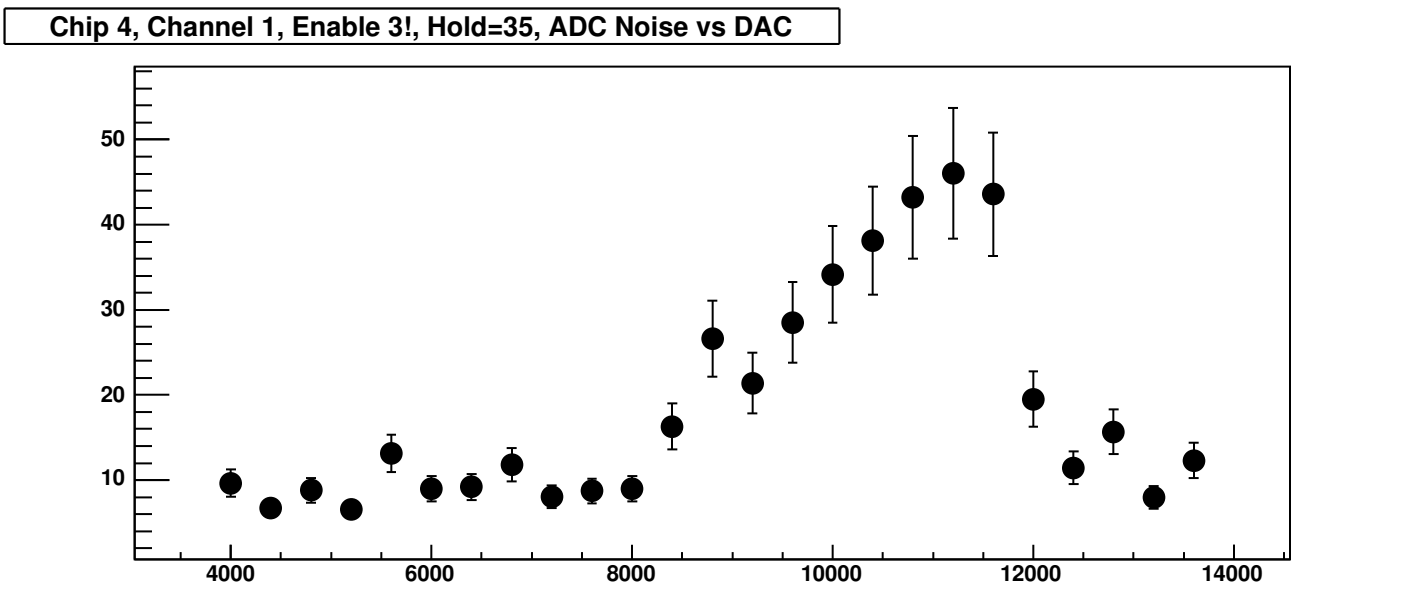

Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

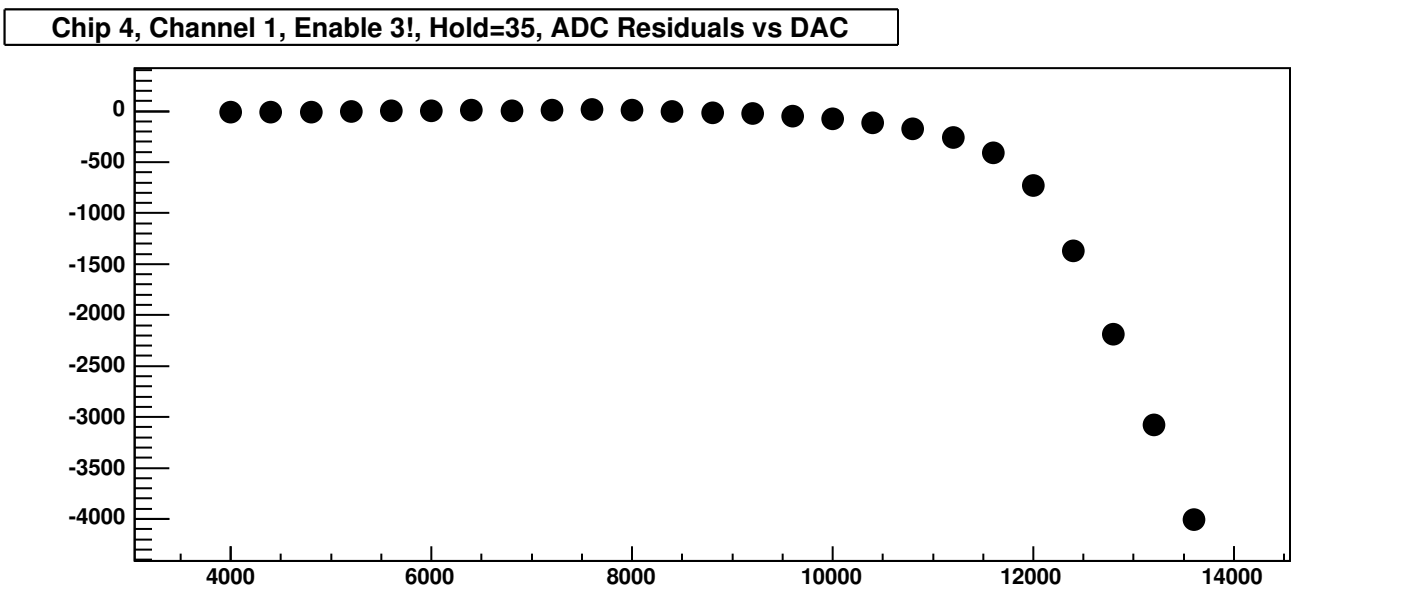

Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.8 / 16
p0 219.1 ± 1.419
p1 0.01381 ± 0.000185
380
360
340
320
300
280
260
4000
6000
8000
10000
12000
14000

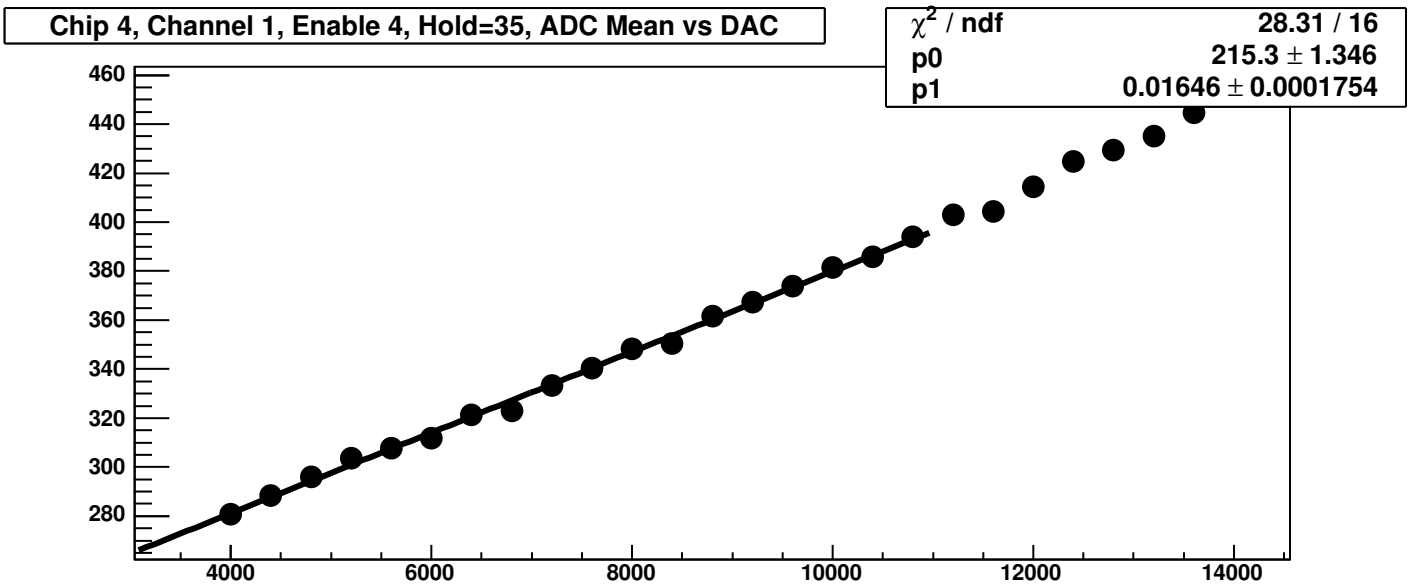

Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

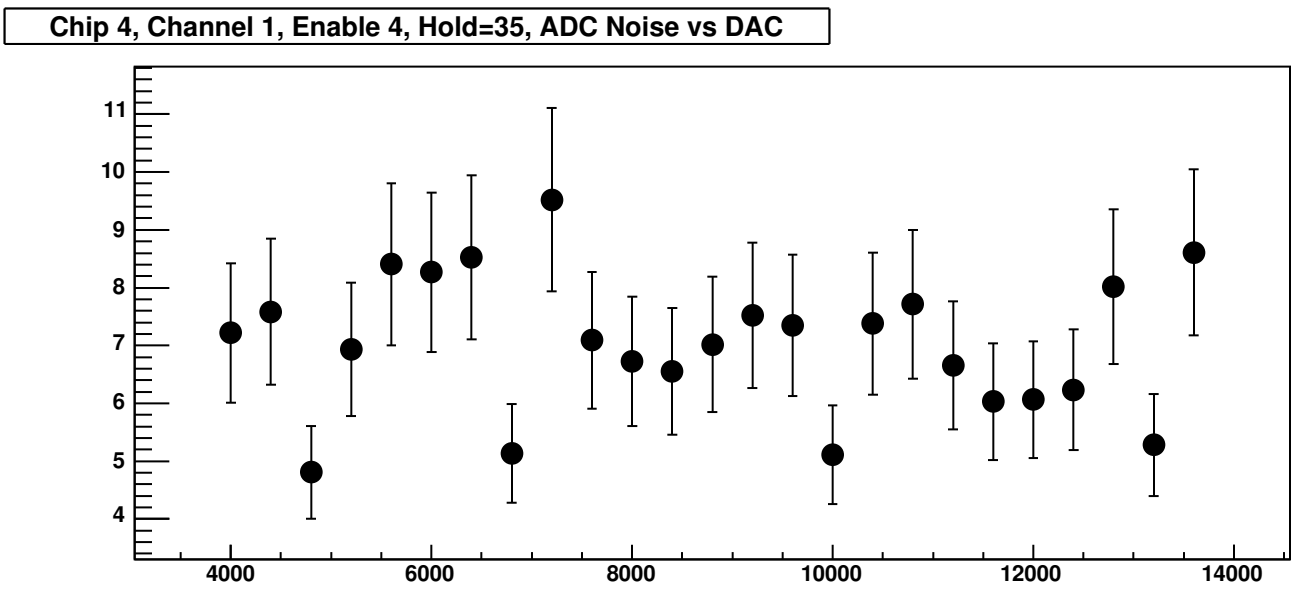

Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

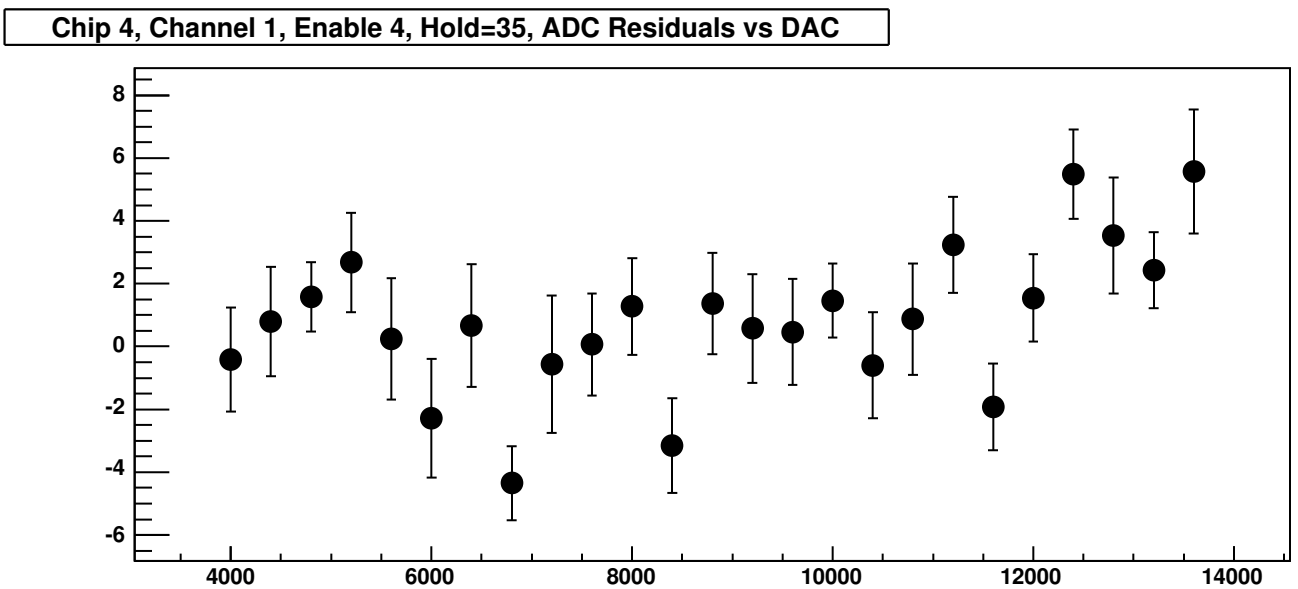

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 845.3 / 16
p0 439.6 ± 2.418
p1 2.497 ± 0.0003851
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

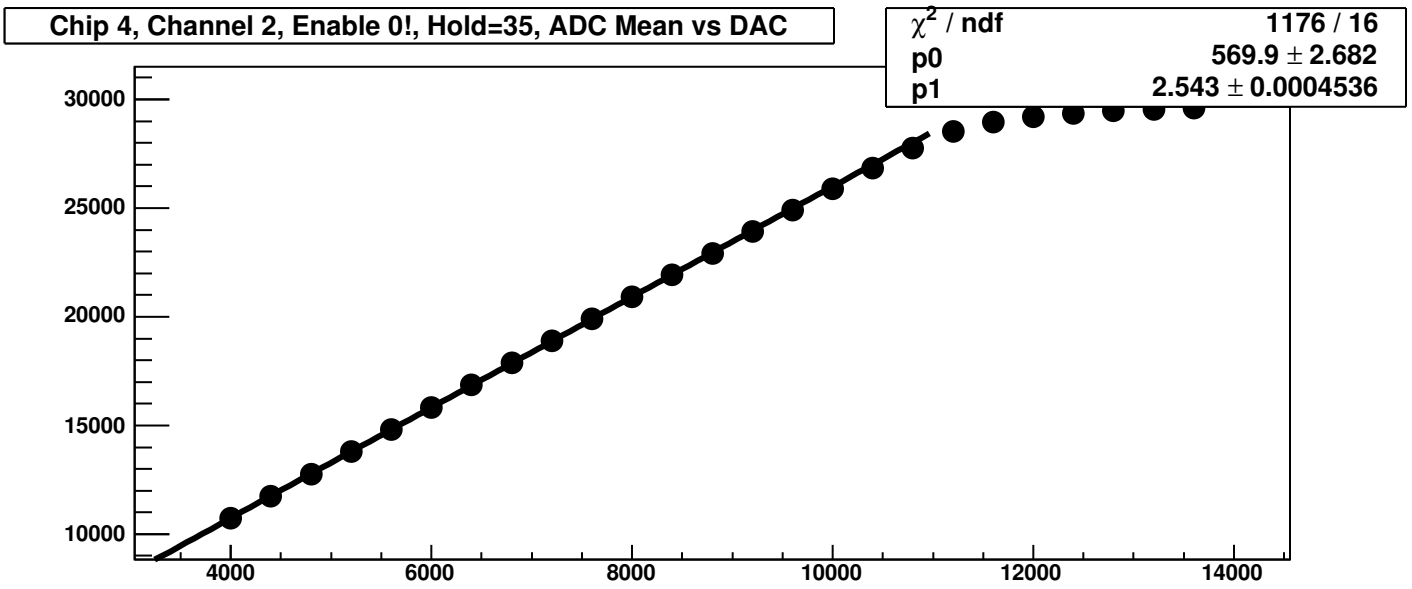

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

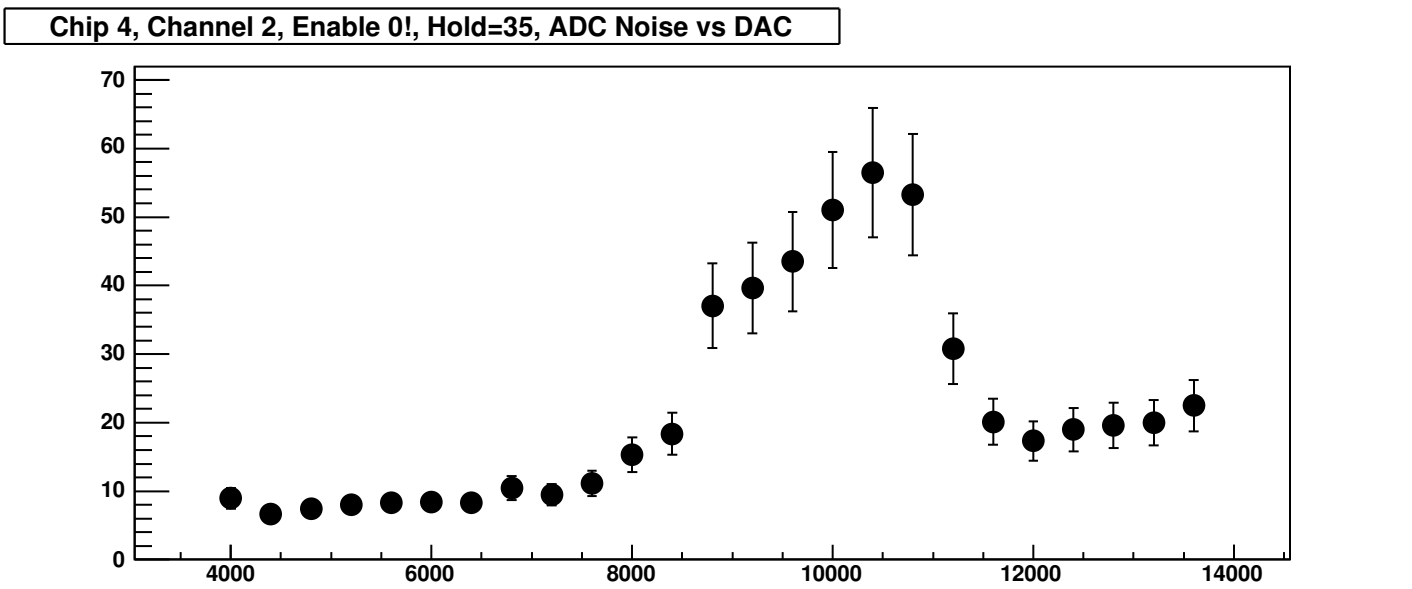

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

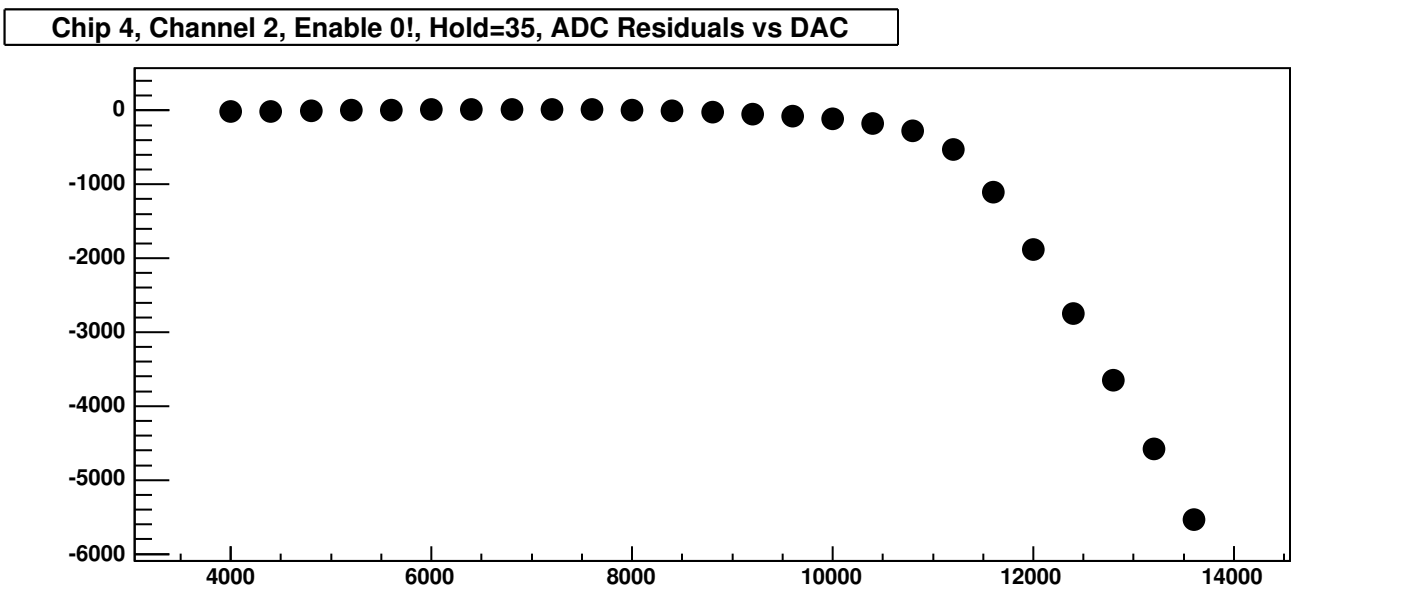

Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.31 / 16
p0 215.3 ± 1.346
p1 0.01646 ± 0.0001754
460
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

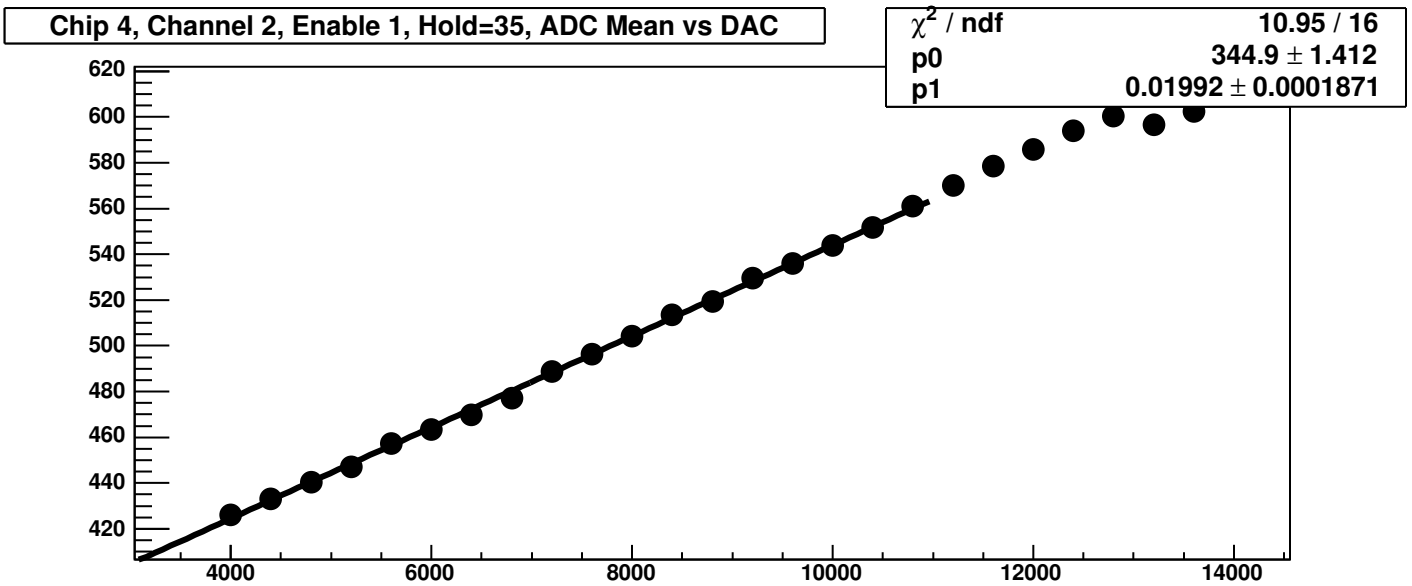

Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

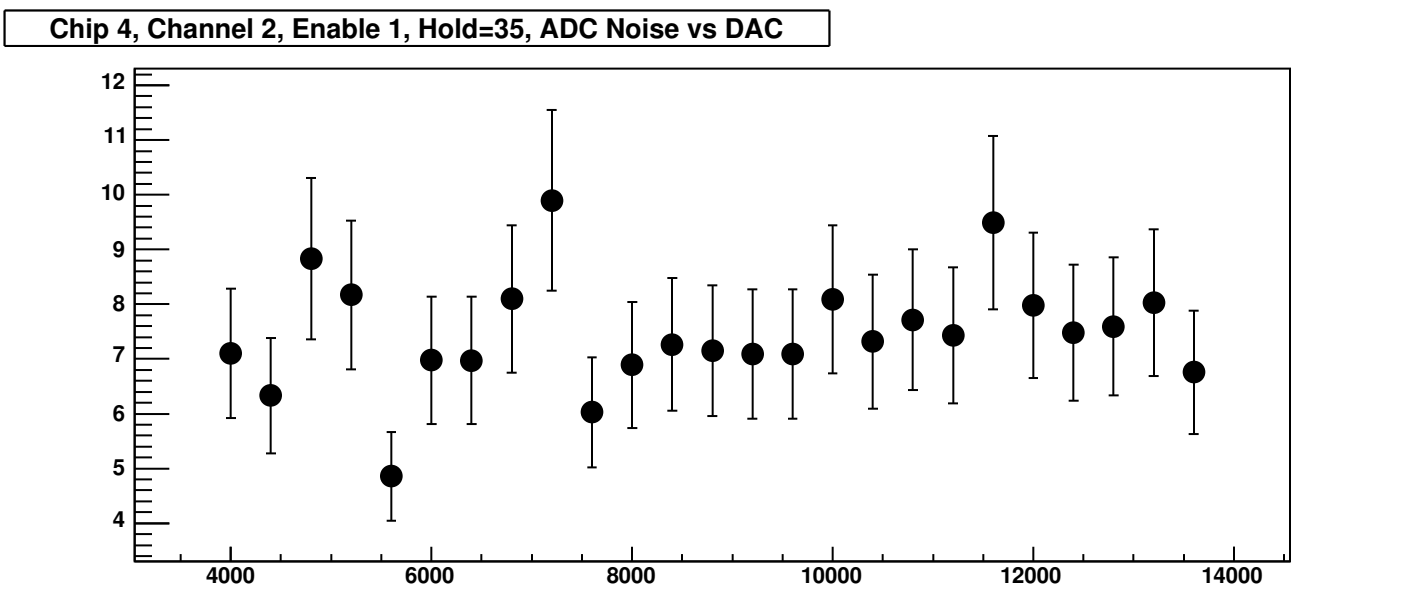

Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

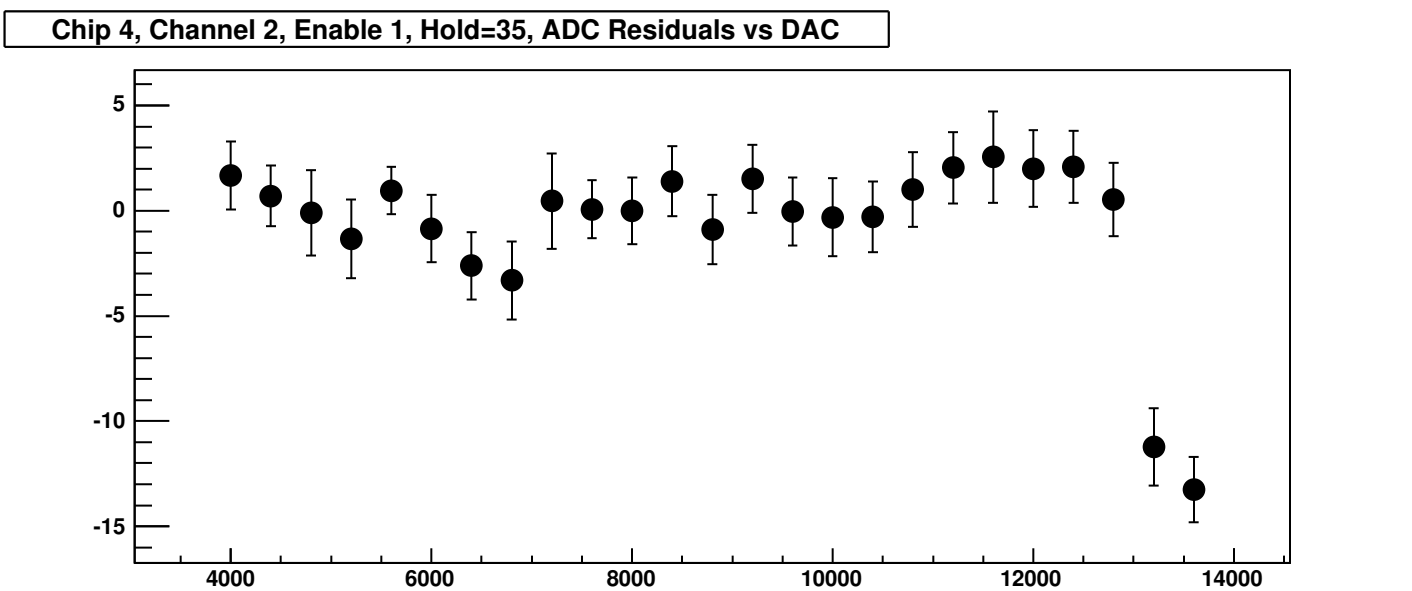

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1176 / 16
p0 569.9 ± 2.682
p1 2.543 ± 0.0004536
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

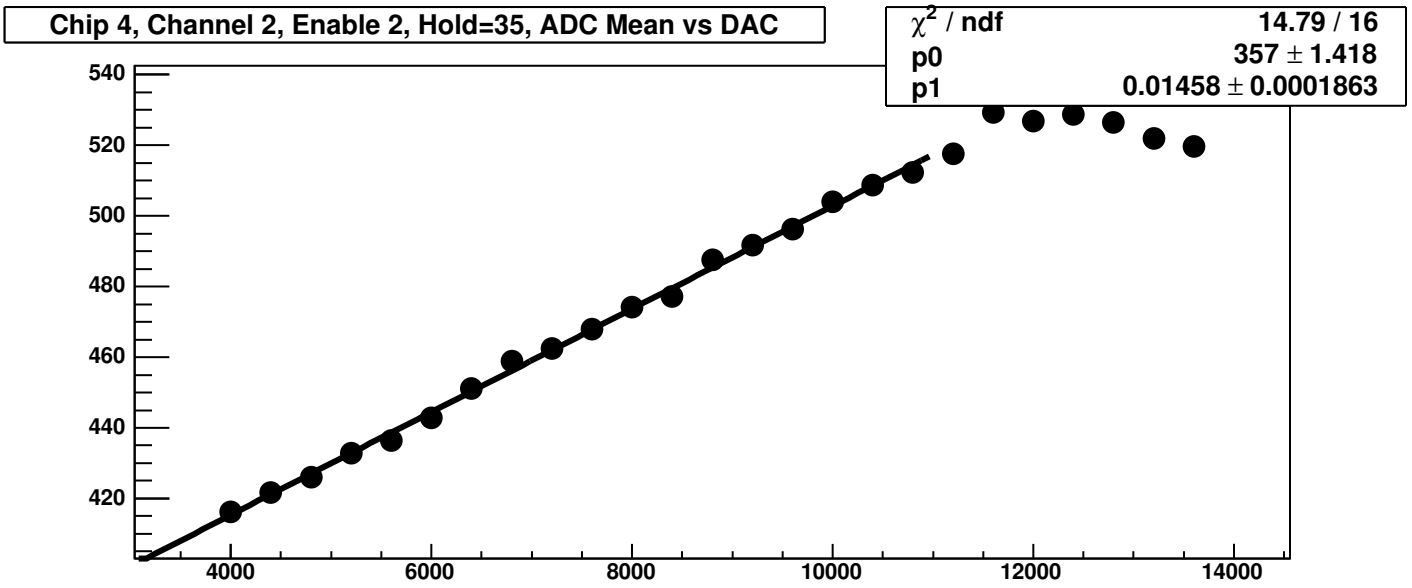

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

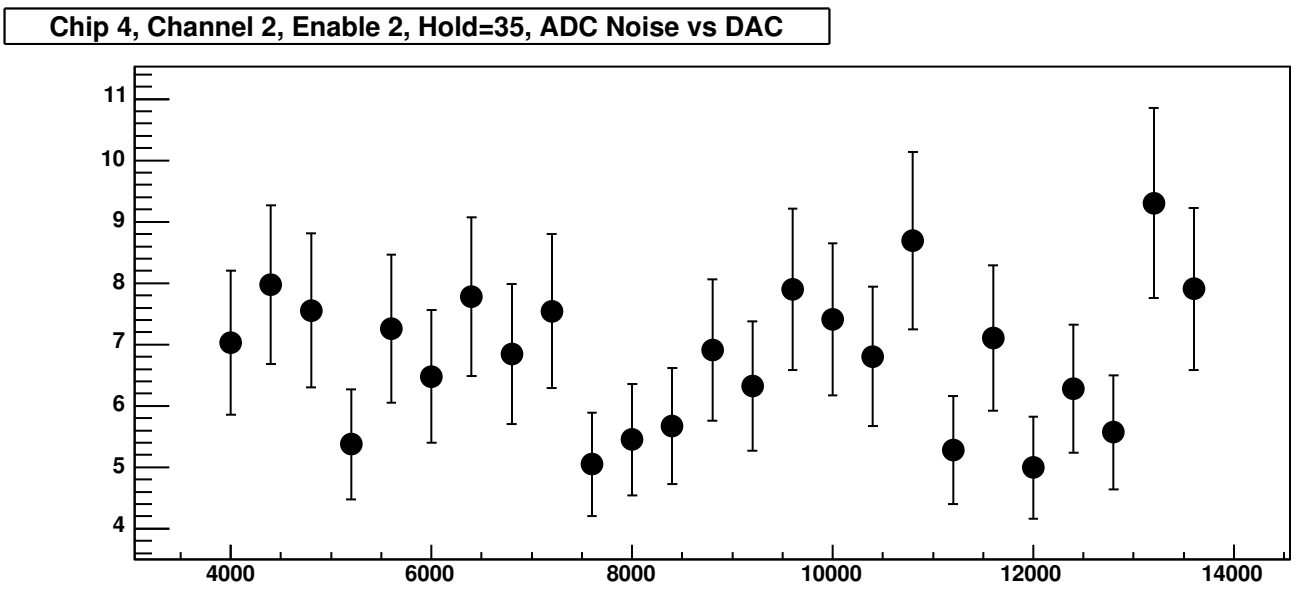

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000

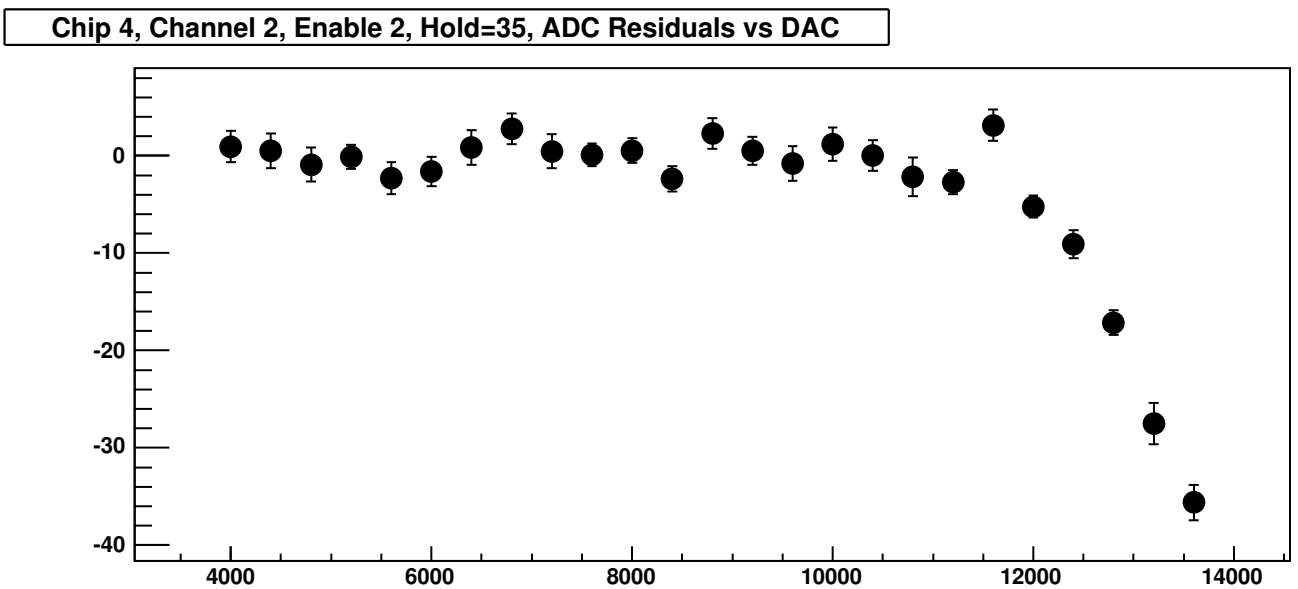

Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 10.95 / 16
p0 344.9 ± 1.412
p1 0.01992 ± 0.0001871
620
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

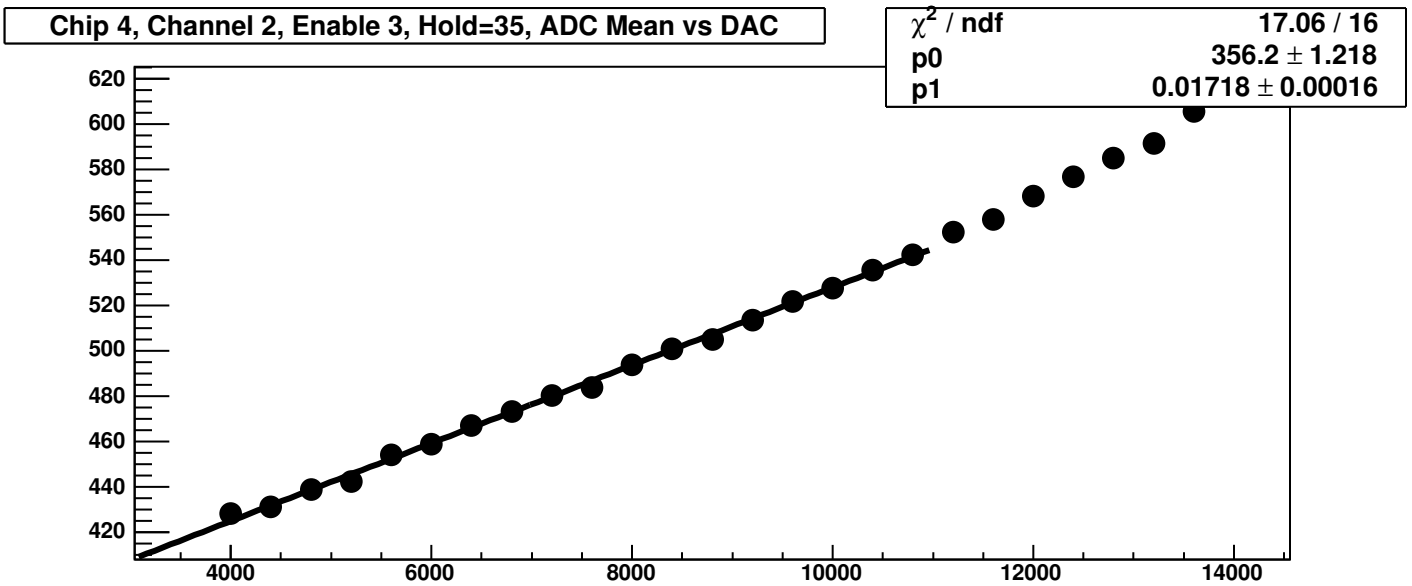

Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

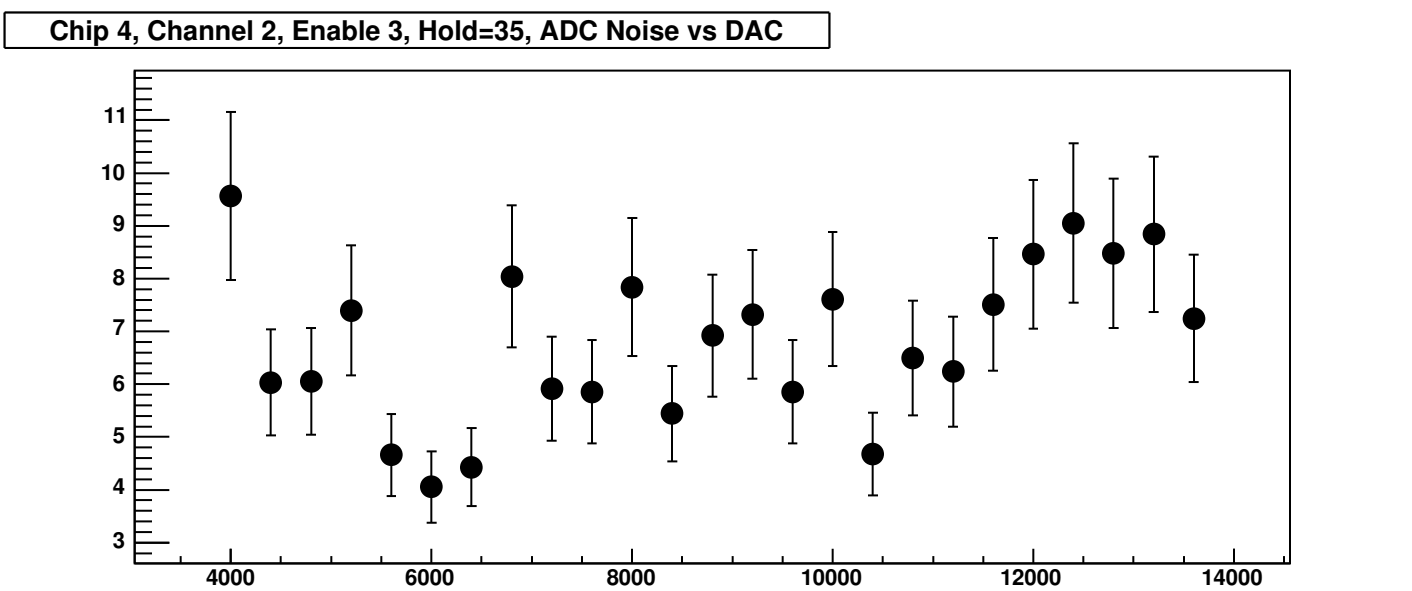

Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

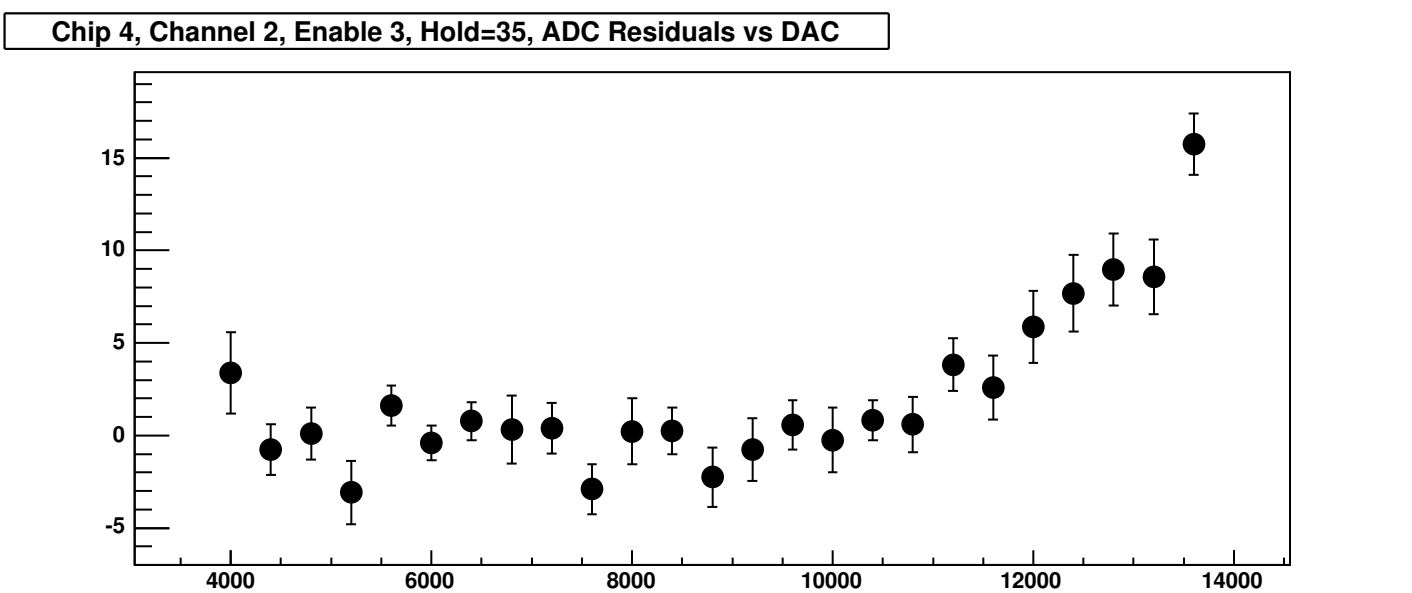

Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 14.79 / 16
p0 357 ± 1.418
p1 0.01458 ± 0.0001863
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

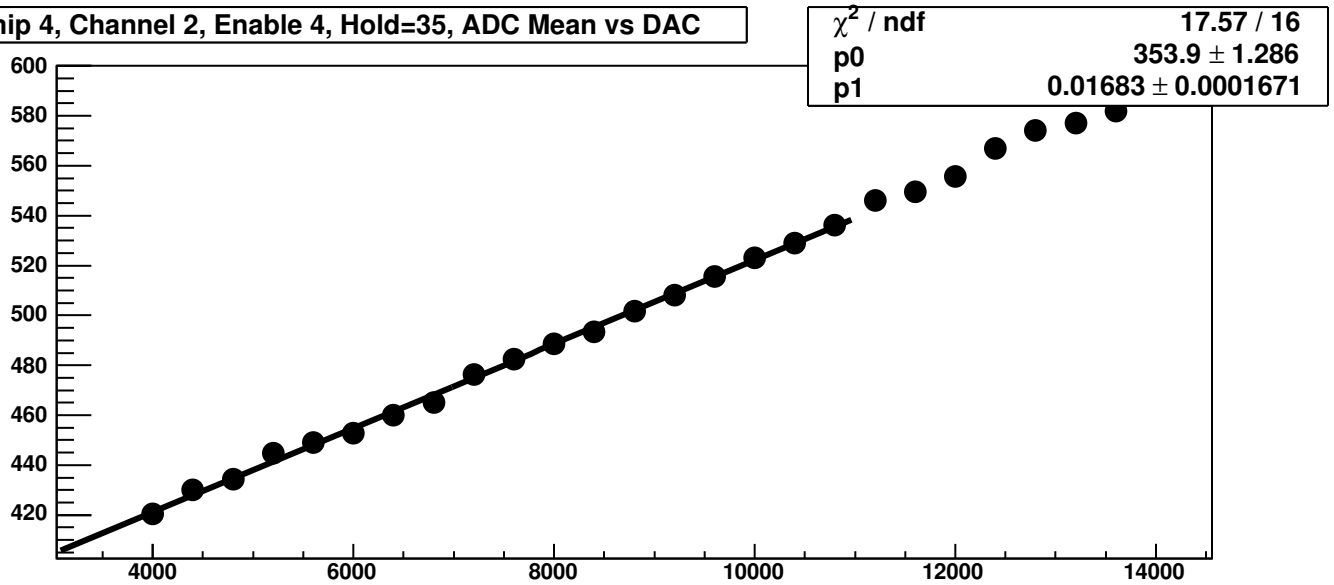

Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.06 / 16
p0 356.2 ± 1.218
p1 0.01718 ± 0.00016
620
600
580
560
540
520
500
480
460
440
420
4000
6000
8000
10000
12000
14000

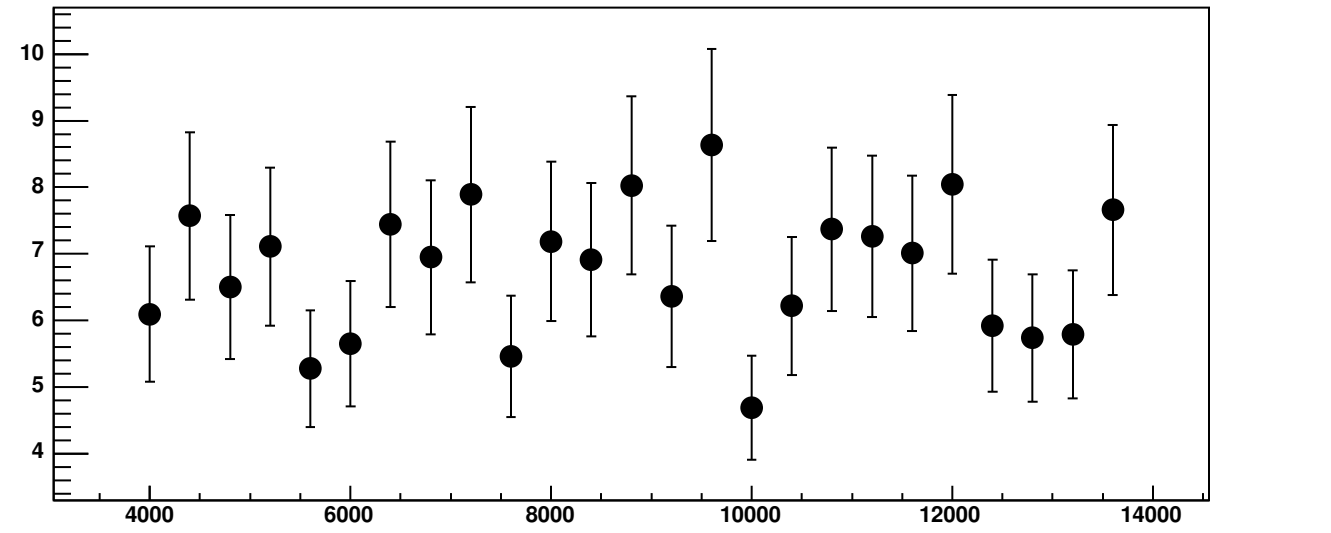

Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

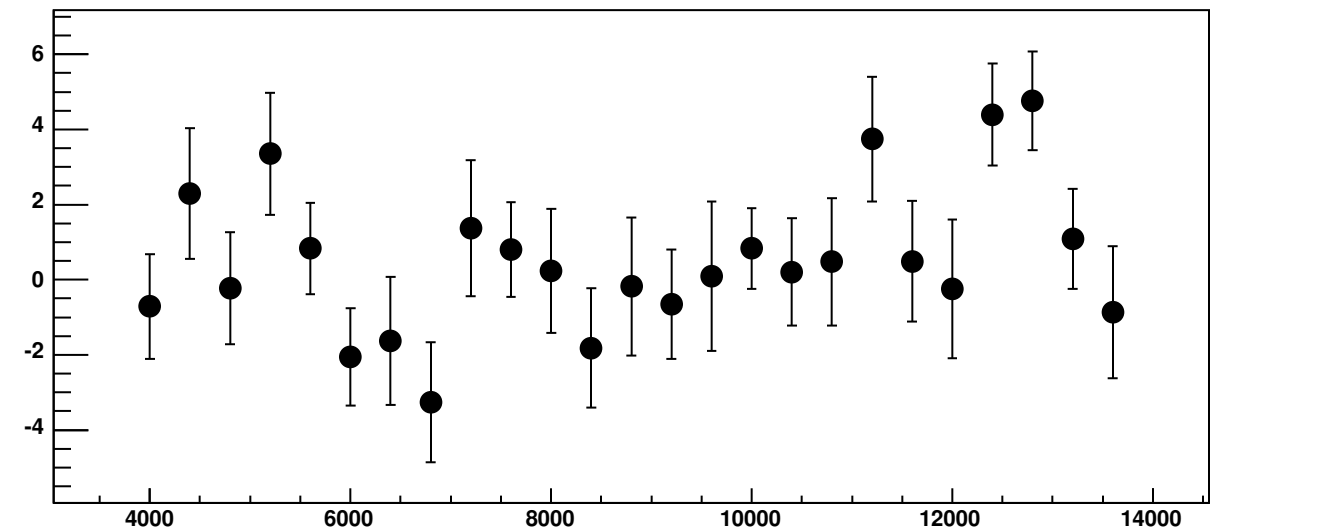
Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

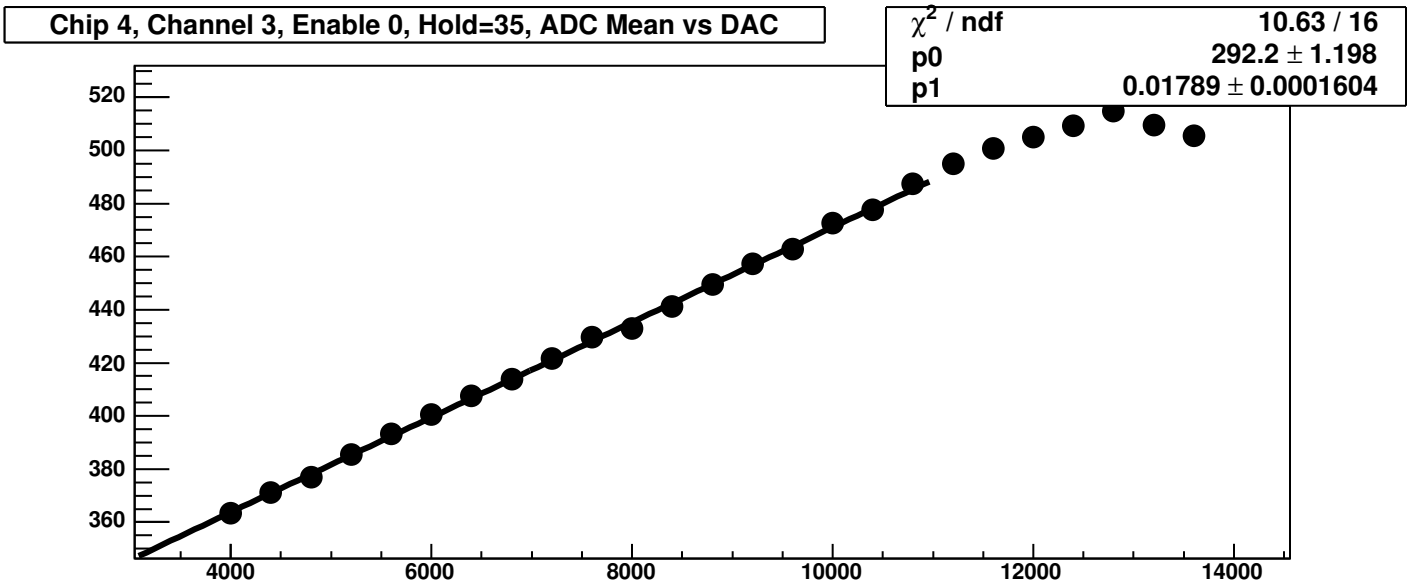


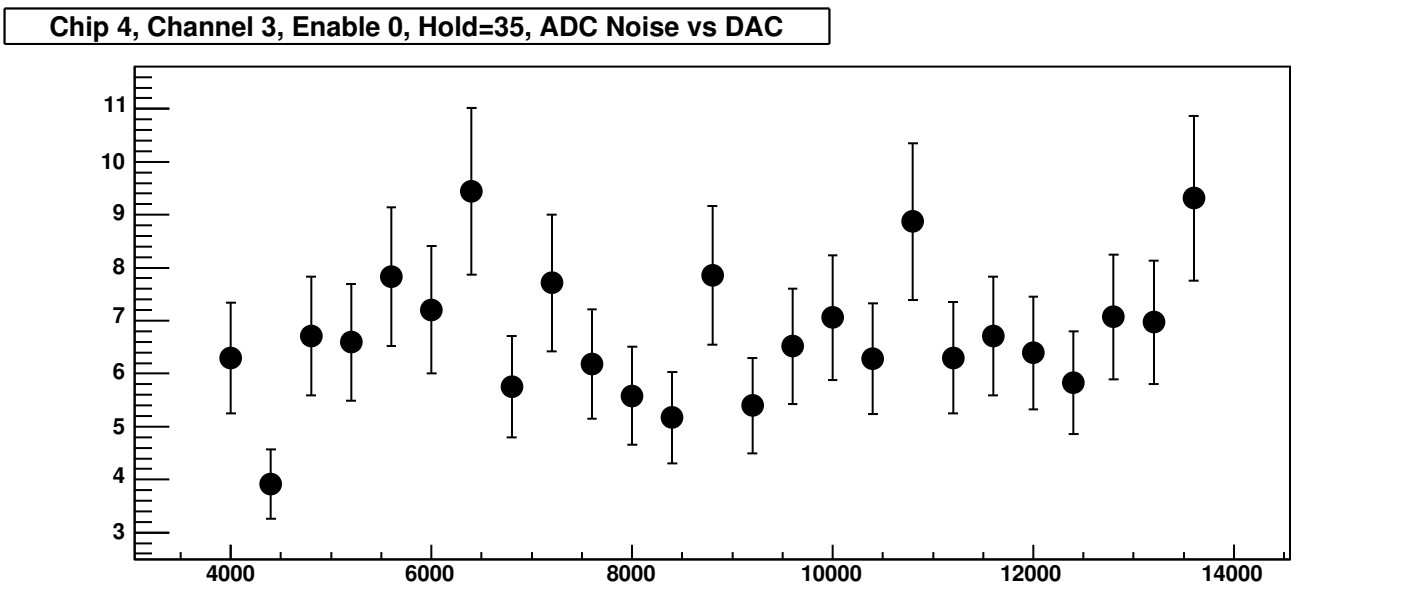
Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

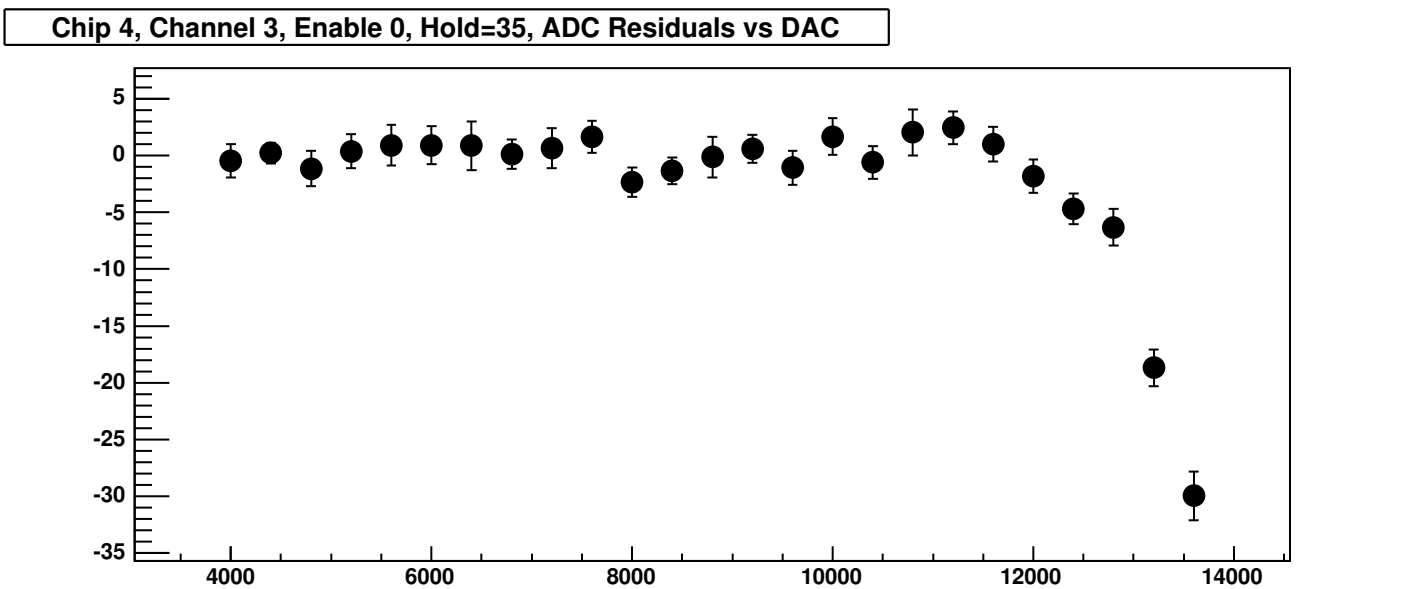


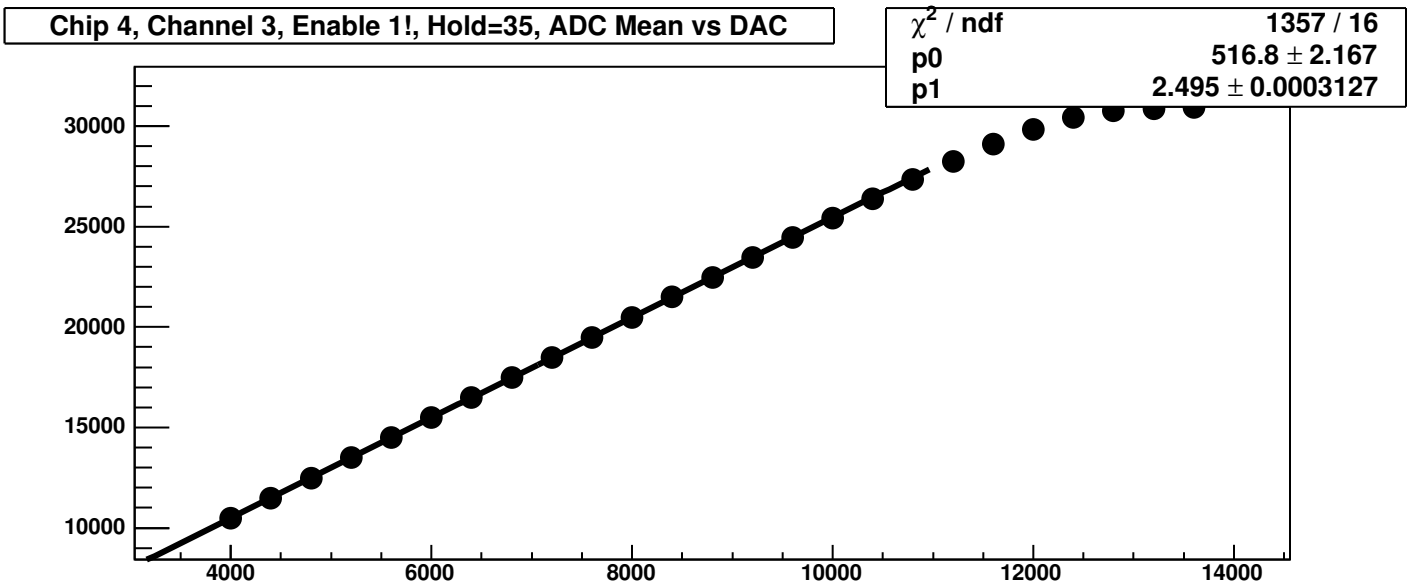
Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

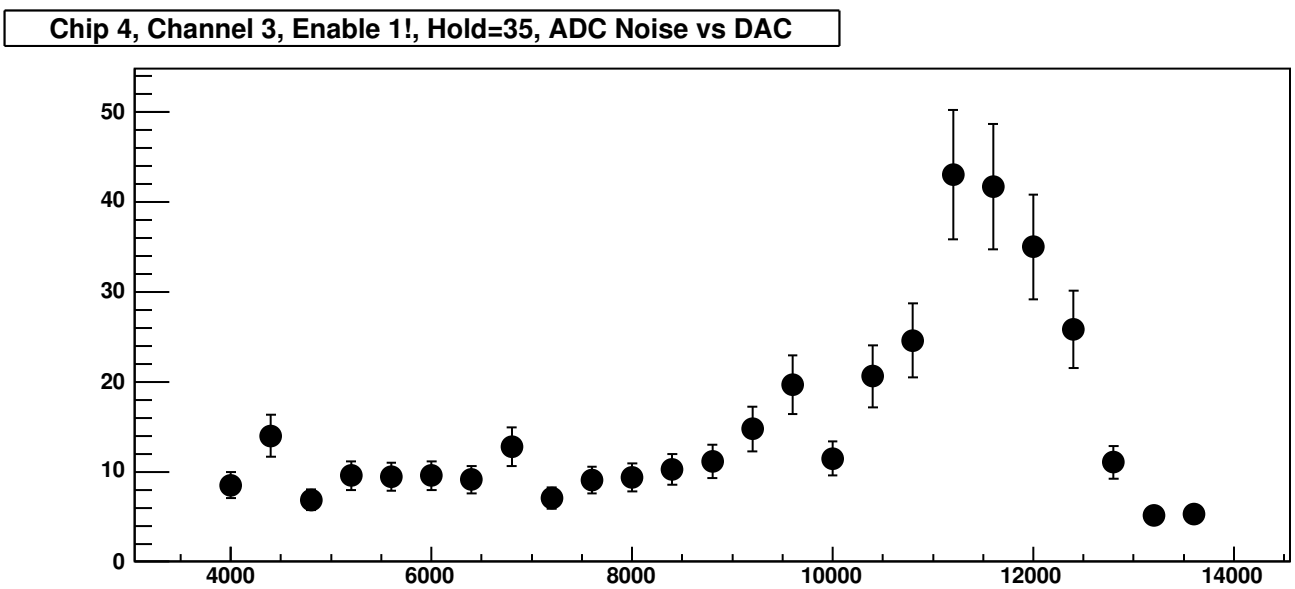


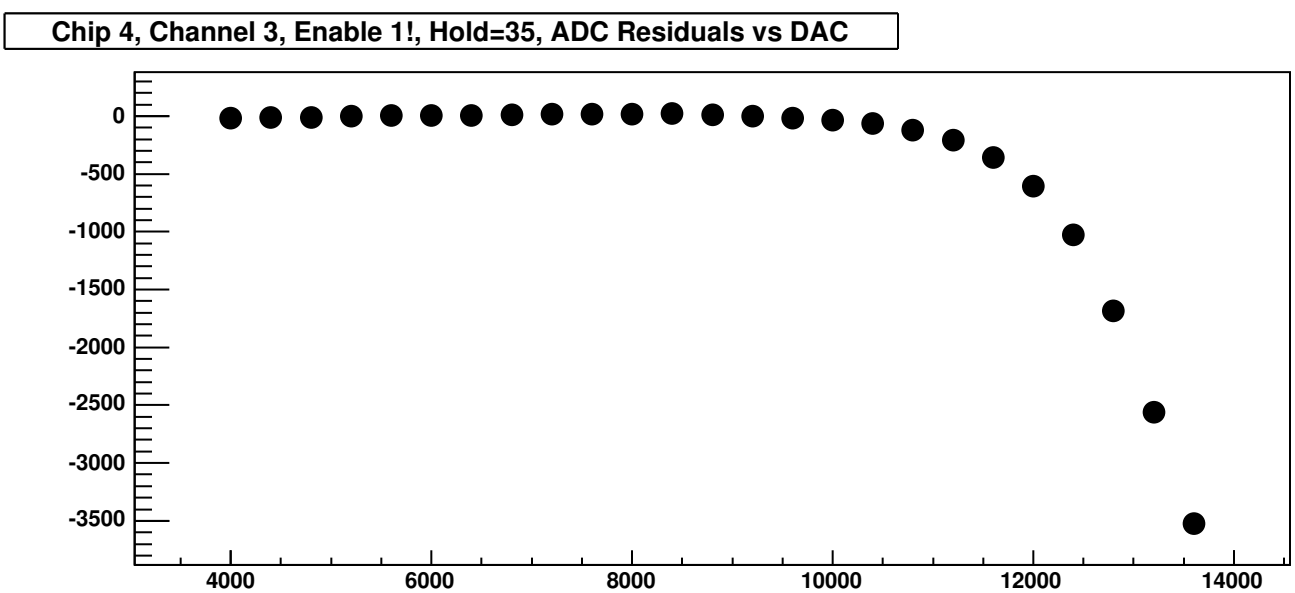

Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 10.63 / 16
p0 292.2 ± 1.198
p1 0.01789 ± 0.0001604
520
500
480
460
440
420
400
380
360
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

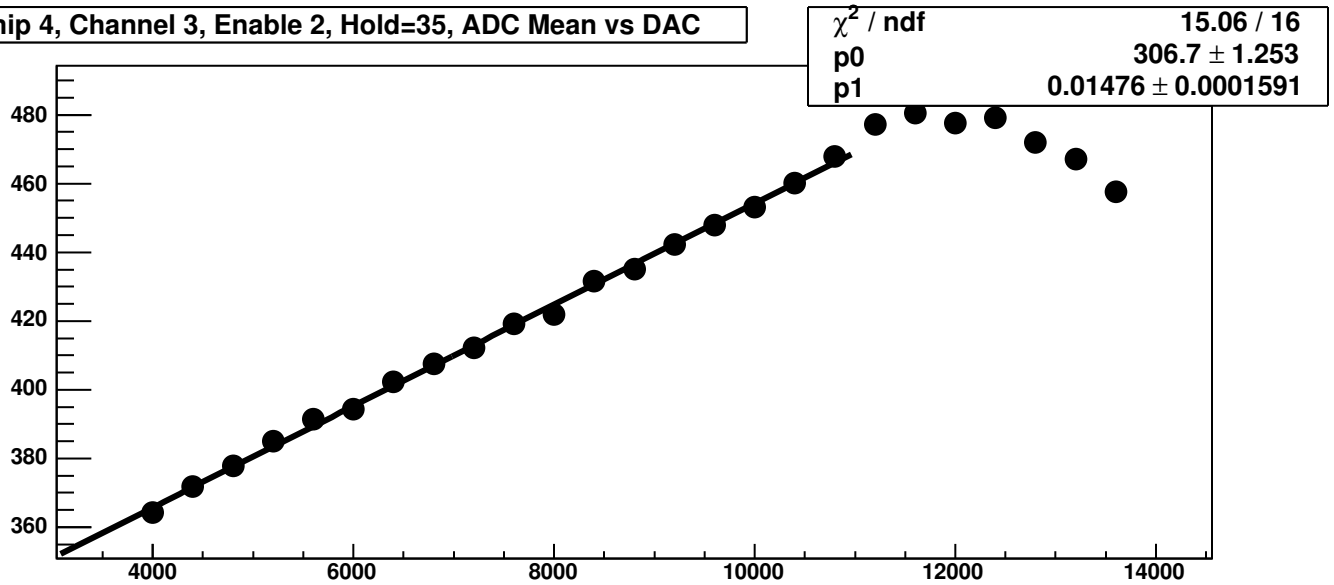

Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1357 / 16
p0 516.8 ± 2.167
p1 2.495 ± 0.0003127
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

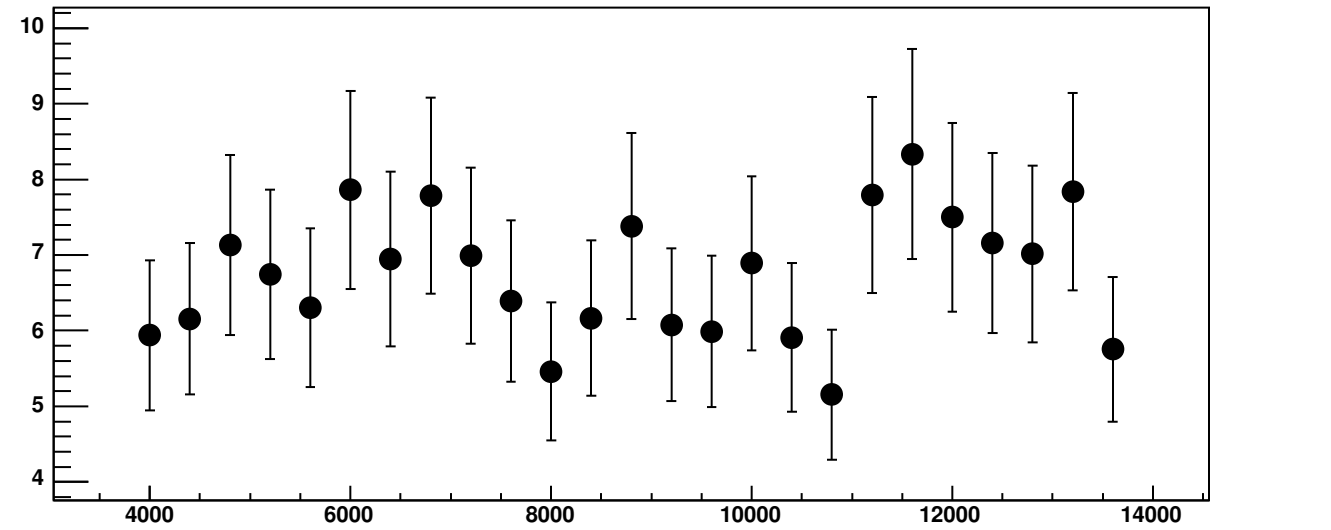

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

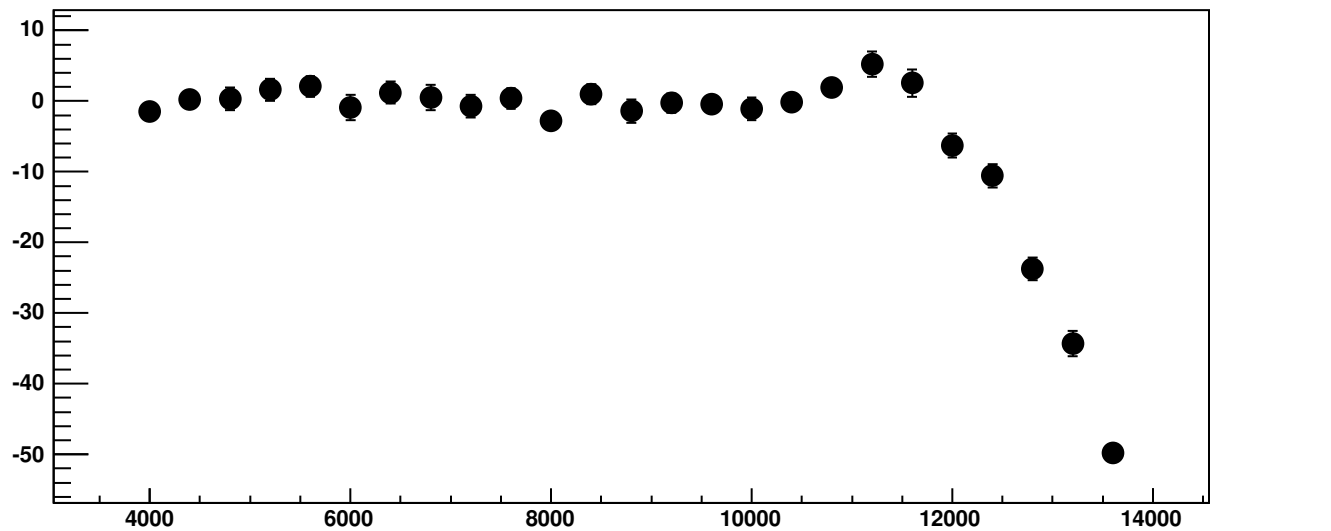
Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



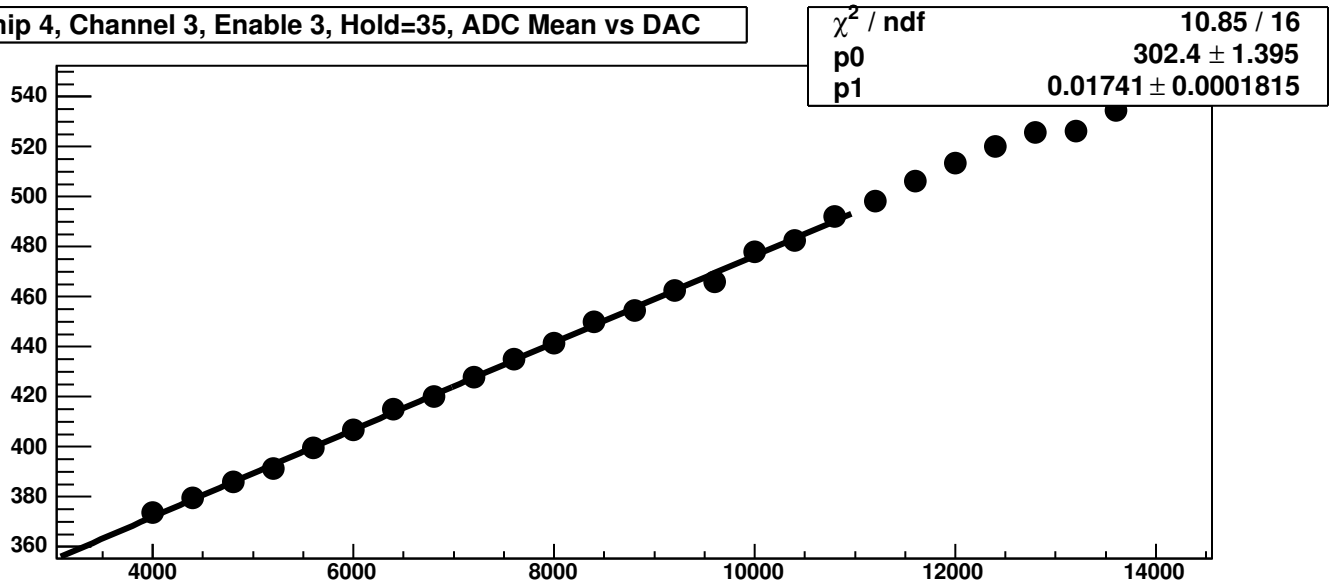
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



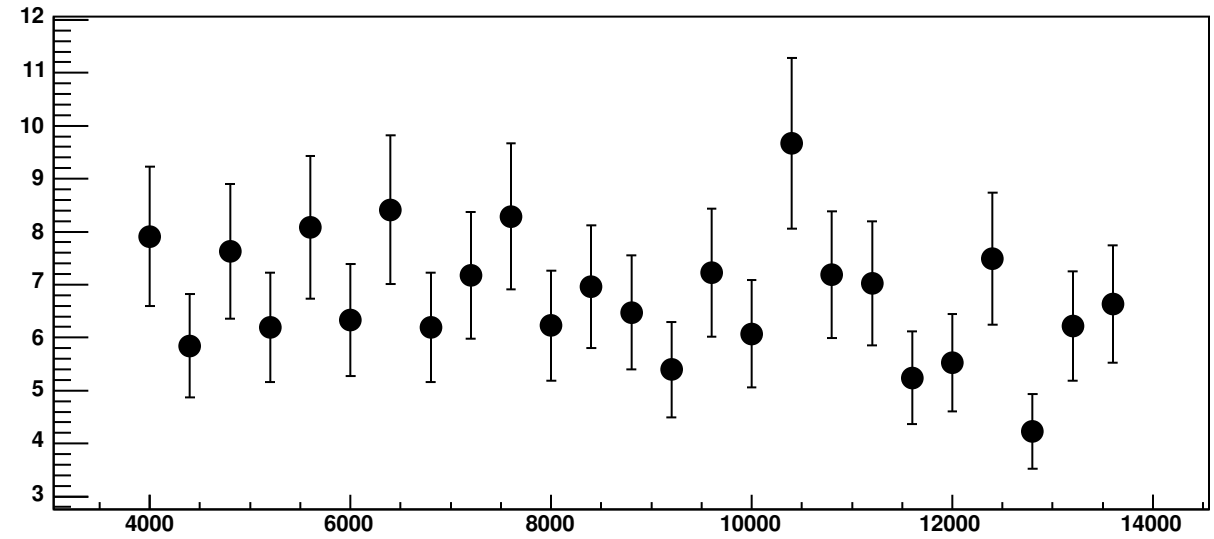
Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



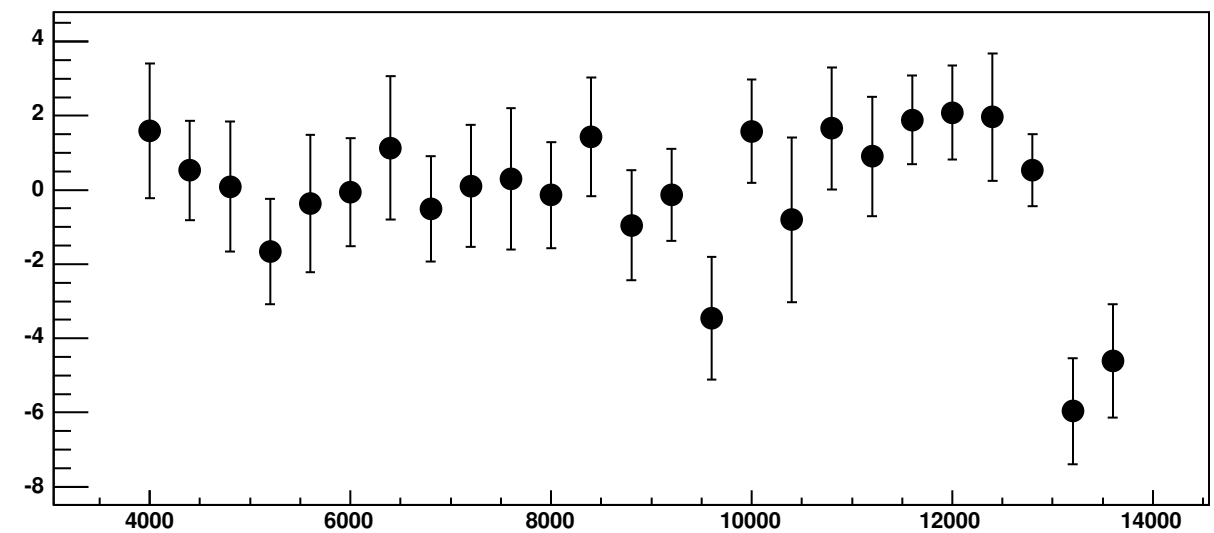
Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



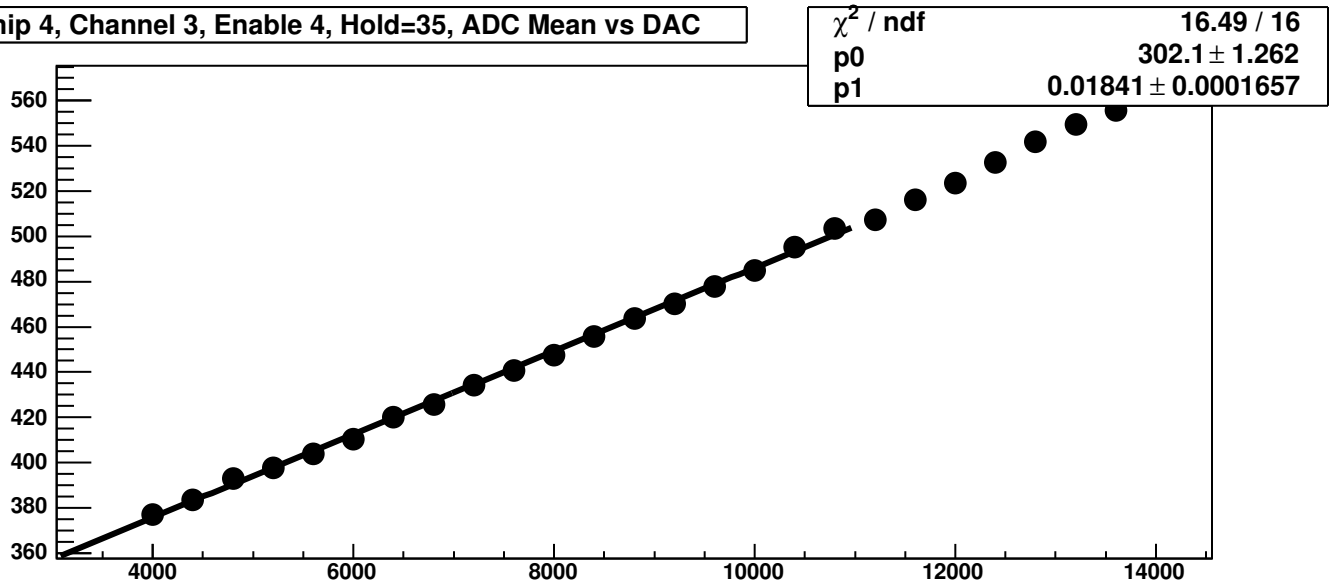
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



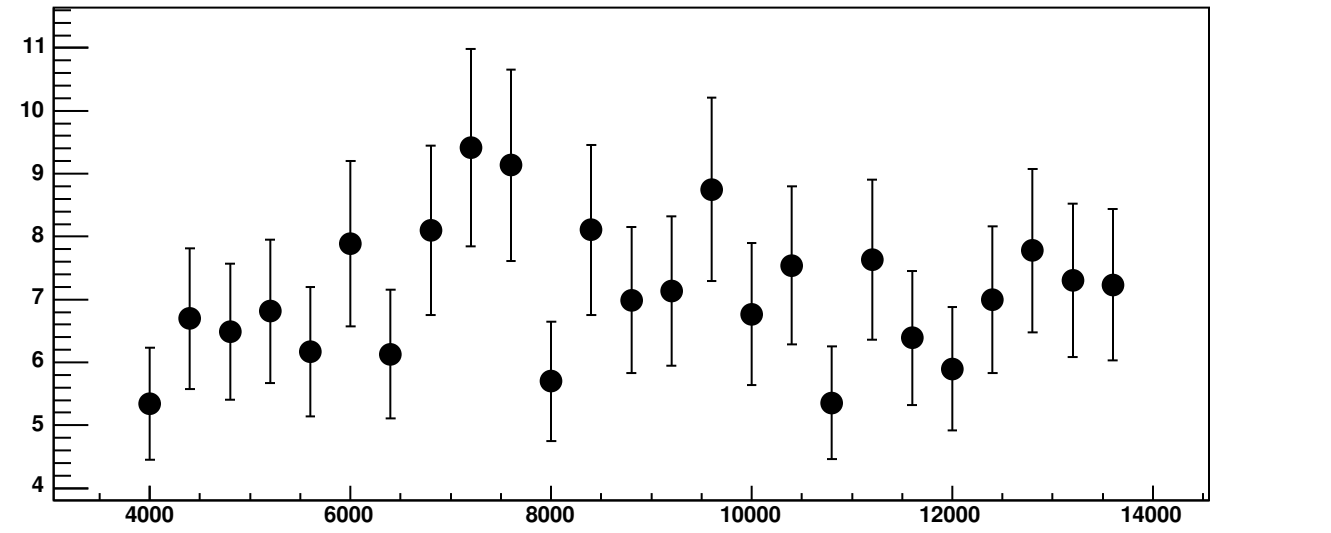
Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



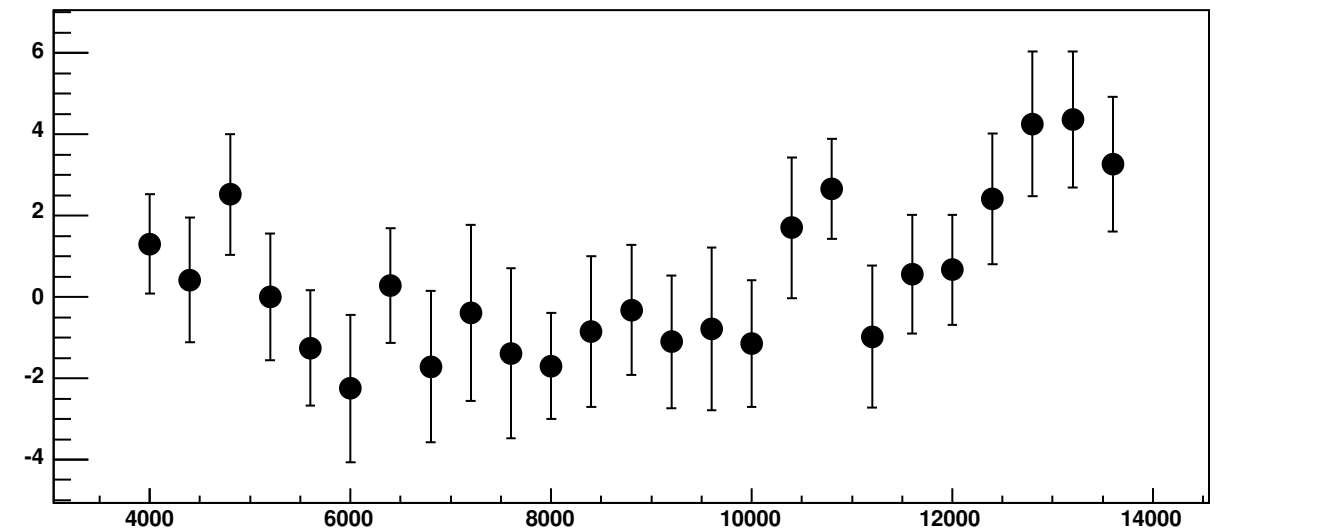
Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

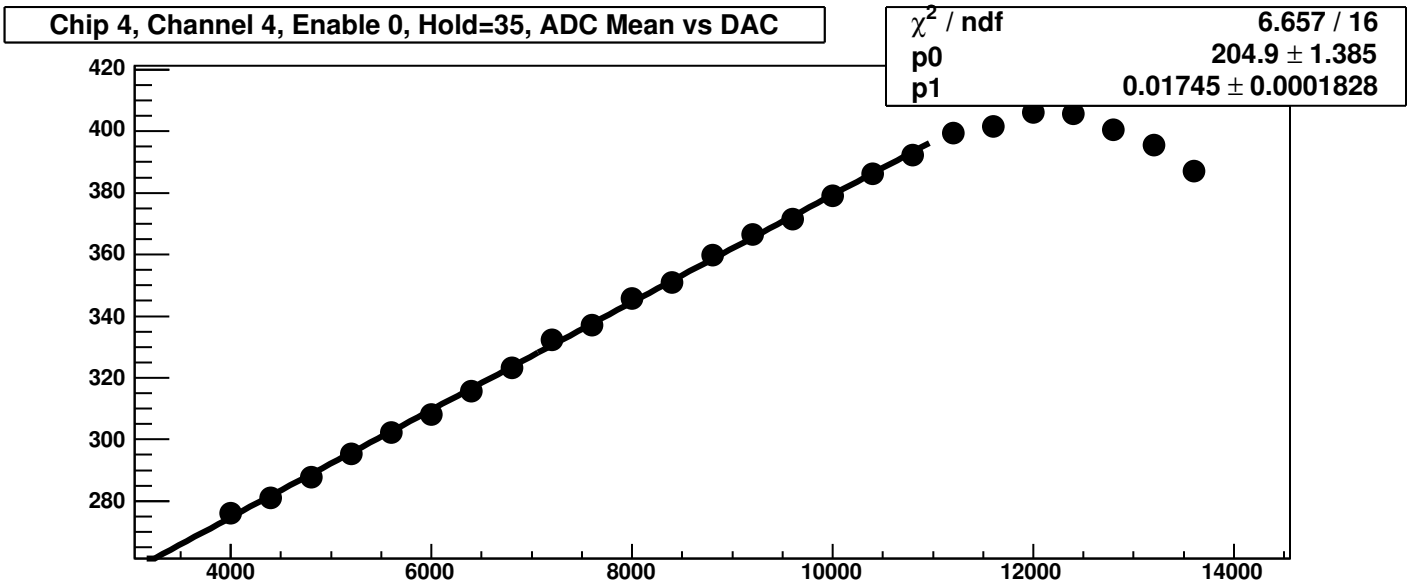


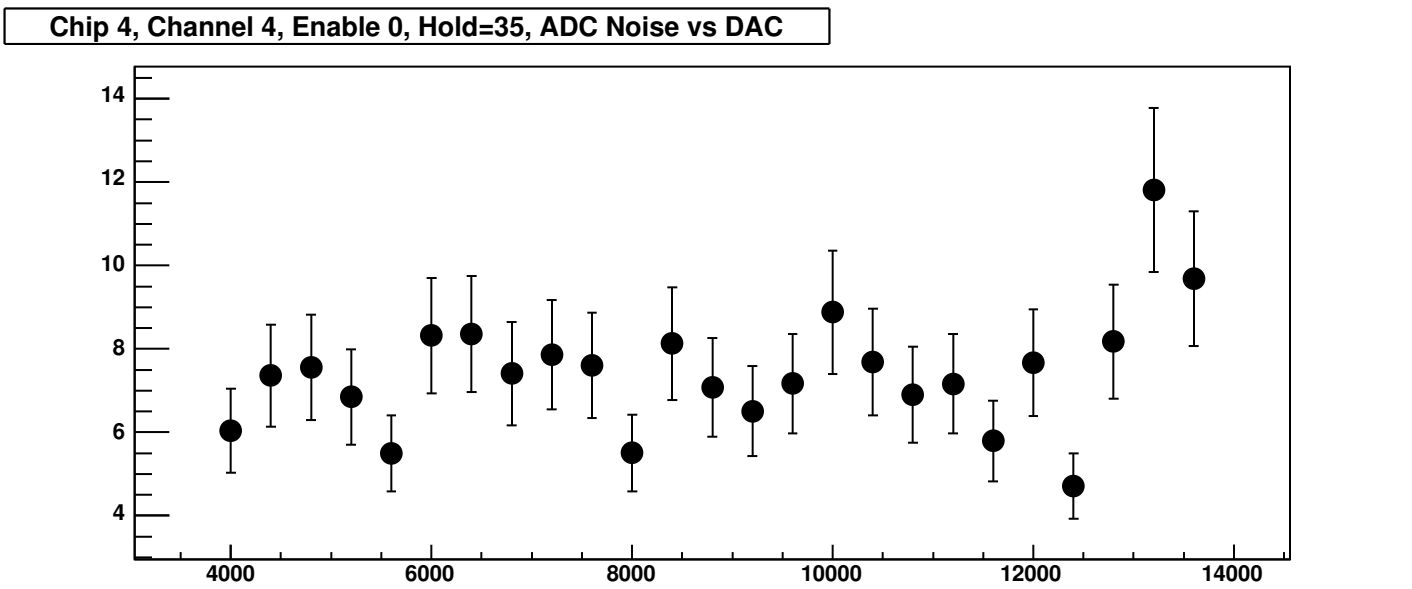
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

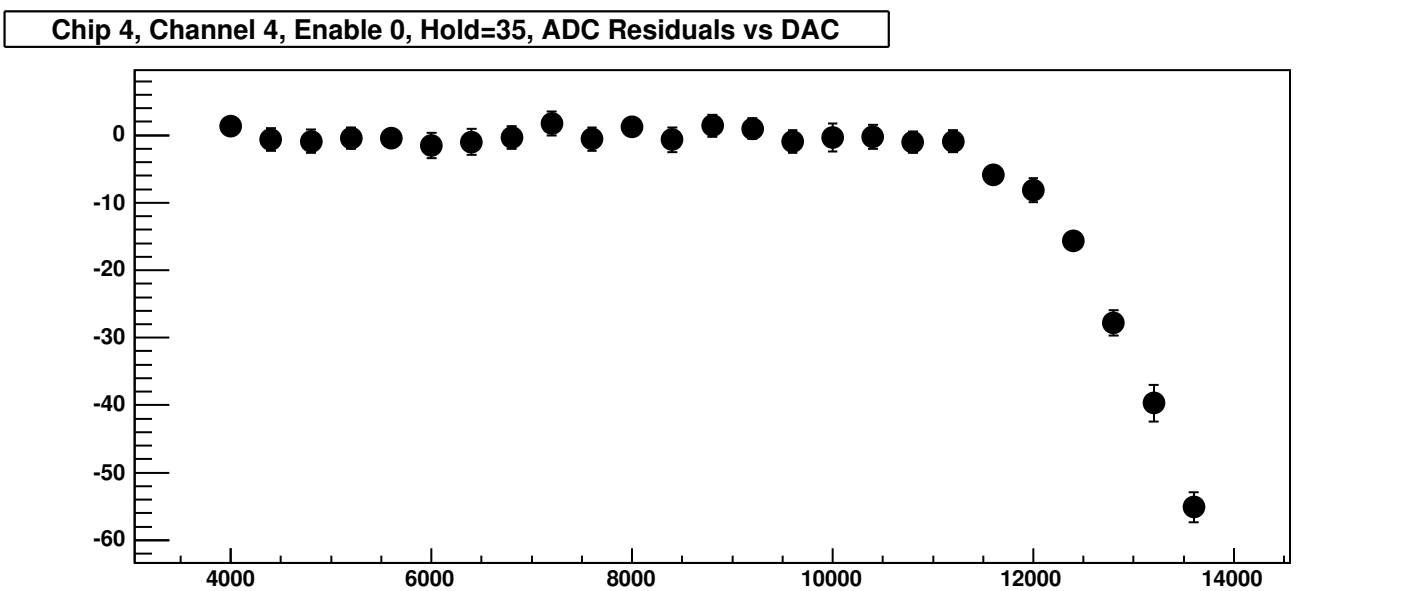


Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

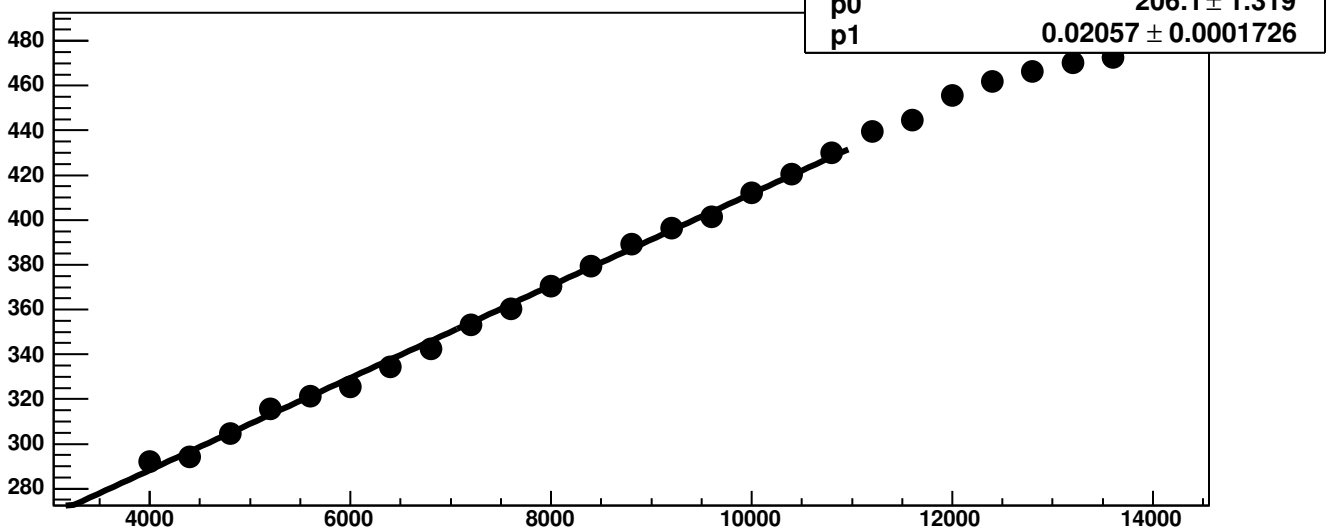



Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.657 / 16
p0 204.9 ± 1.385
p1 0.01745 ± 0.0001828
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

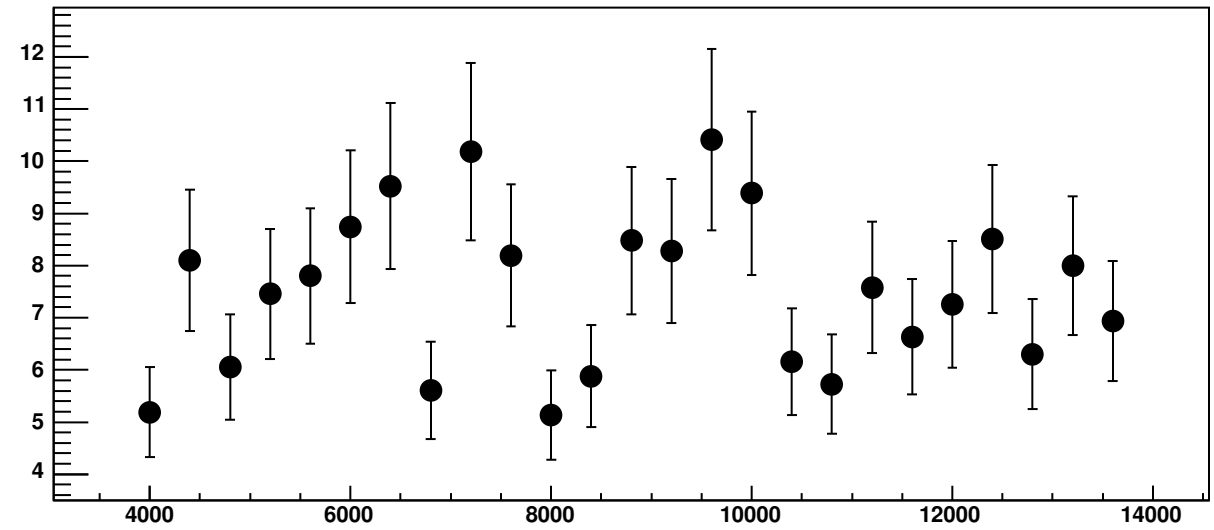

Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
-50
-60
4000
6000
8000
10000
12000
14000

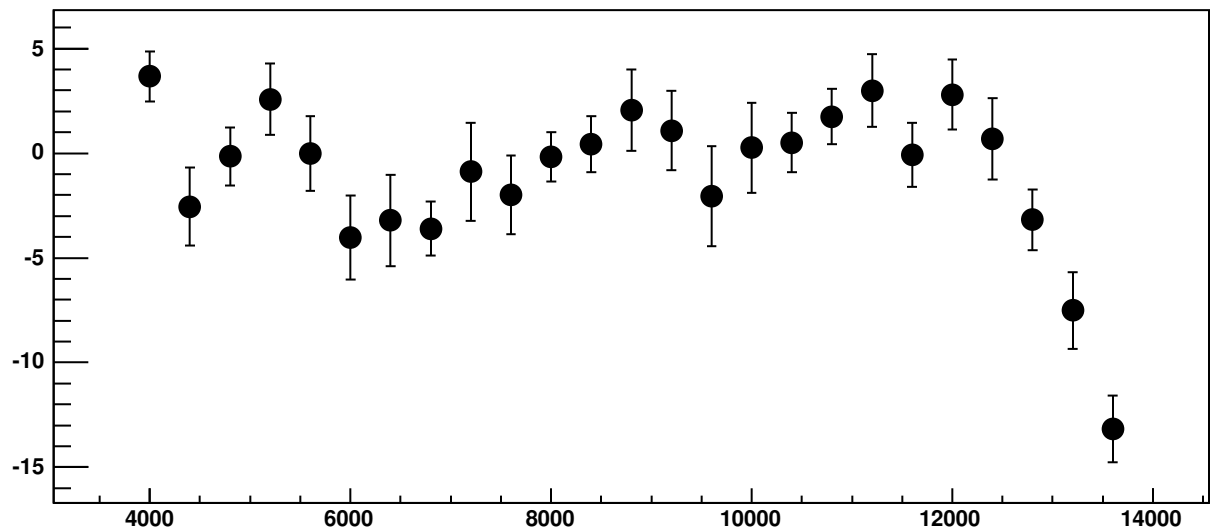
Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



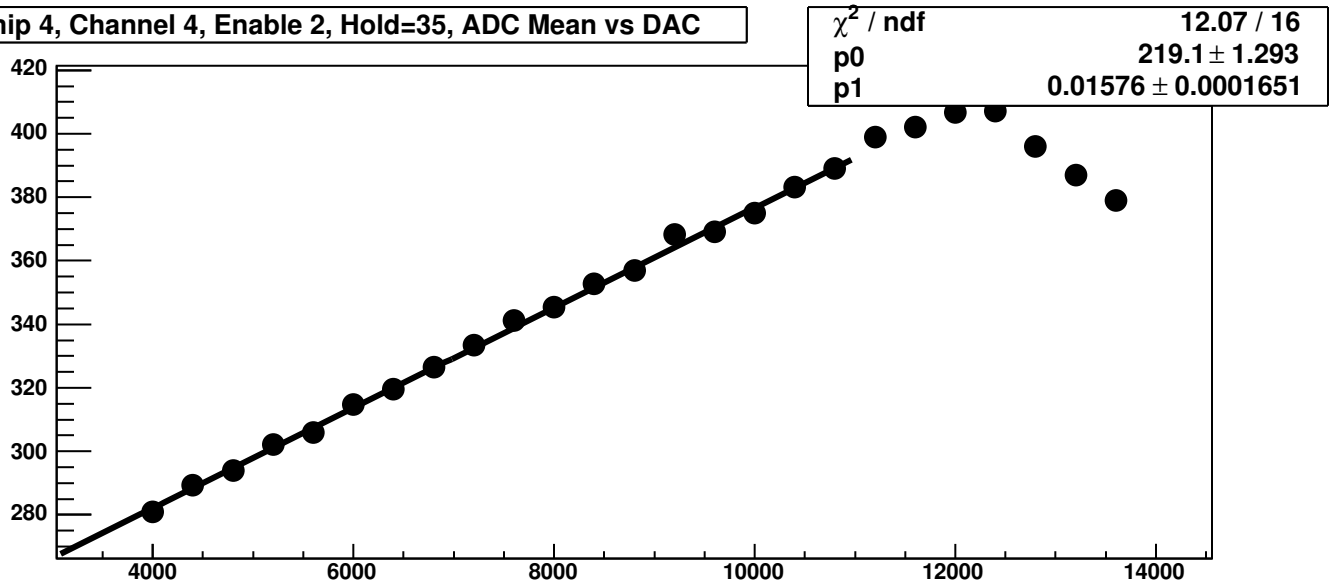
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



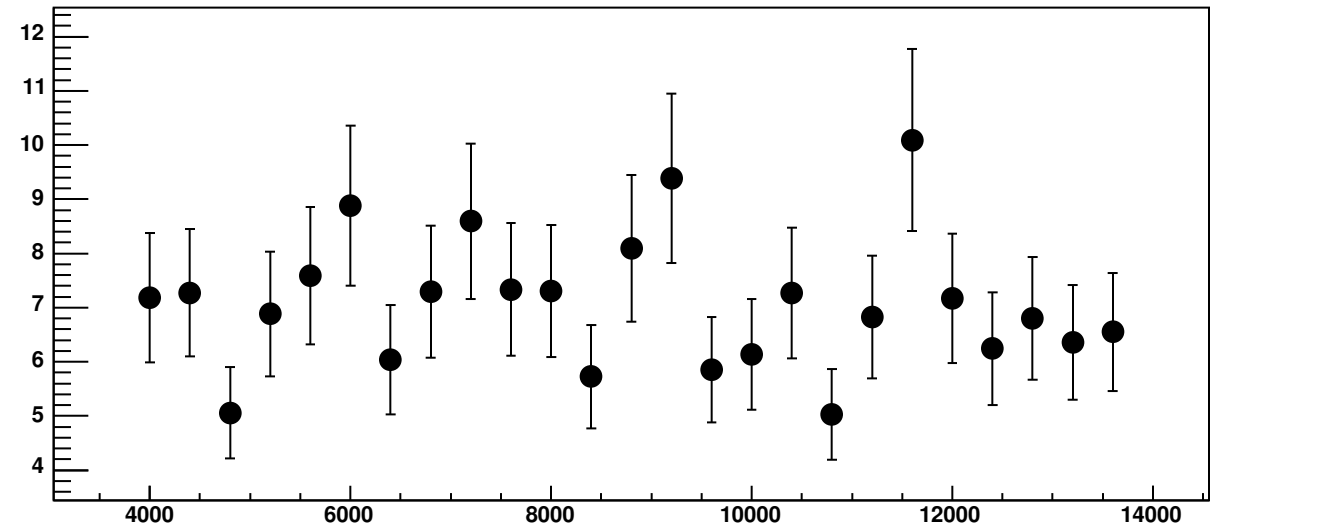
Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



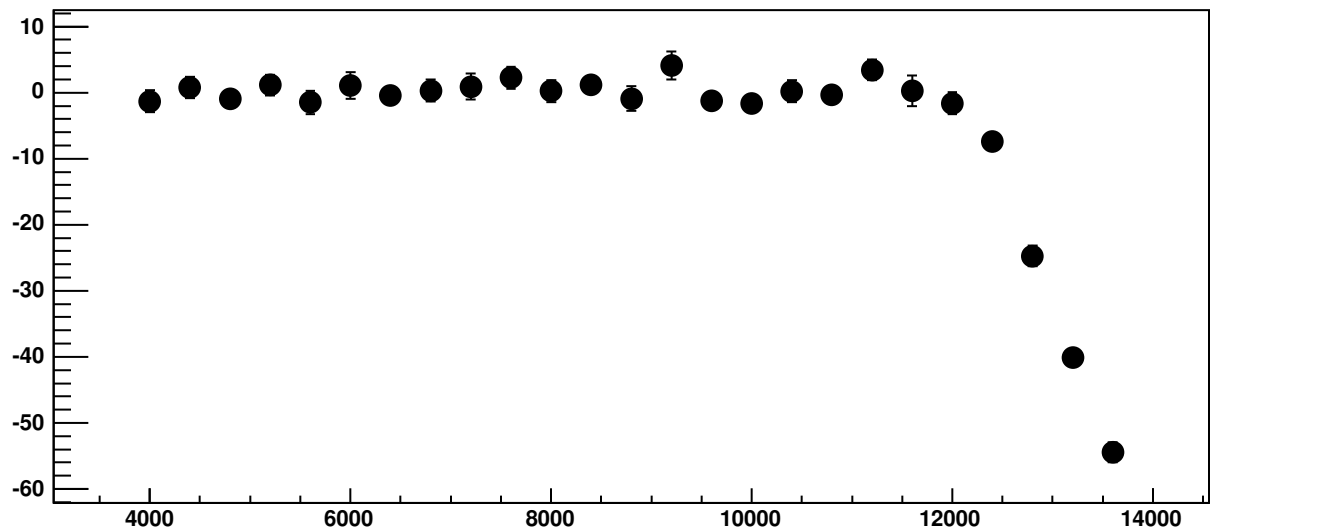
Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



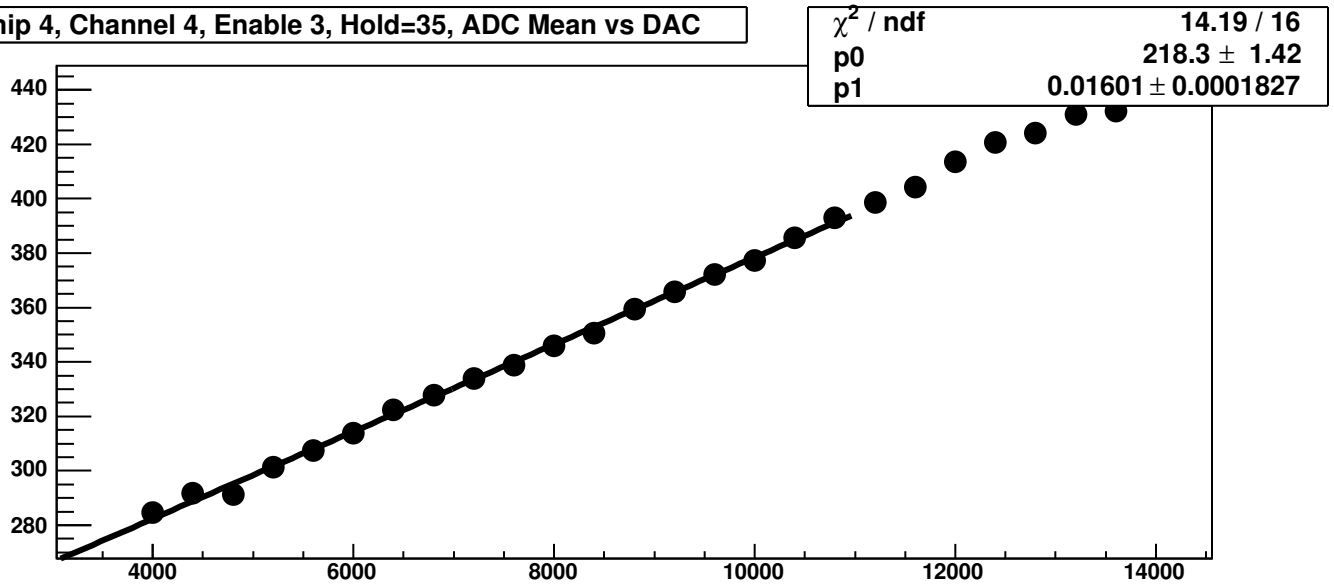
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



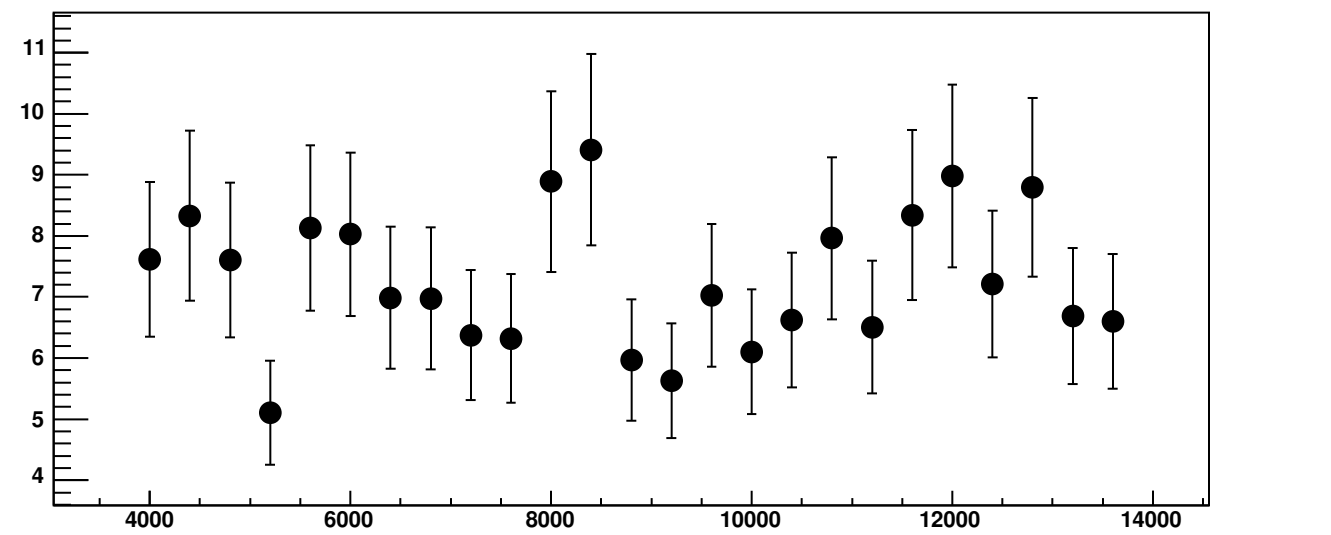
Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



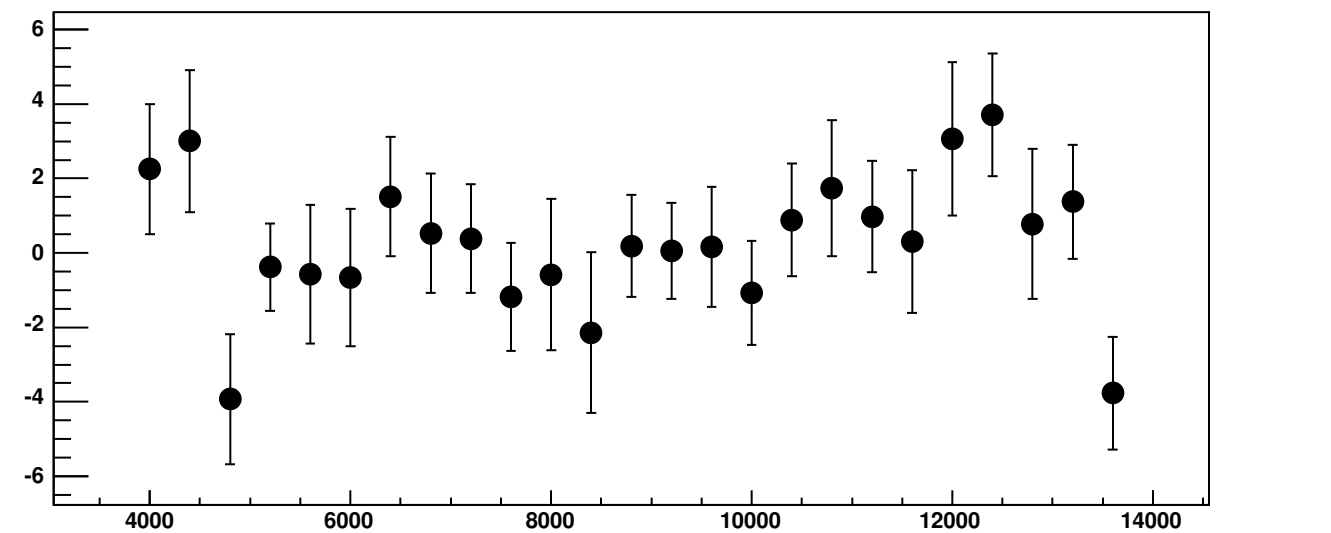
Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

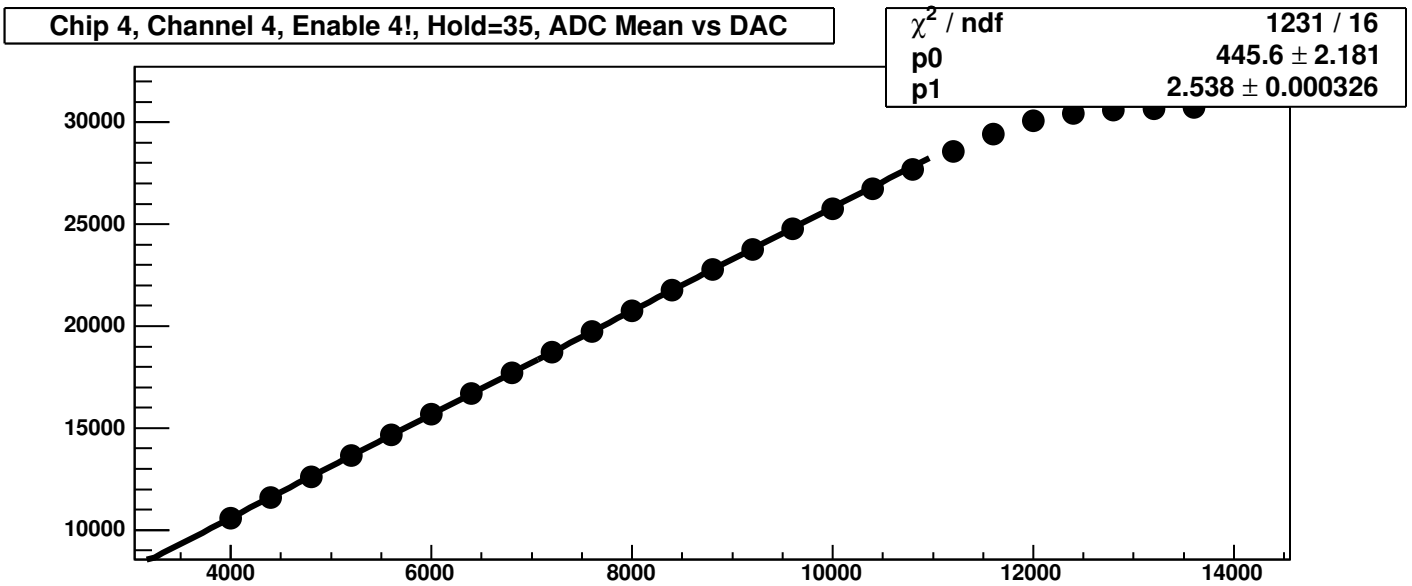


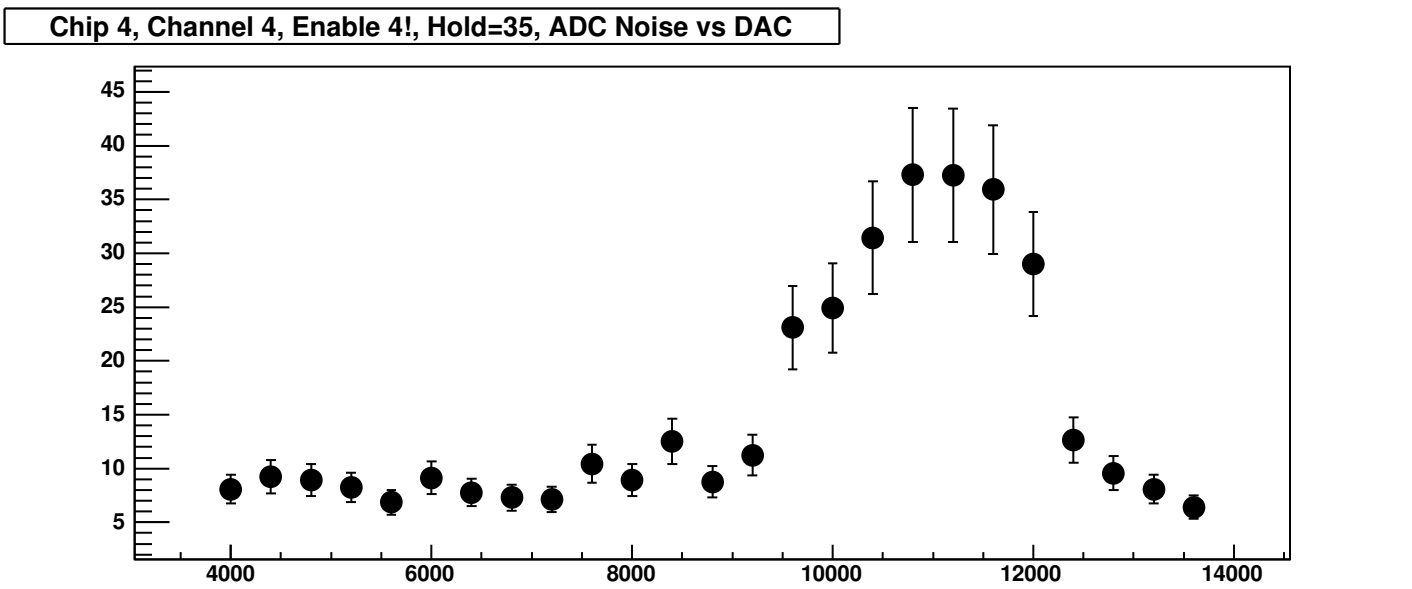
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

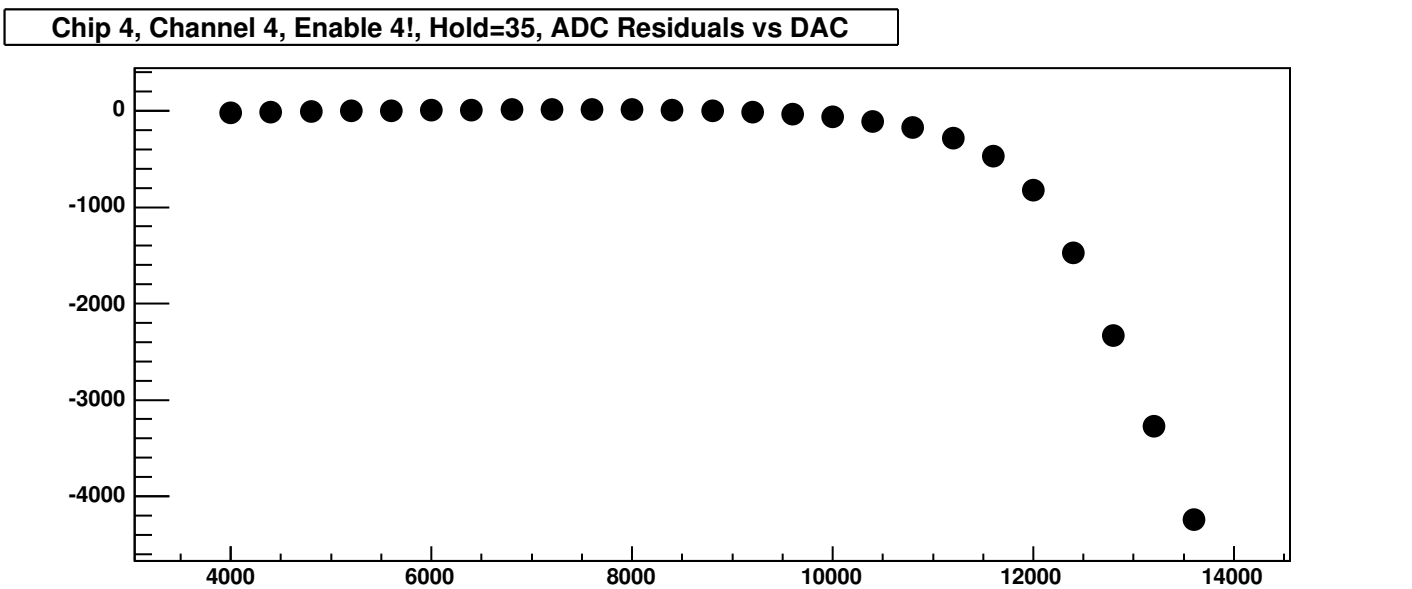


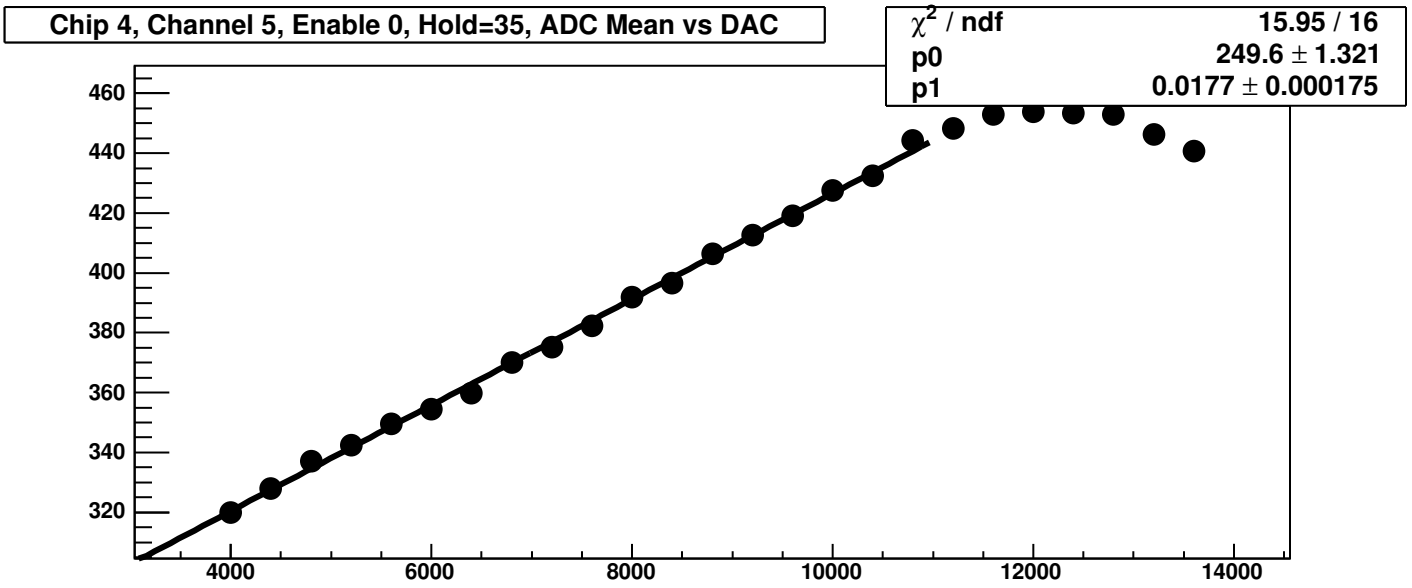
Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

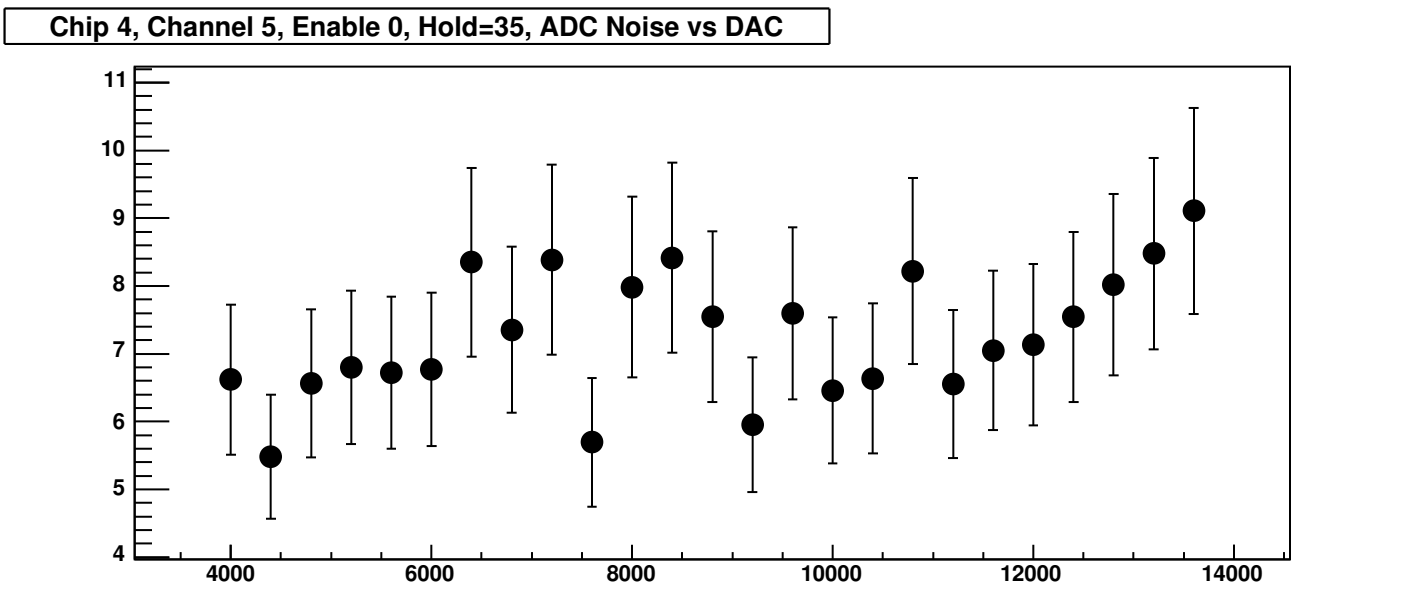


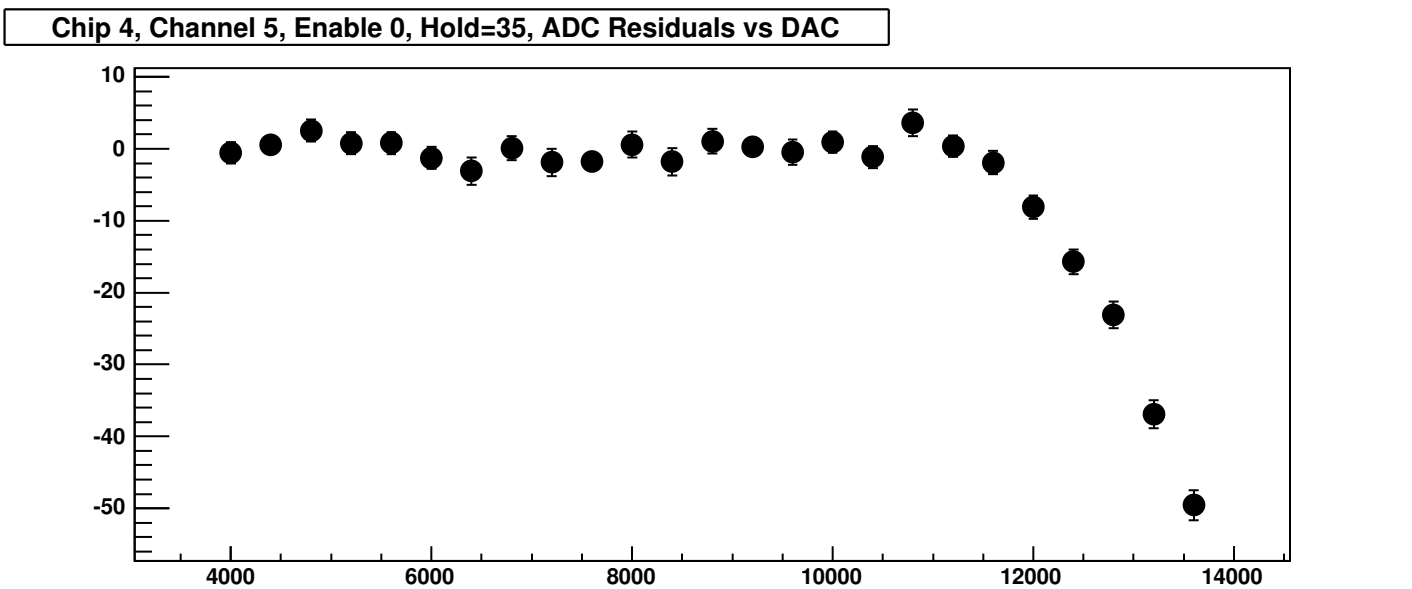

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1231 / 16
p0 445.6 ± 2.181
p1 2.538 ± 0.000326
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

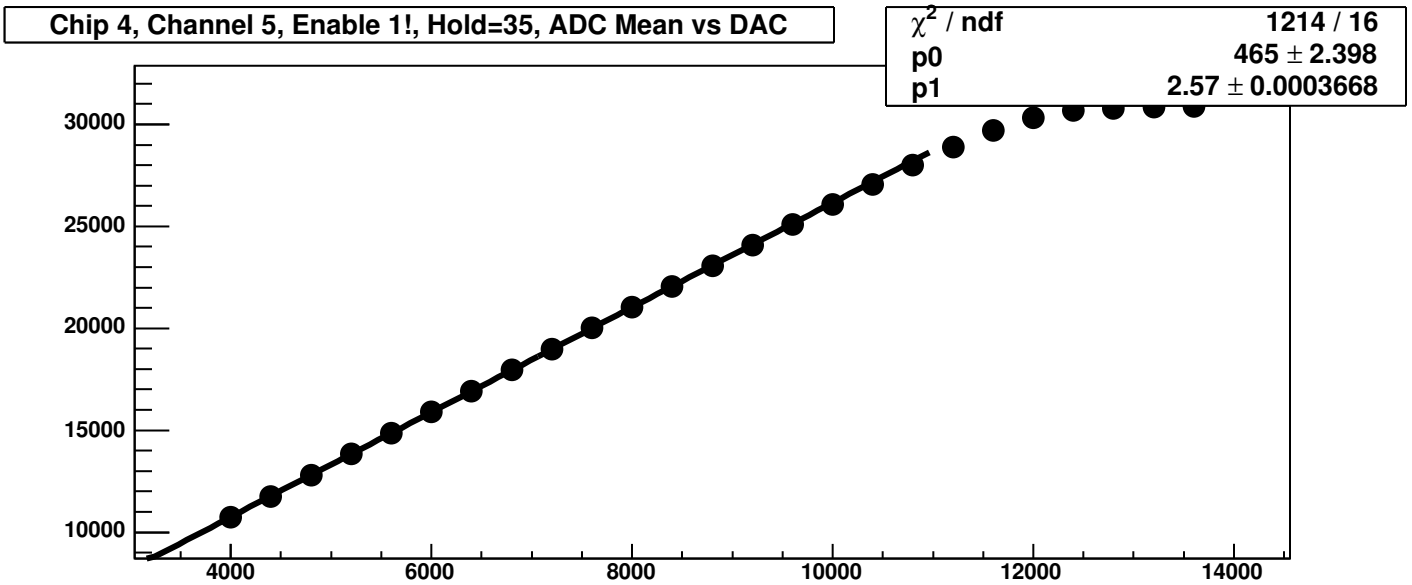

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

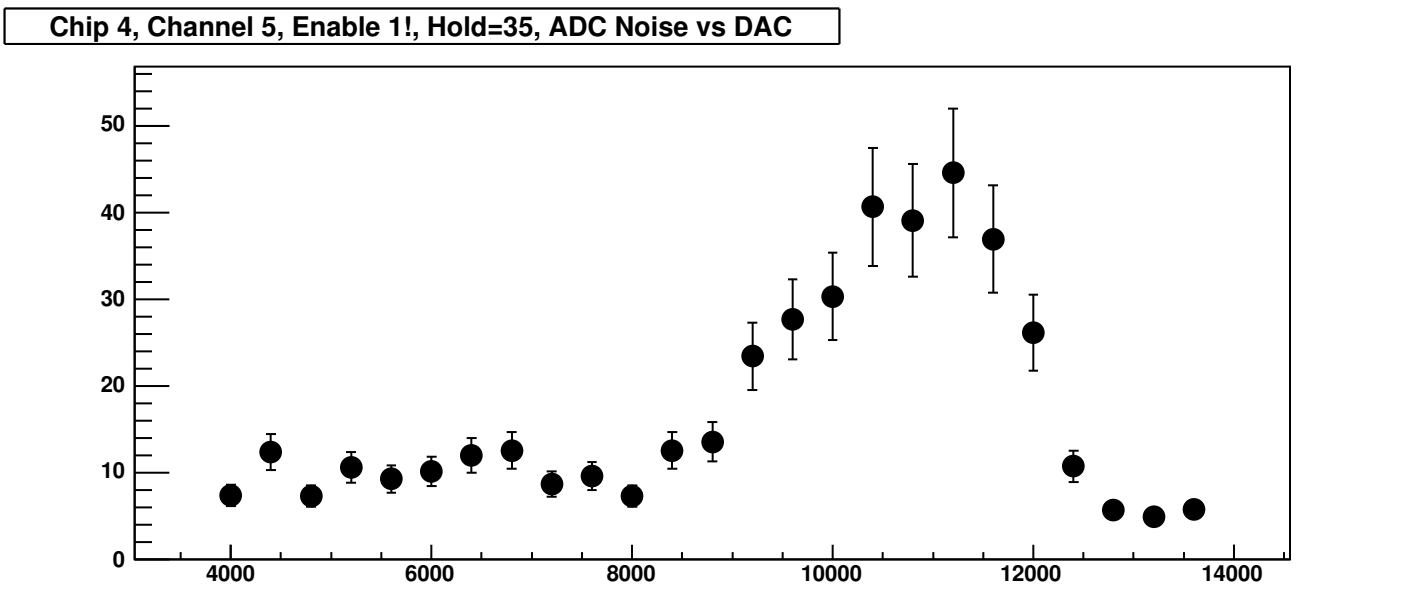

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

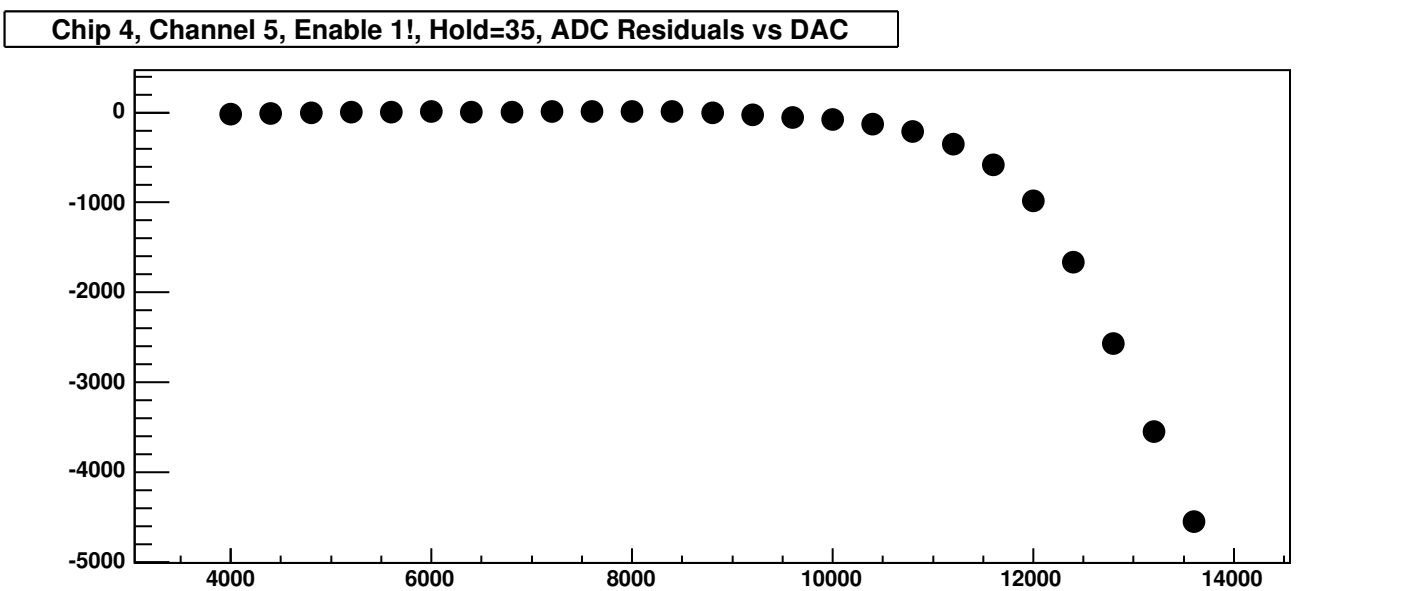

Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 15.95 / 16
p0 249.6 ± 1.321
p1 0.0177 ± 0.000175
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

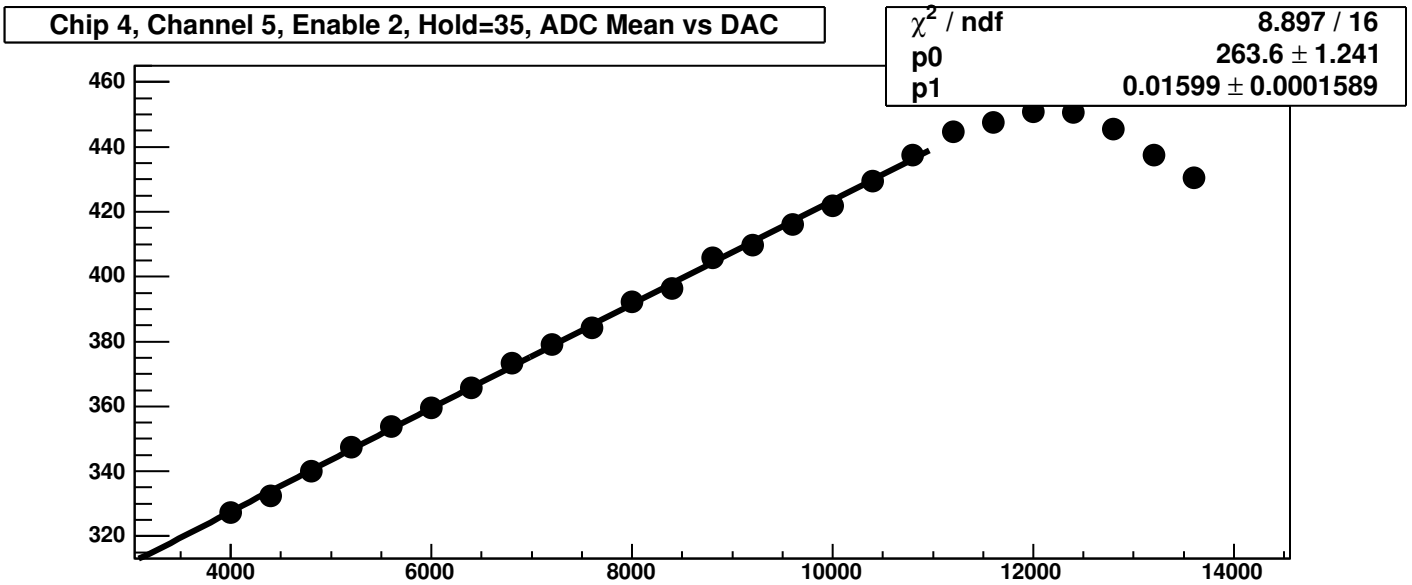

Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

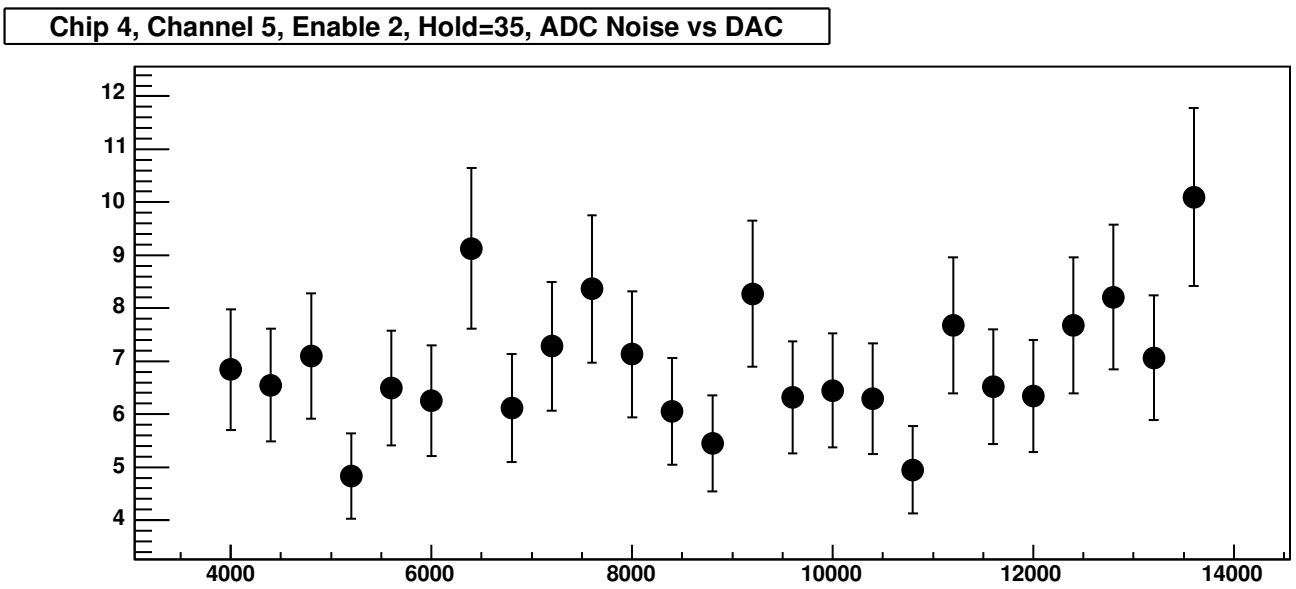

Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

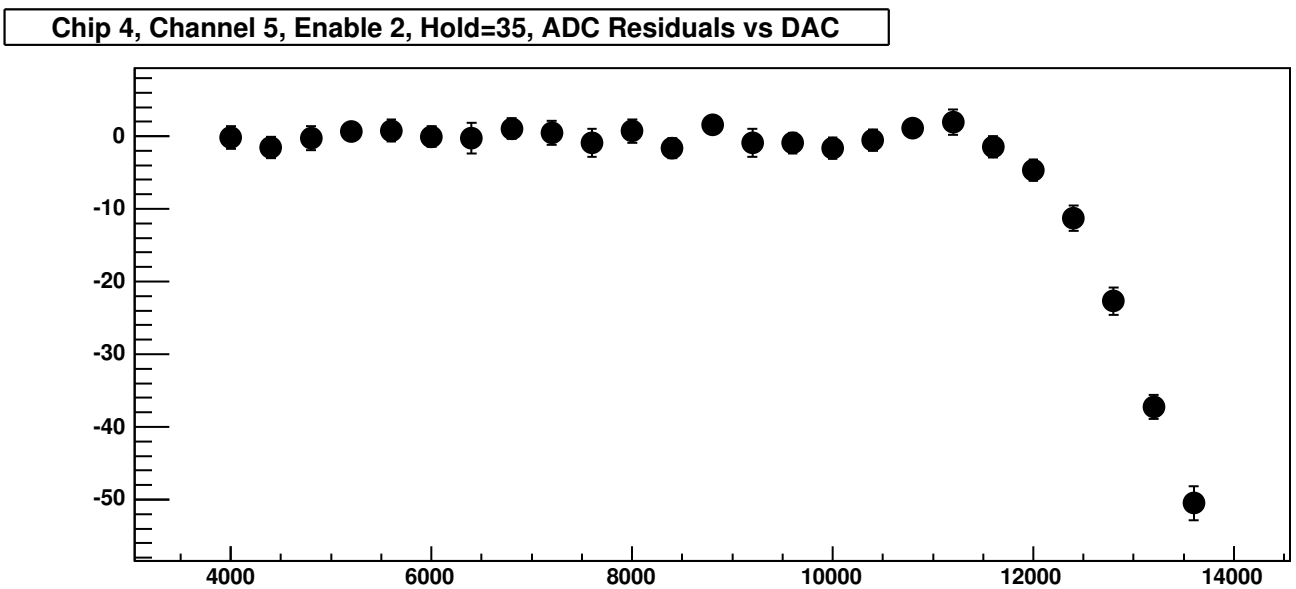

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1214 / 16
p0 465 ± 2.398
p1 2.57 ± 0.0003668
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

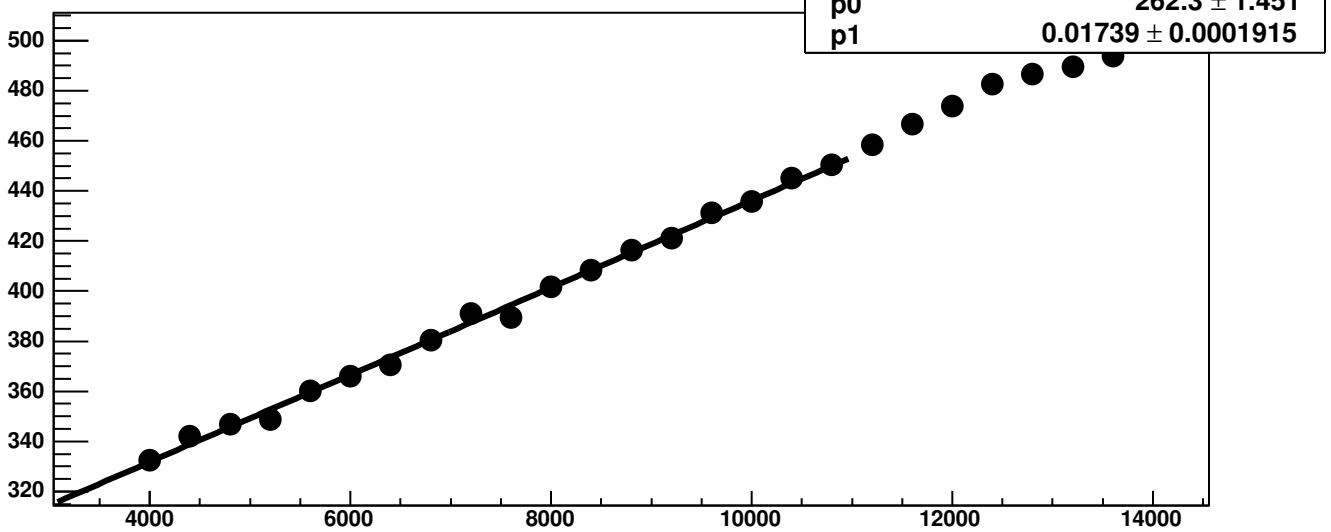

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 8.897 / 16
p0 263.6 ± 1.241
p1 0.01599 ± 0.0001589
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

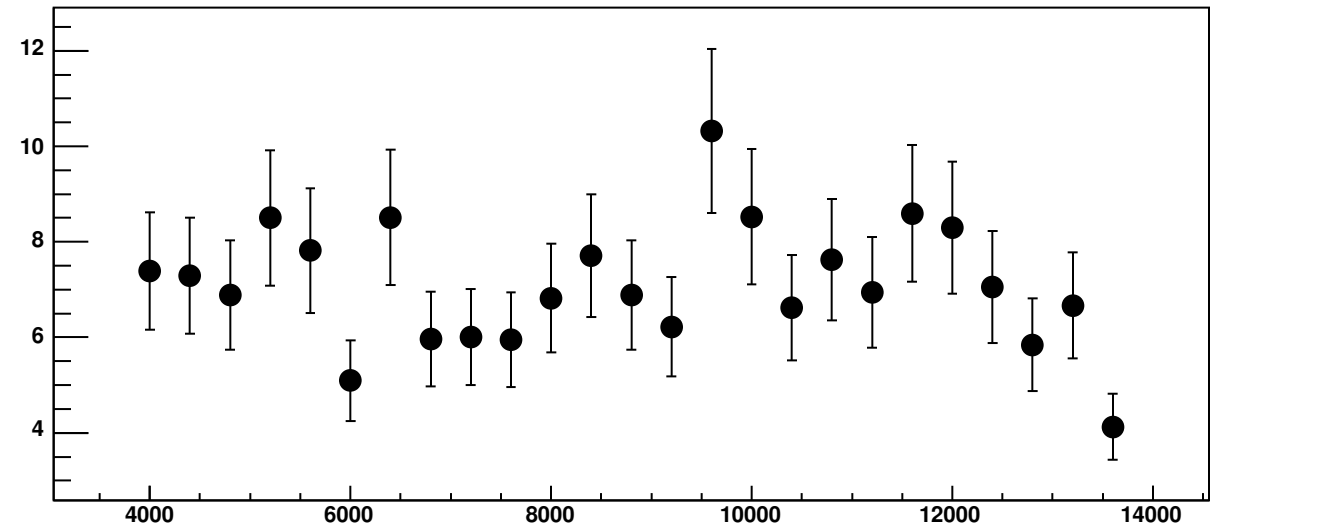

Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

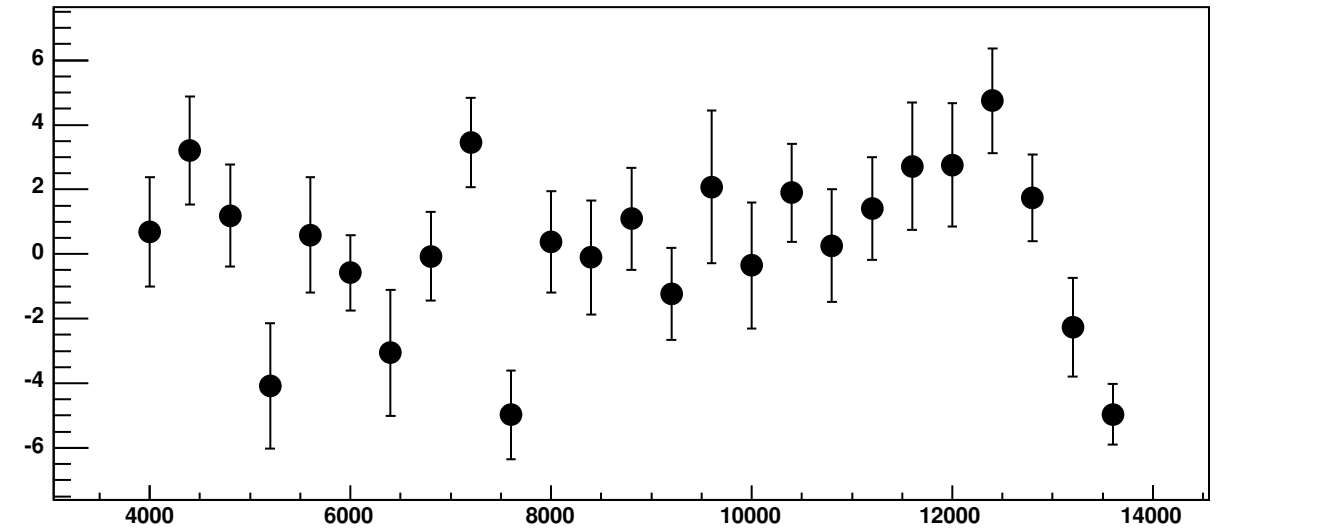
Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



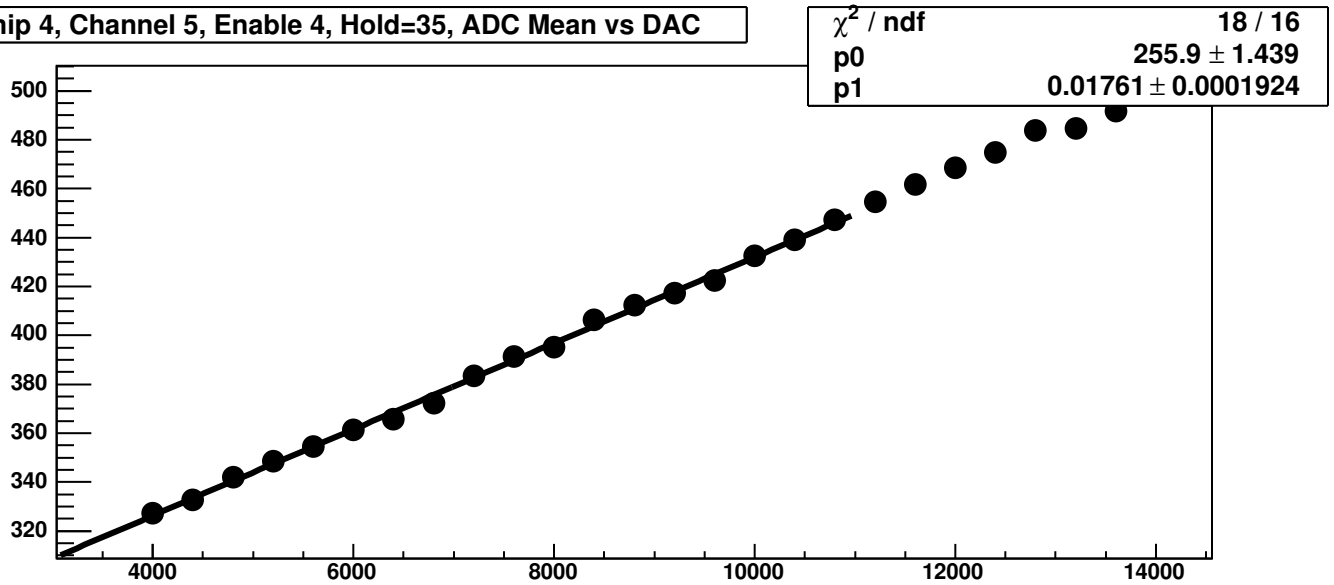
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



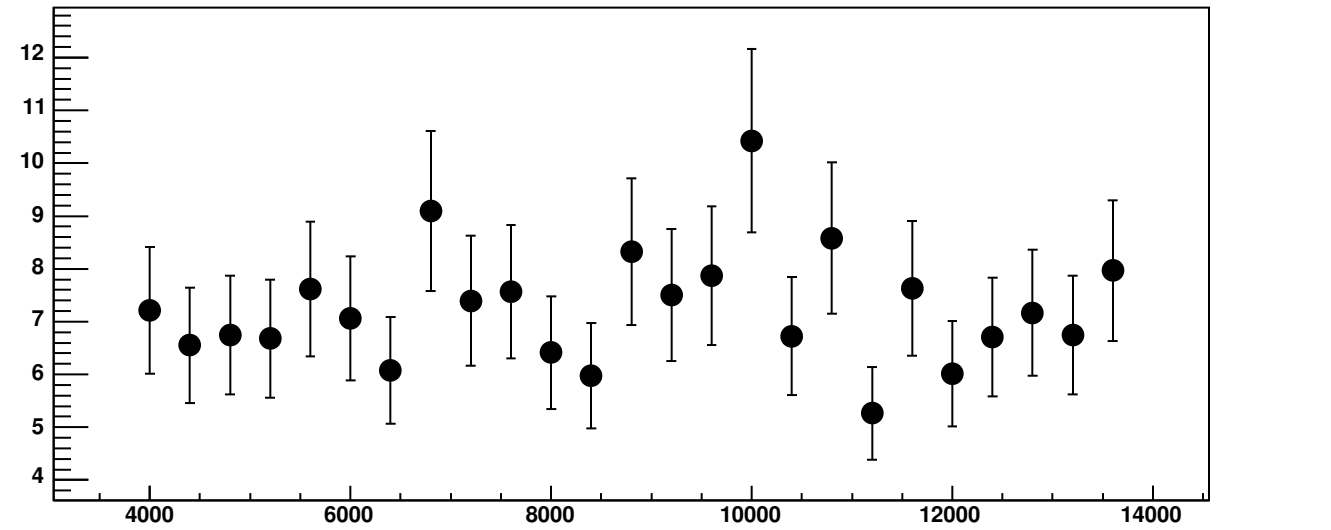
Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



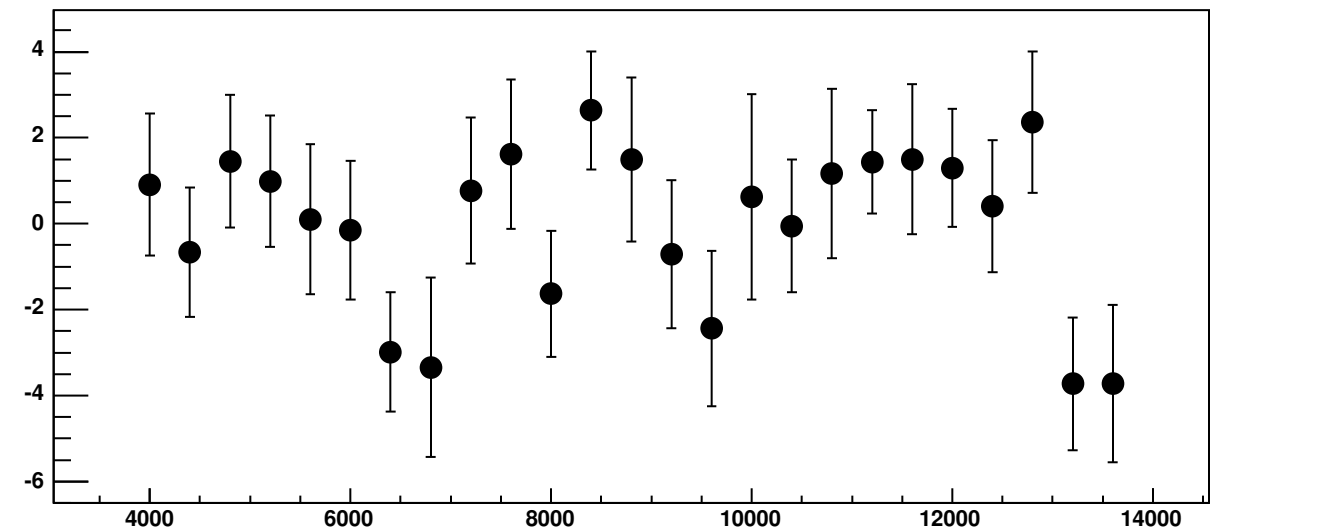
Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



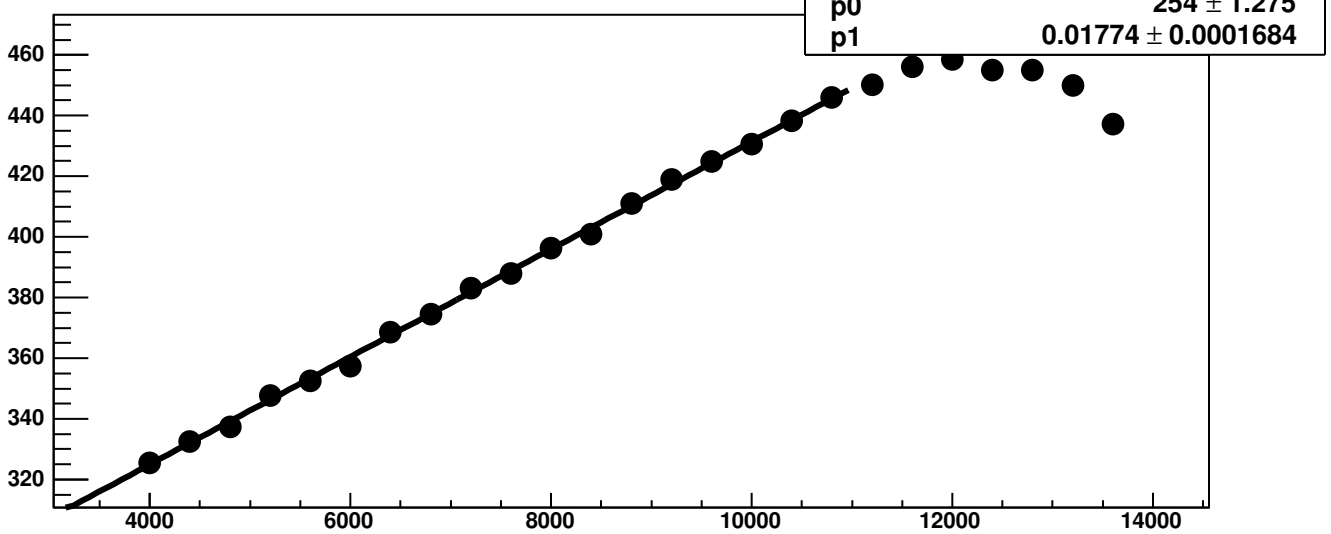
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



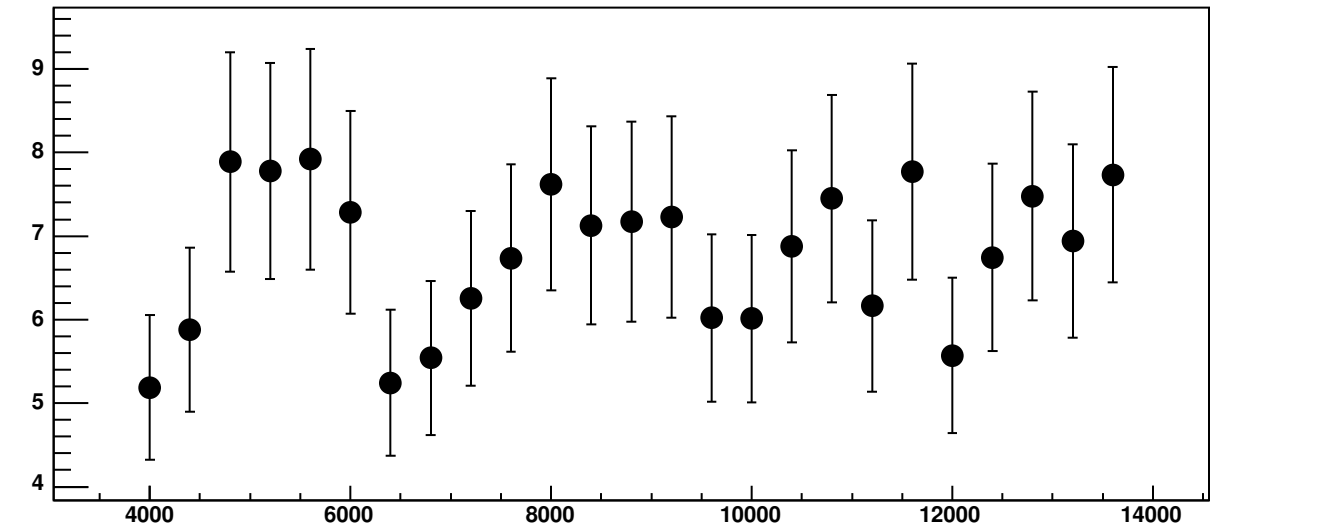
Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



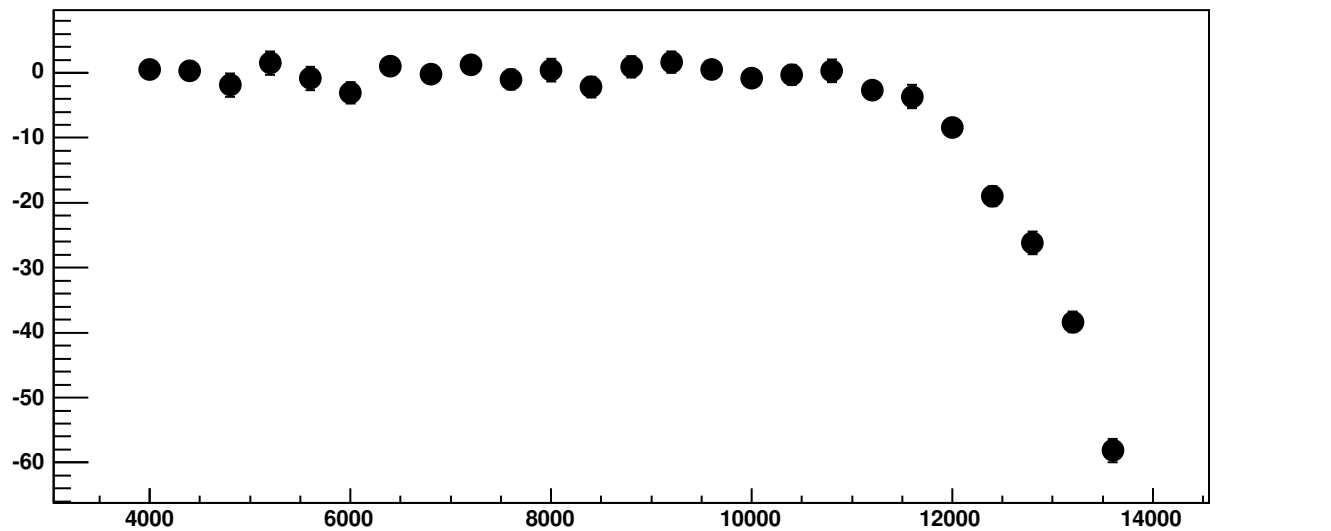
Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

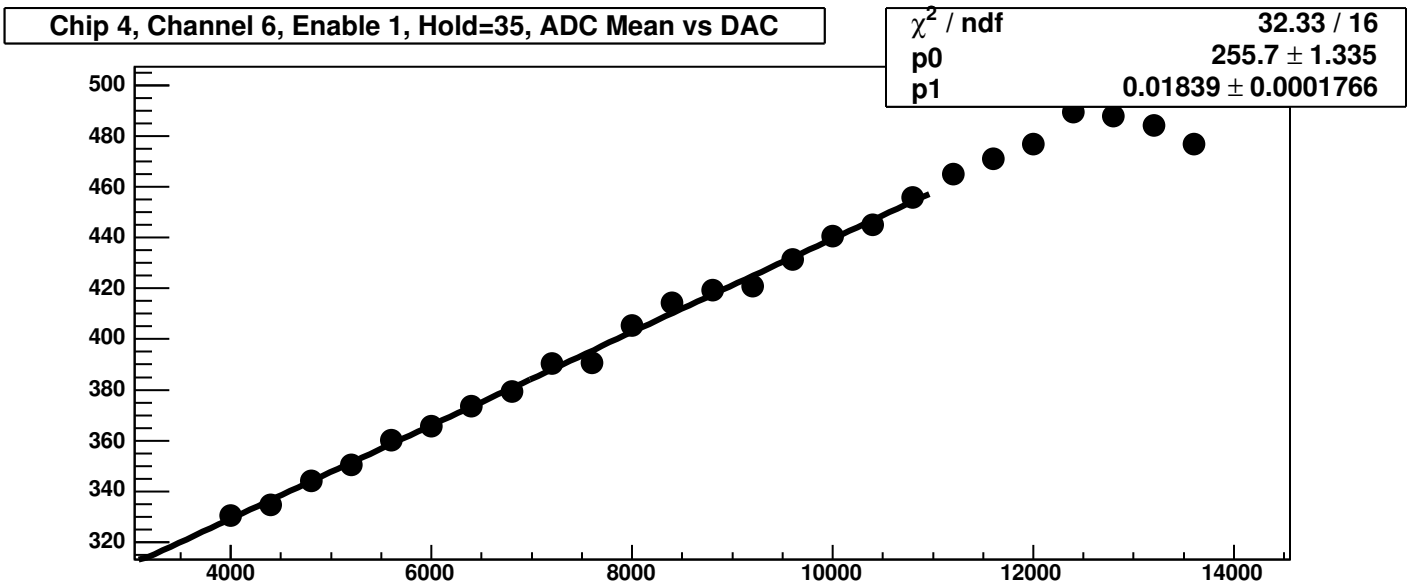


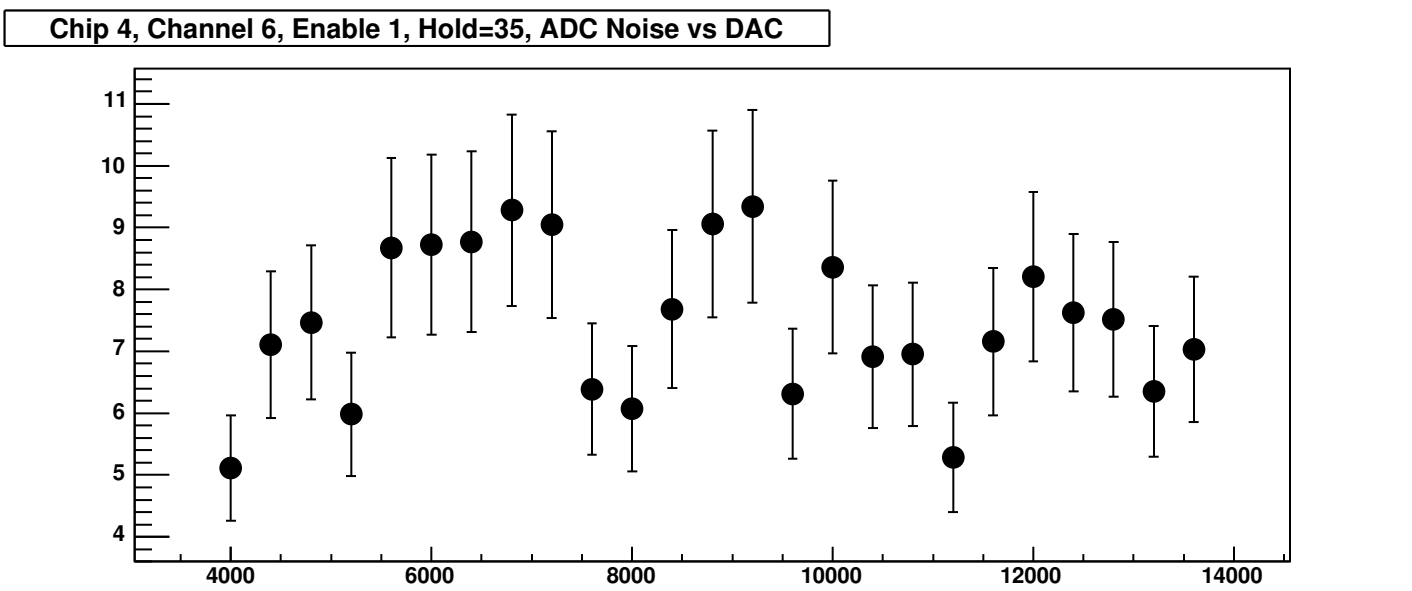
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

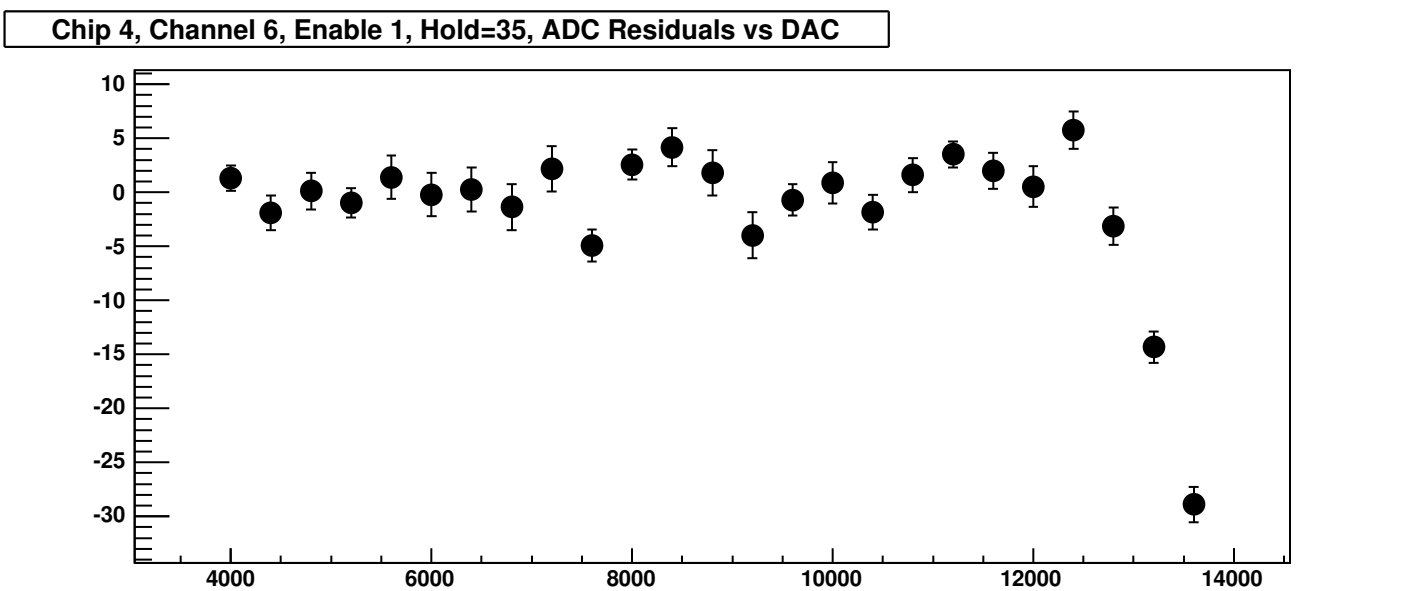


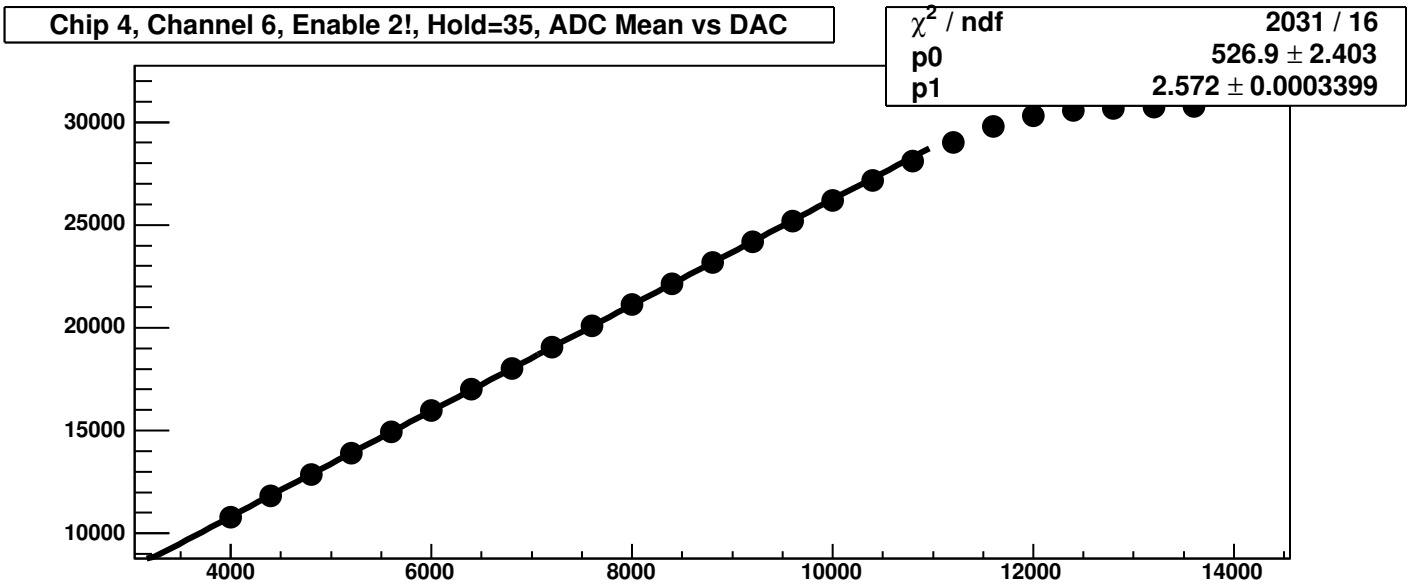
Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

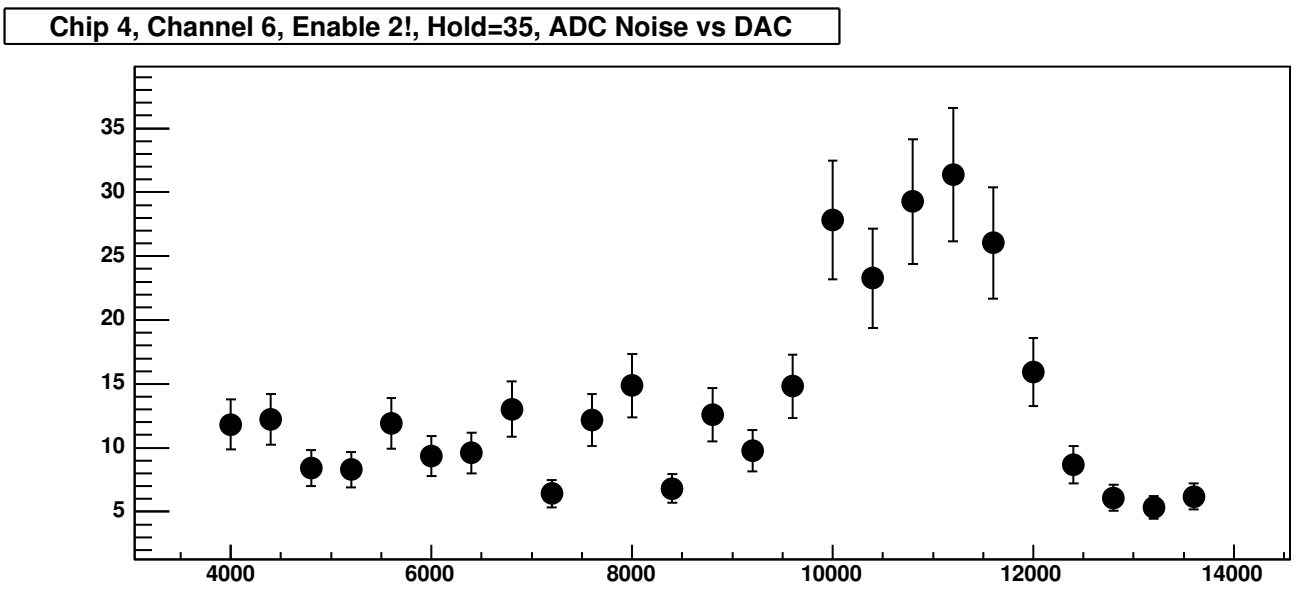


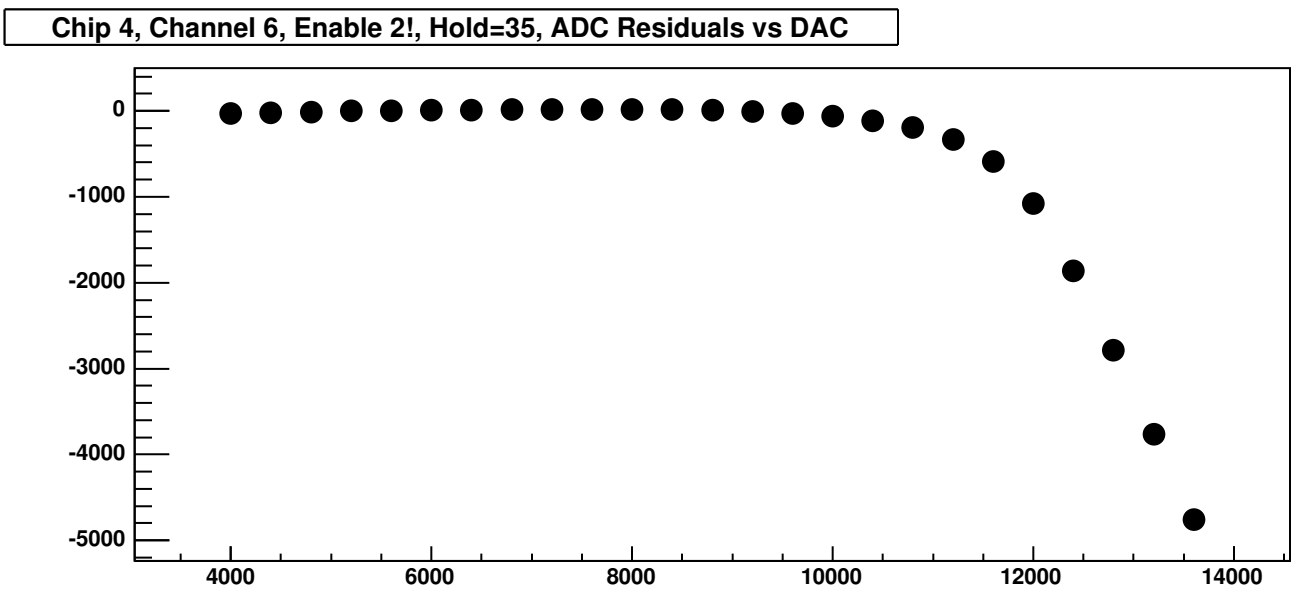

Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.33 / 16
p0 255.7 ± 1.335
p1 0.01839 ± 0.0001766
500
480
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

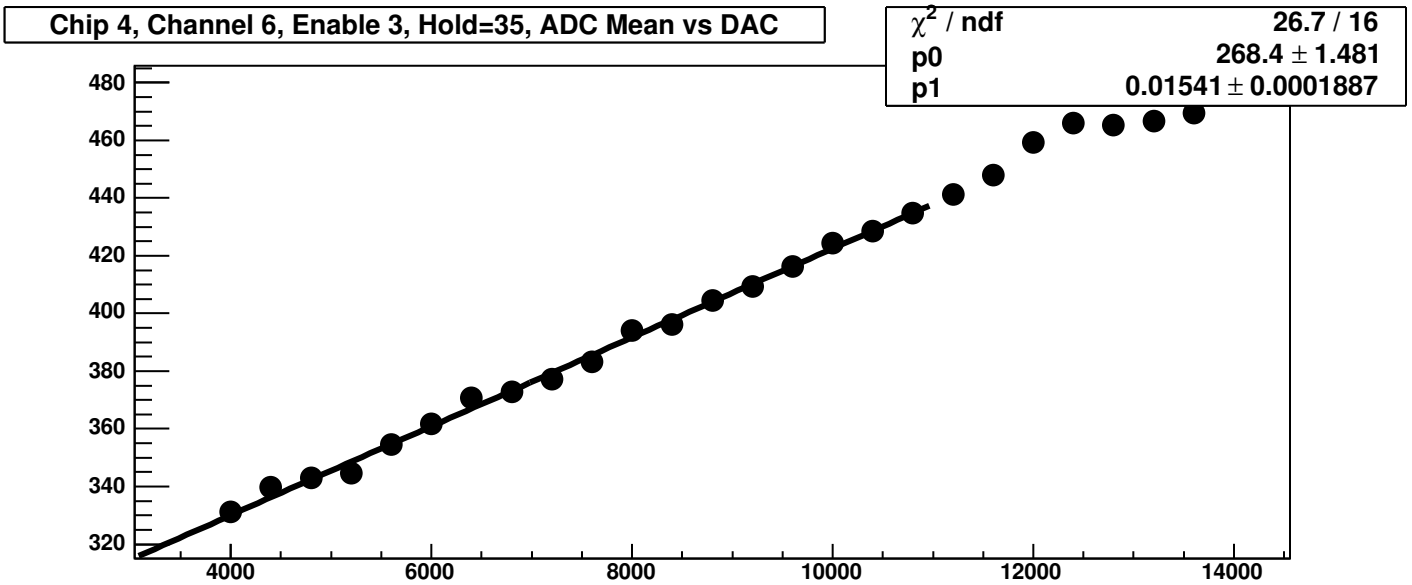

Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

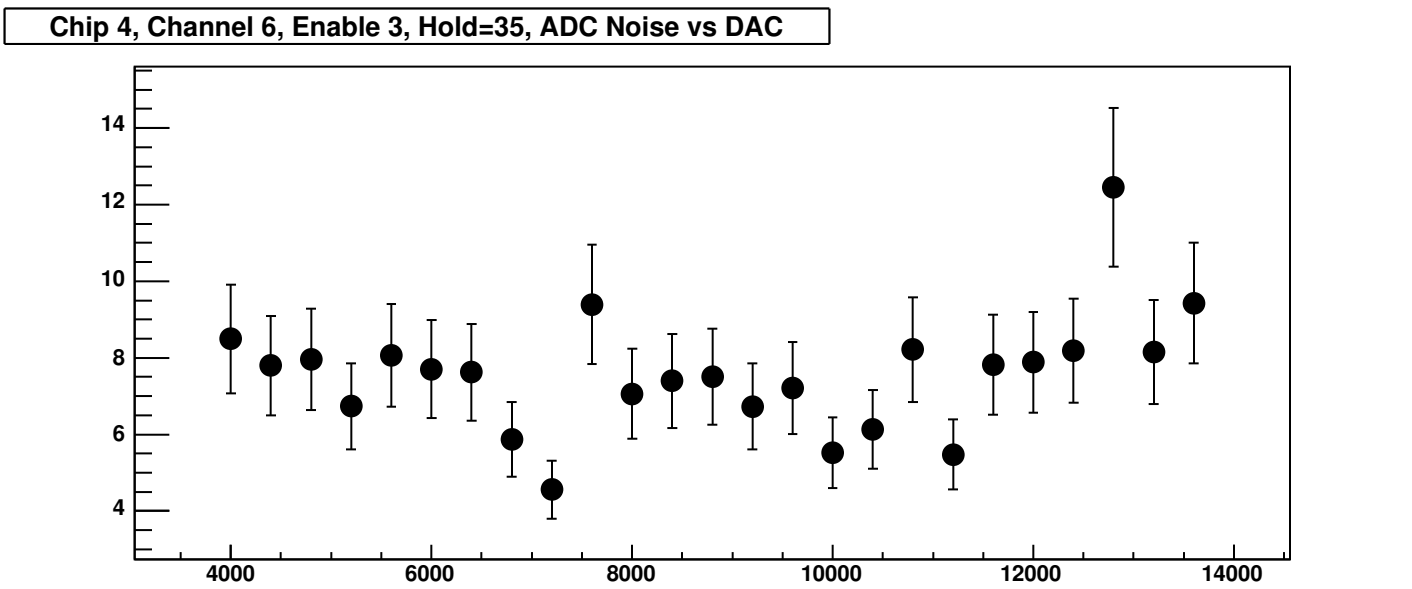

Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

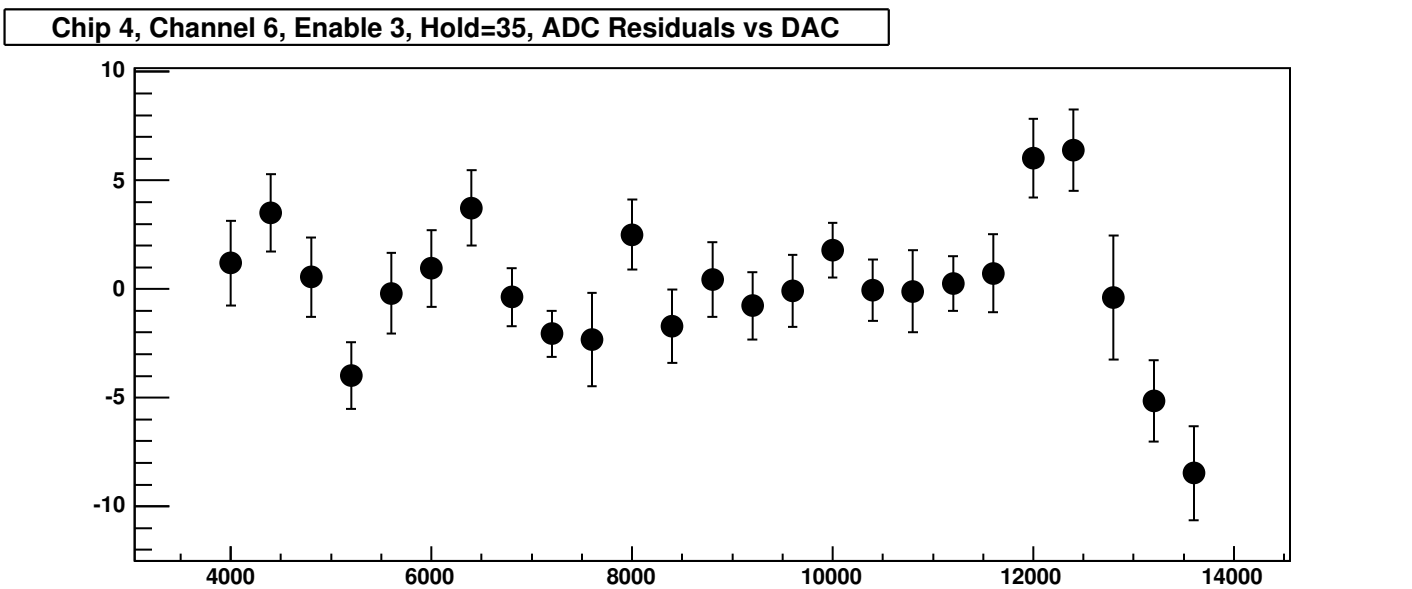

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 2031 / 16
p0 526.9 ± 2.403
p1 2.572 ± 0.0003399
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

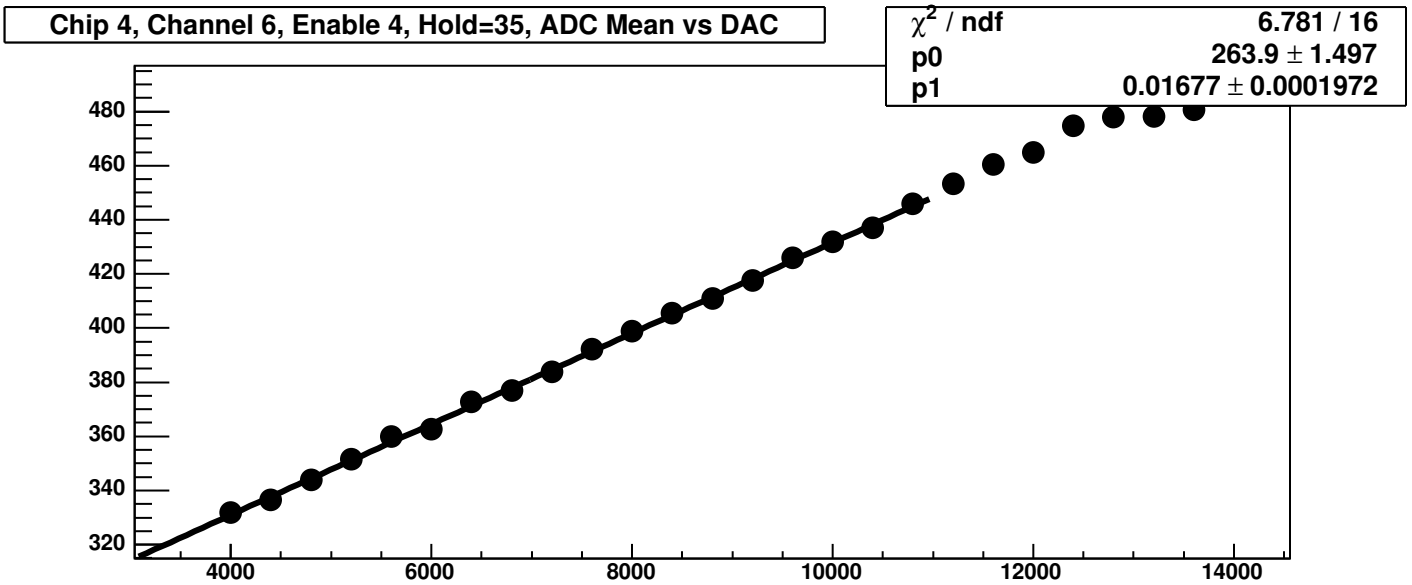

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

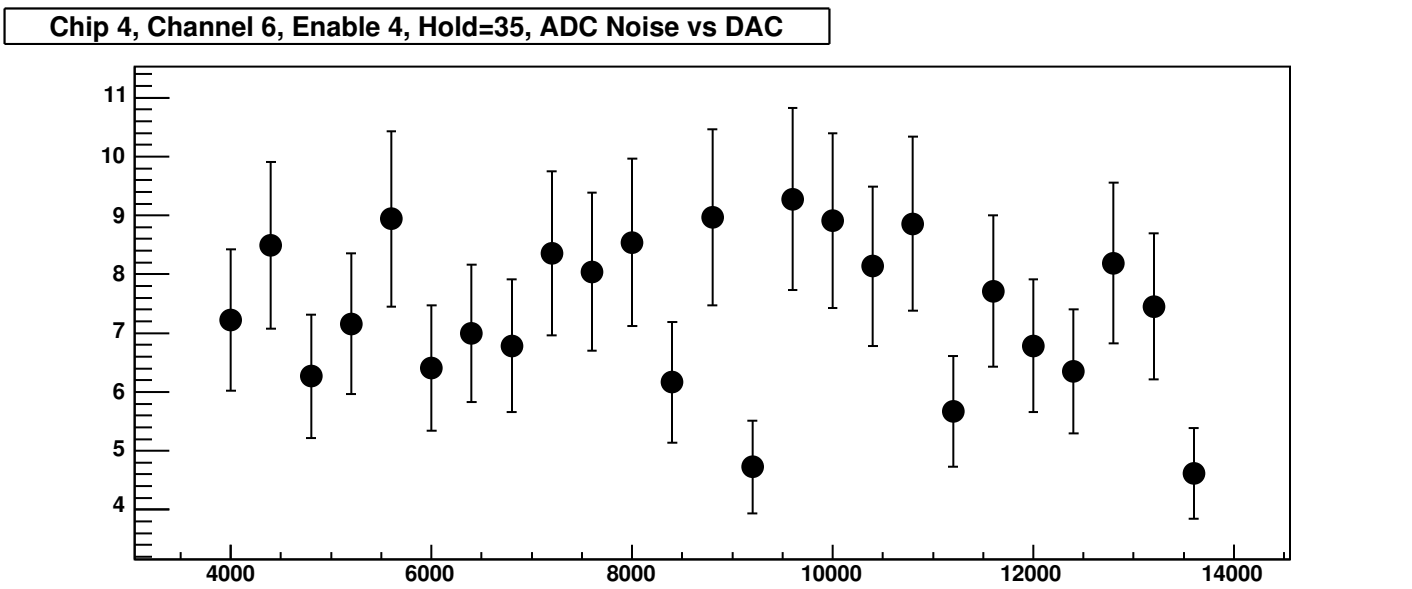

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

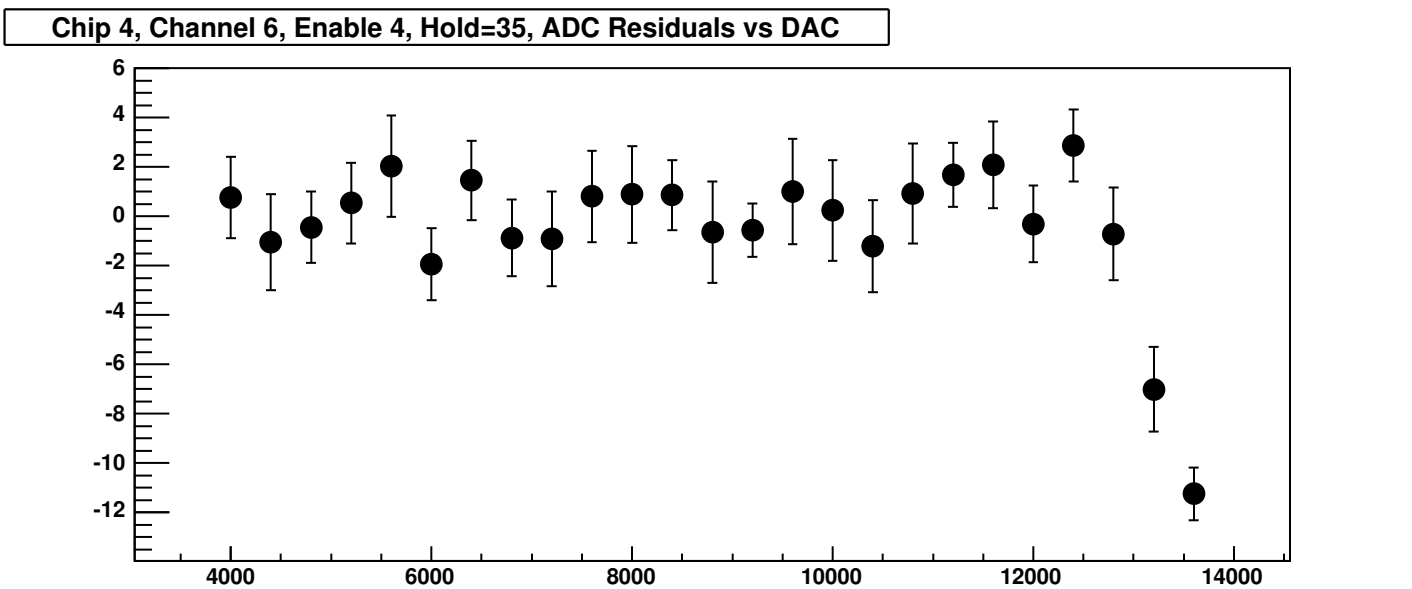

Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.7 / 16
p0 268.4 ± 1.481
p1 0.01541 ± 0.0001887
480
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

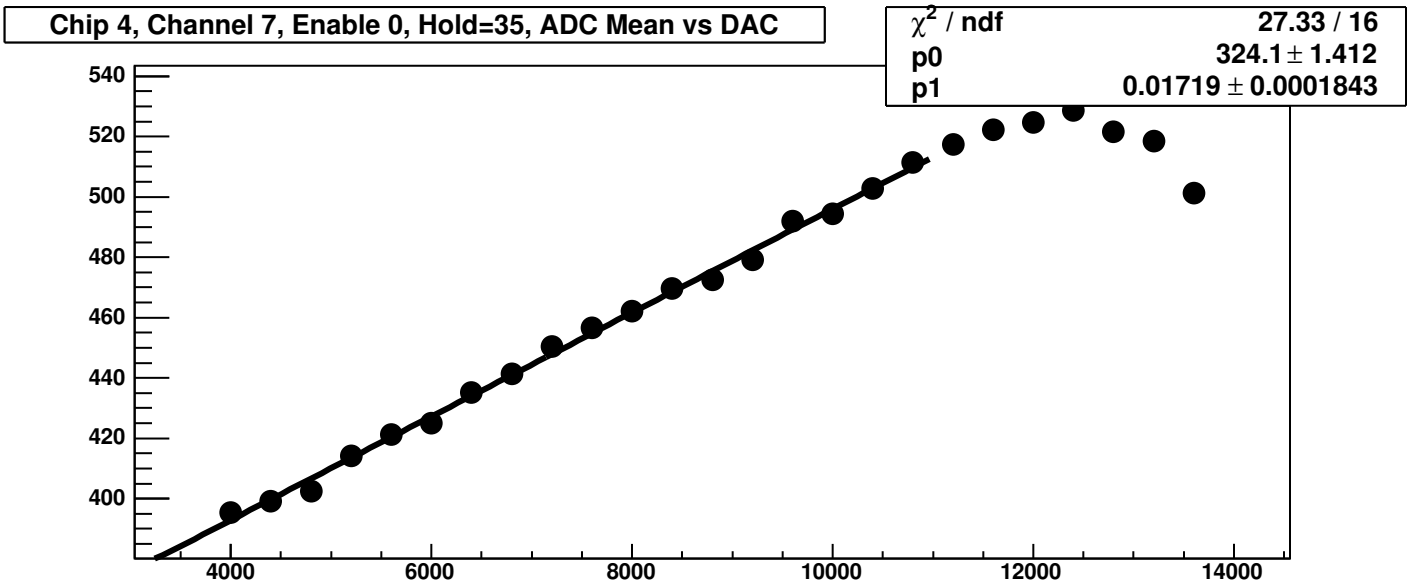

Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

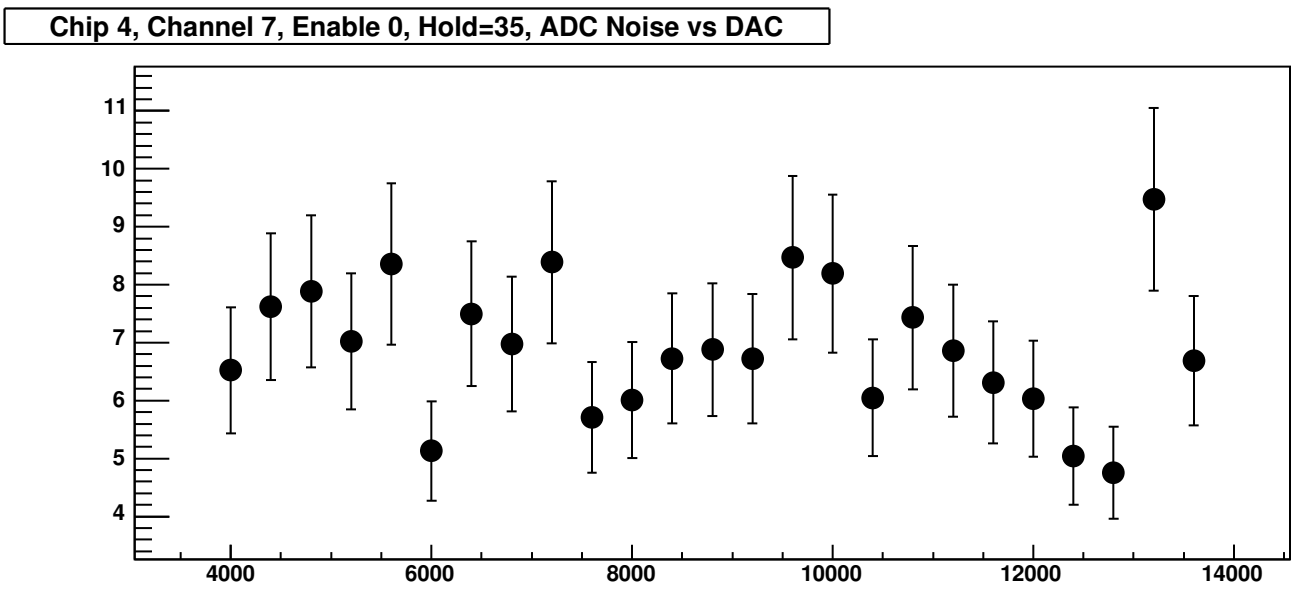

Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

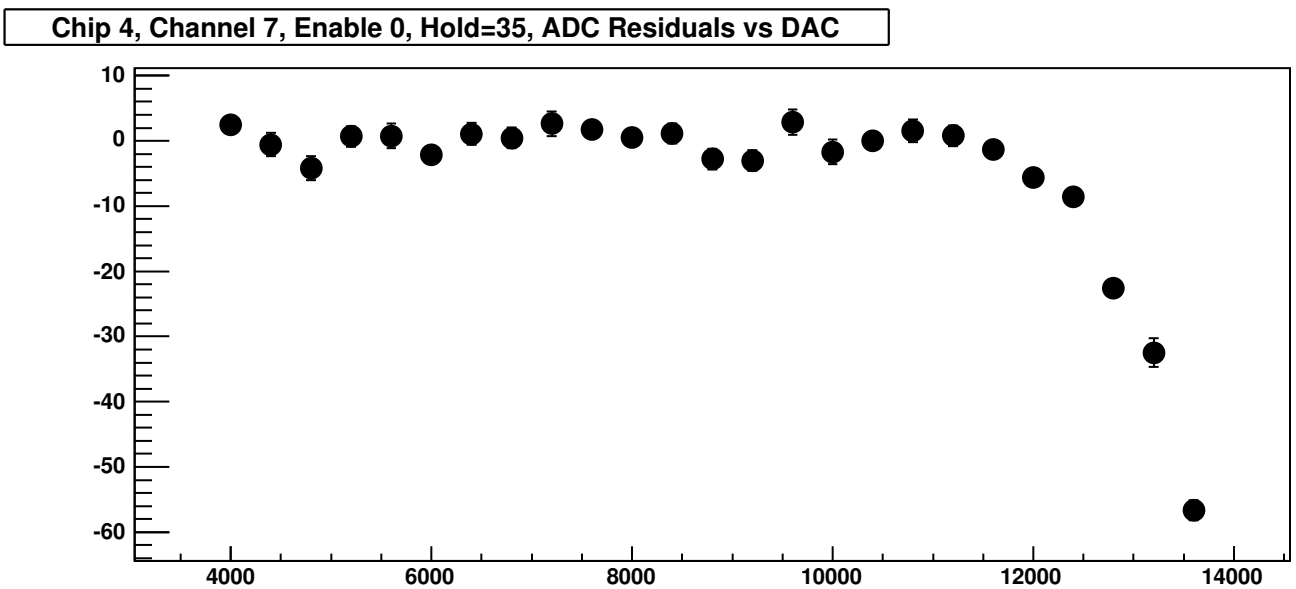

Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 6.781 / 16
p0 263.9 ± 1.497
p1 0.01677 ± 0.0001972
480
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

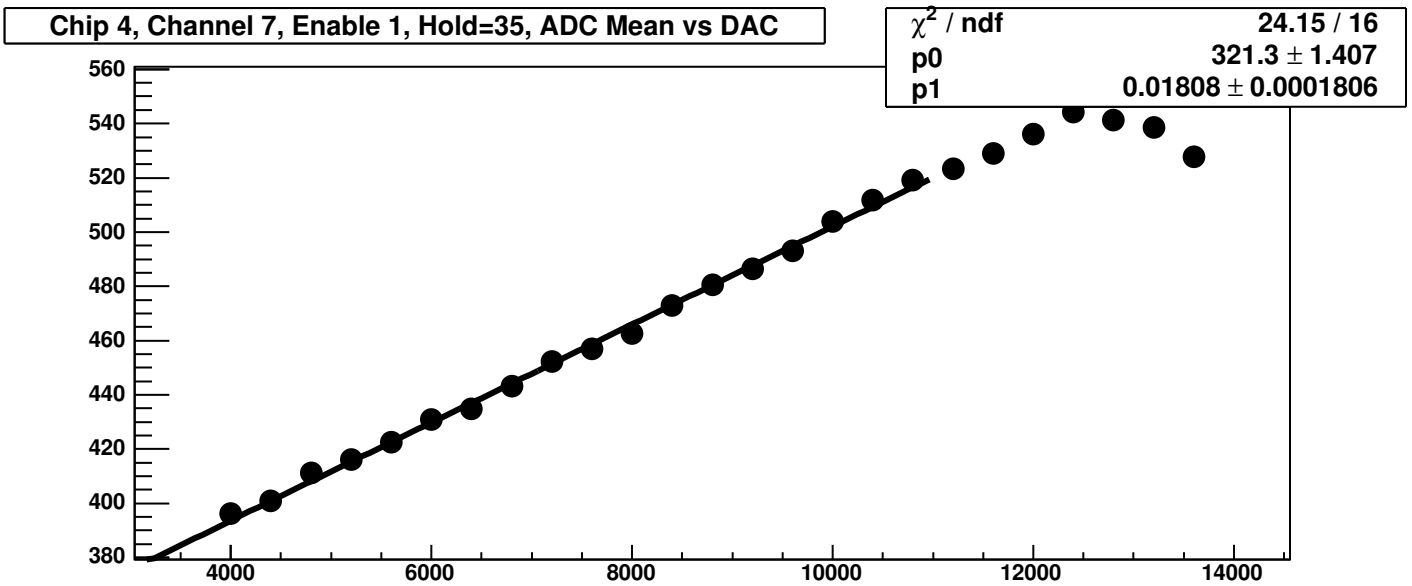

Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

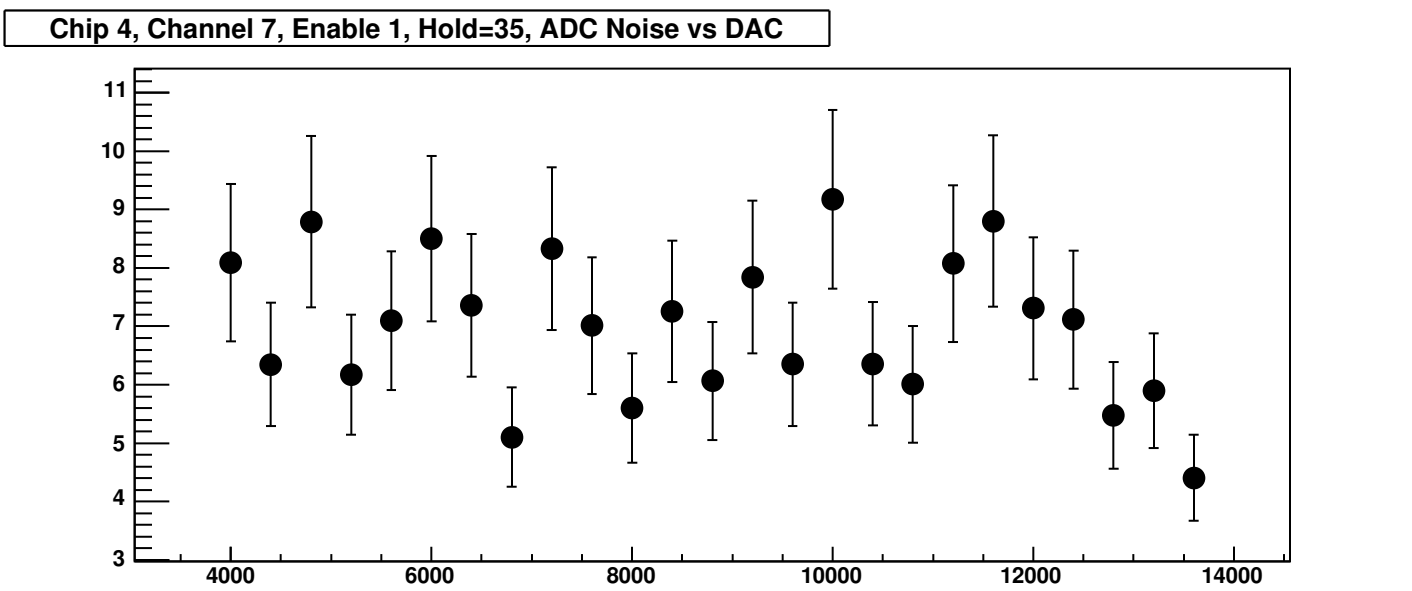

Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
4000
6000
8000
10000
12000
14000

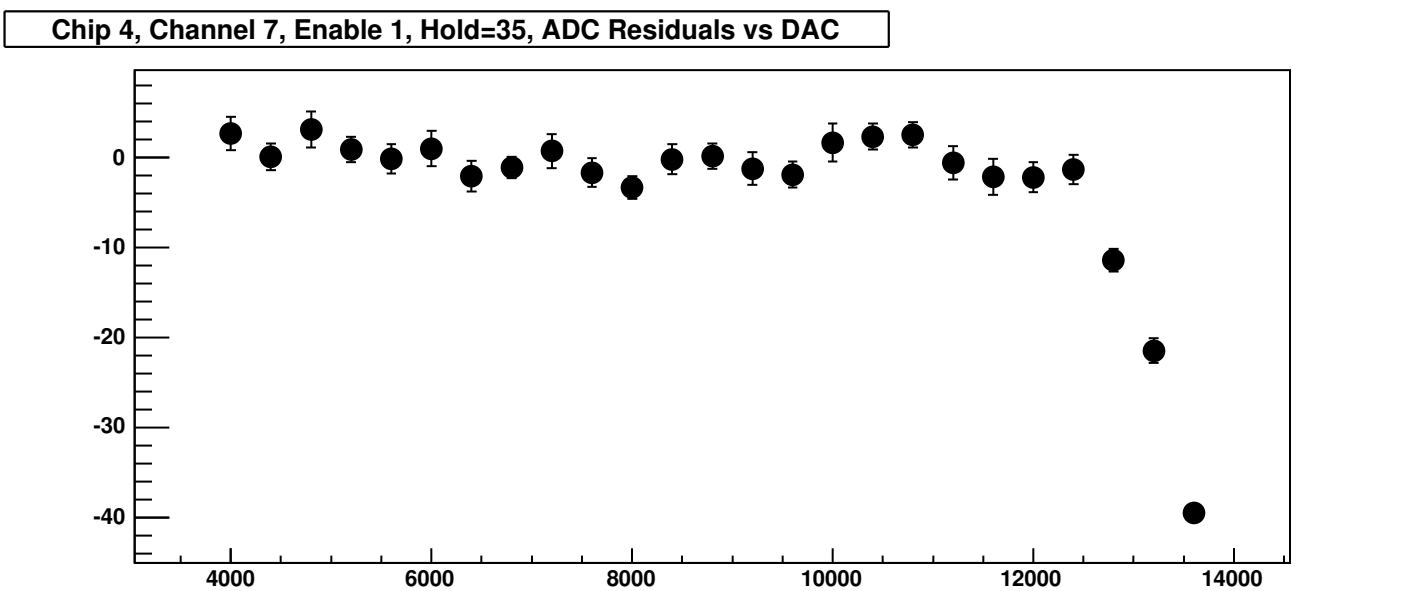

Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.33 / 16
p0 324.1 ± 1.412
p1 0.01719 ± 0.0001843
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

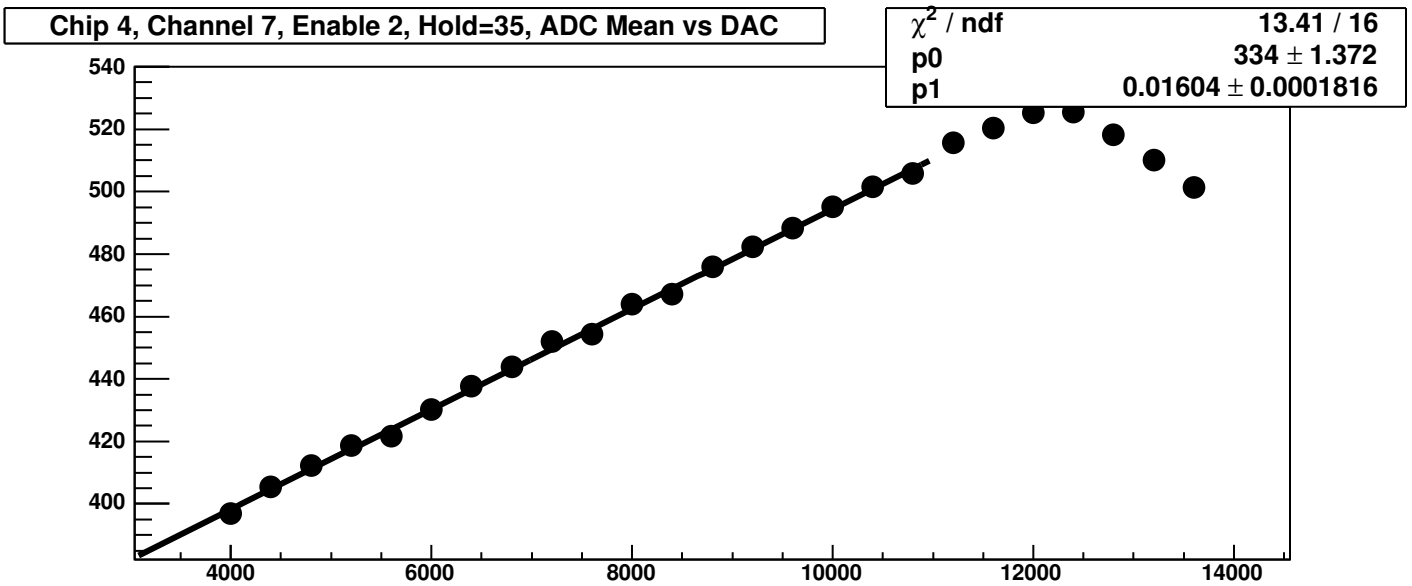

Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

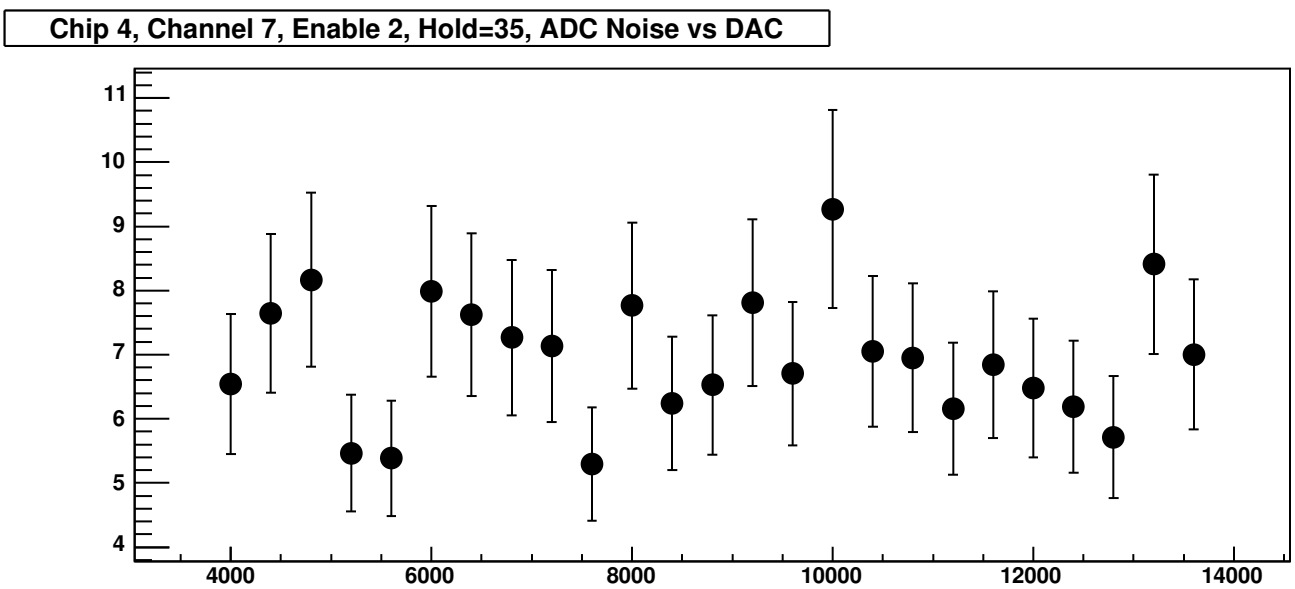

Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
4000
6000
8000
10000
12000
14000

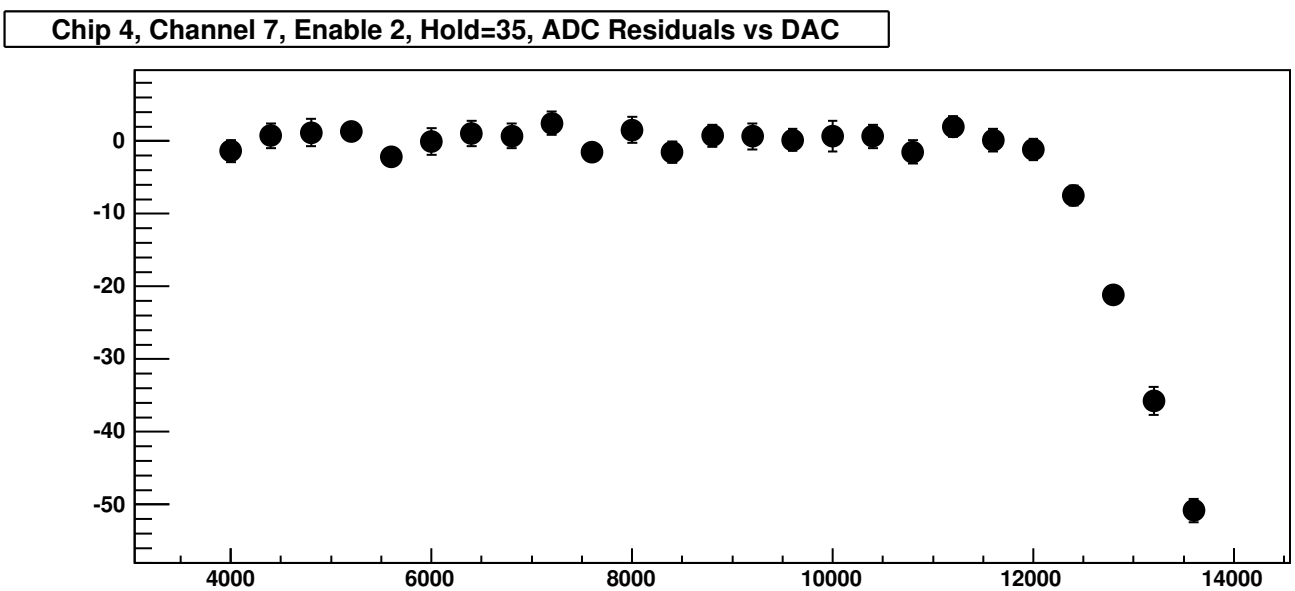

Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.15 / 16
p0 321.3 ± 1.407
p1 0.01808 ± 0.0001806
560
540
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000

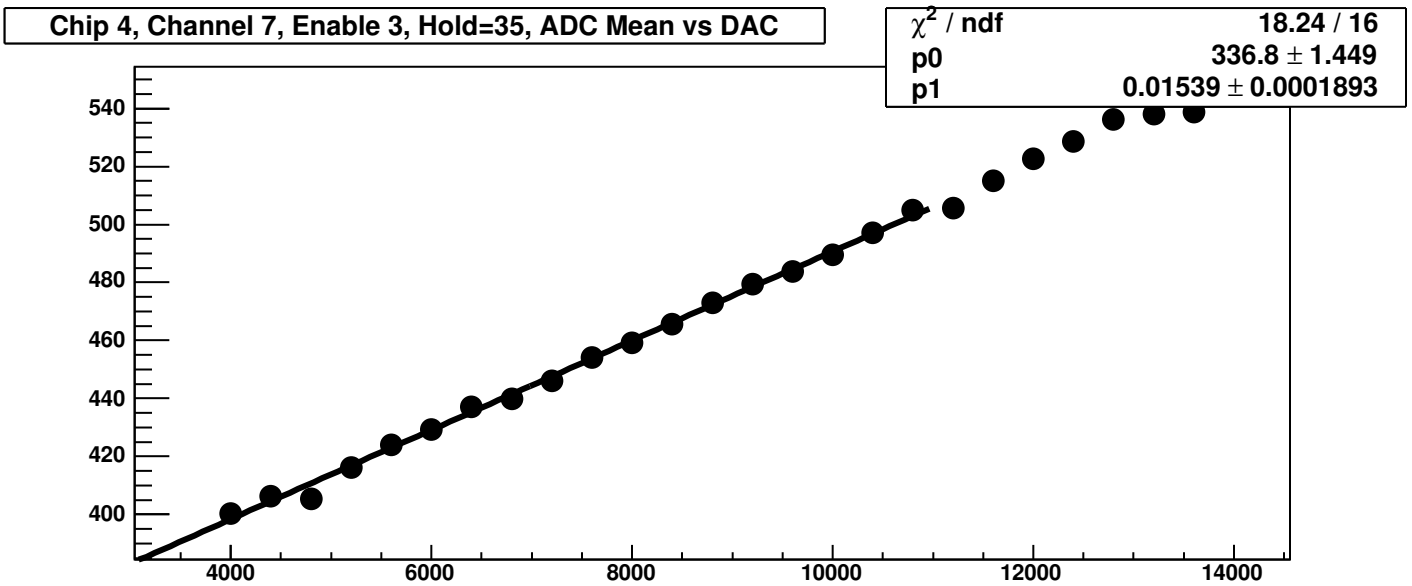

Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

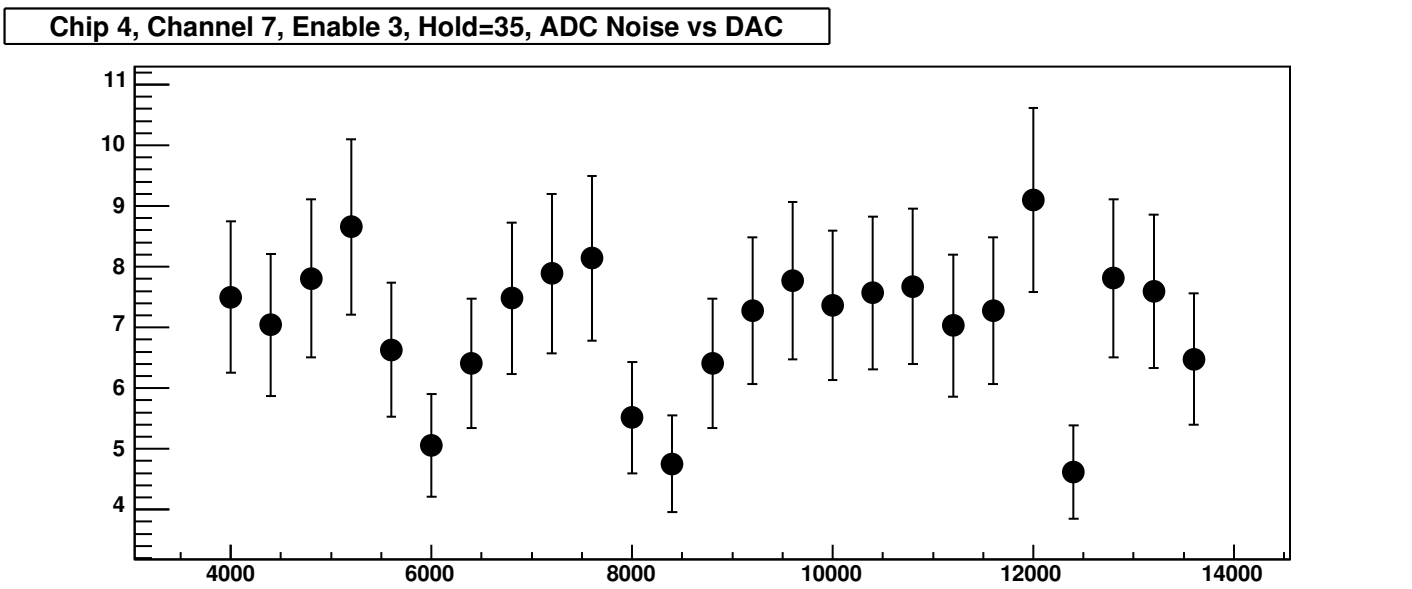

Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

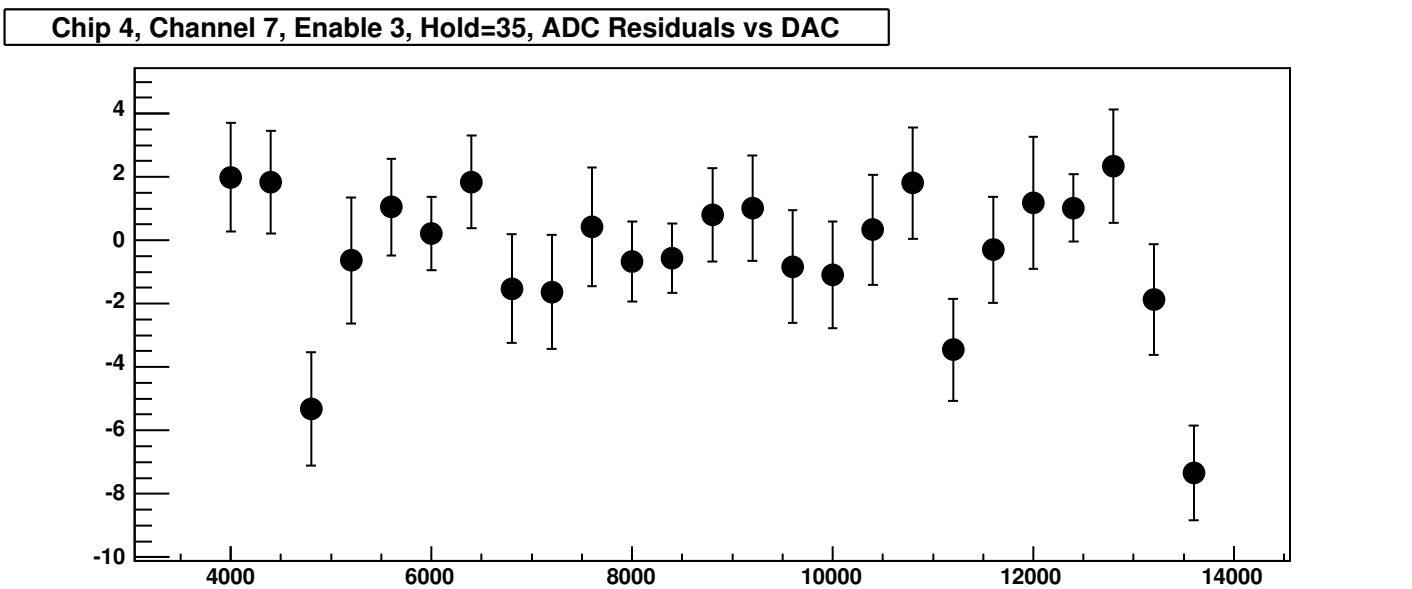

Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 13.41 / 16
p0 334 ± 1.372
p1 0.01604 ± 0.0001816
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

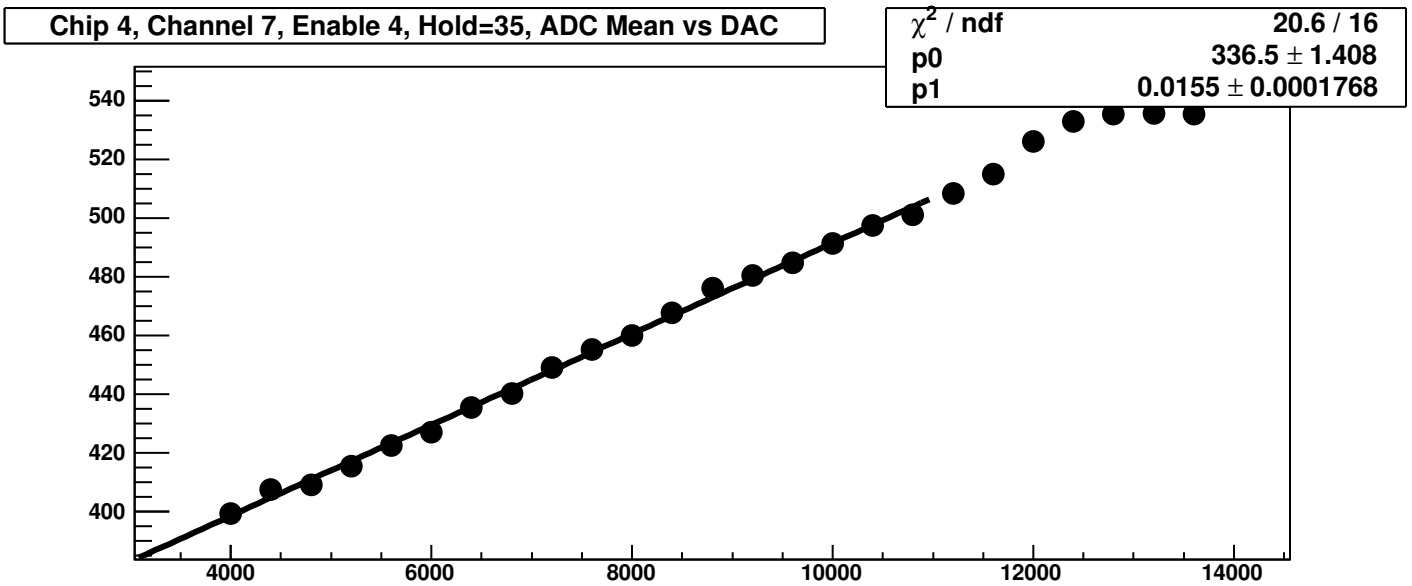

Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

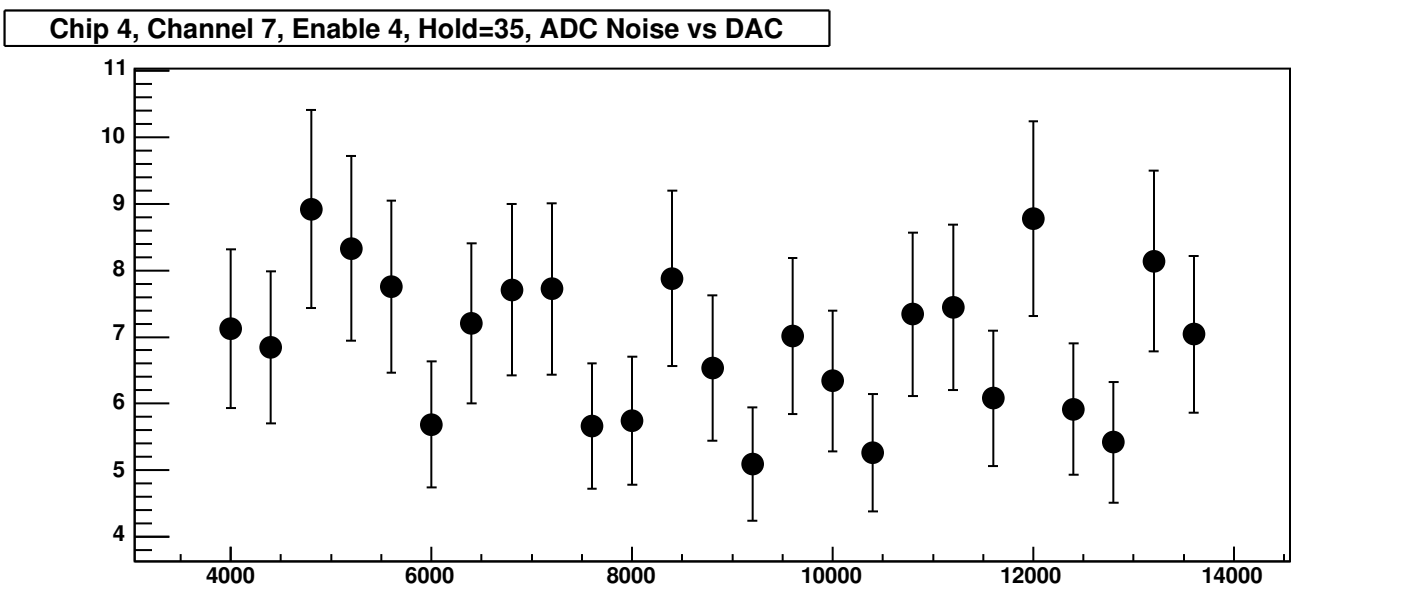

Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

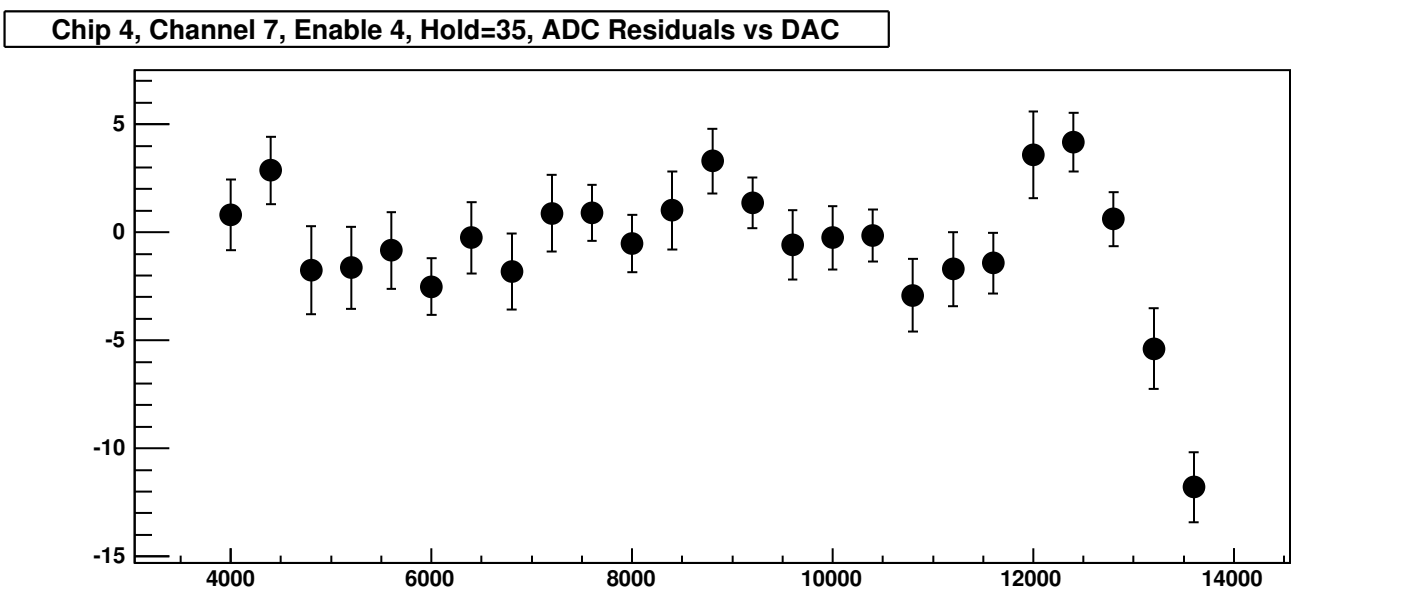

Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.24 / 16
p0 336.8 ± 1.449
p1 0.01539 ± 0.0001893
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

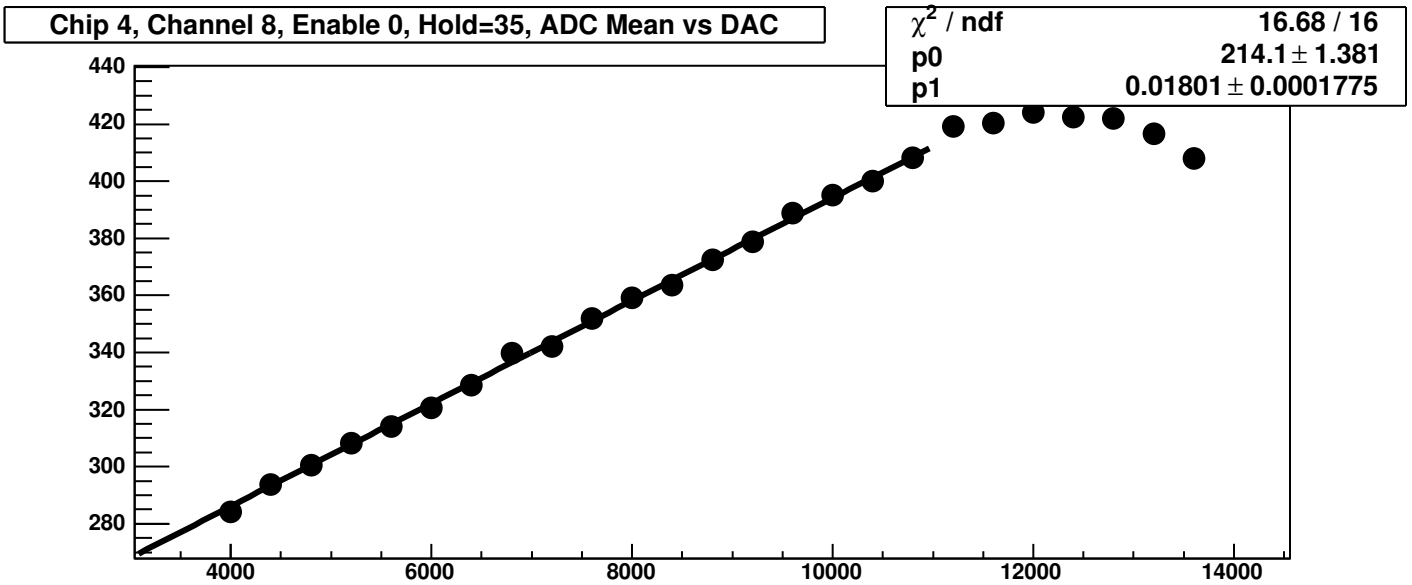

Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

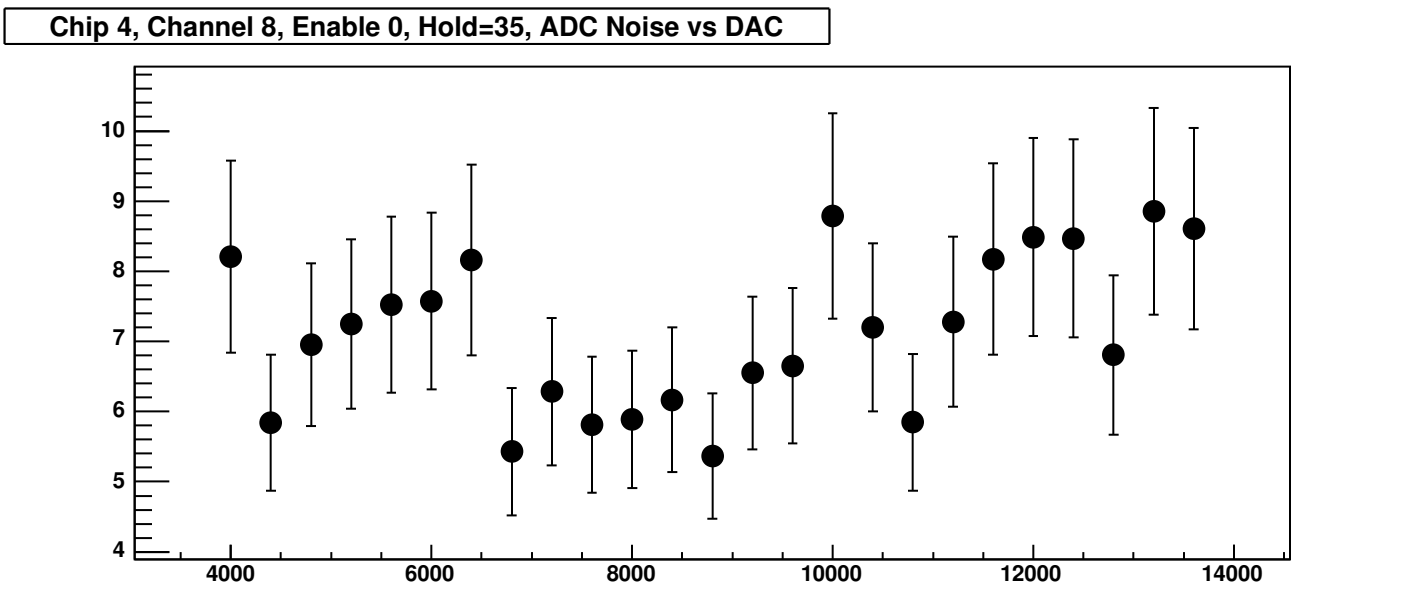

Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

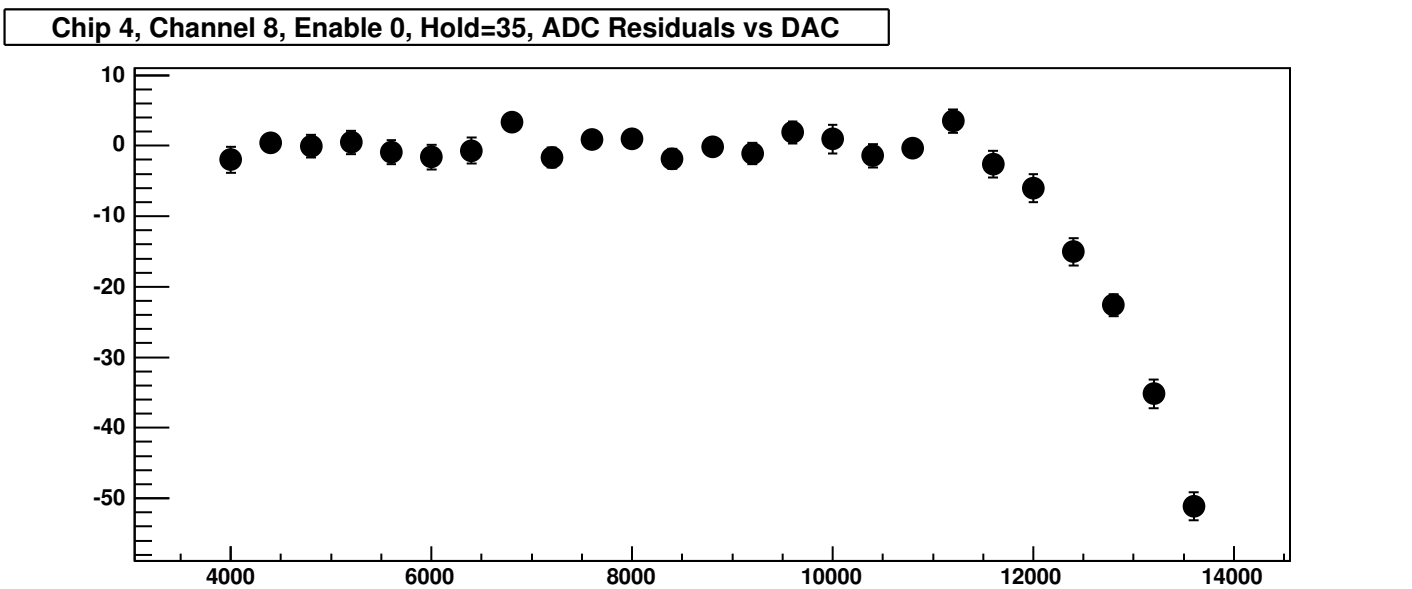

Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.6 / 16
p0 336.5 ± 1.408
p1 0.0155 ± 0.0001768
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

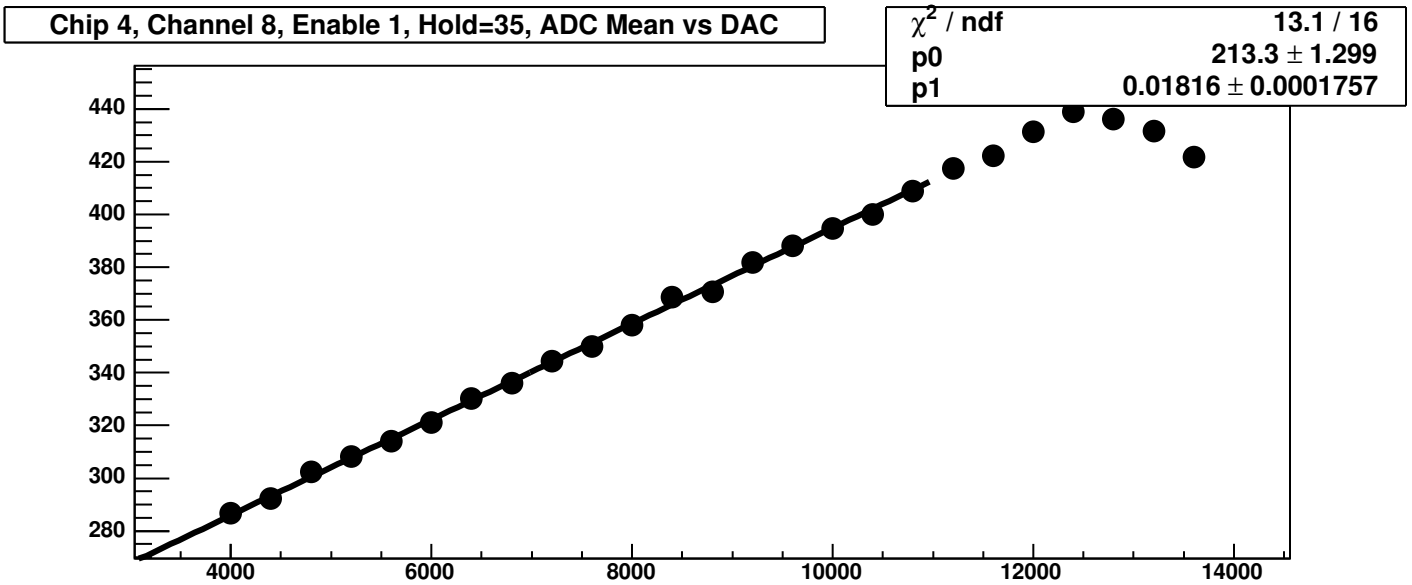

Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

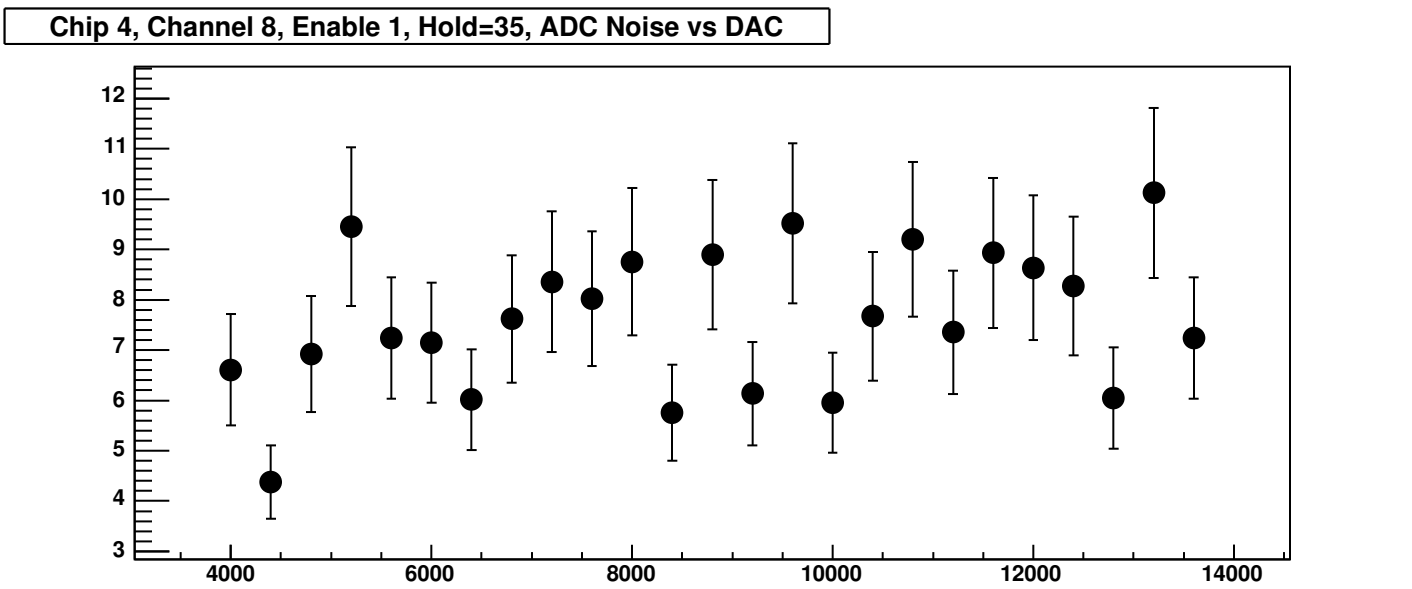

Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

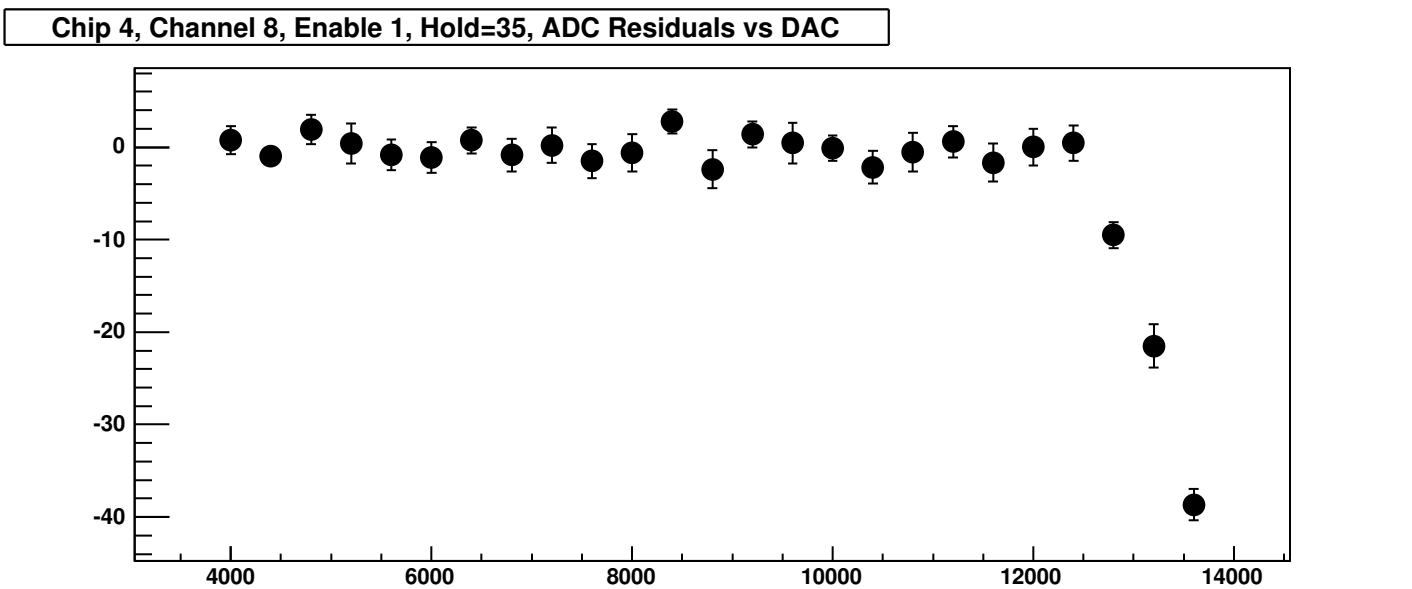

Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ^2 / ndf 16.68 / 16
p0 214.1 ± 1.381
p1 0.01801 ± 0.0001775
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

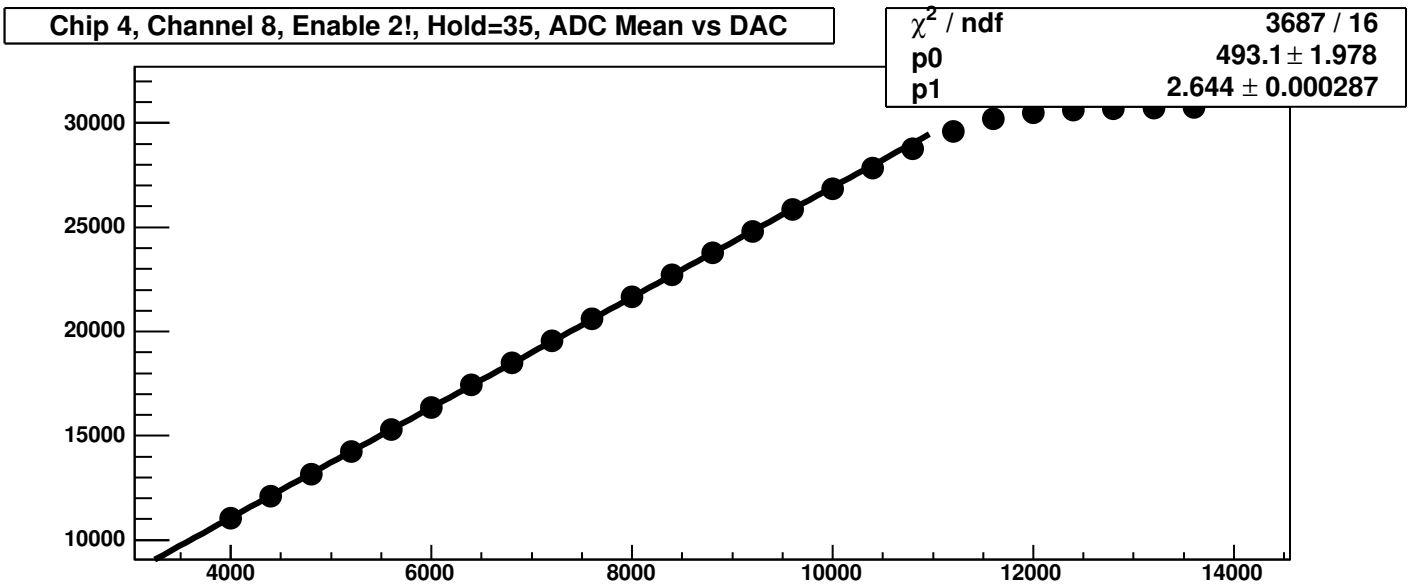

Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

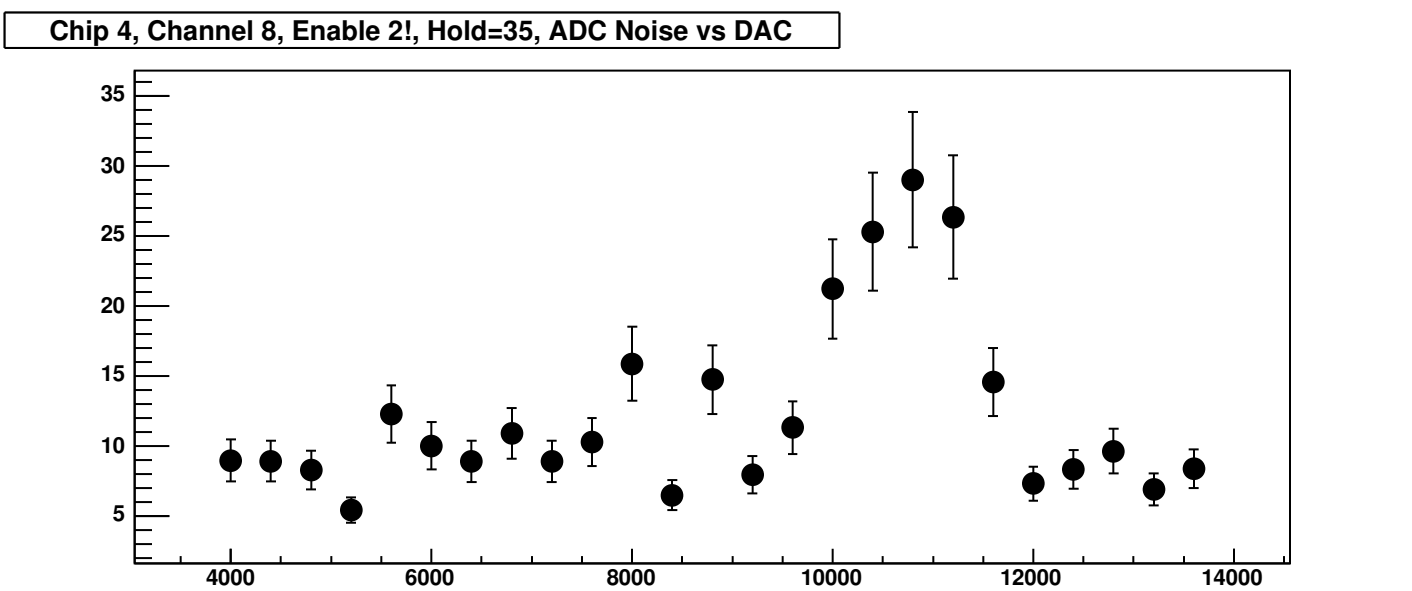

Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
4000
6000
8000
10000
12000
14000

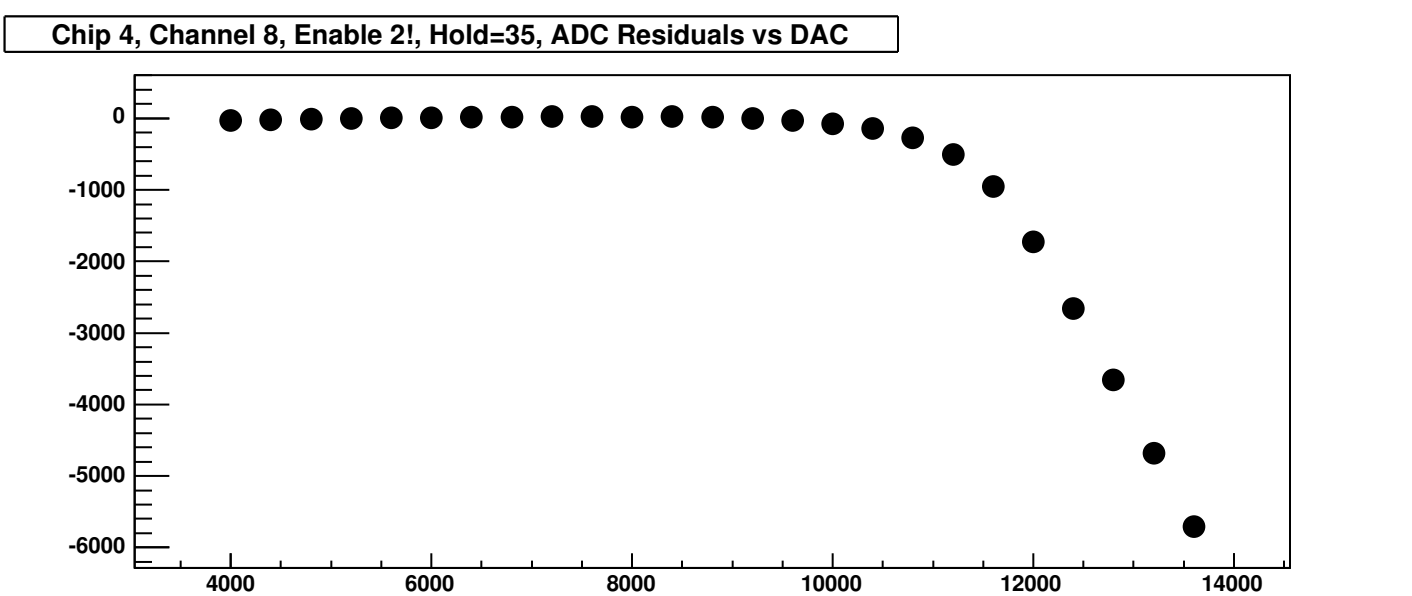

Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 13.1 / 16
p0 213.3 ± 1.299
p1 0.01816 ± 0.0001757
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

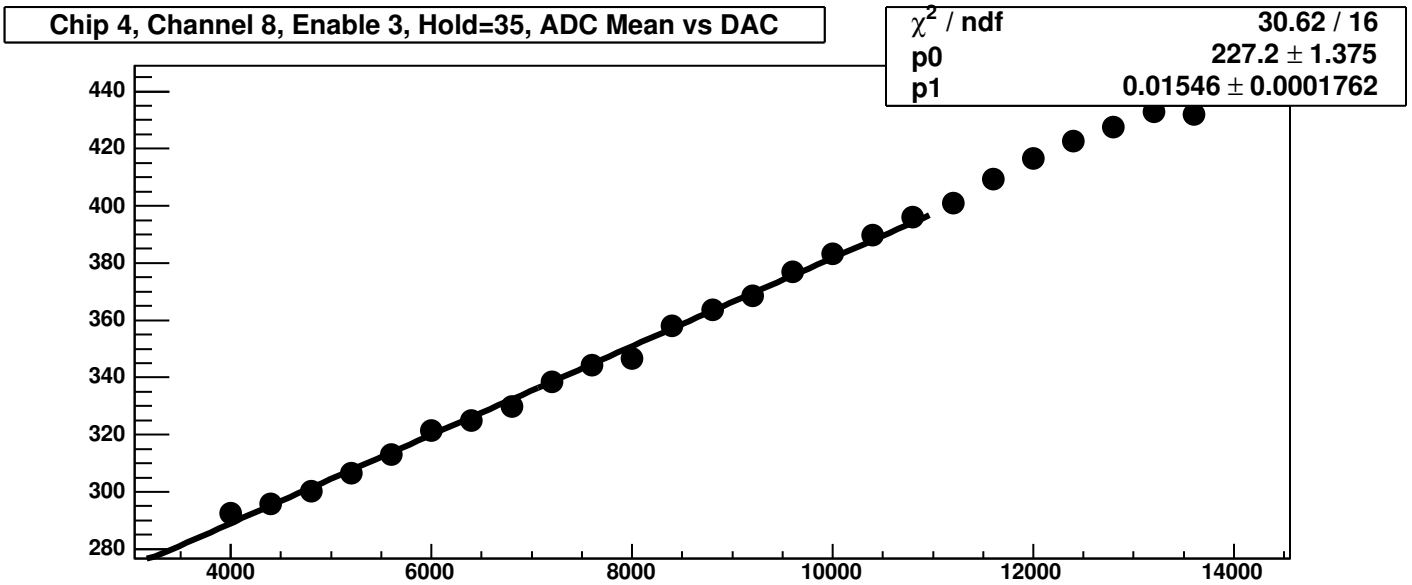

Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

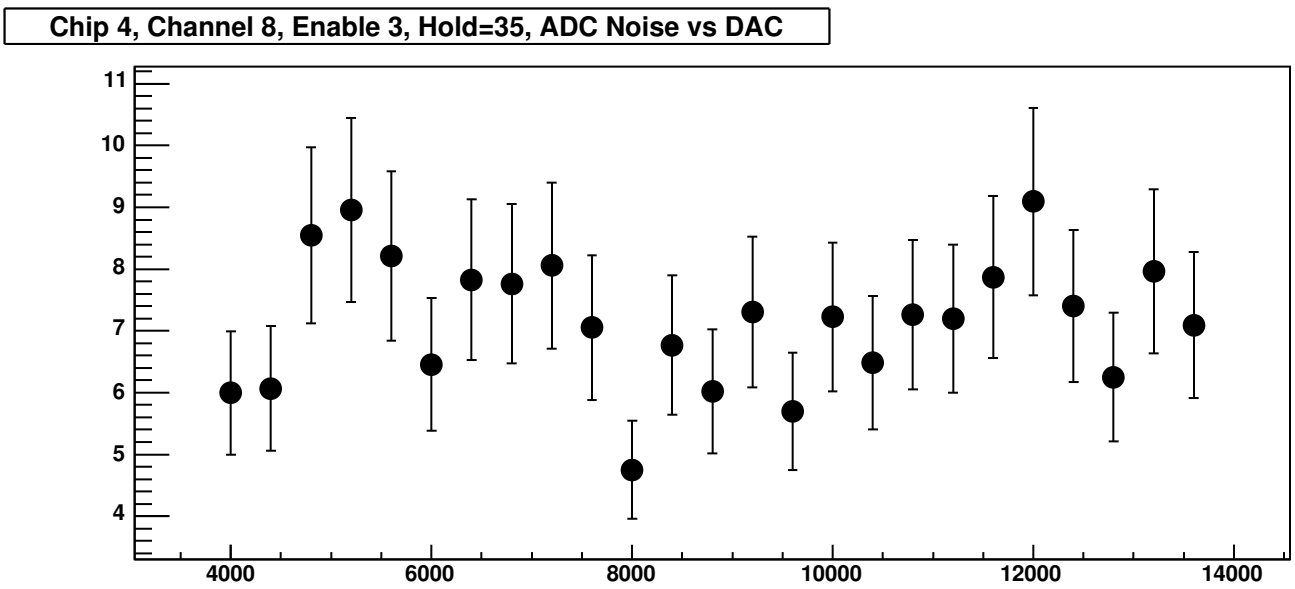

Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

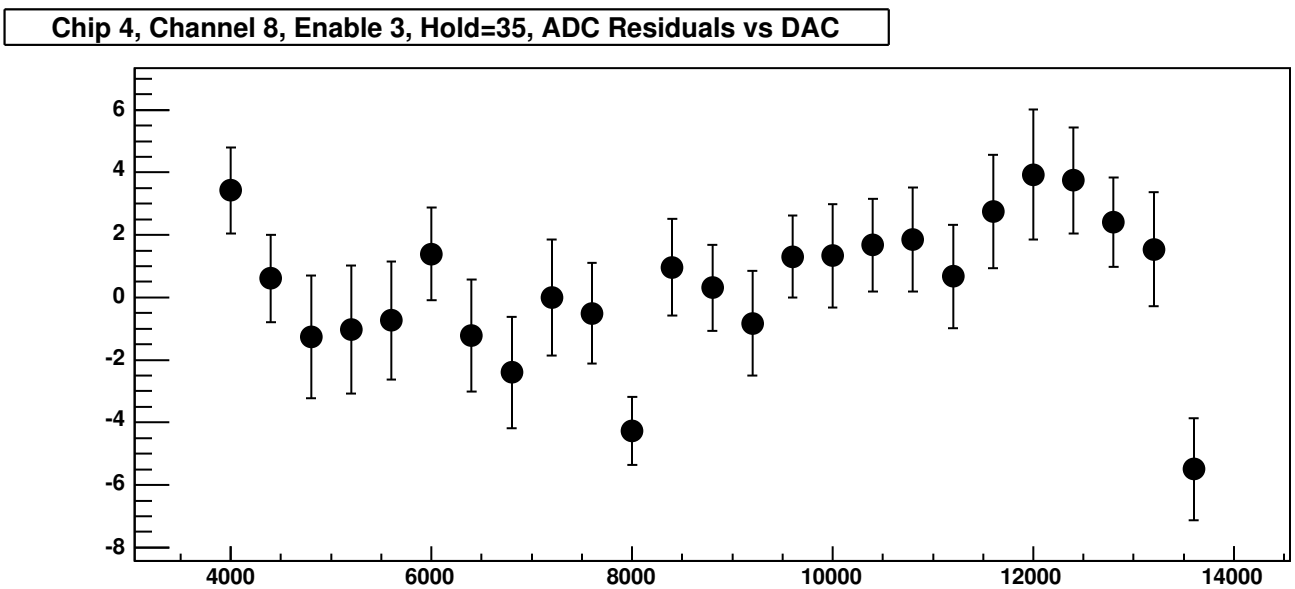

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 3687 / 16
p0 493.1 ± 1.978
p1 2.644 ± 0.000287
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

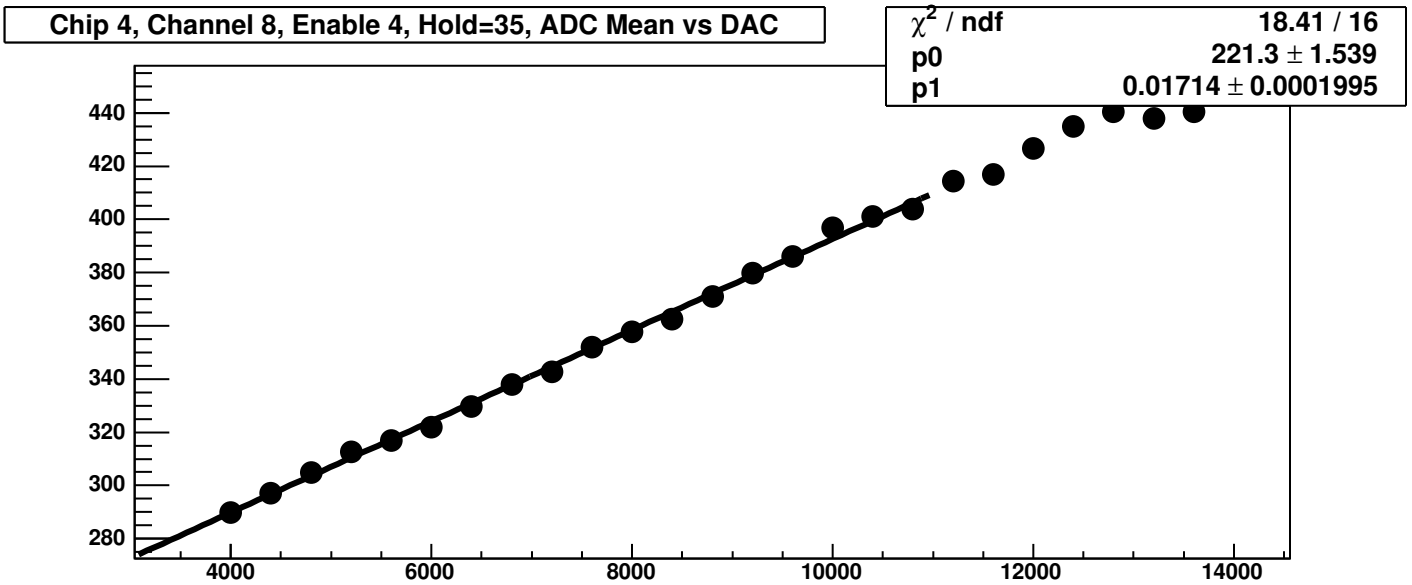

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

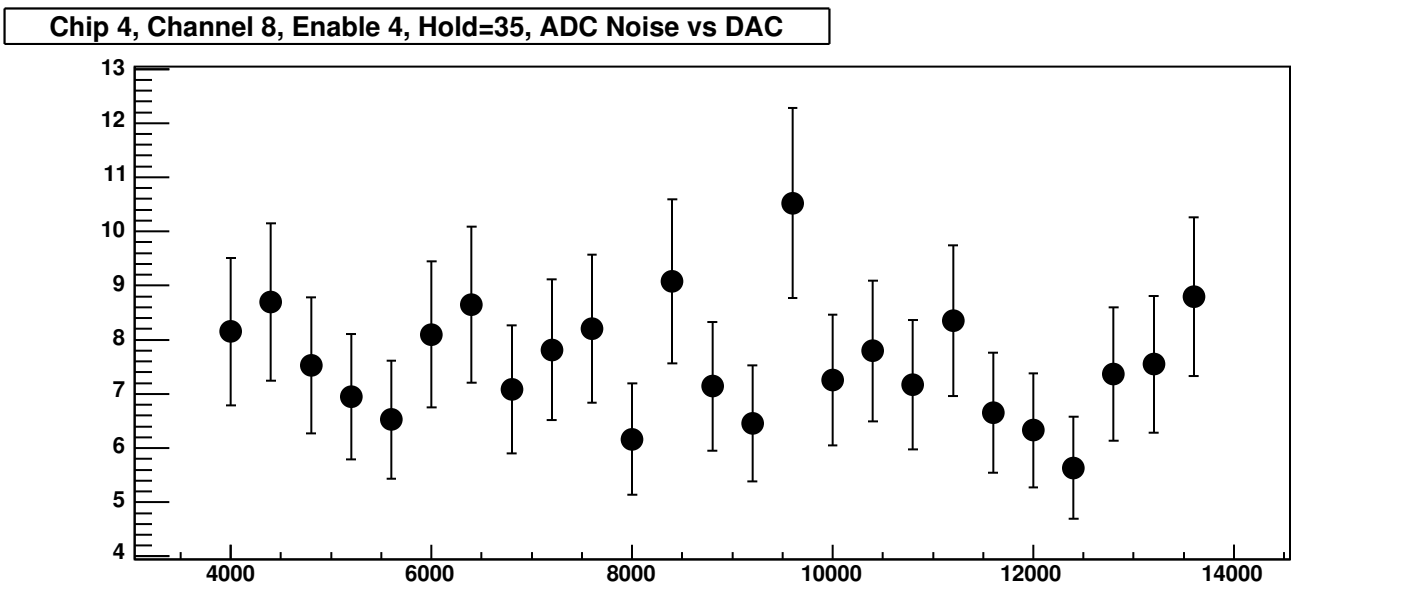

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000

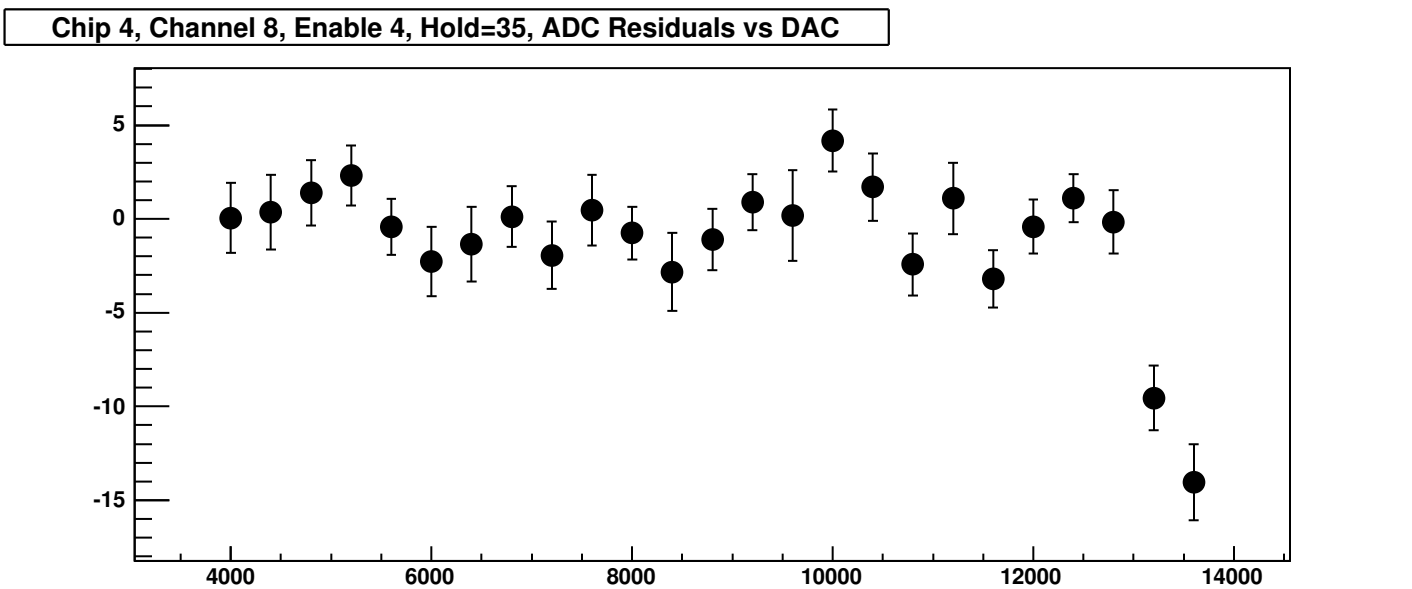

Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.62 / 16
p0 227.2 ± 1.375
p1 0.01546 ± 0.0001762
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

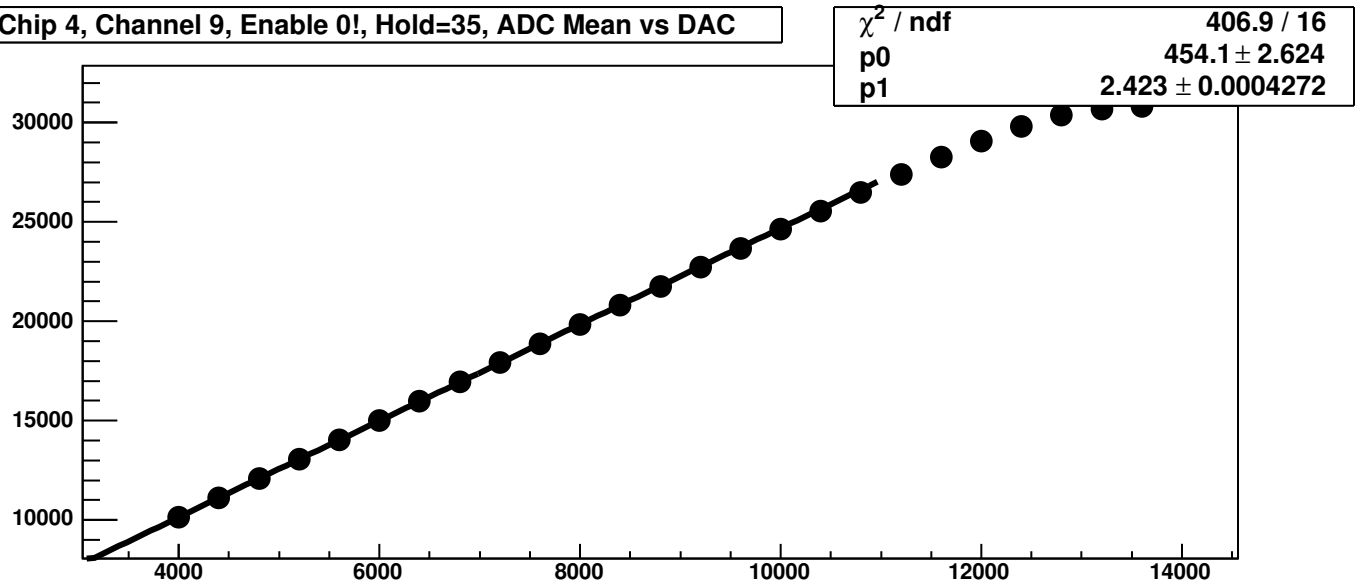

Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.41 / 16
p0 221.3 ± 1.539
p1 0.01714 ± 0.0001995
440
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

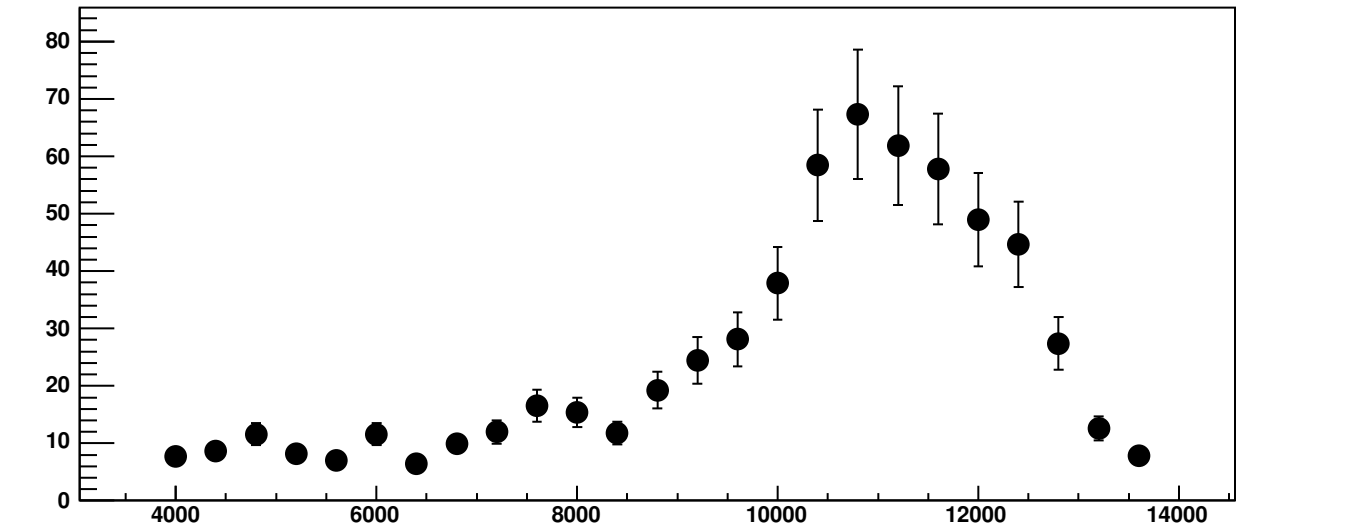

Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

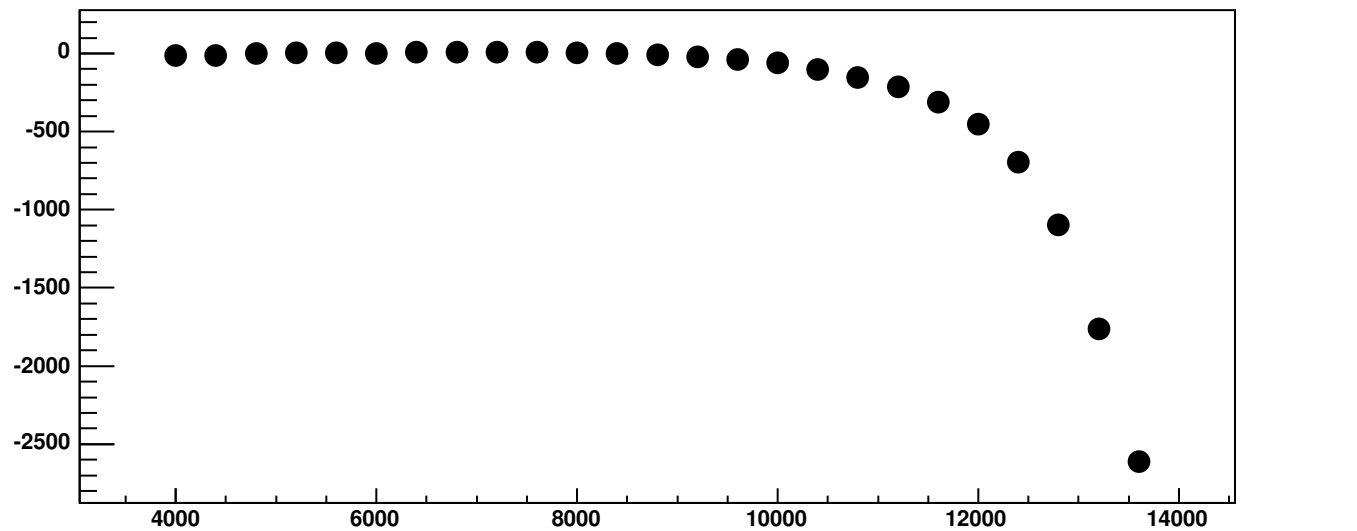
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



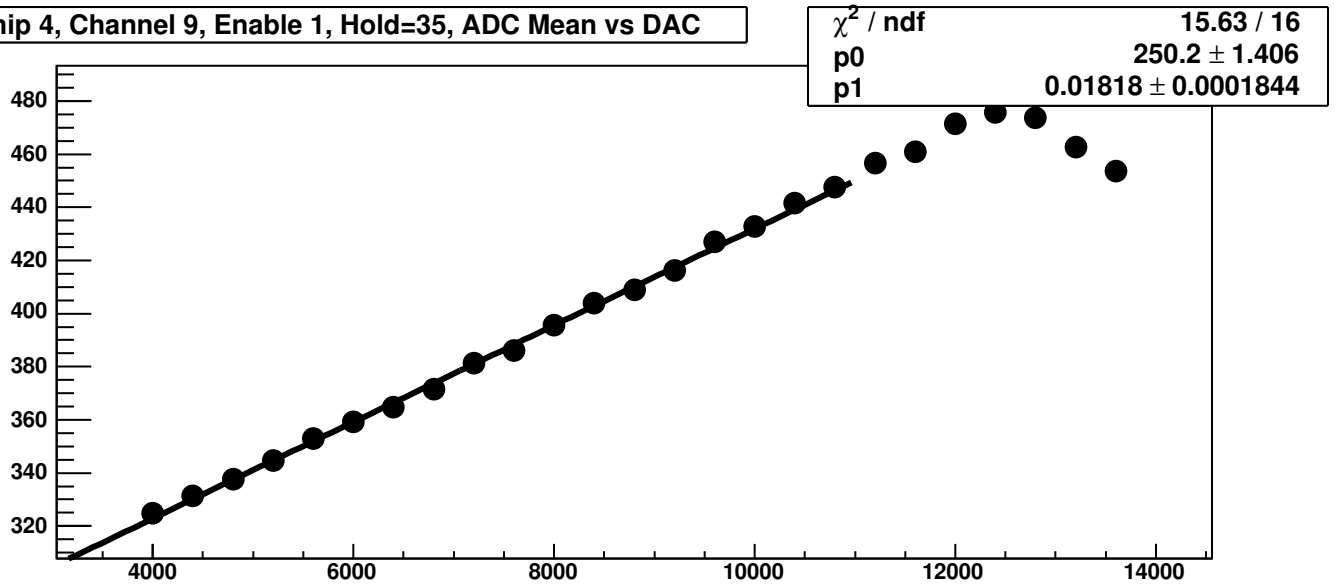
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



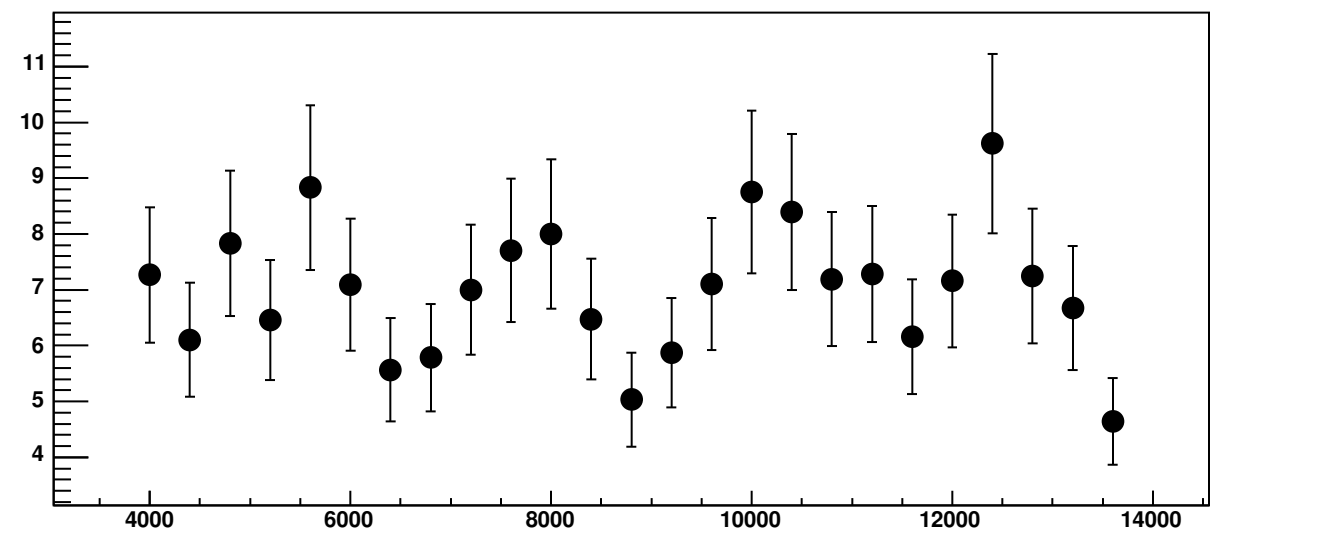
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



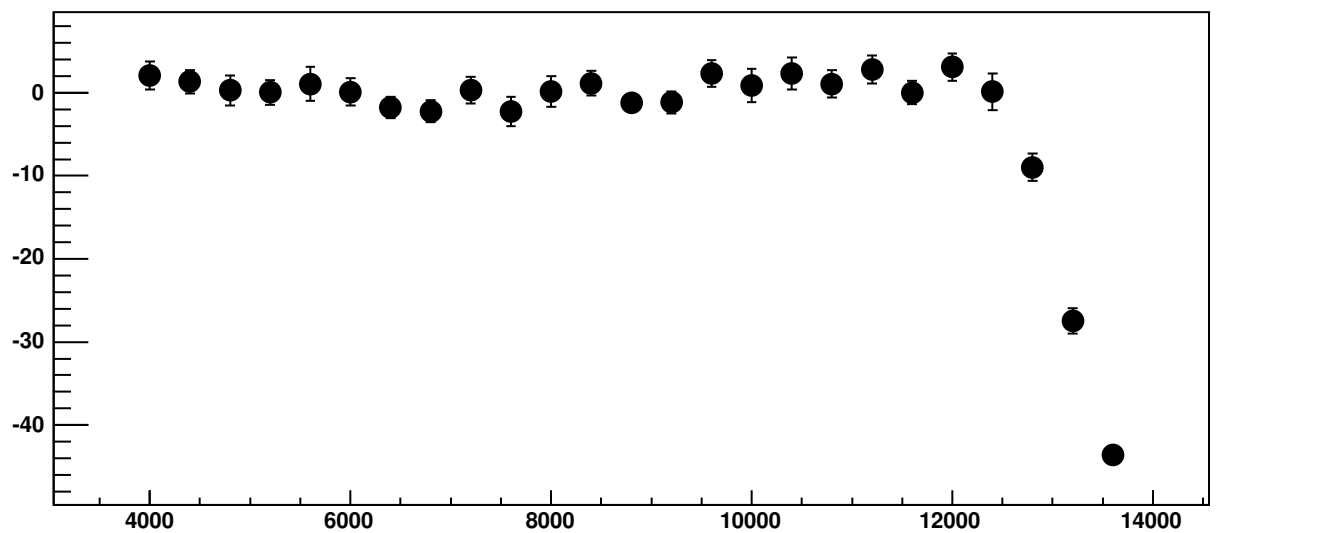
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

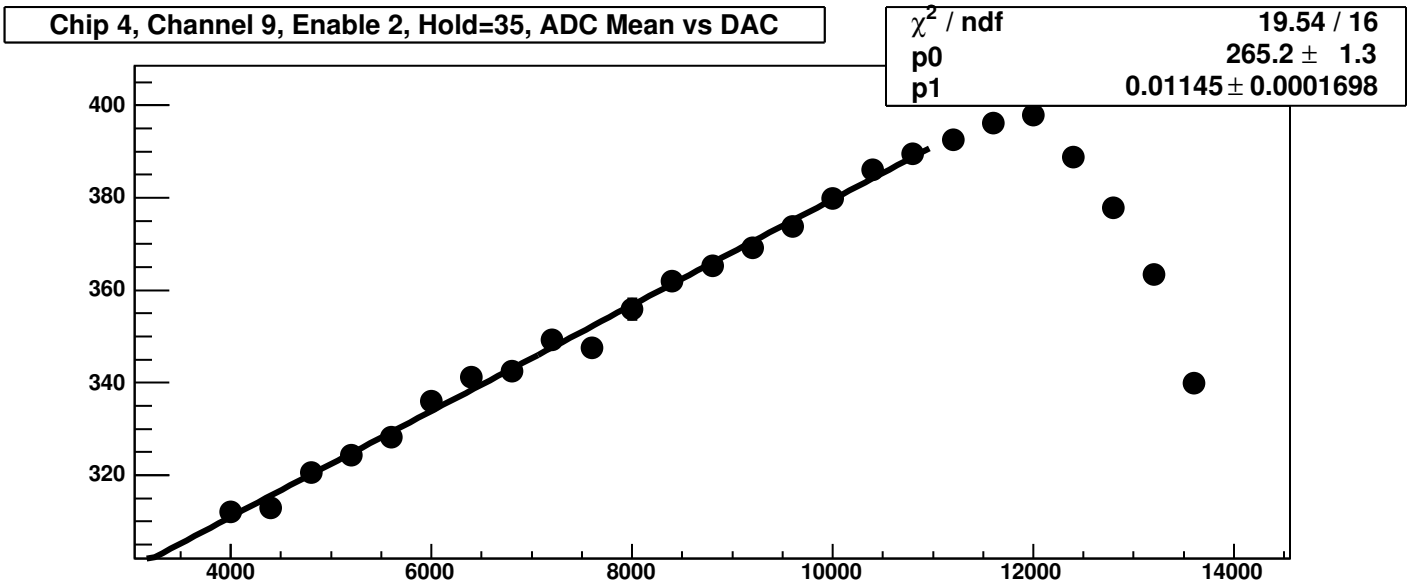


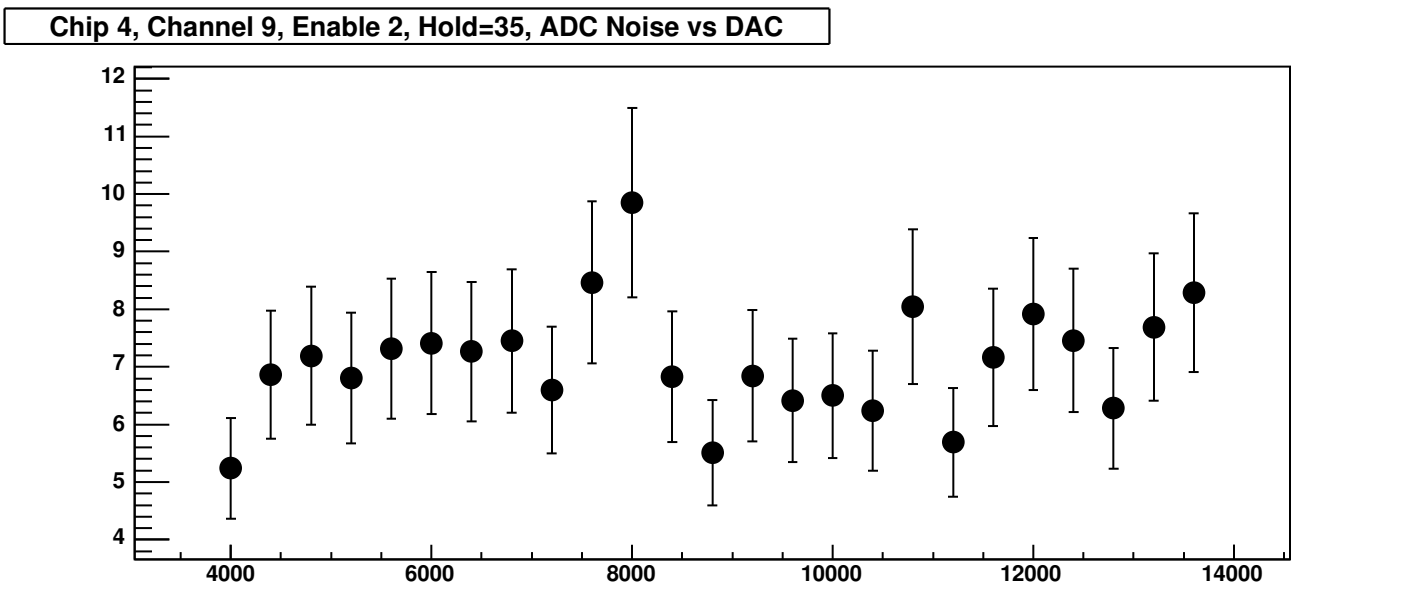
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

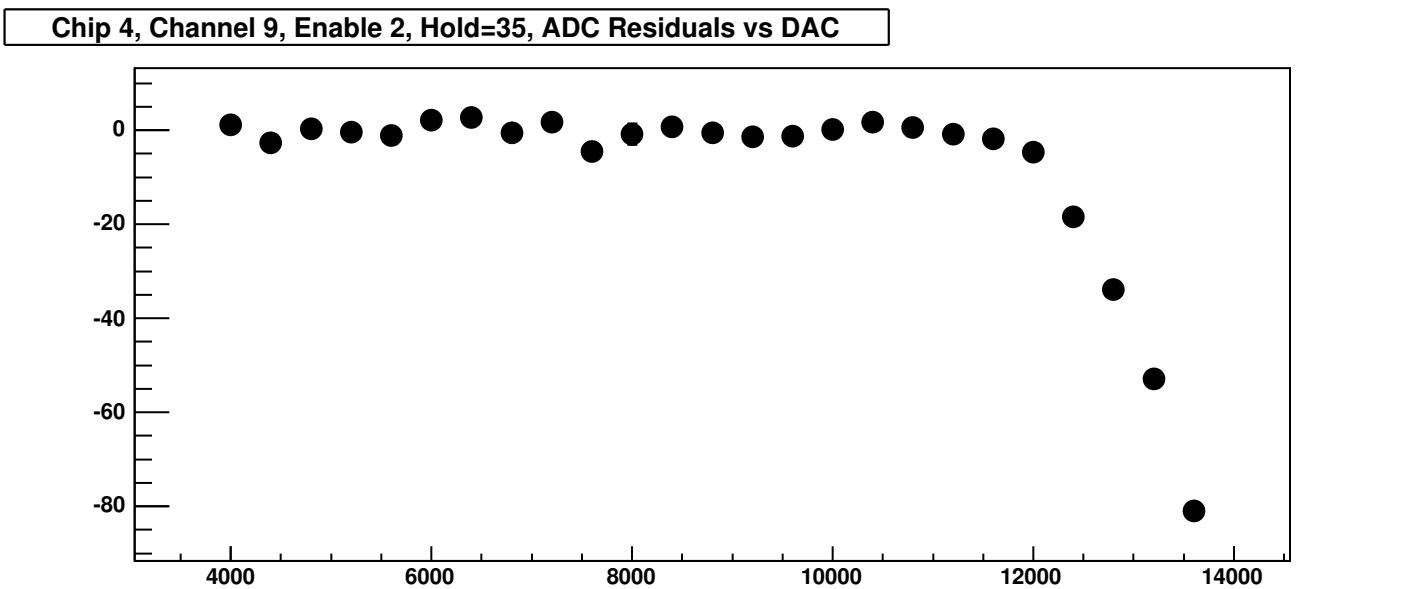


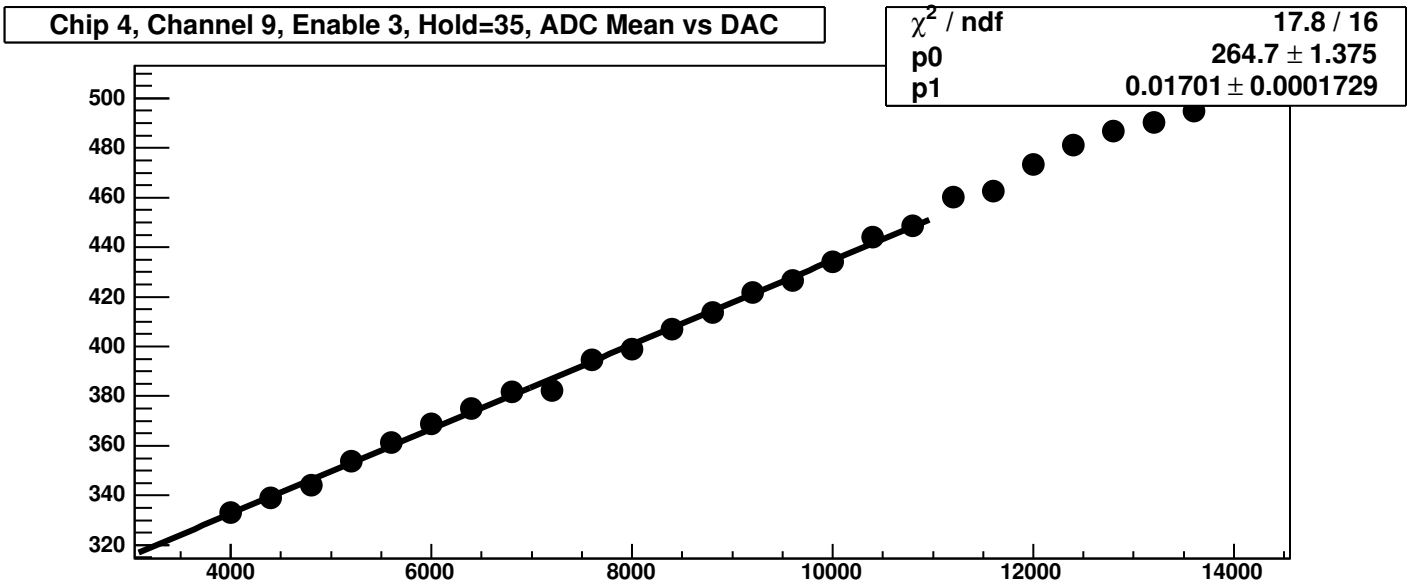
Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

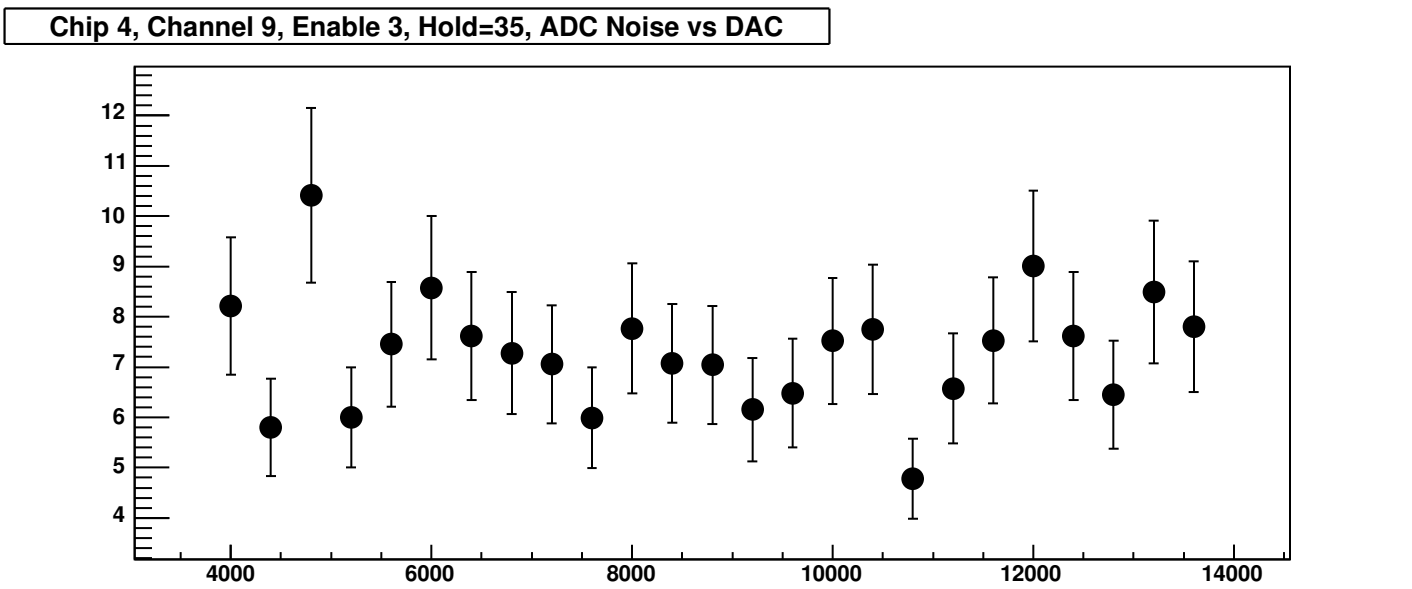


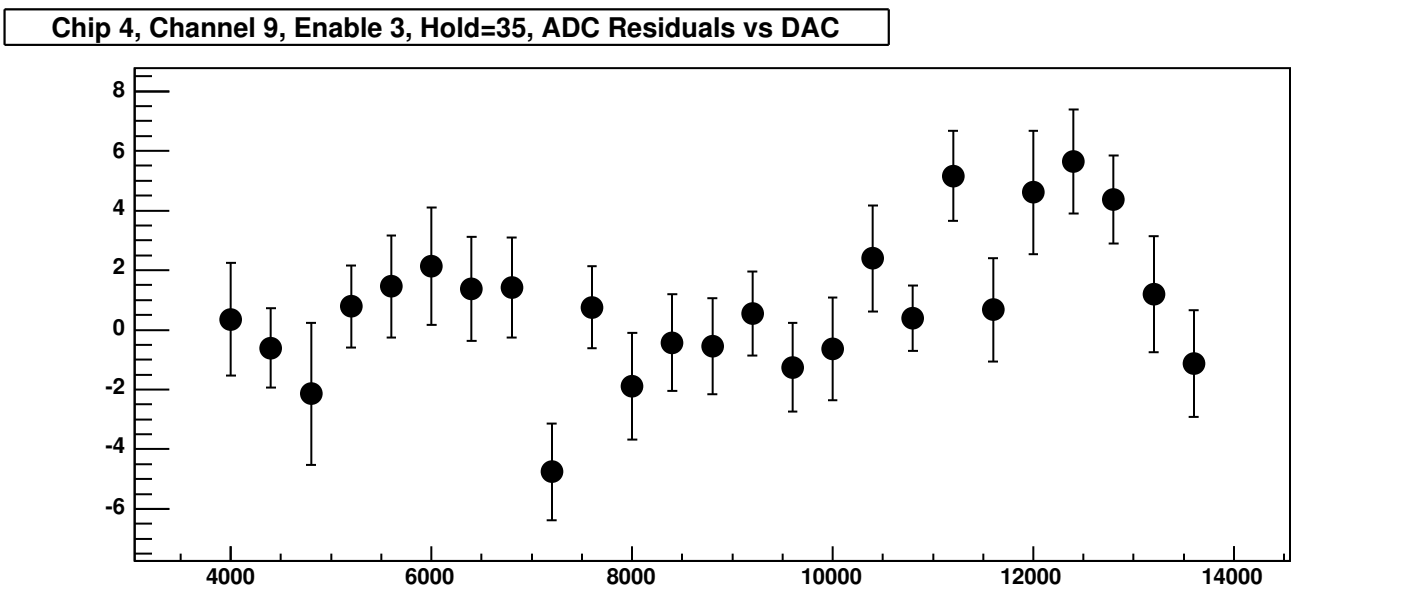

Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.54 / 16
p0 265.2 ± 1.3
p1 0.01145 ± 0.0001698
400
380
360
340
320
4000
6000
8000
10000
12000
14000

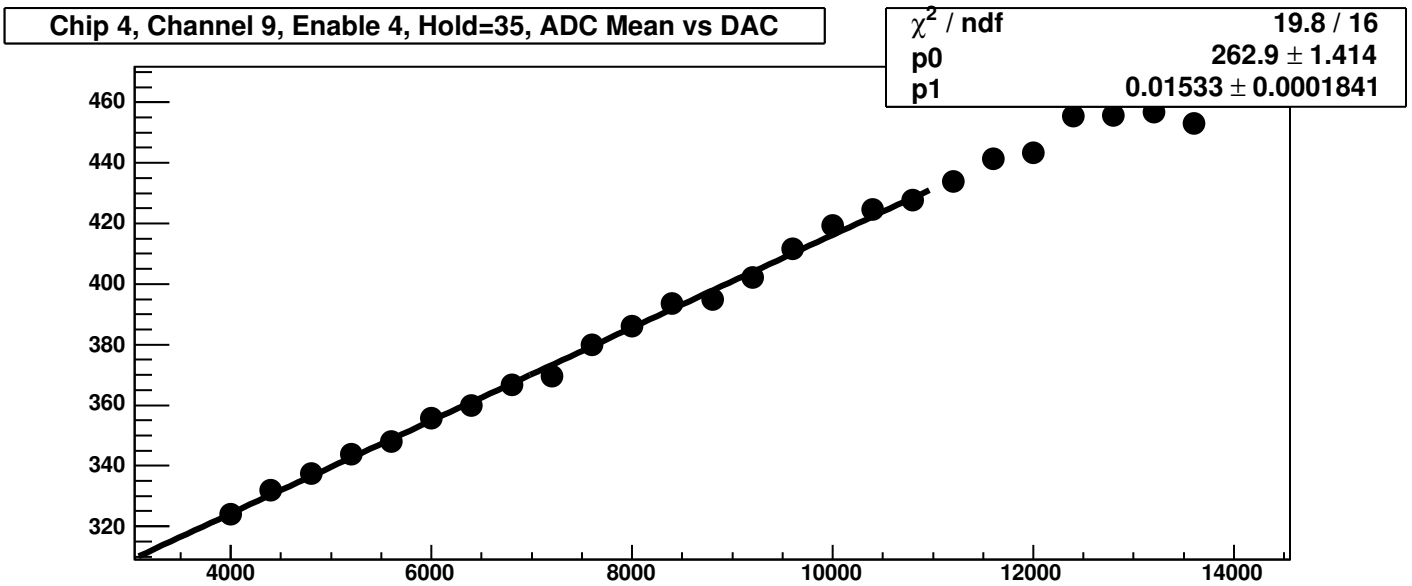

Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

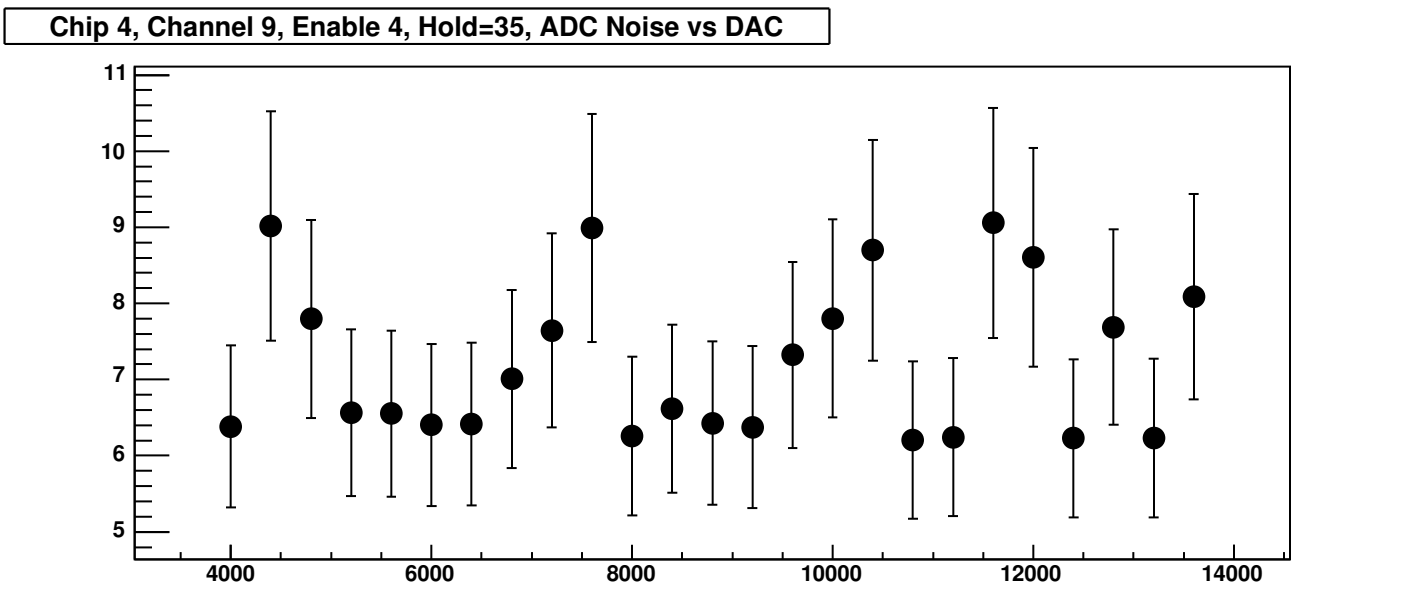

Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
0
-20
-40
-60
-80
4000
6000
8000
10000
12000
14000

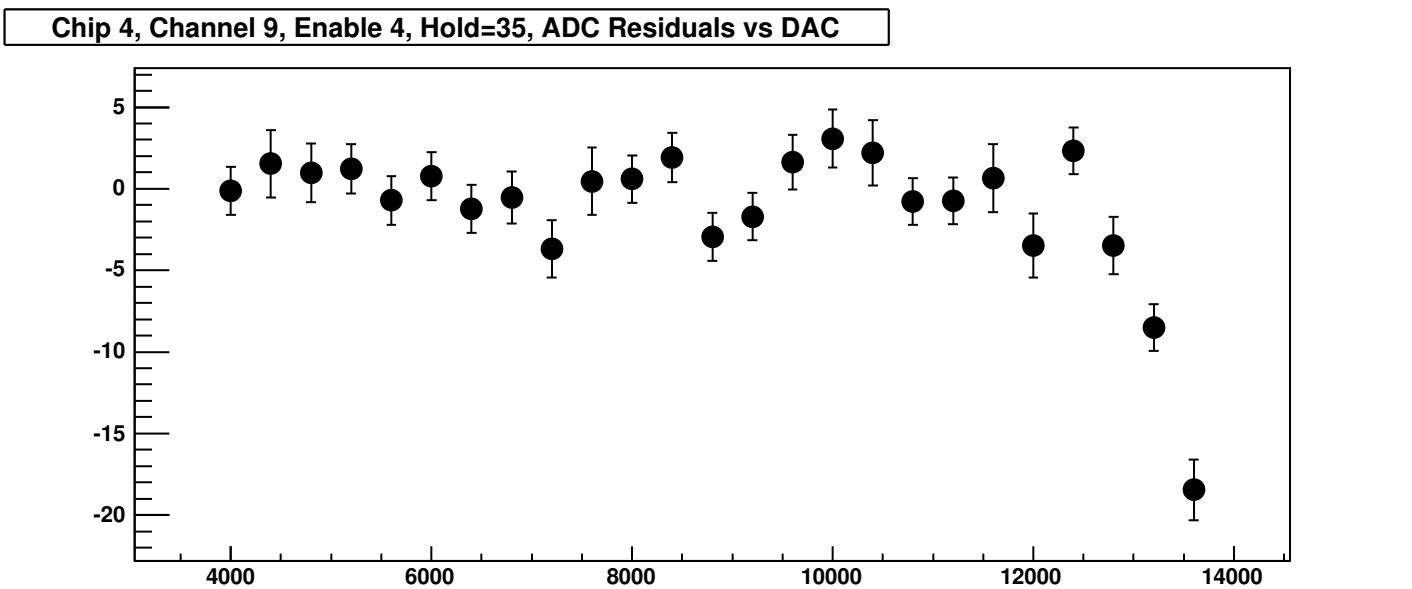

Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.8 / 16
p0 264.7 ± 1.375
p1 0.01701 ± 0.0001729
500
480
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000

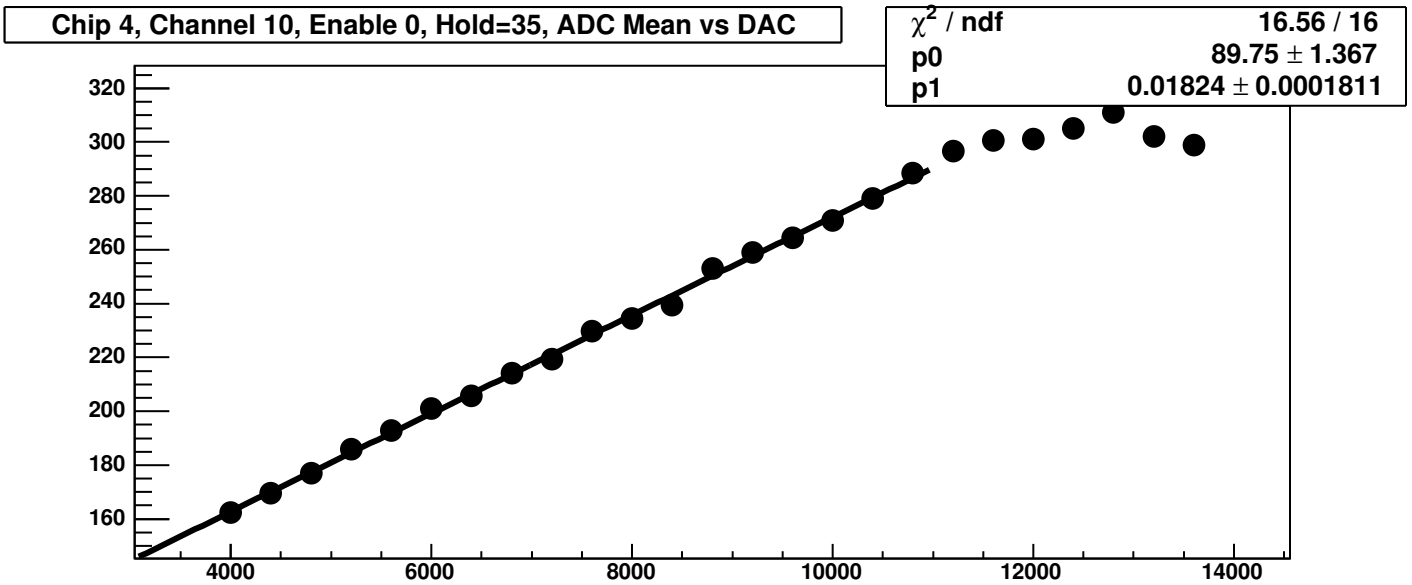

Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

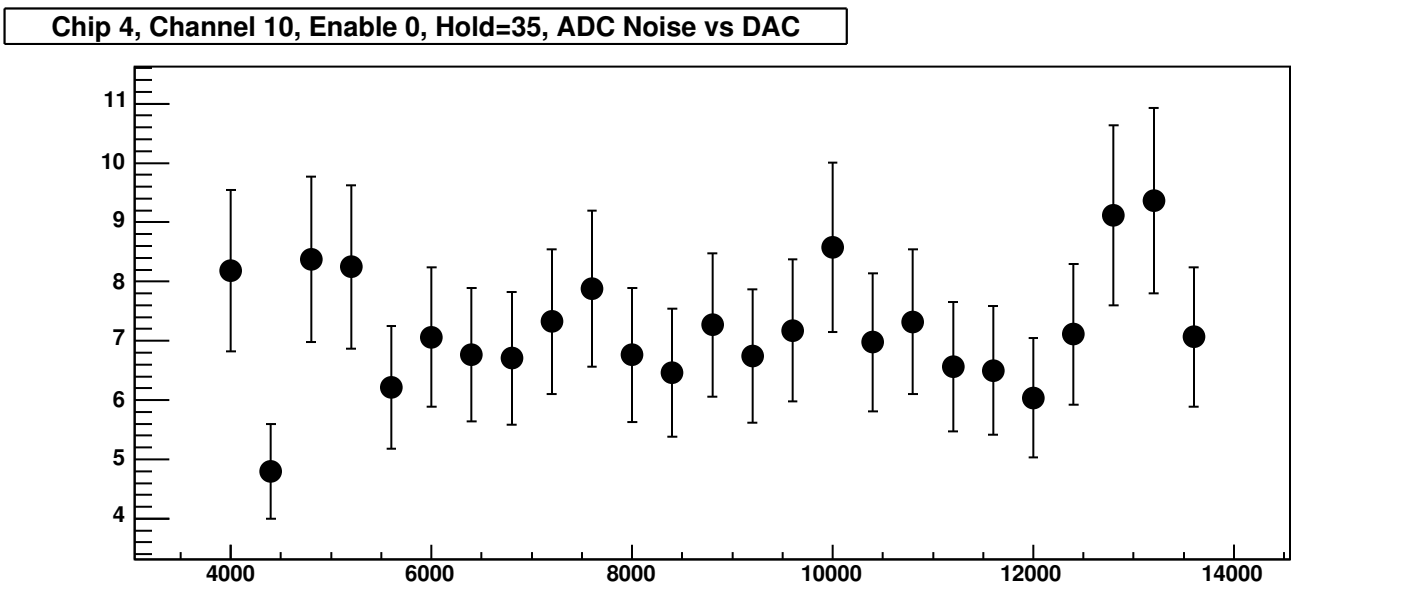

Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

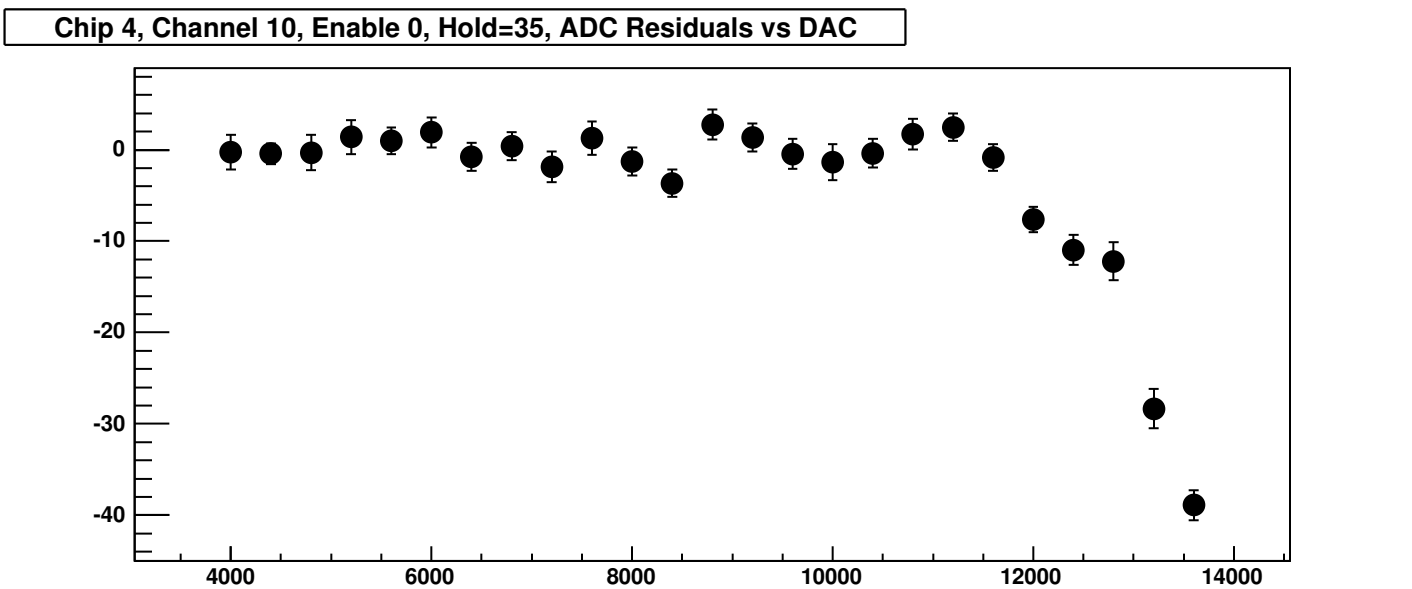

Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.8 / 16
p0 262.9 ± 1.414
p1 0.01533 ± 0.0001841
460
440
420
400
380
360
340
320
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

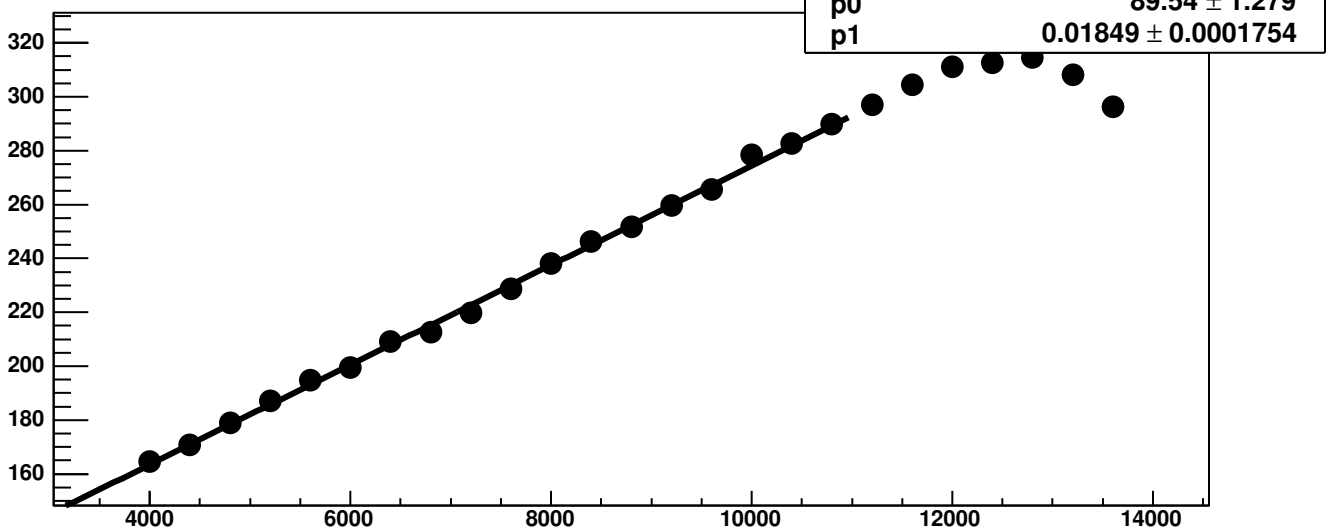

Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 16.56 / 16
p0 89.75 ± 1.367
p1 0.01824 ± 0.0001811
320
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

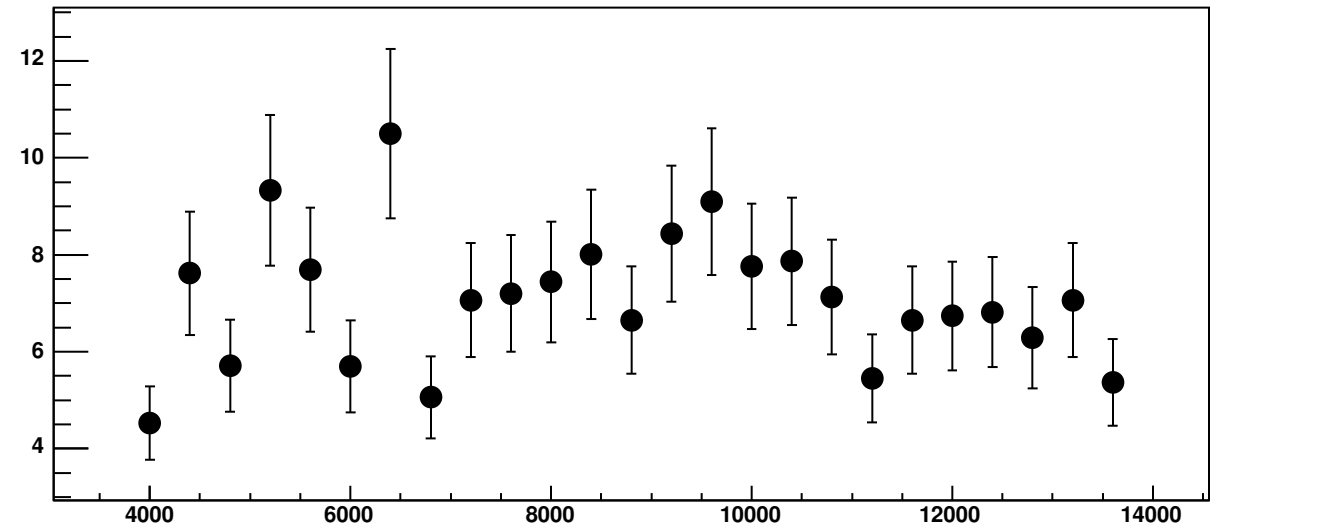

Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

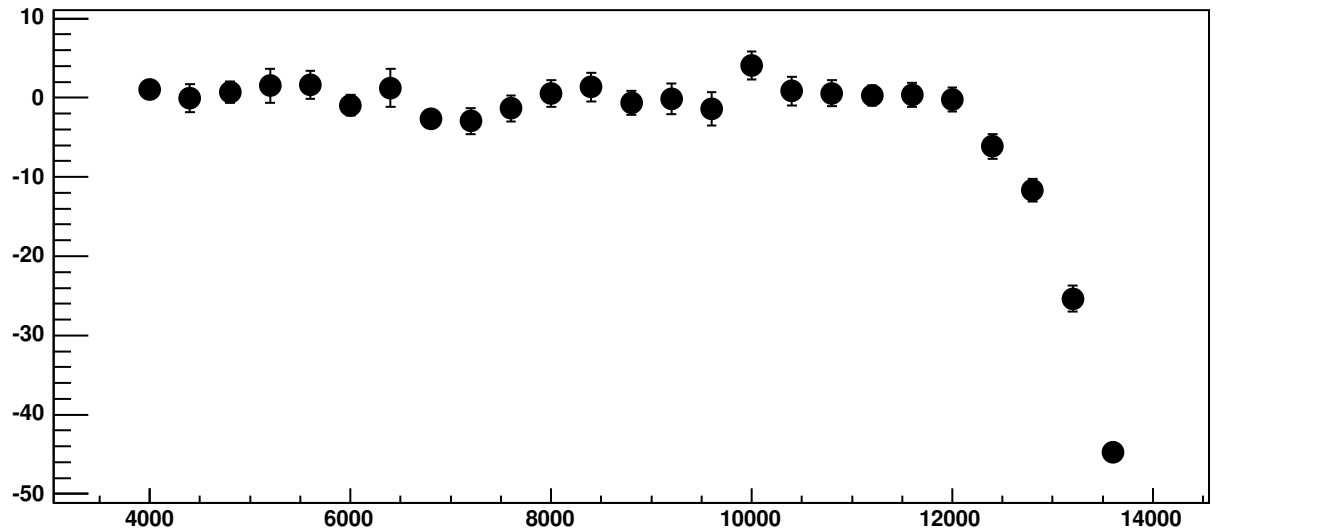
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

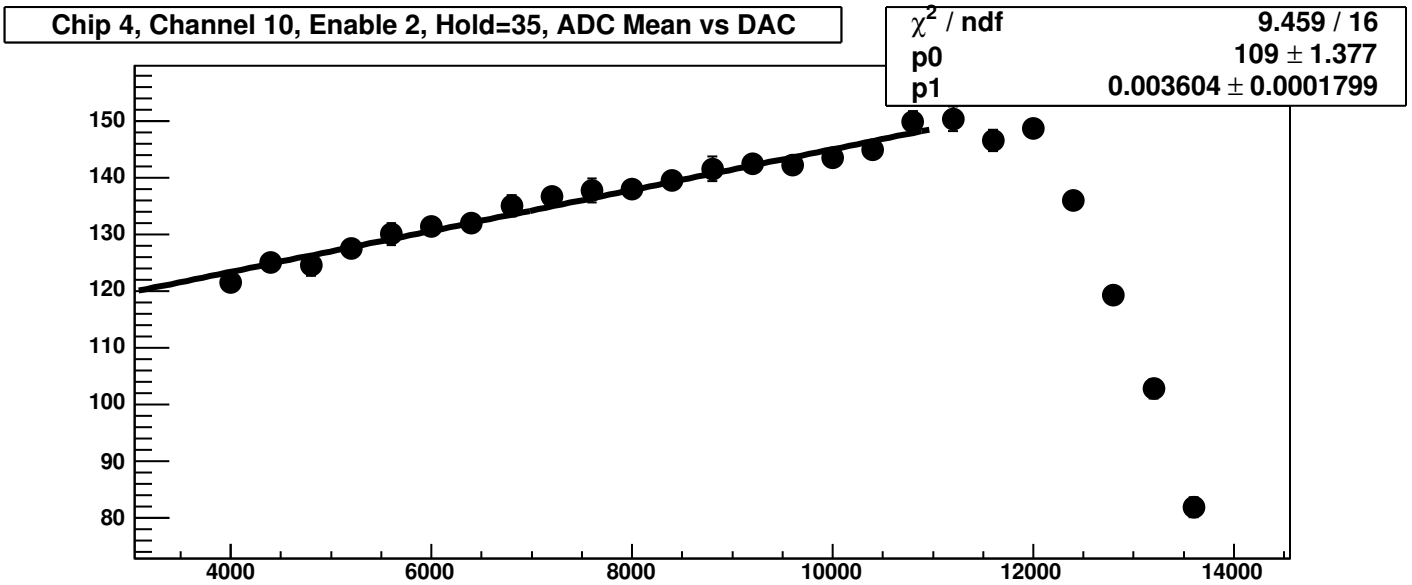


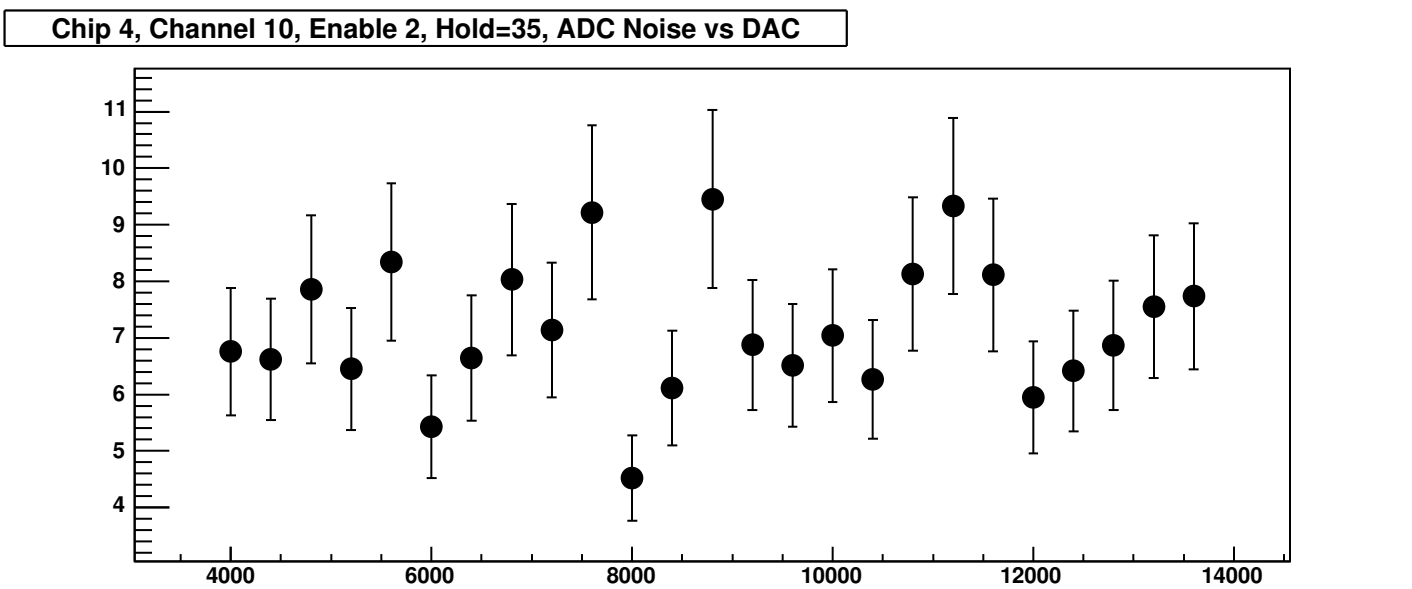
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

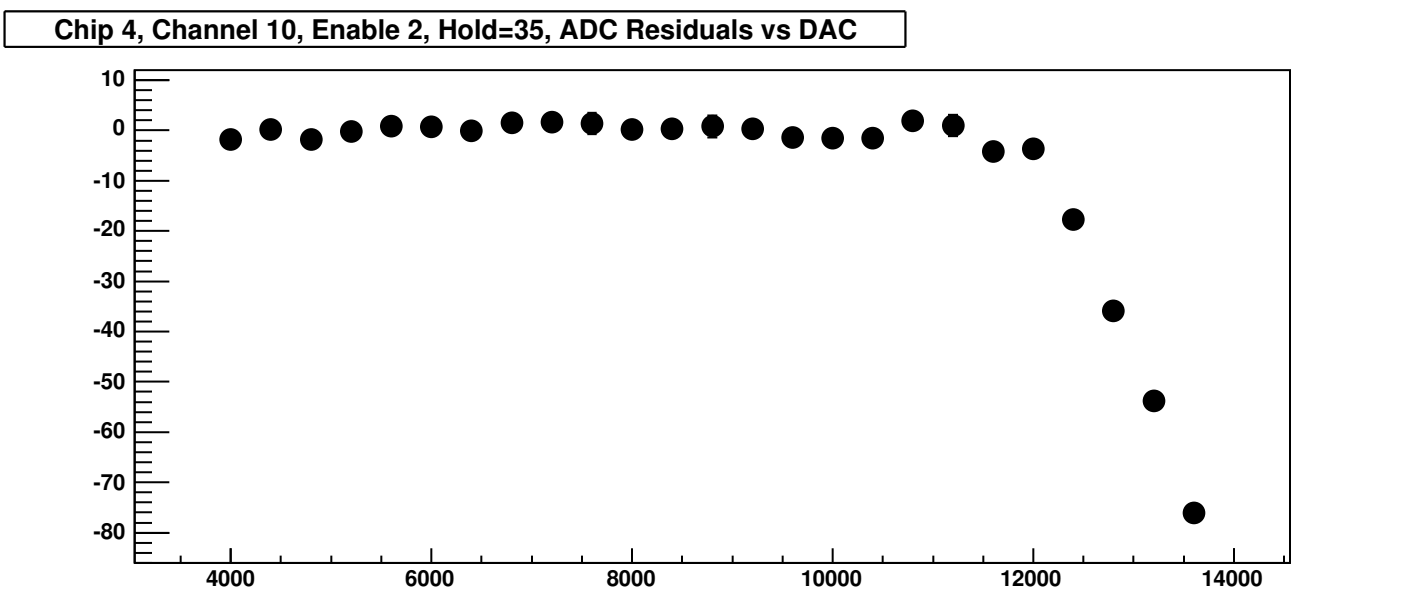


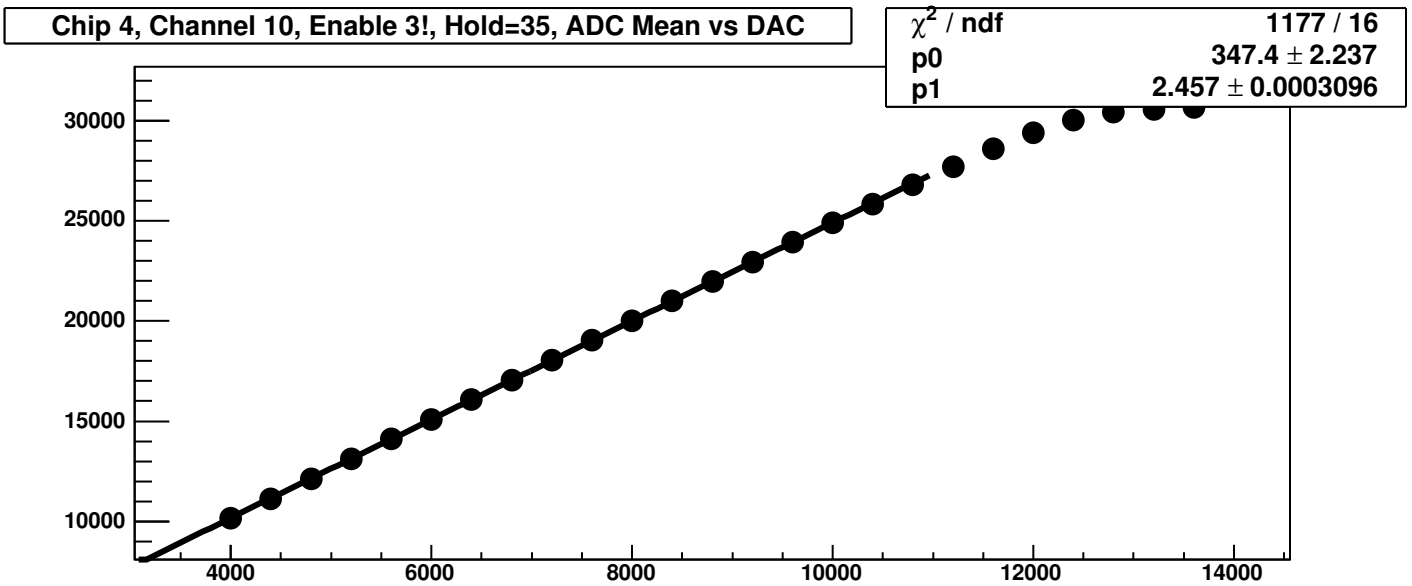
Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

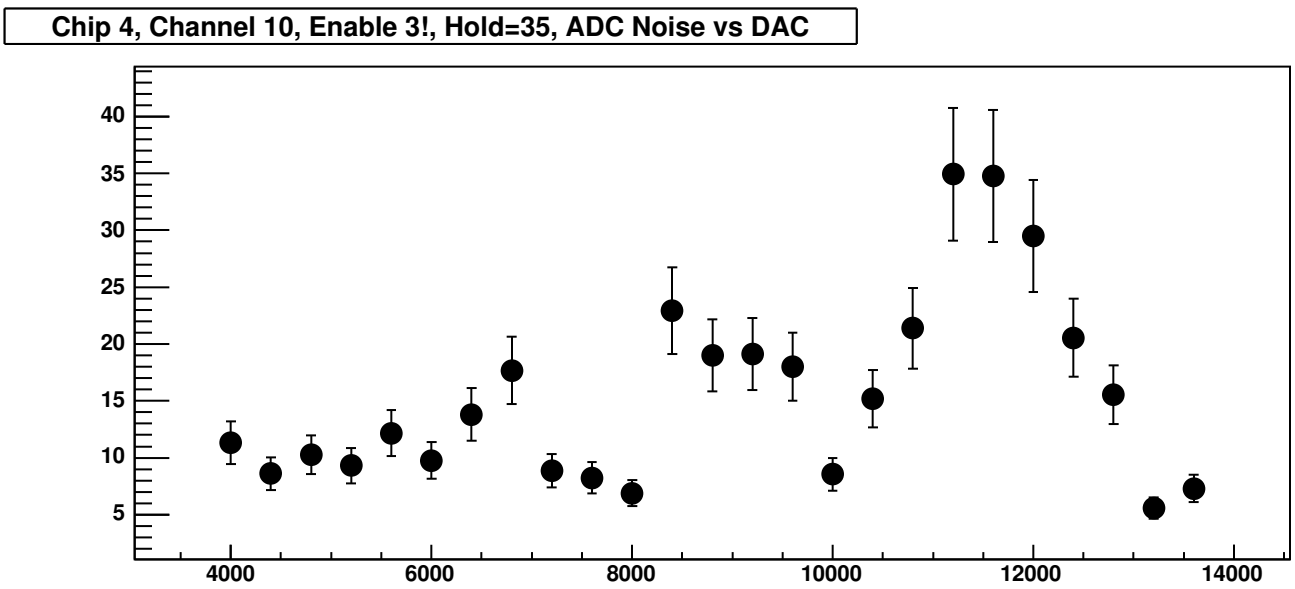


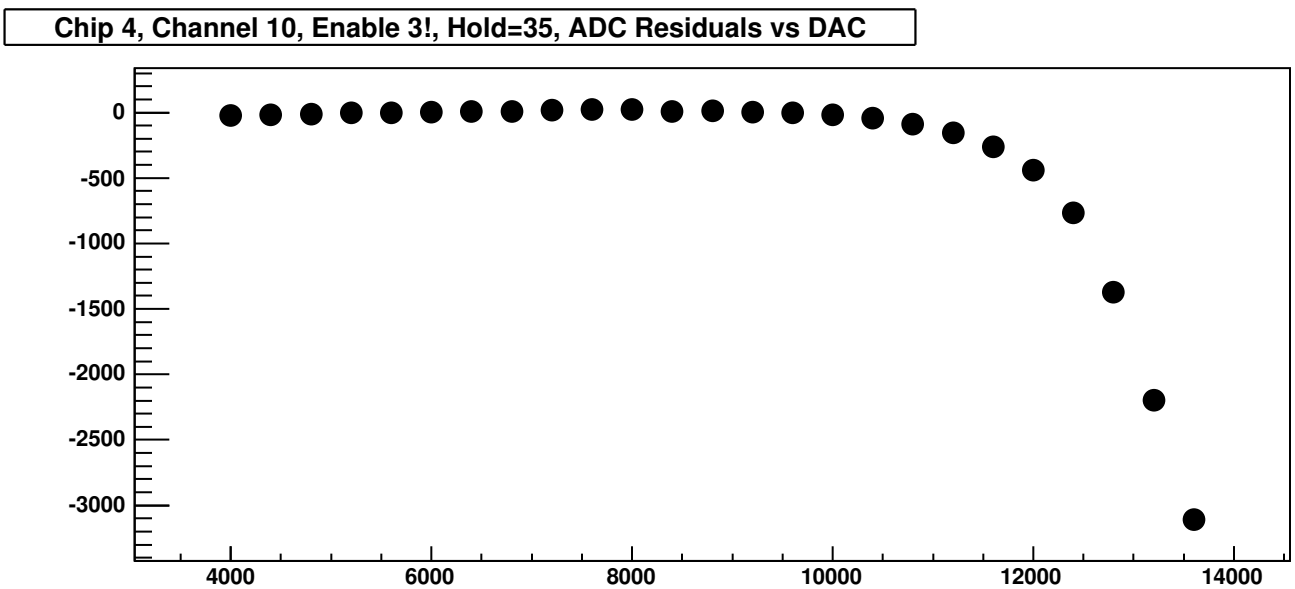

Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 9.459 / 16
p0 109 ± 1.377
p1 0.003604 ± 0.0001799
150
140
130
120
110
100
90
80
4000
6000
8000
10000
12000
14000

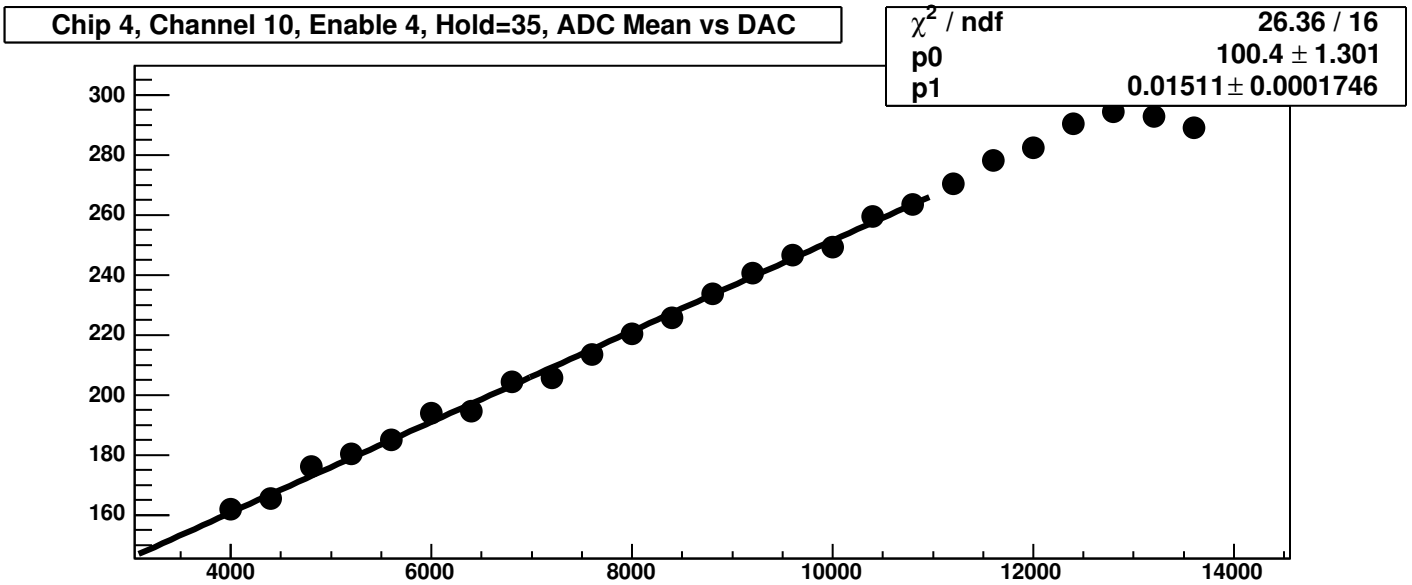

Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

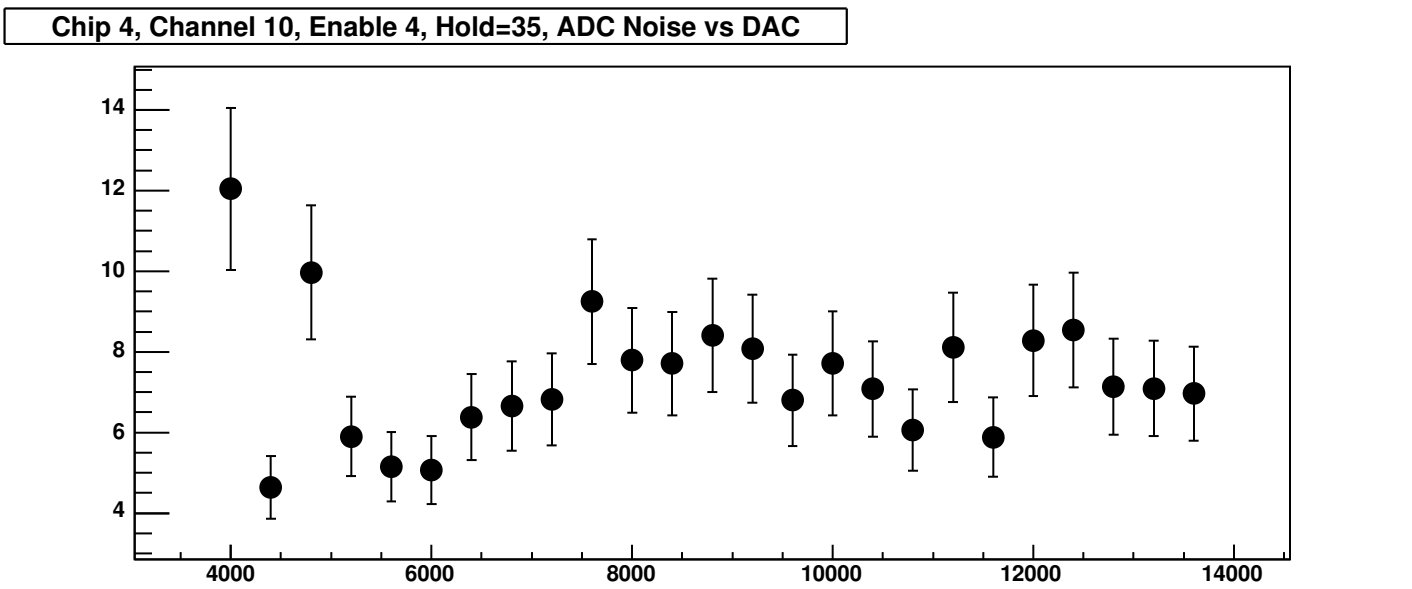

Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
-80
4000
6000
8000
10000
12000
14000

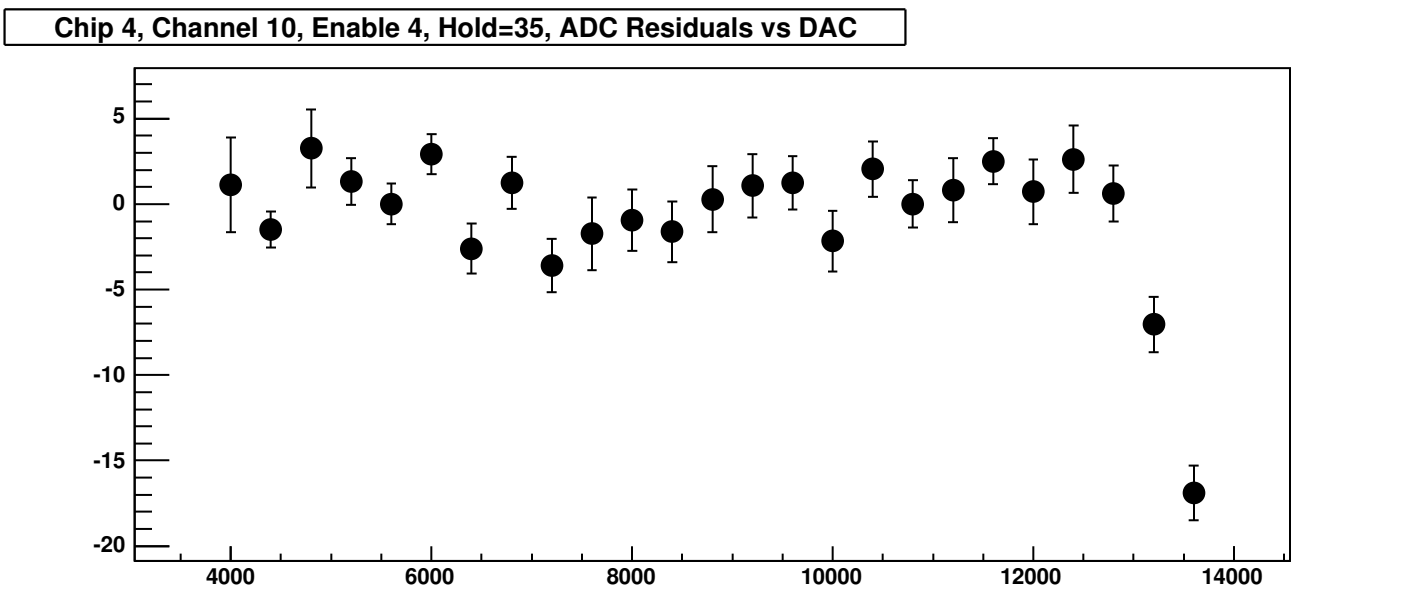

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1177 / 16
p0 347.4 ± 2.237
p1 2.457 ± 0.0003096
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

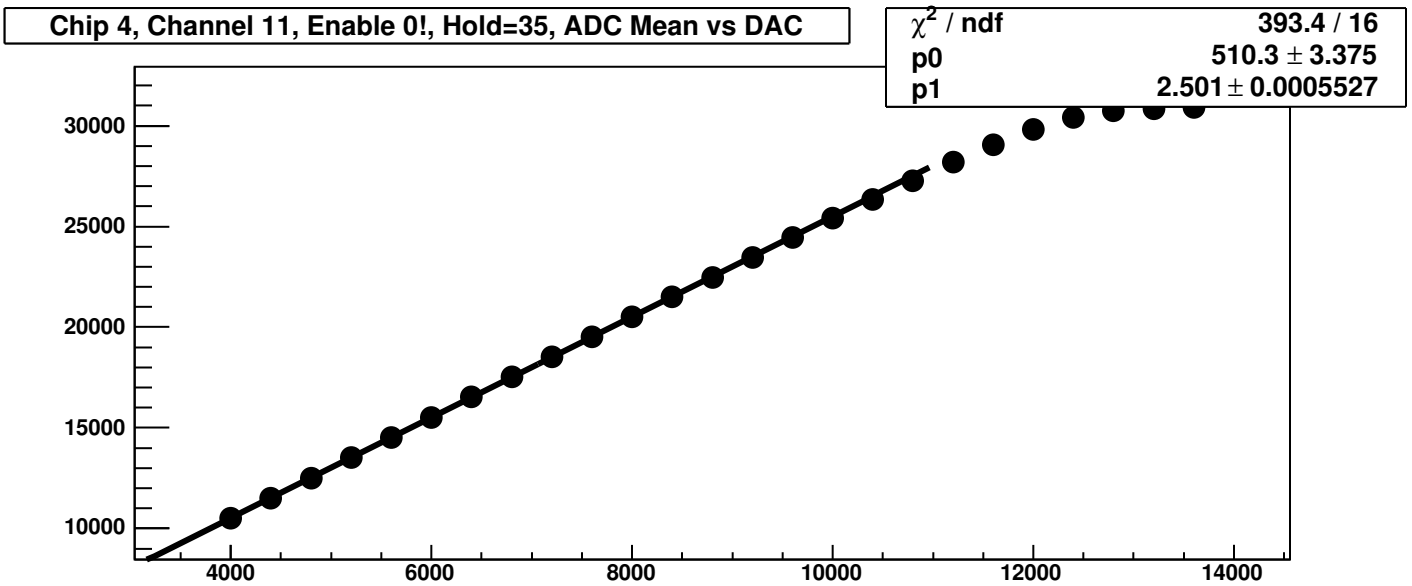

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

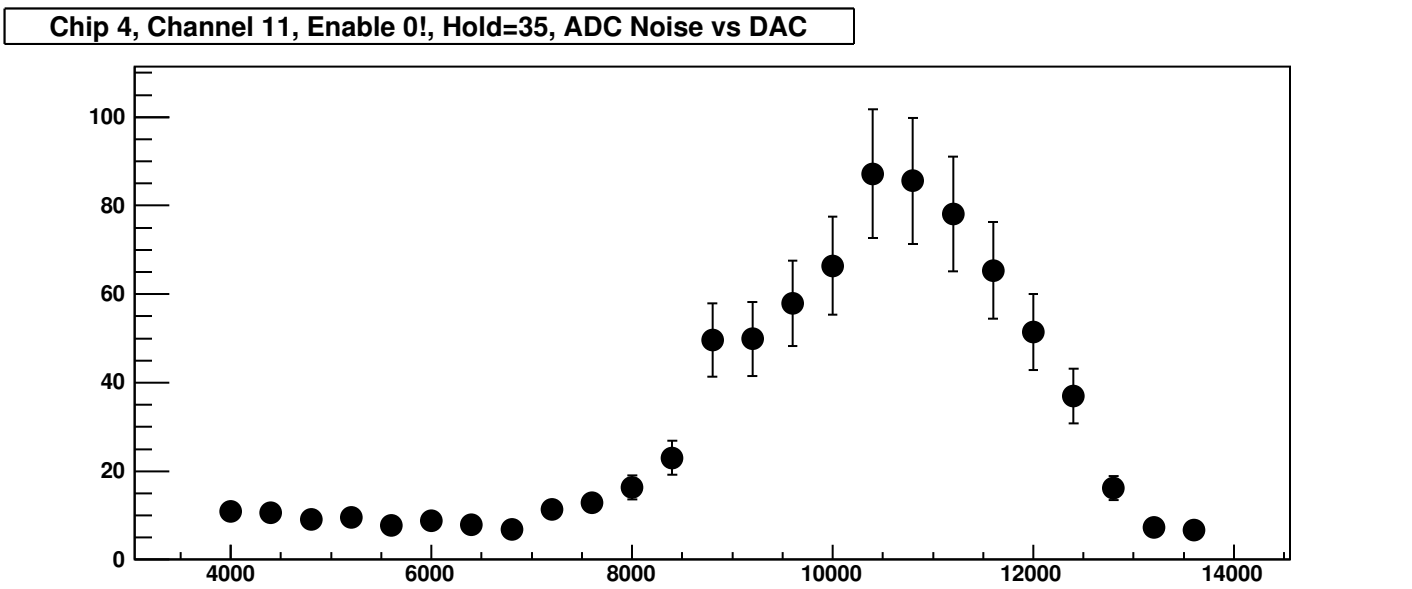

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
4000
6000
8000
10000
12000
14000

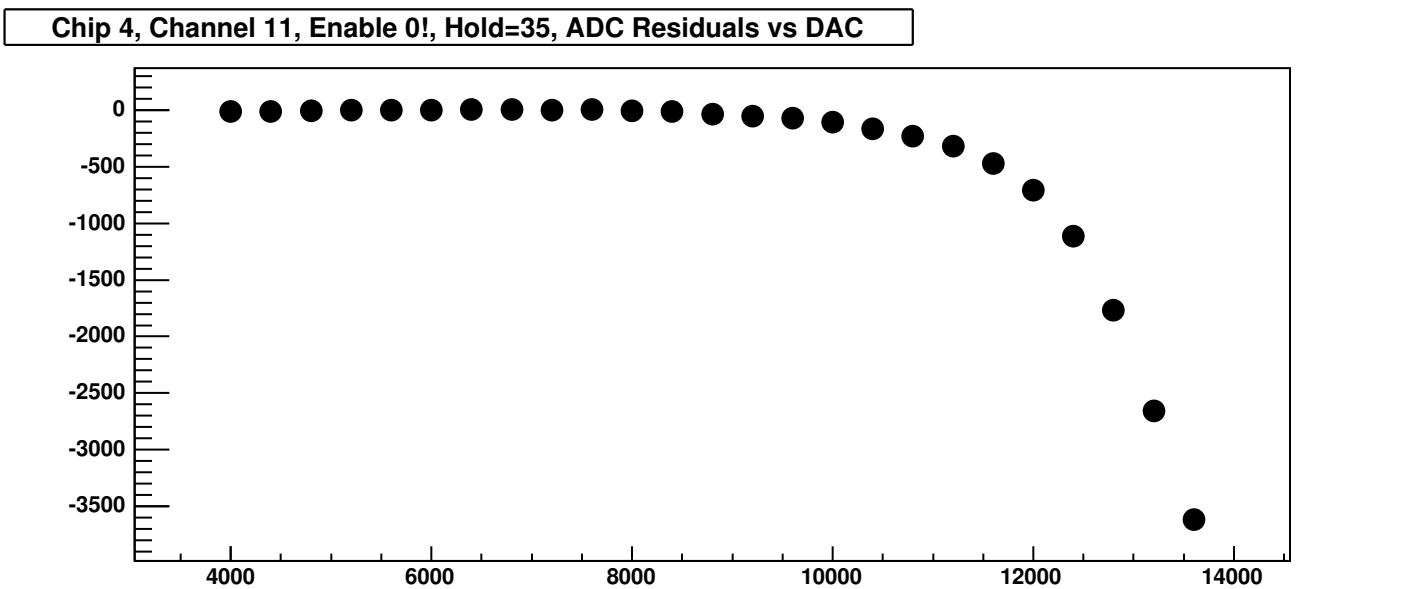

Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.36 / 16
p0 100.4 ± 1.301
p1 0.01511 ± 0.0001746
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

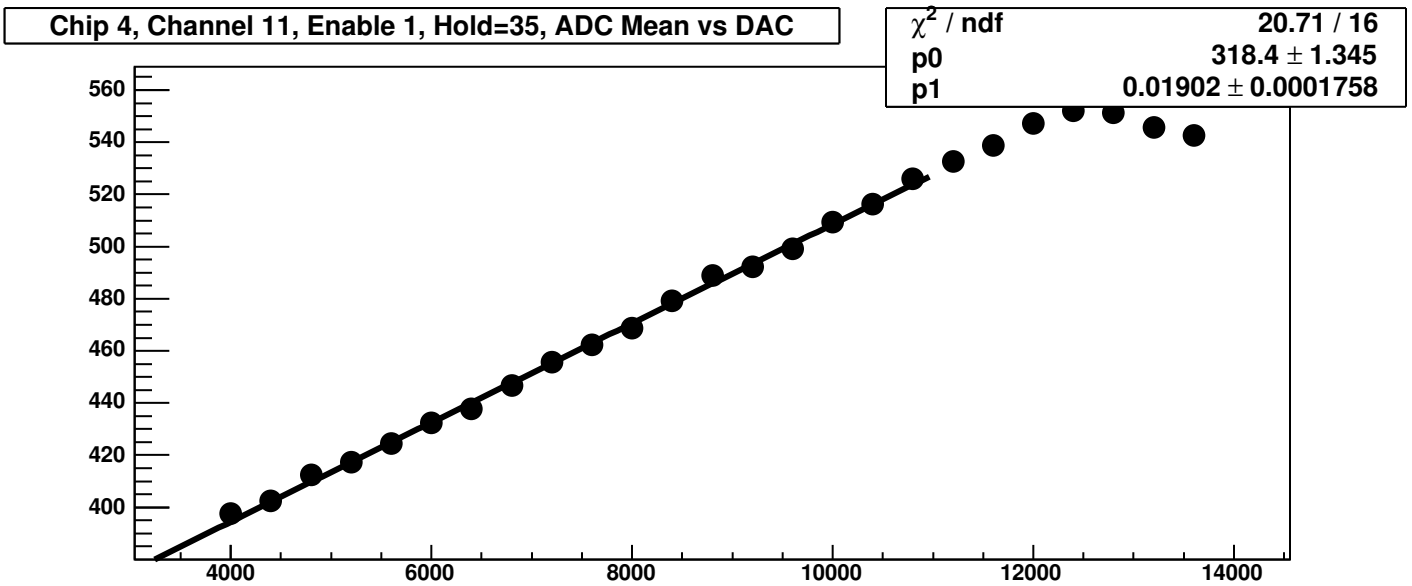

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

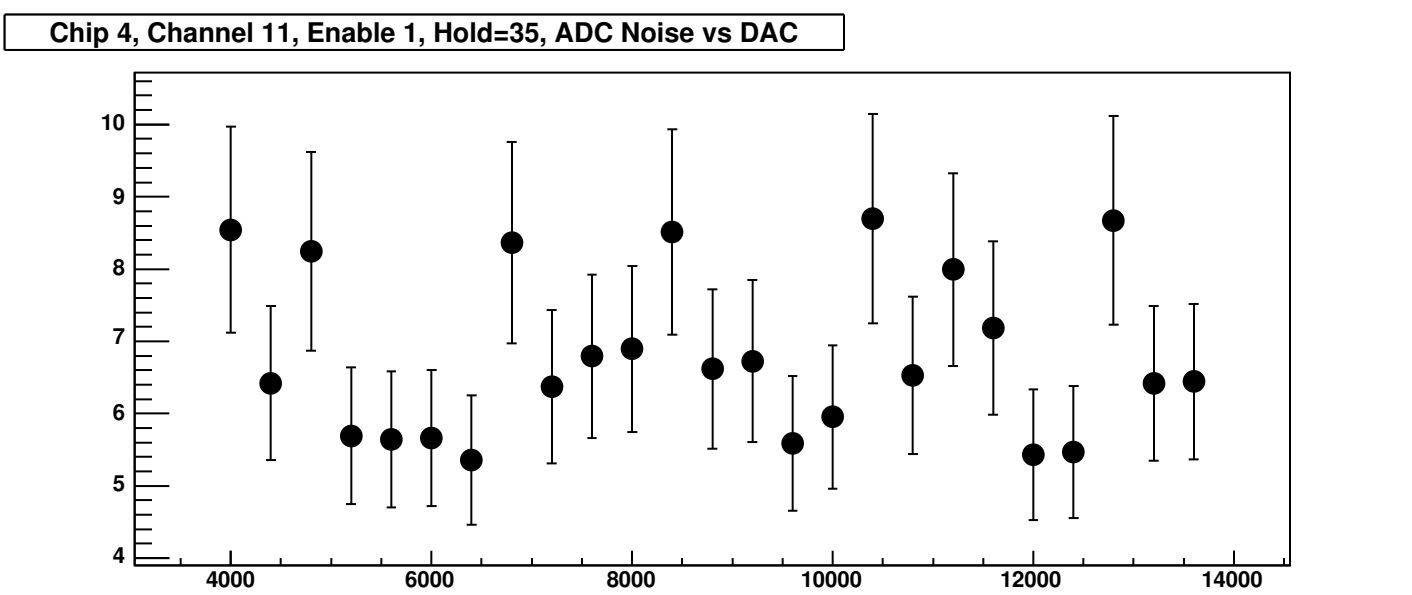

Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
4000
6000
8000
10000
12000
14000

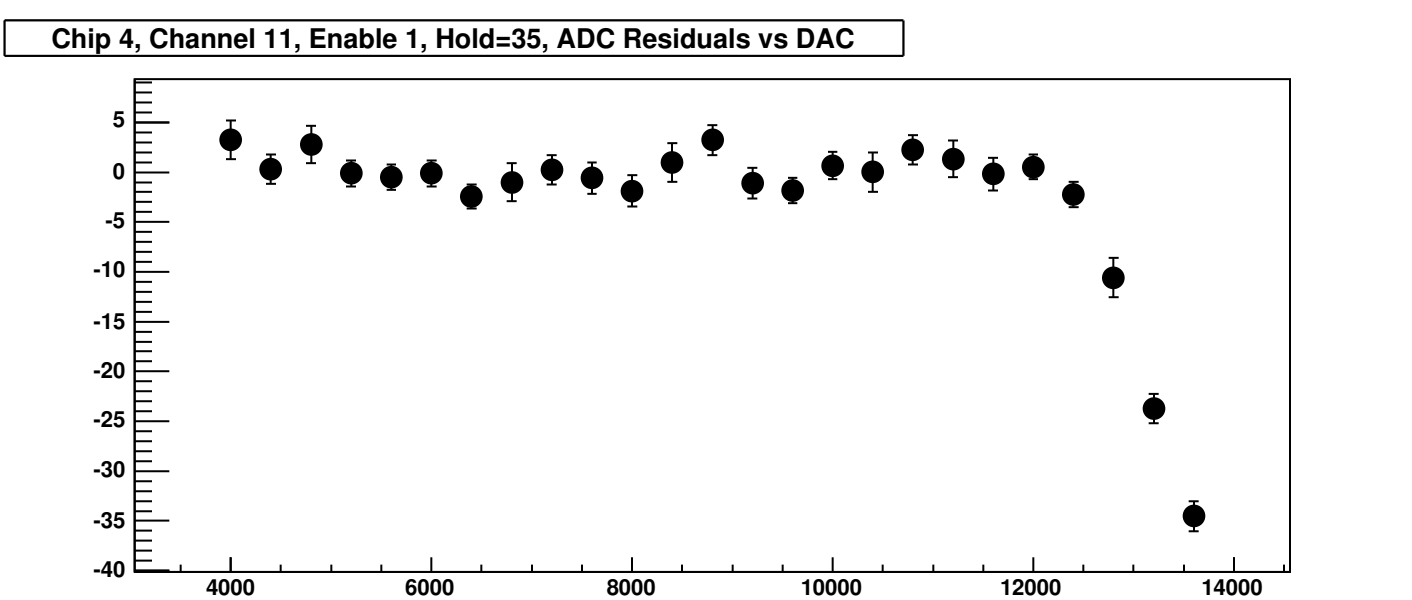

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 393.4 / 16
p0 510.3 ± 3.375
p1 2.501 ± 0.0005527
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

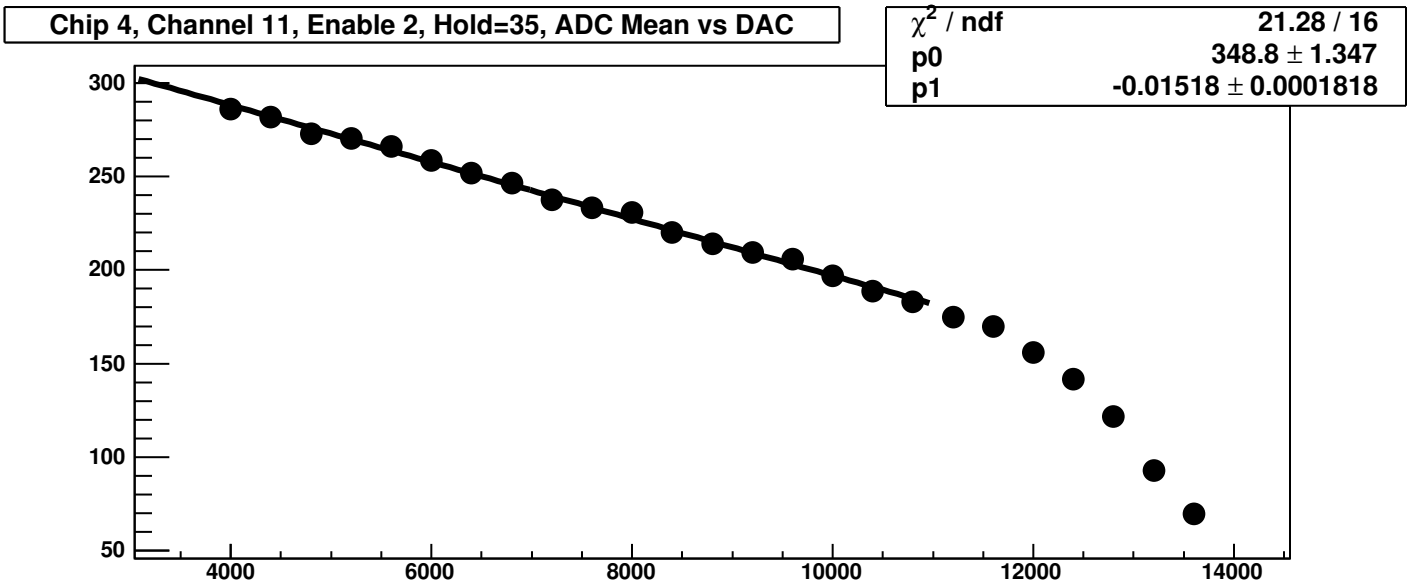

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

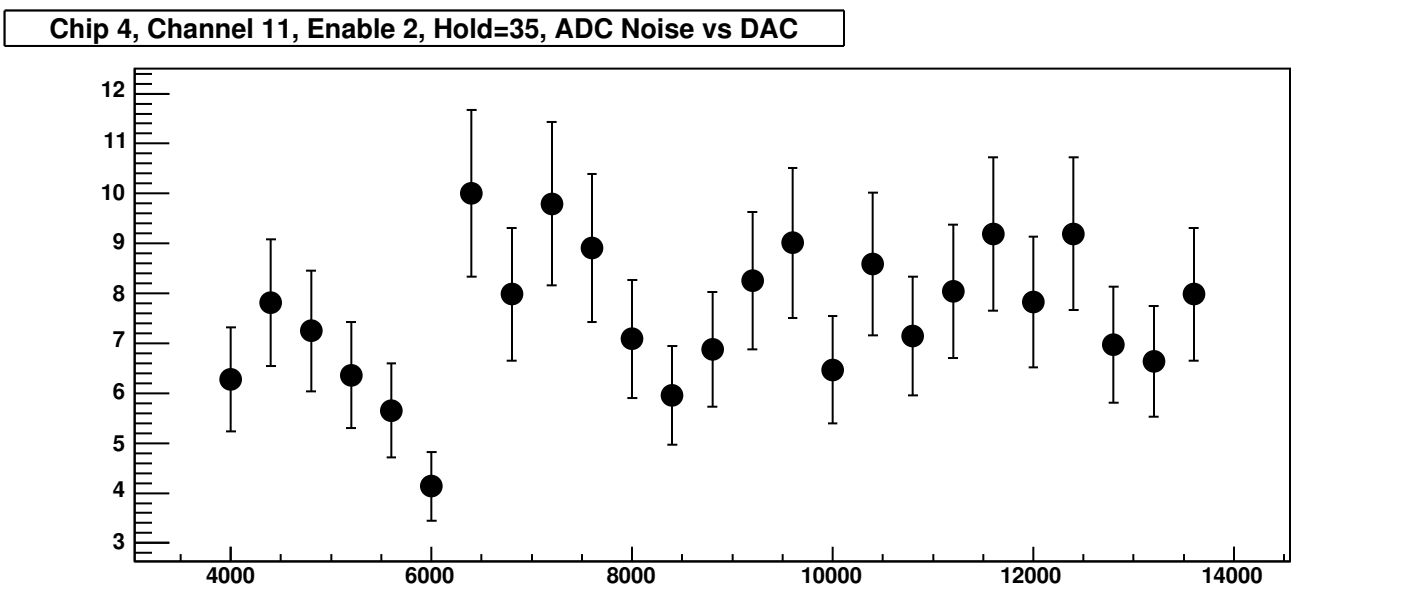

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

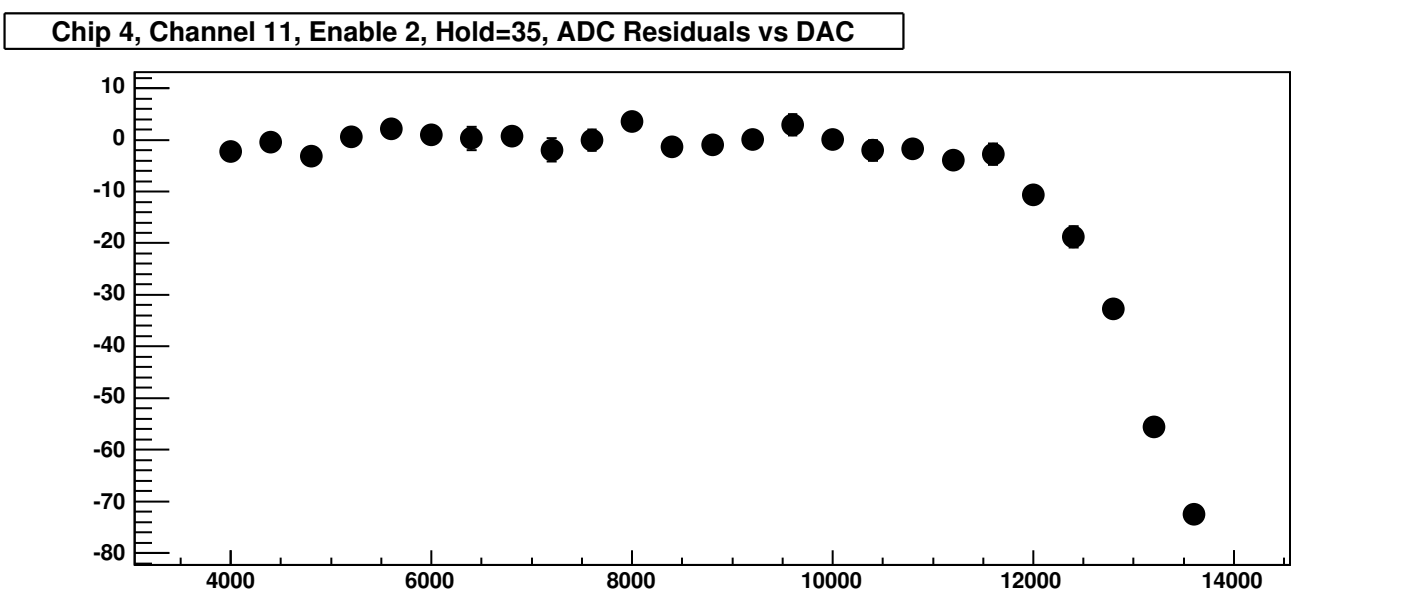

Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.71 / 16
p0 318.4 ± 1.345
p1 0.01902 ± 0.0001758
560
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

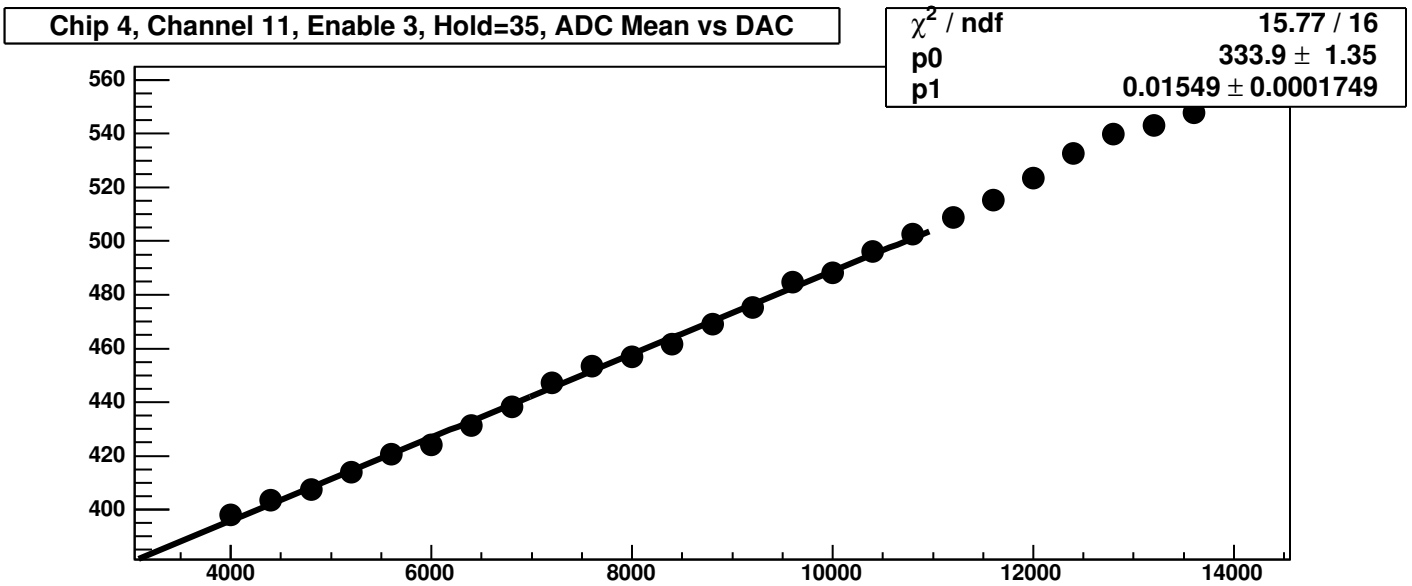

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

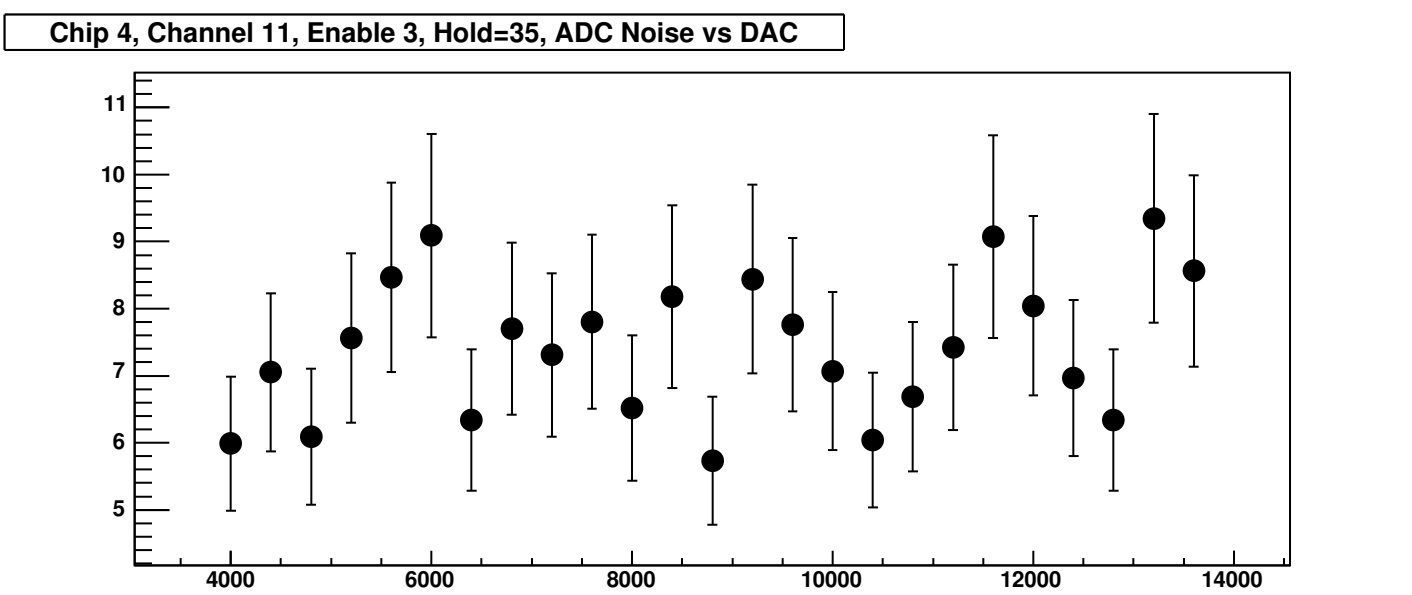

Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
-40
4000
6000
8000
10000
12000
14000

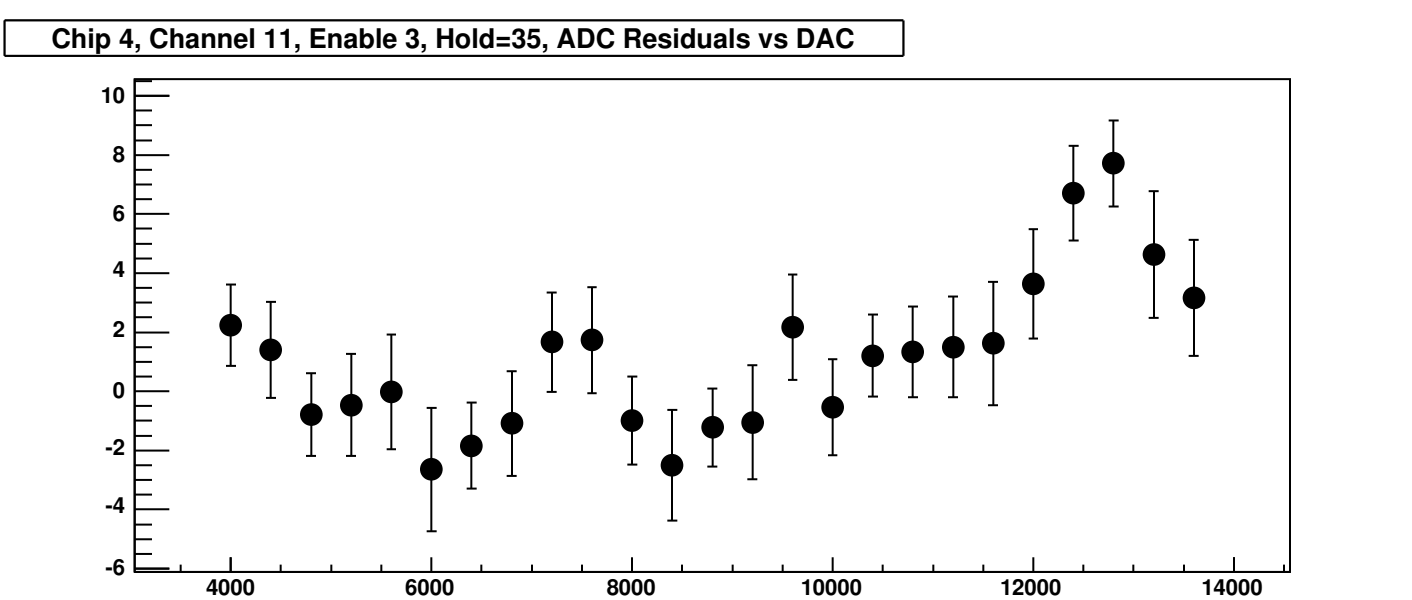

Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.28 / 16
p0 348.8 ± 1.347
p1 -0.01518 ± 0.0001818
300
250
200
150
100
50
4000
6000
8000
10000
12000
14000

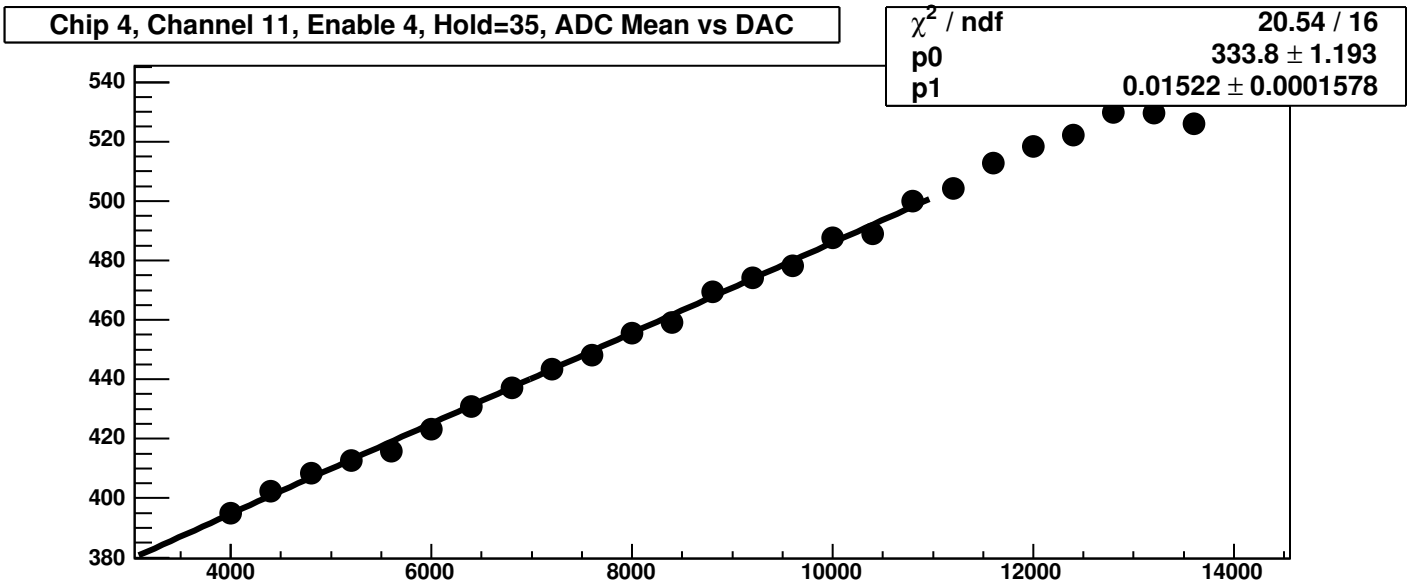

Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

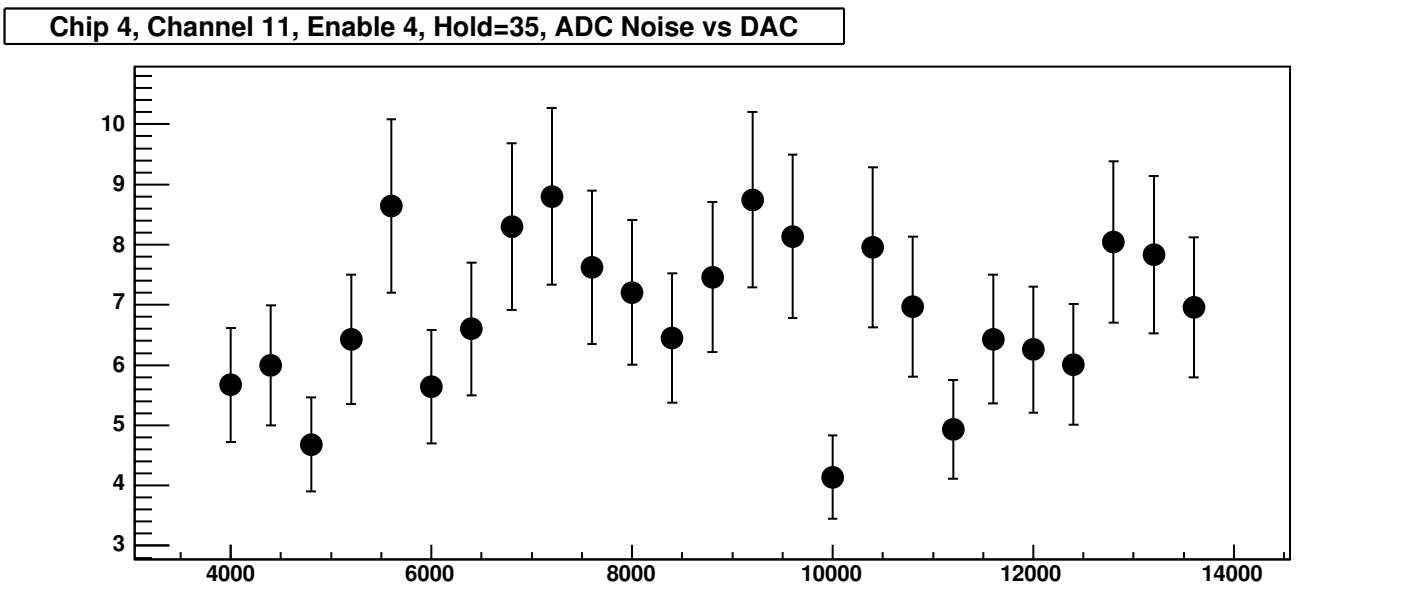

Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
-80
4000
6000
8000
10000
12000
14000

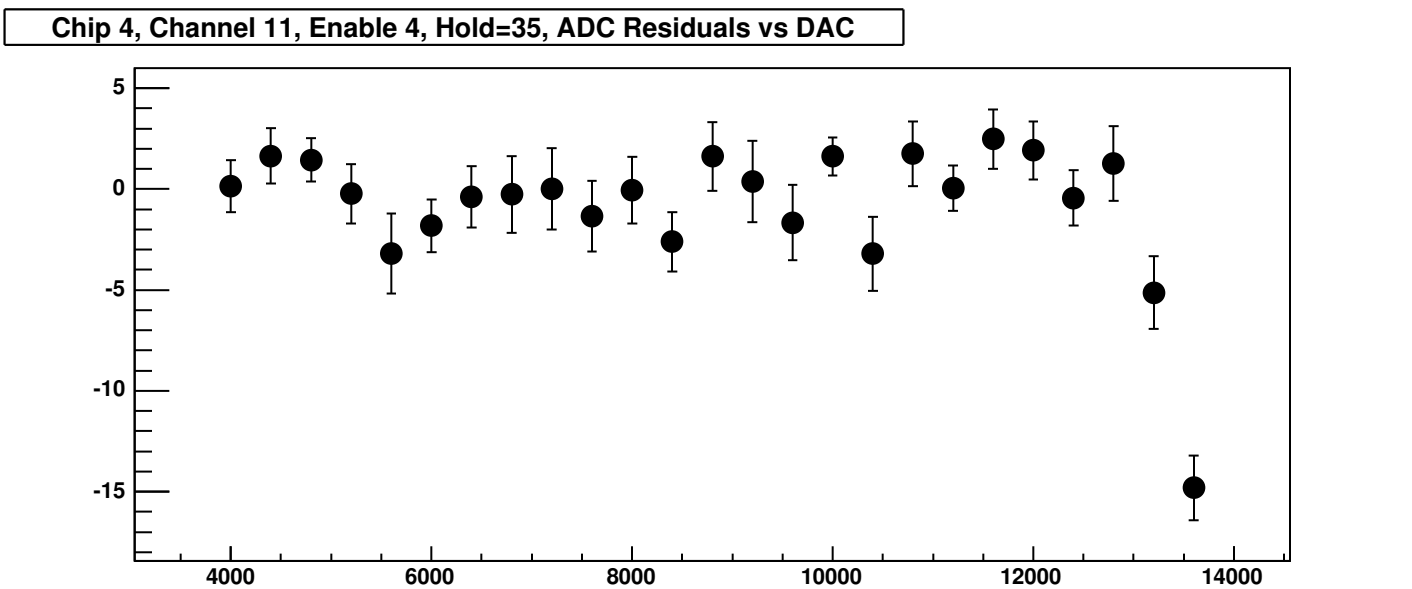

Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 15.77 / 16
p0 333.9 ± 1.35
p1 0.01549 ± 0.0001749
560
540
520
500
480
460
440
420
400
4000
6000
8000
10000
12000
14000

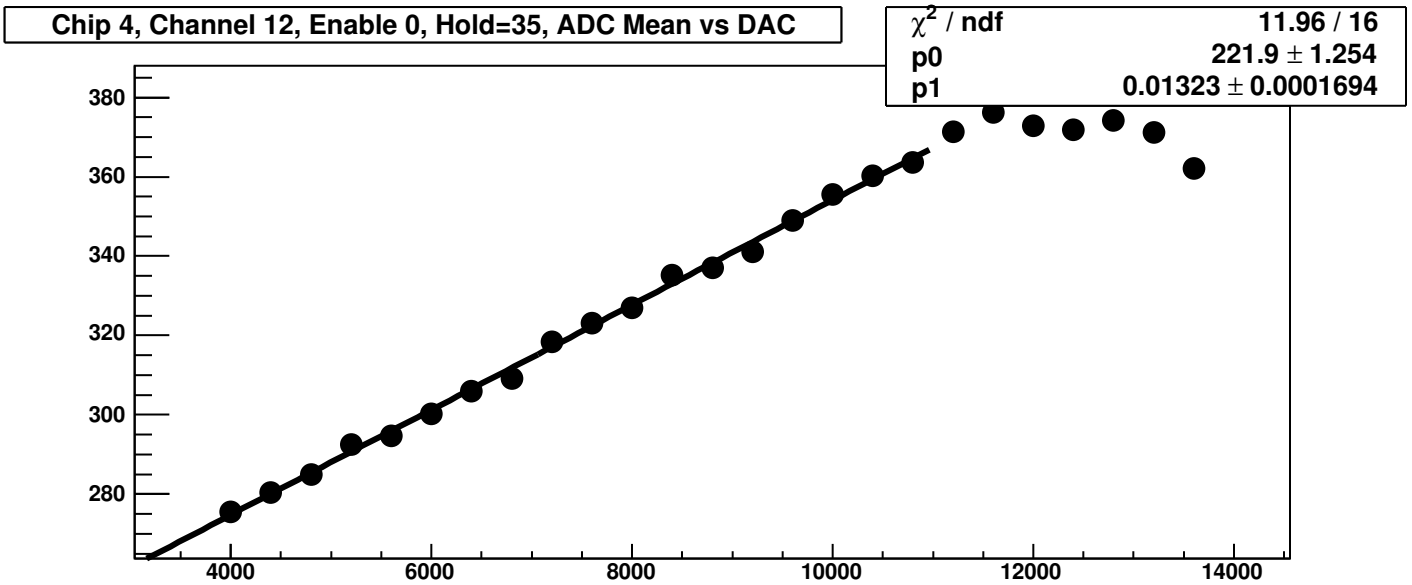

Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

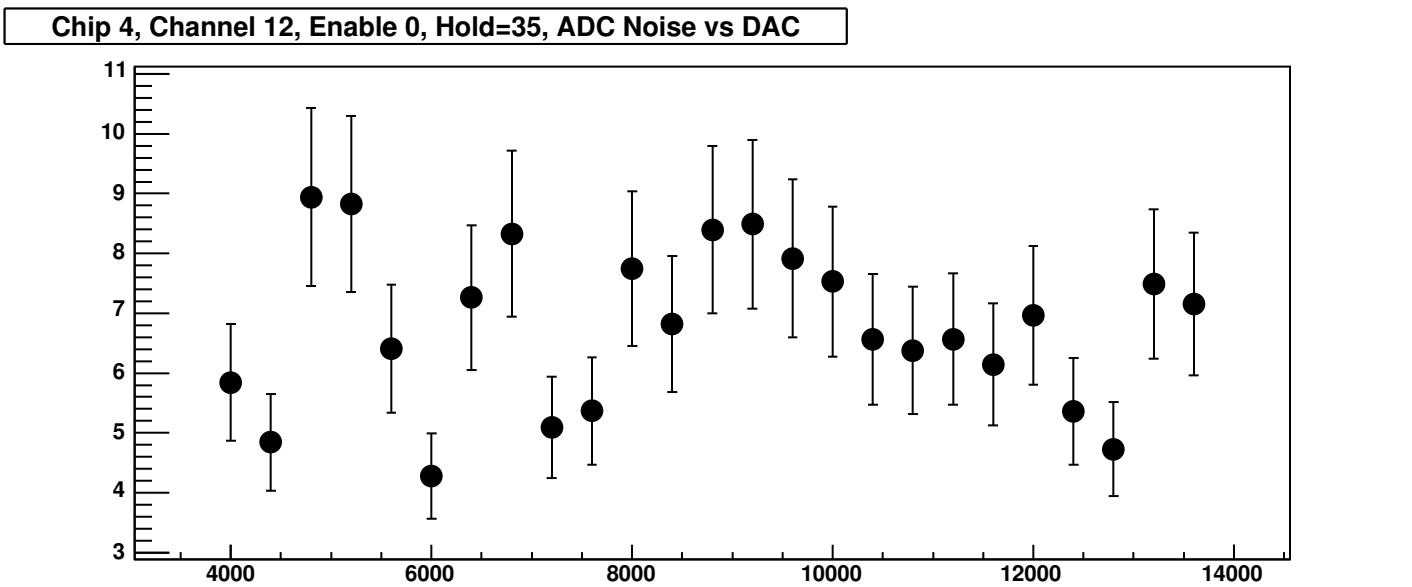

Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

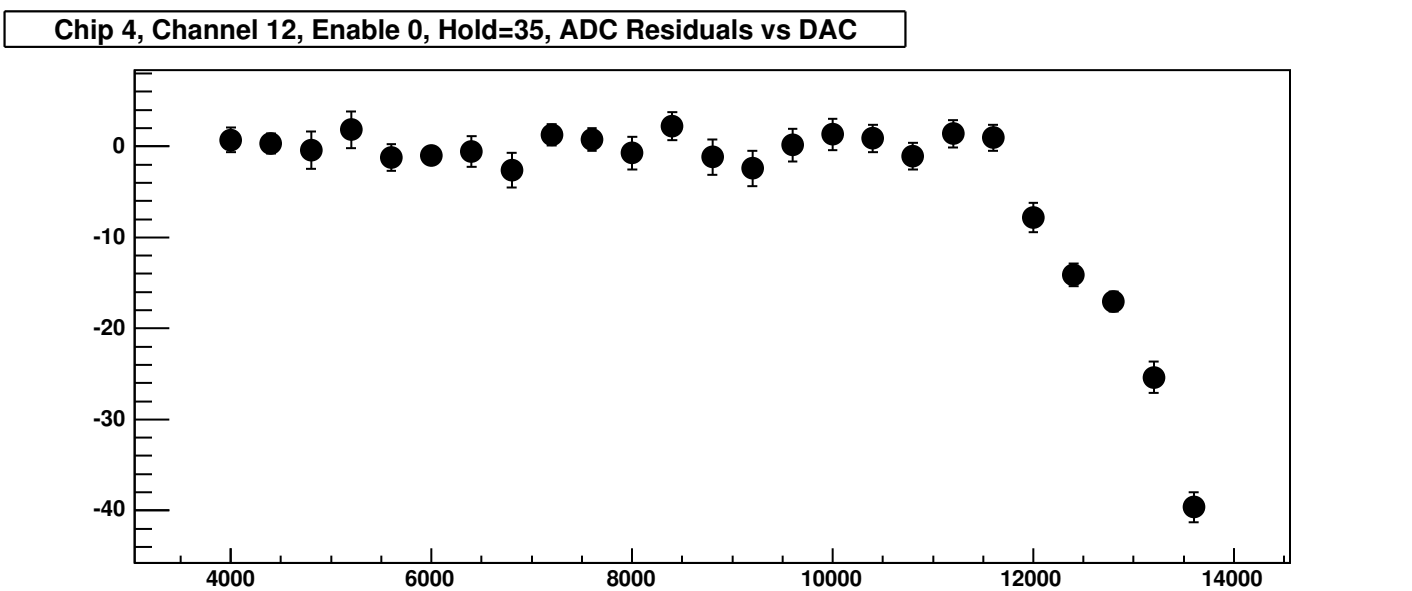

Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.54 / 16
p0 333.8 ± 1.193
p1 0.01522 ± 0.0001578
540
520
500
480
460
440
420
400
380
4000
6000
8000
10000
12000
14000

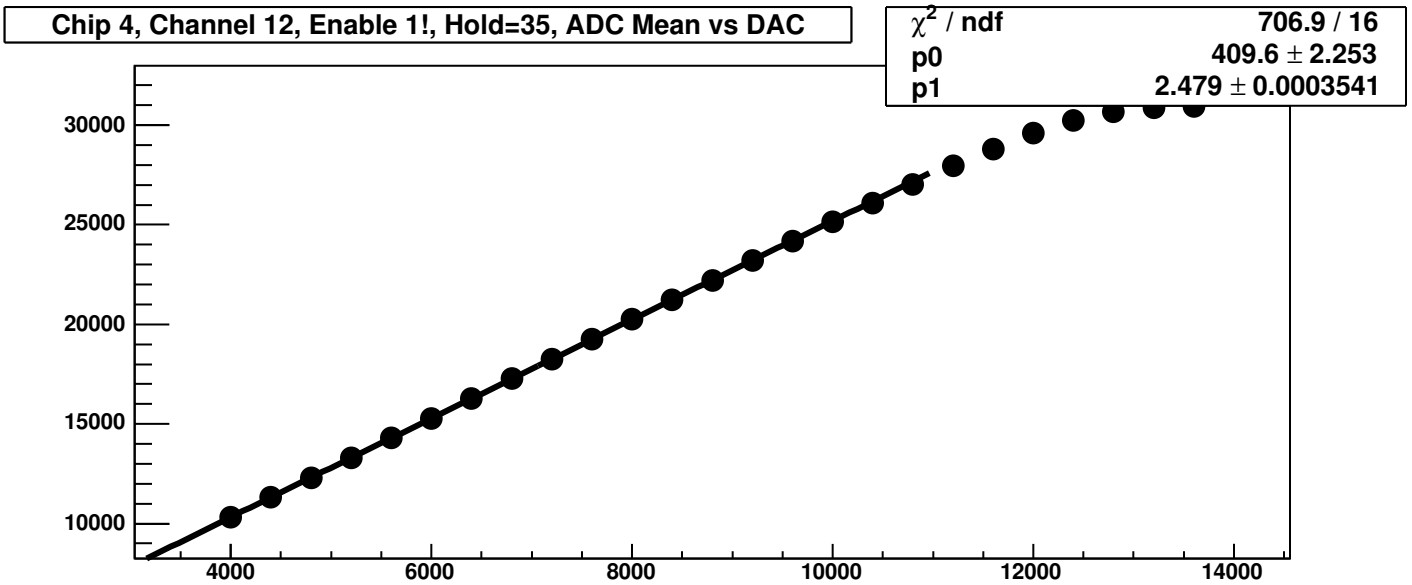

Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

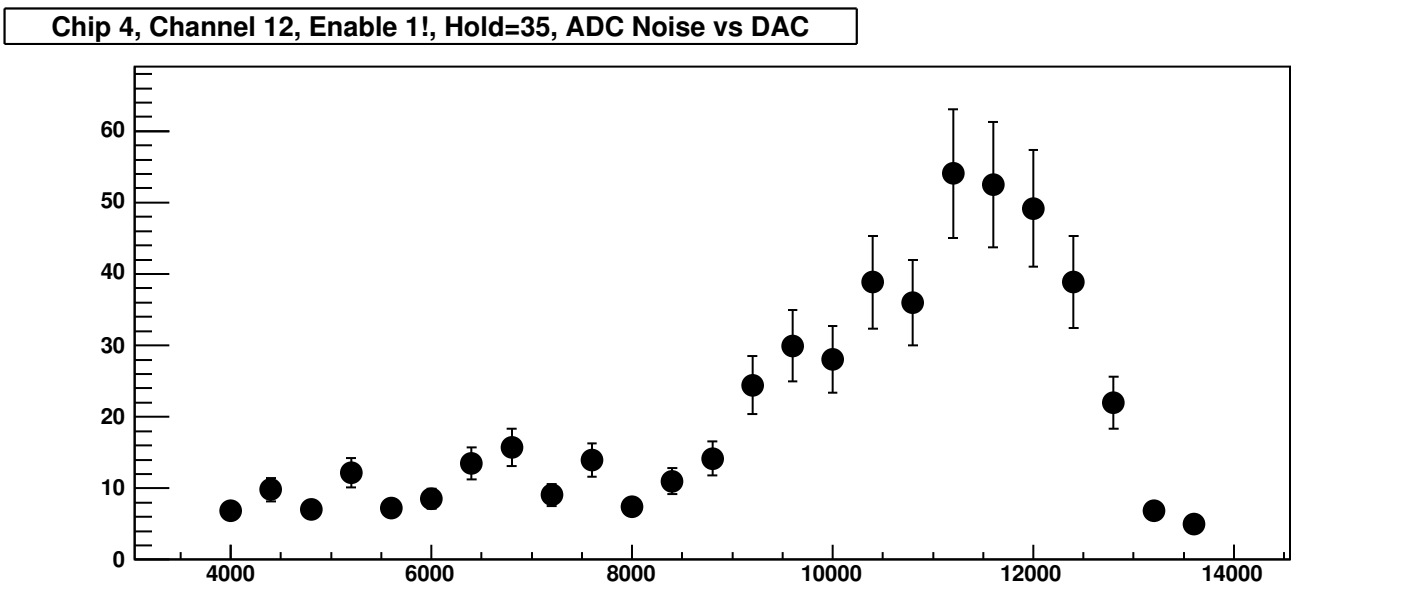

Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000

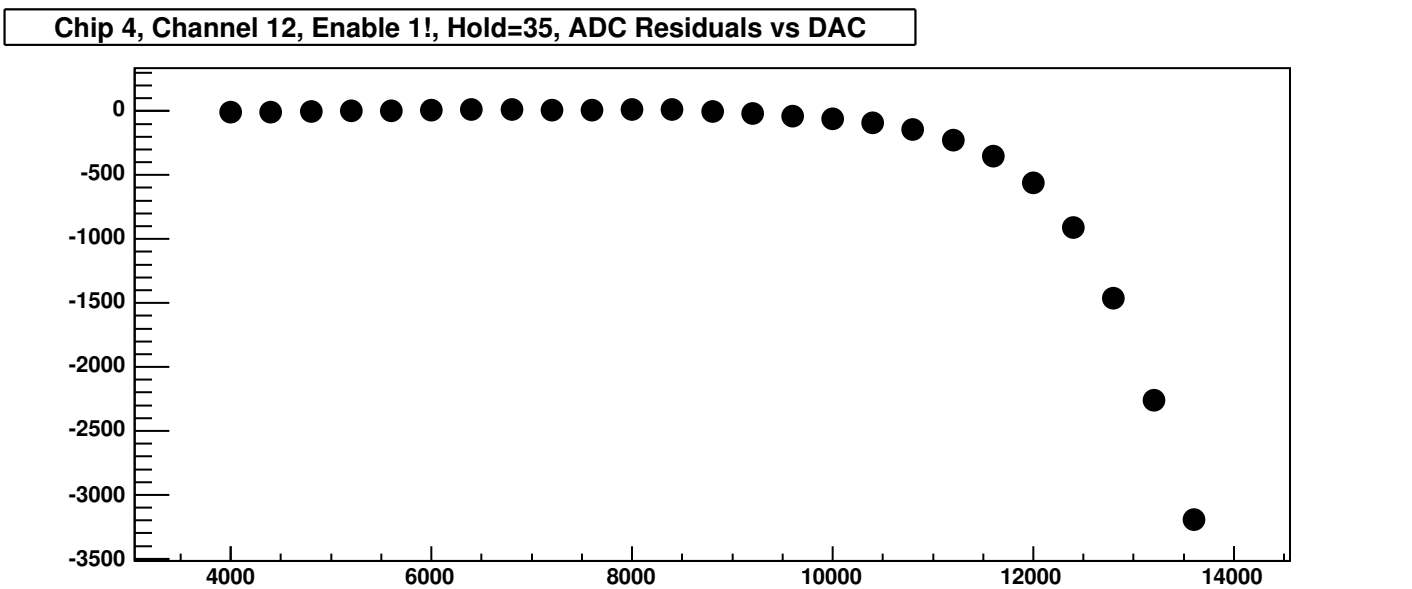

Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 11.96 / 16
p0 221.9 ± 1.254
p1 0.01323 ± 0.0001694
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

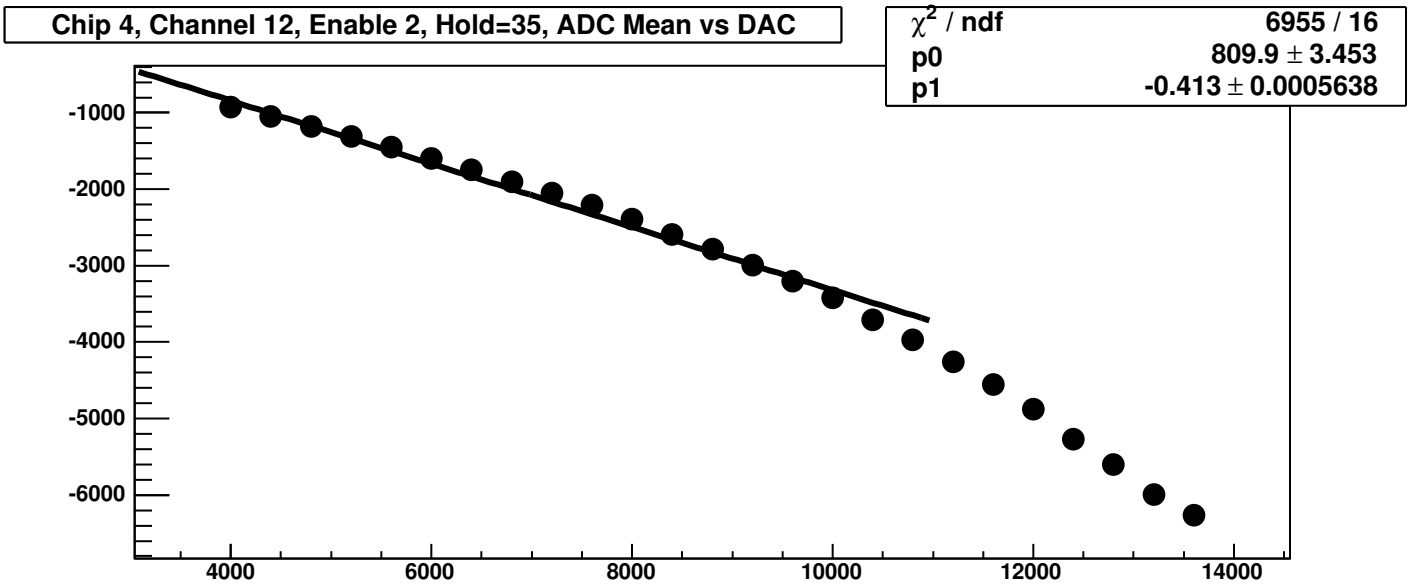

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

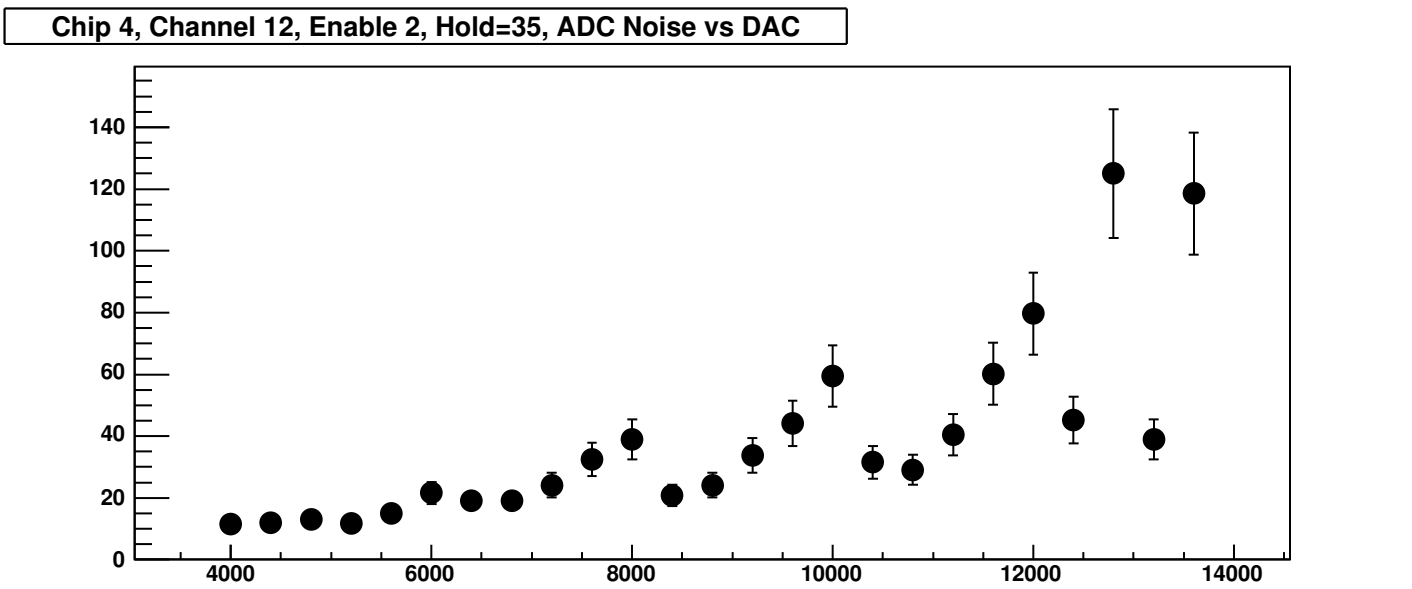

Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

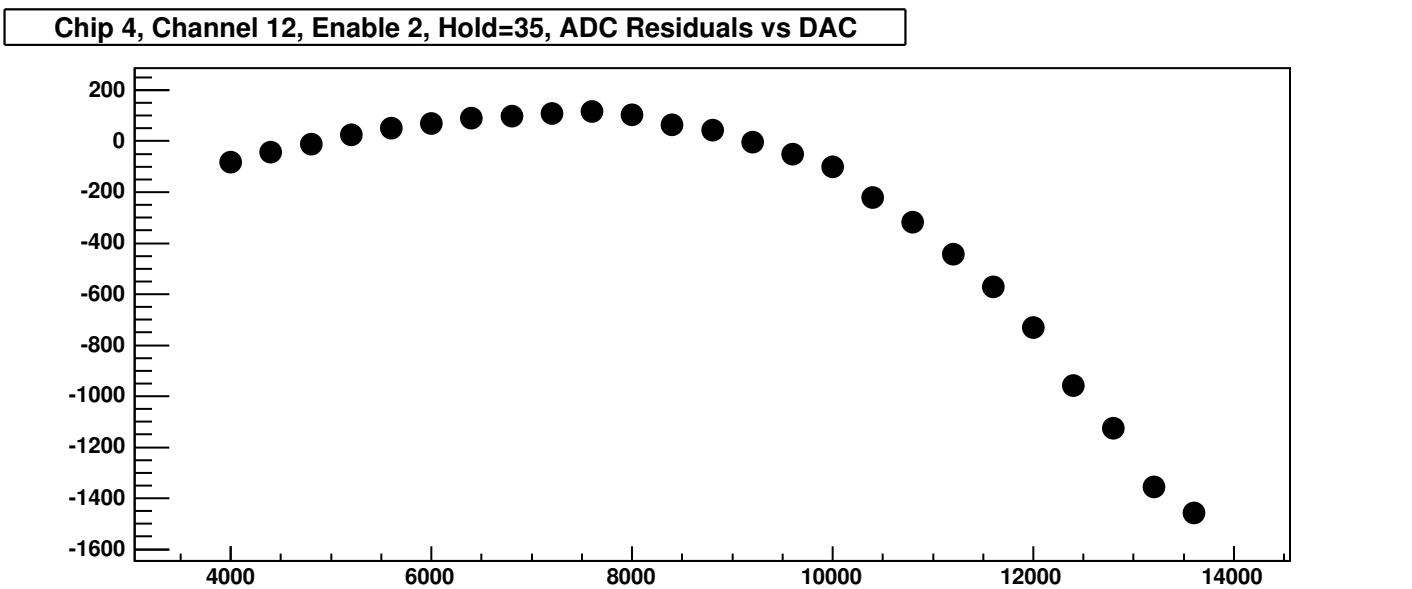

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 706.9 / 16
p0 409.6 ± 2.253
p1 2.479 ± 0.0003541
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

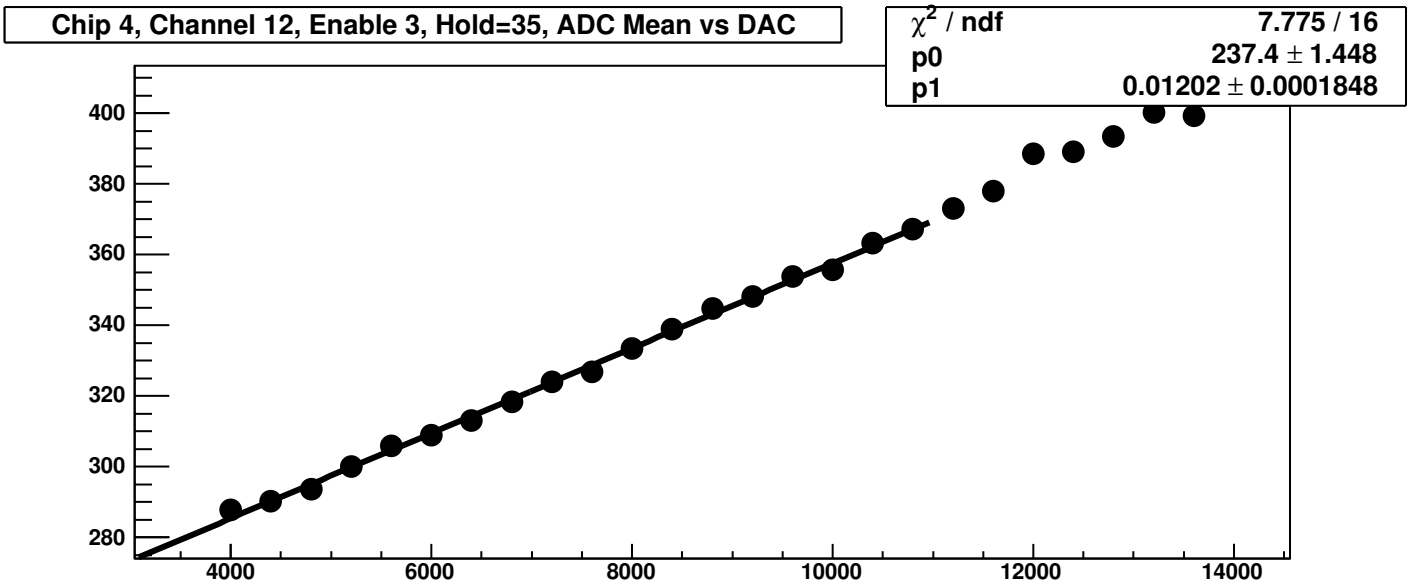

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

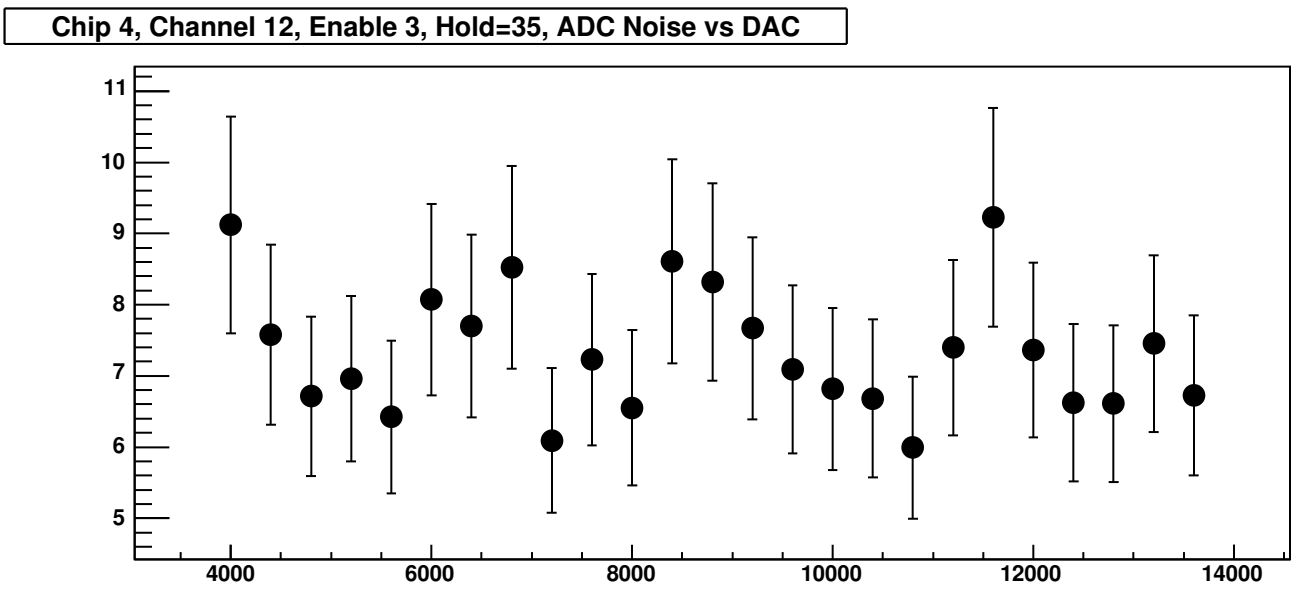

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

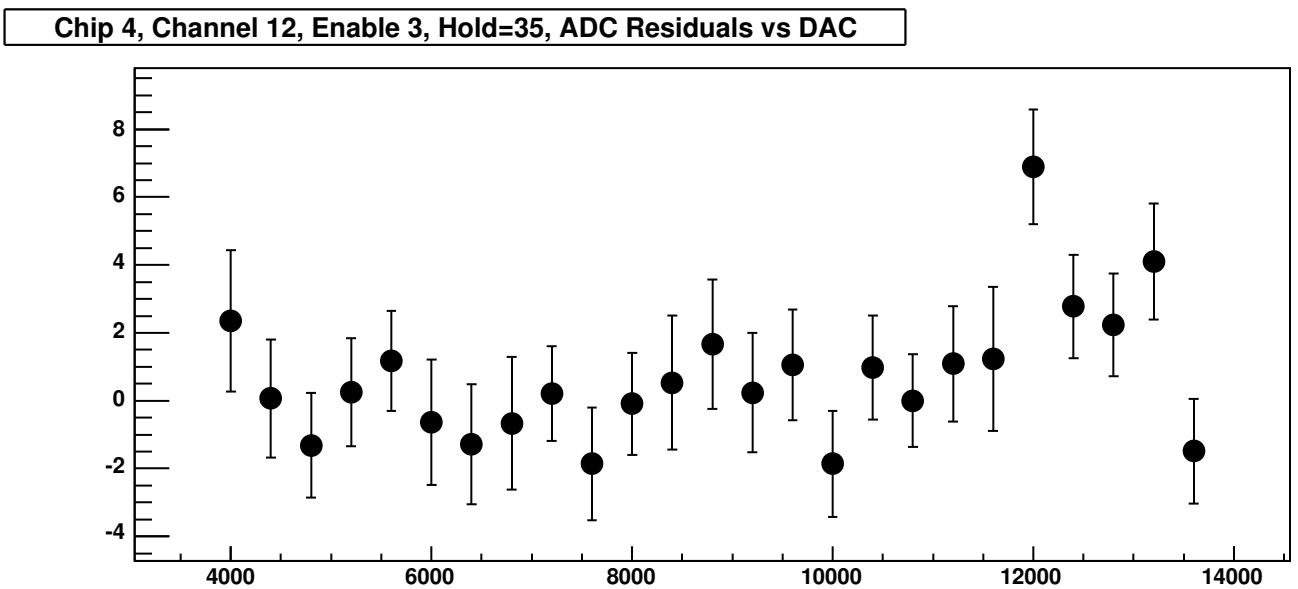

Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 6955 / 16
p0 809.9 ± 3.453
p1 -0.413 ± 0.0005638
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000

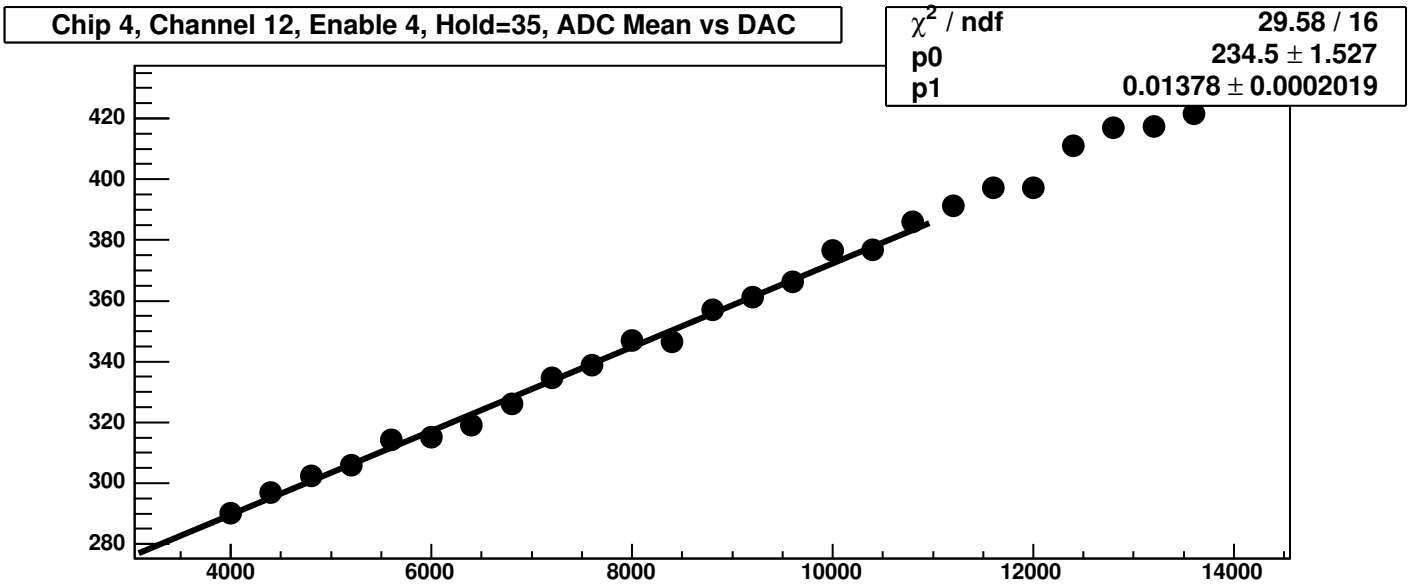

Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

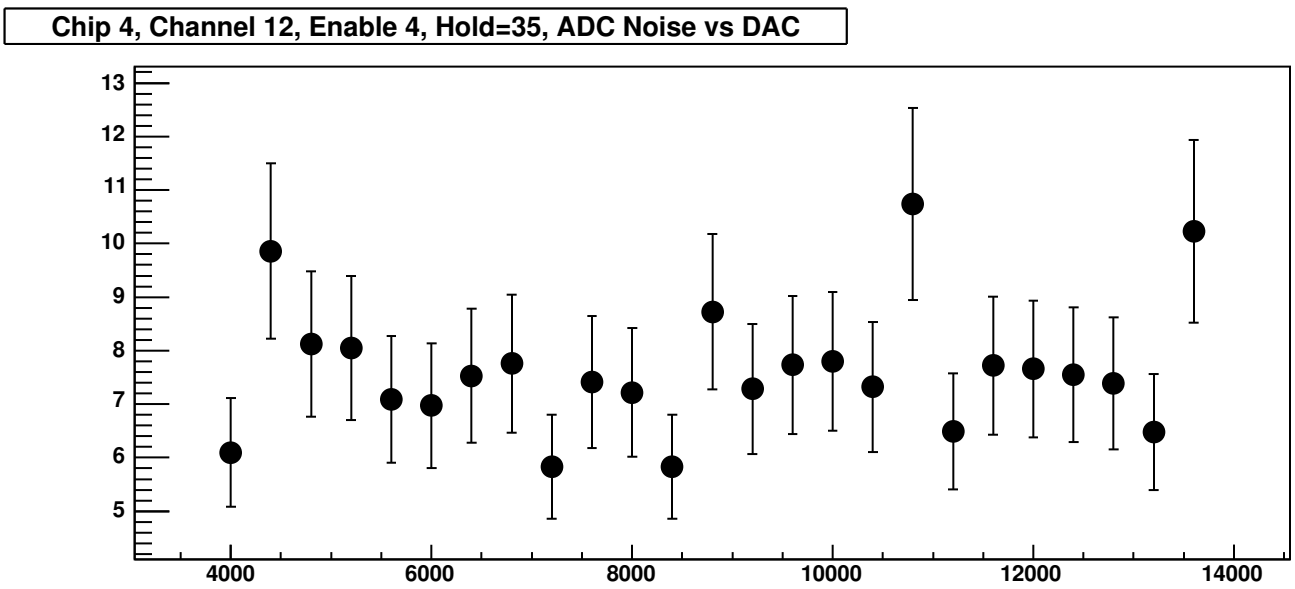

Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
4000
6000
8000
10000
12000
14000

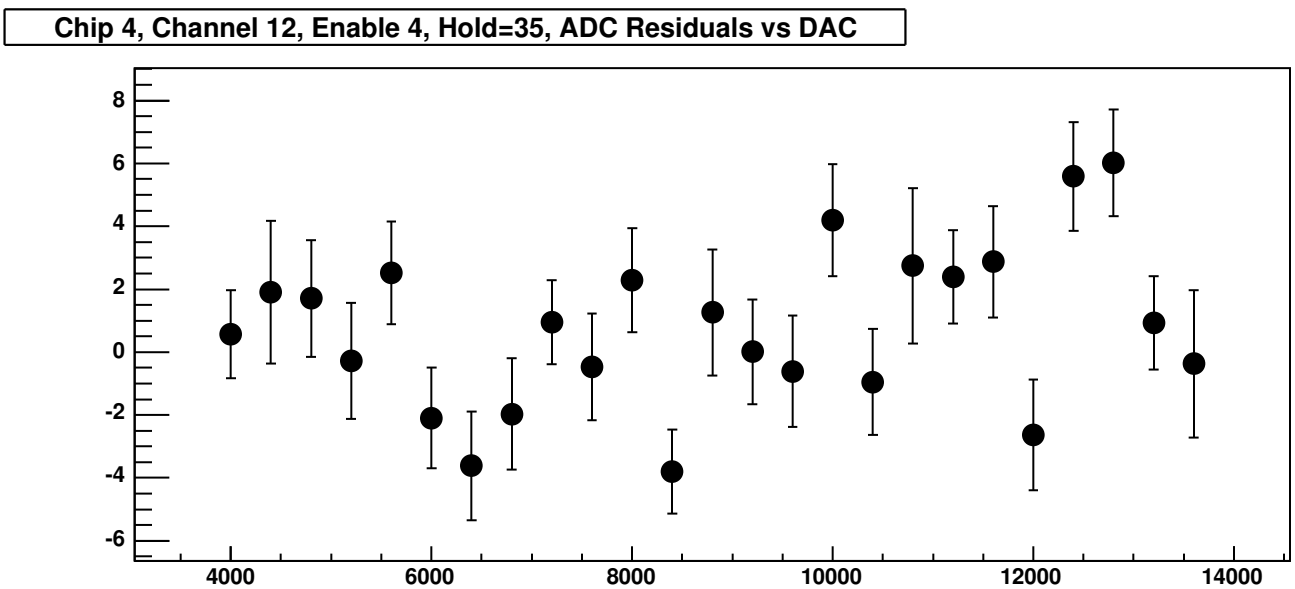

Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 7.775 / 16
p0 237.4 ± 1.448
p1 0.01202 ± 0.0001848
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

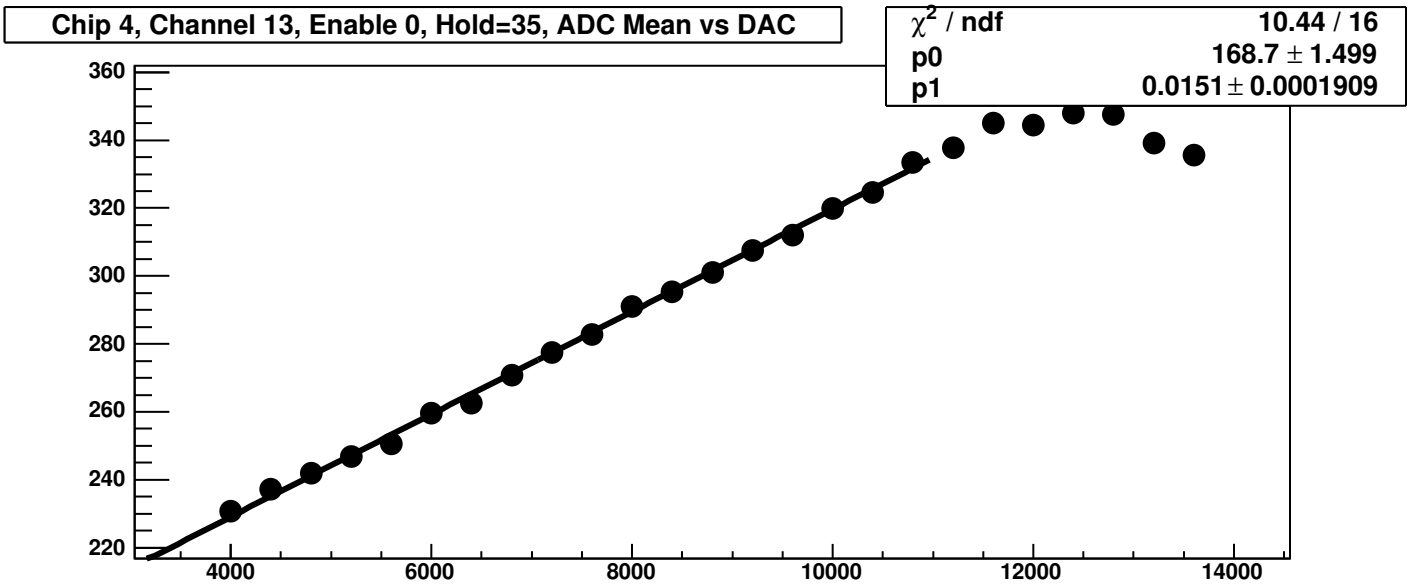

Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

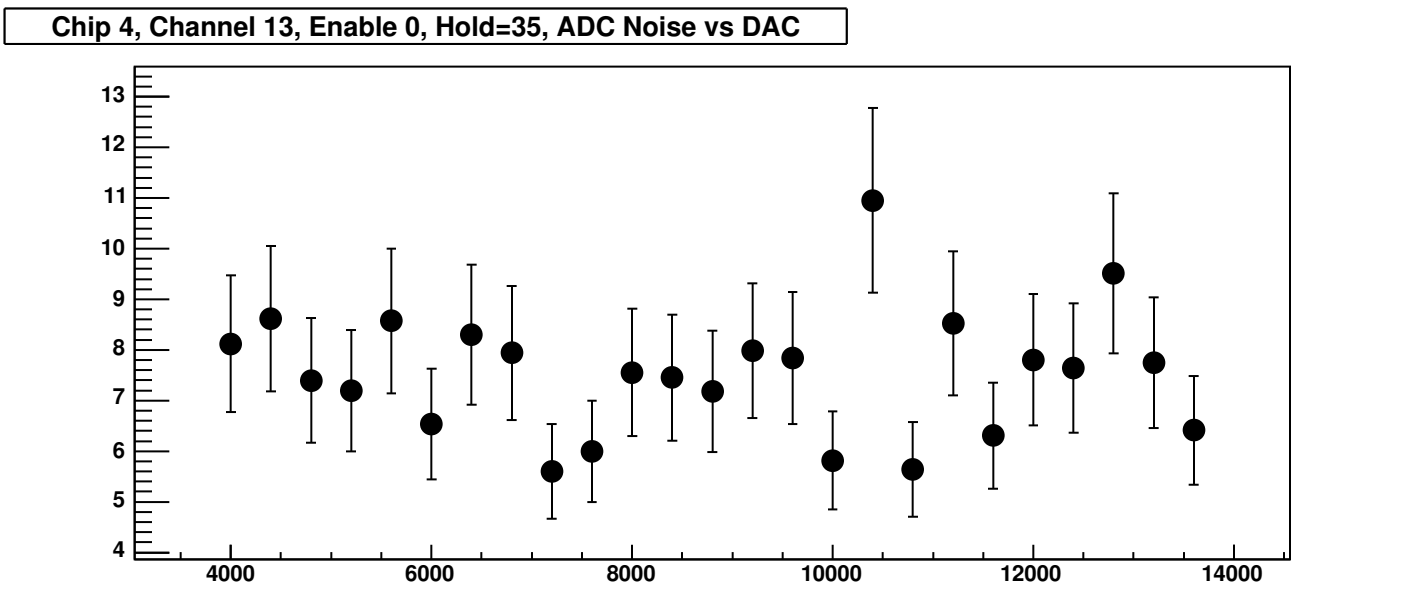

Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

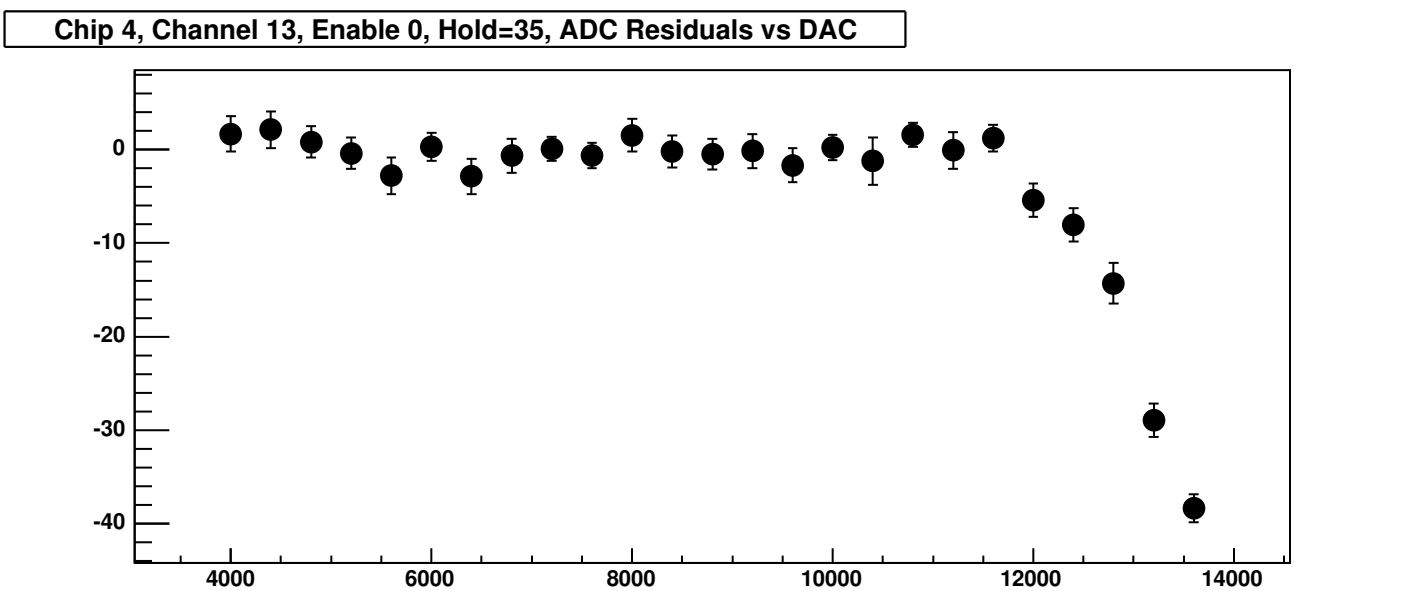

Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.58 / 16
p0 234.5 ± 1.527
p1 0.01378 ± 0.0002019
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000


Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

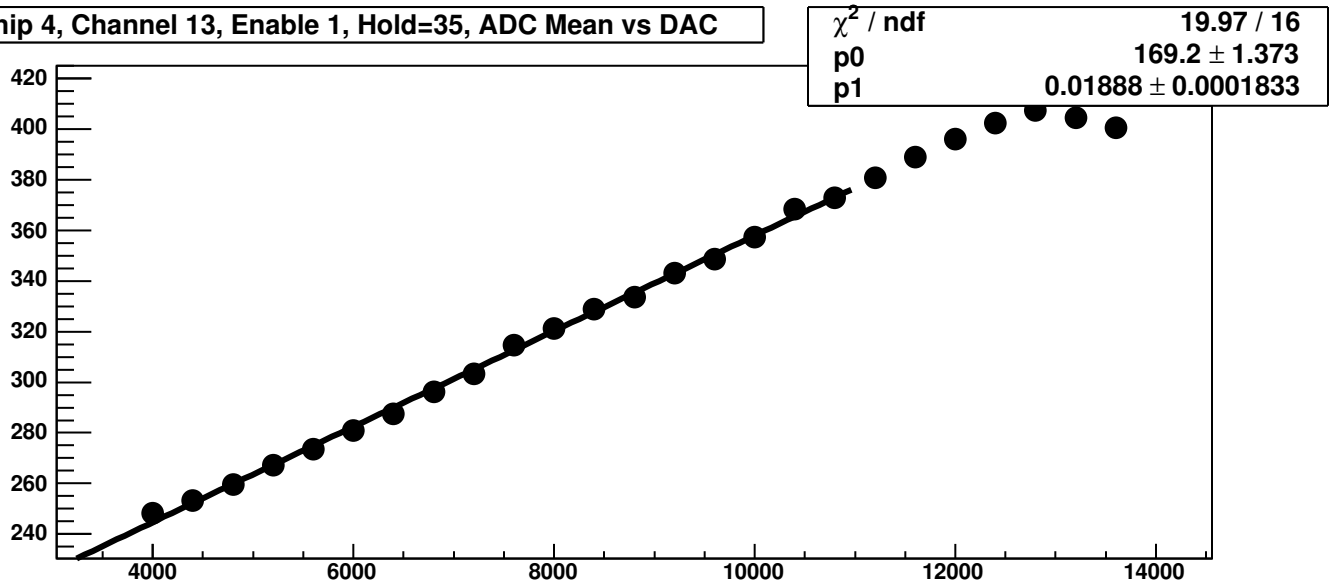

Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 10.44 / 16
p0 168.7 ± 1.499
p1 0.0151 ± 0.0001909
360
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000

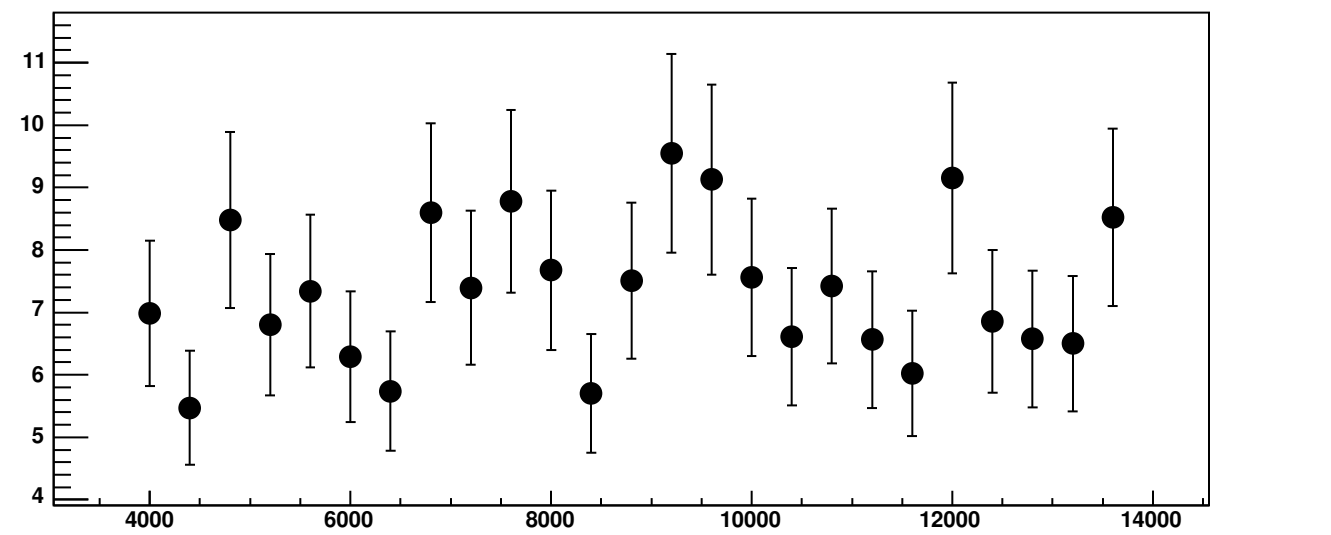

Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

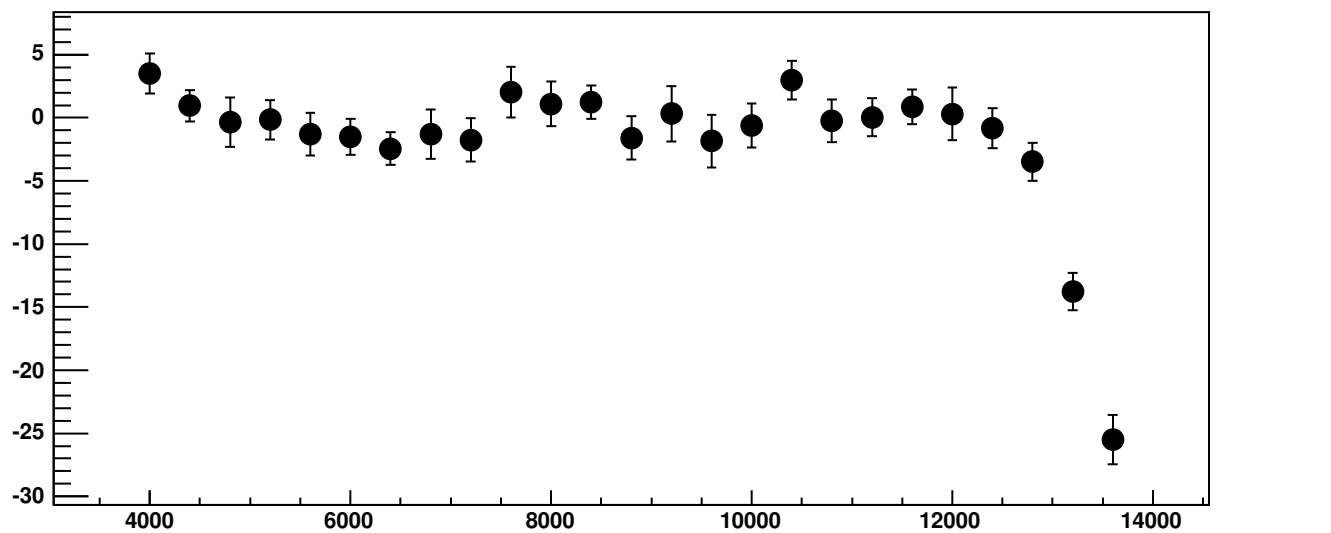
Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

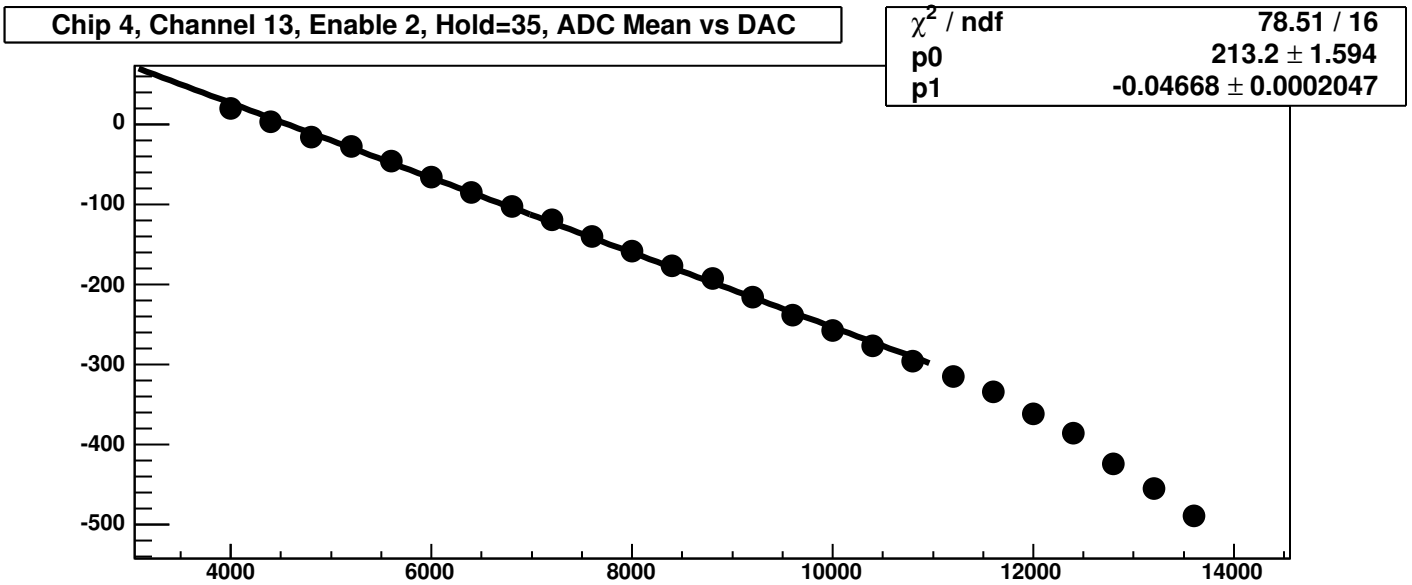


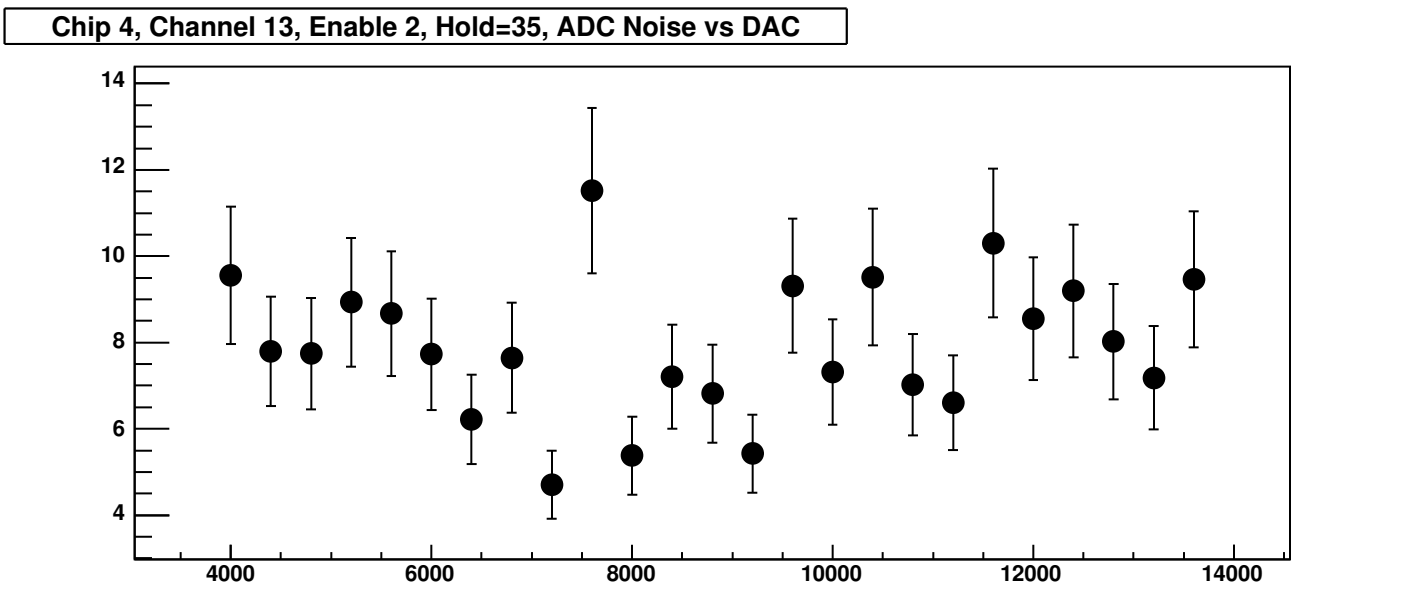
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

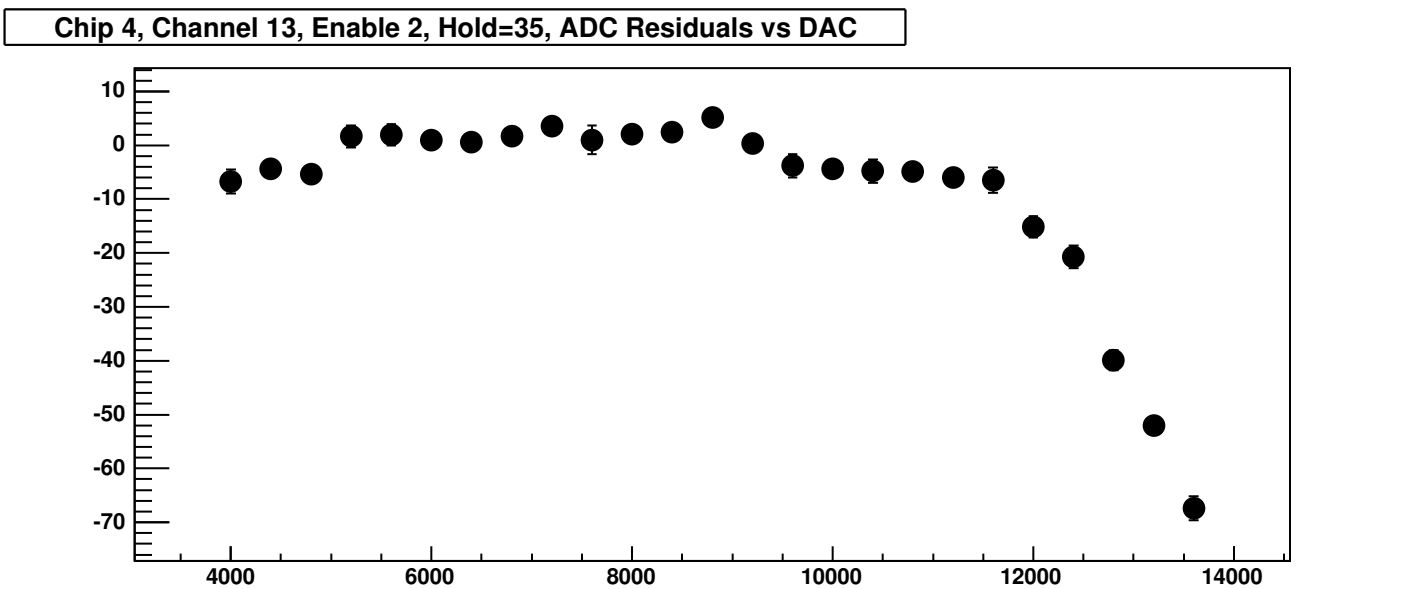


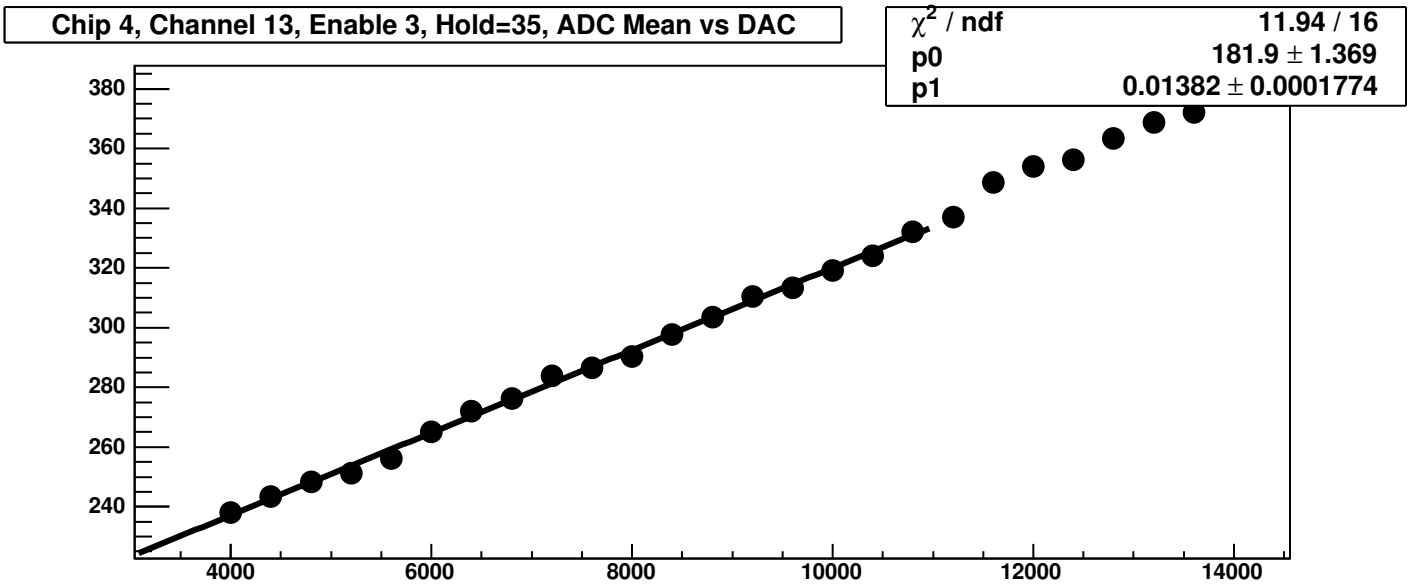
Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

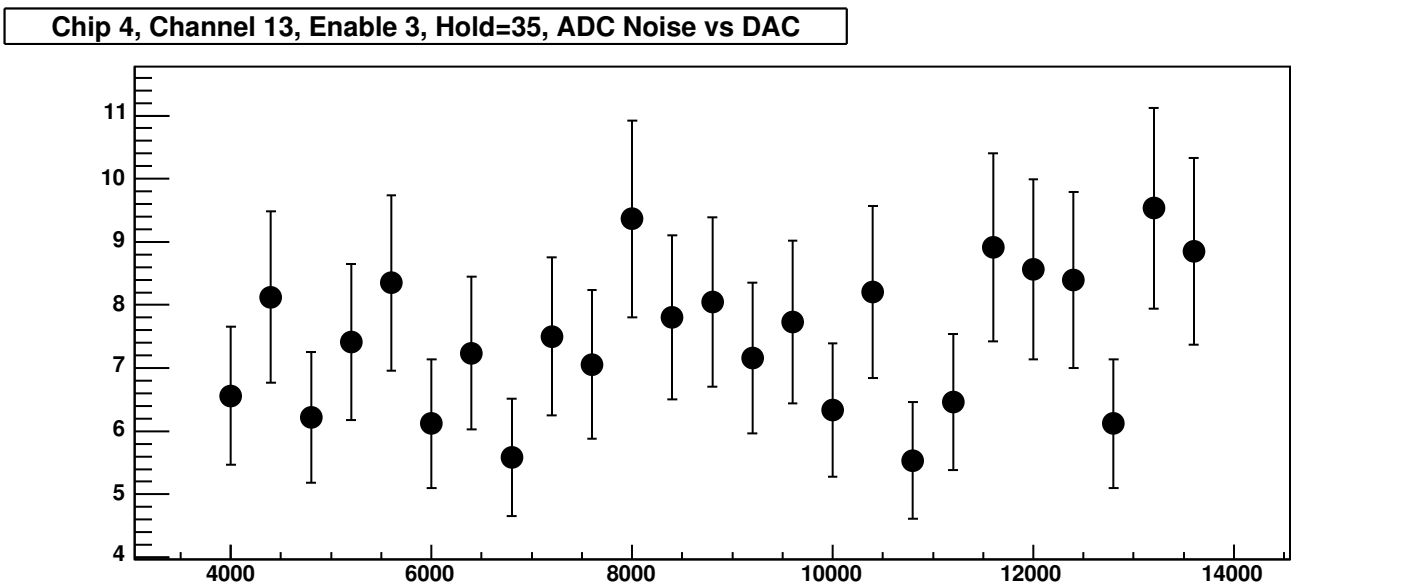


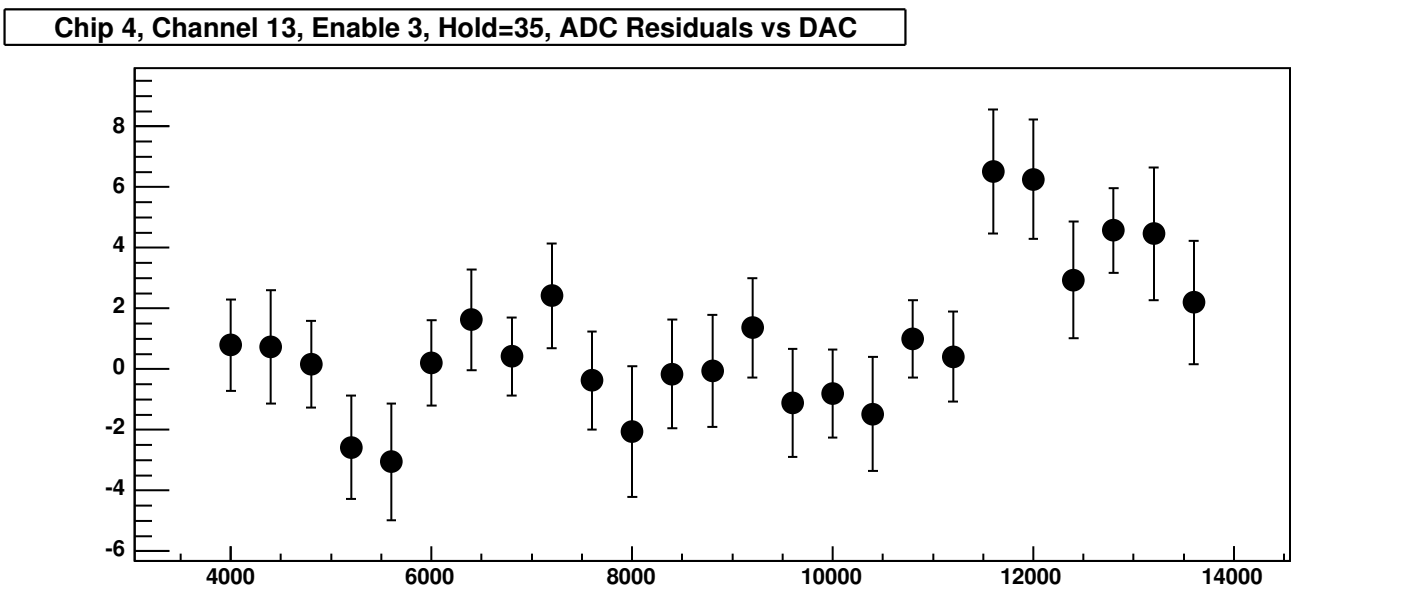

Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 78.51 / 16
p0 213.2 ± 1.594
p1 -0.04668 ± 0.0002047
0
-100
-200
-300
-400
-500
4000
6000
8000
10000
12000
14000

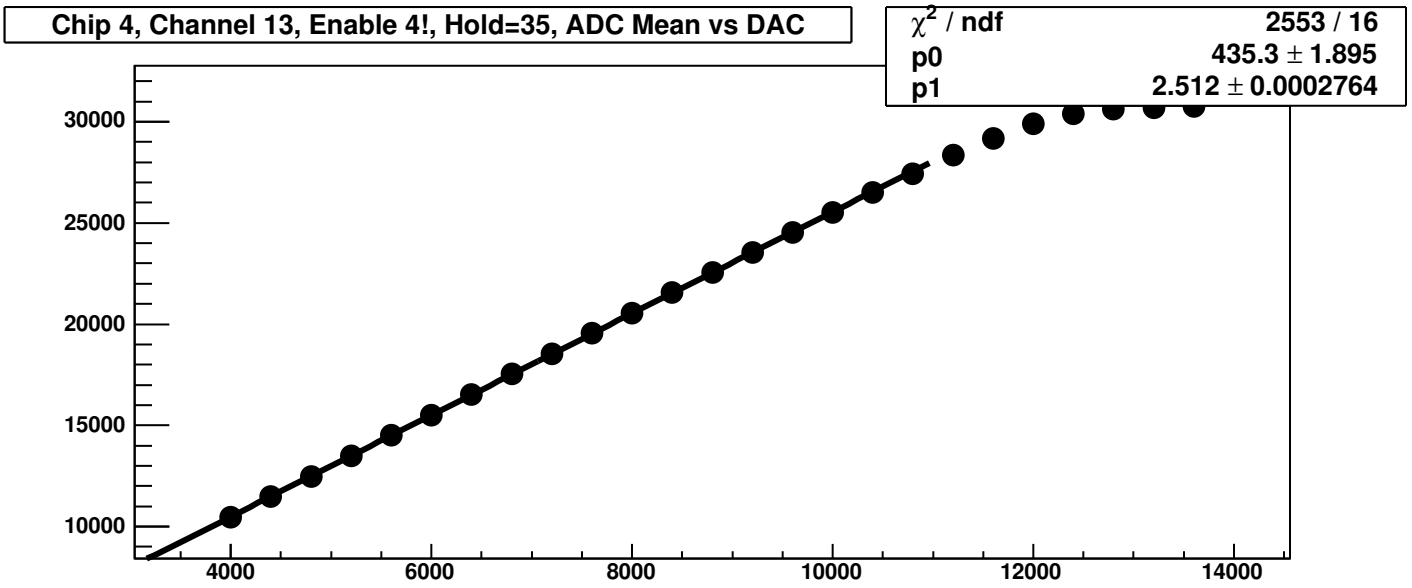

Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

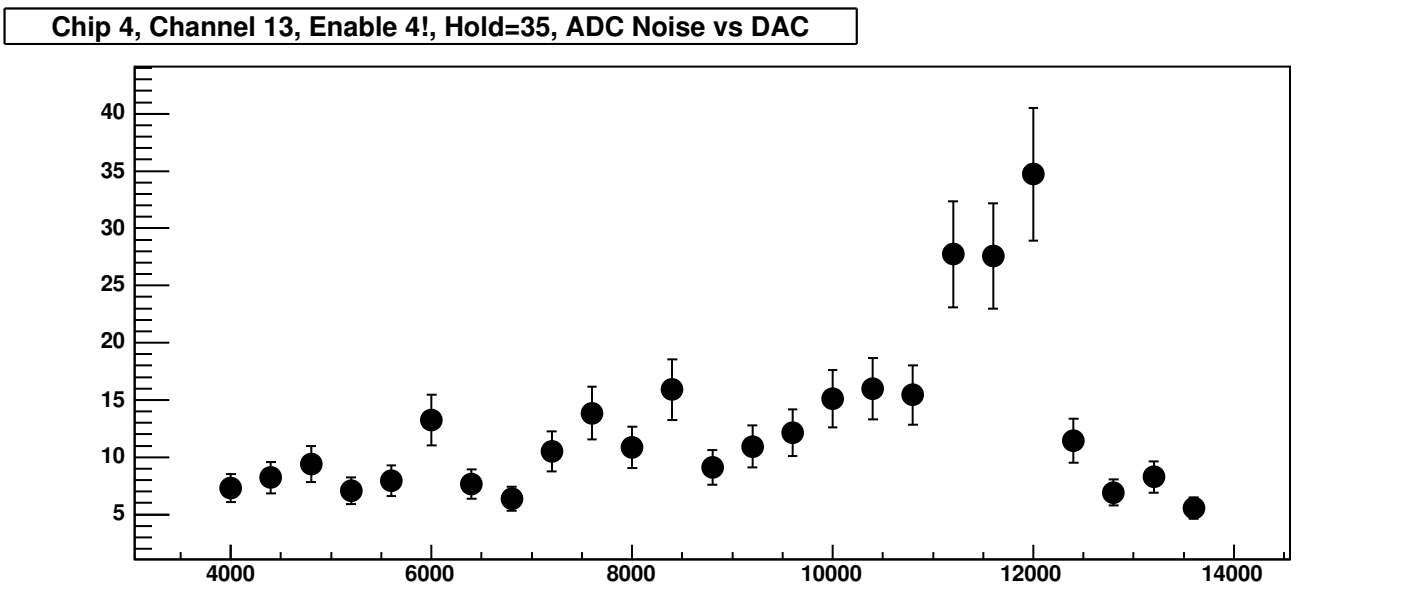

Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
-70
4000
6000
8000
10000
12000
14000

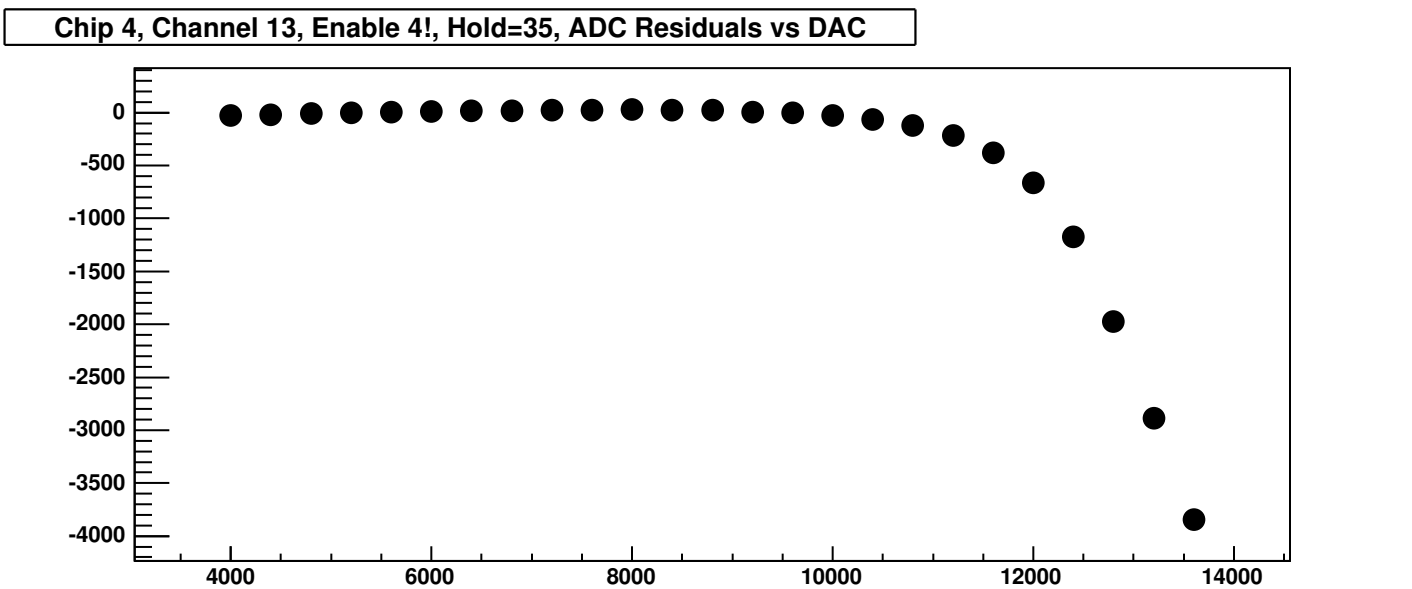

Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 11.94 / 16
p0 181.9 ± 1.369
p1 0.01382 ± 0.0001774
380
360
340
320
300
280
260
240
4000
6000
8000
10000
12000
14000

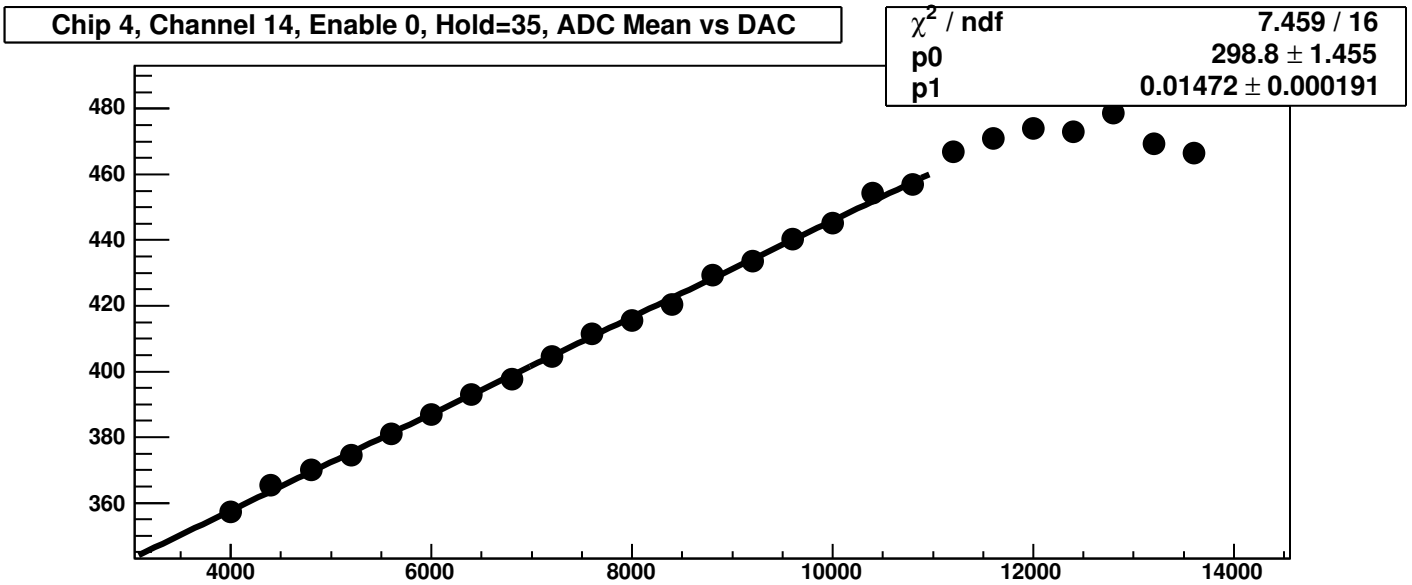

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

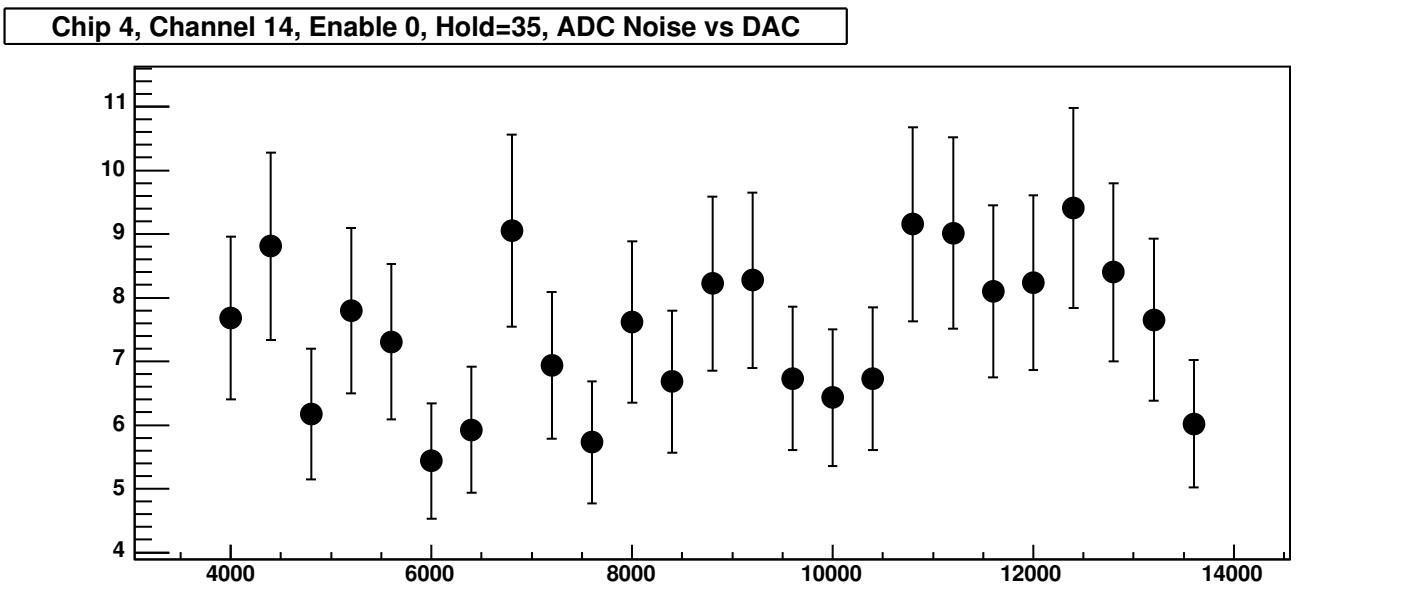

Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

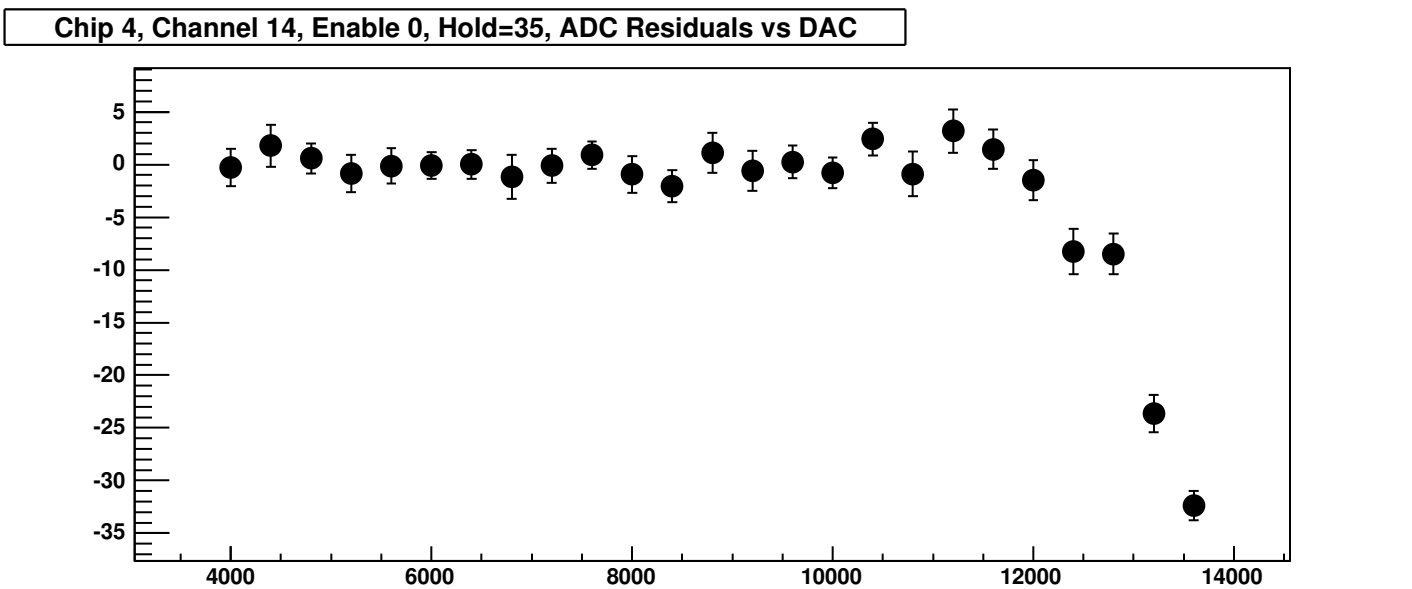

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2553 / 16
p0 435.3 ± 1.895
p1 2.512 ± 0.0002764
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

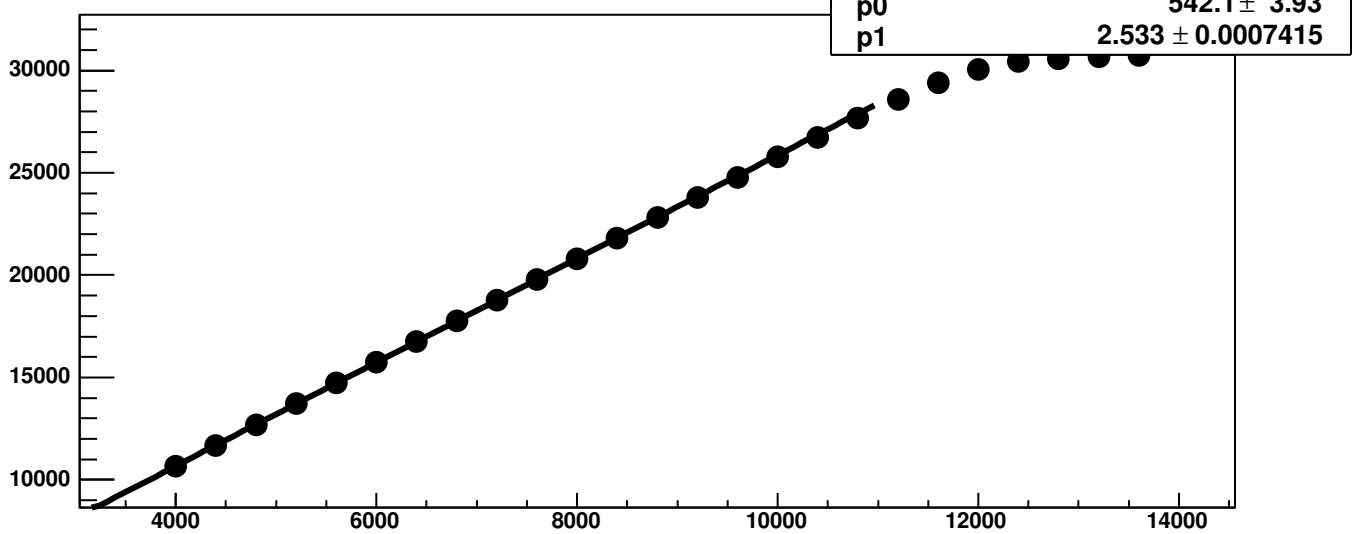

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.459 / 16
p0 298.8 ± 1.455
p1 0.01472 ± 0.000191
480
460
440
420
400
380
360
4000
6000
8000
10000
12000
14000

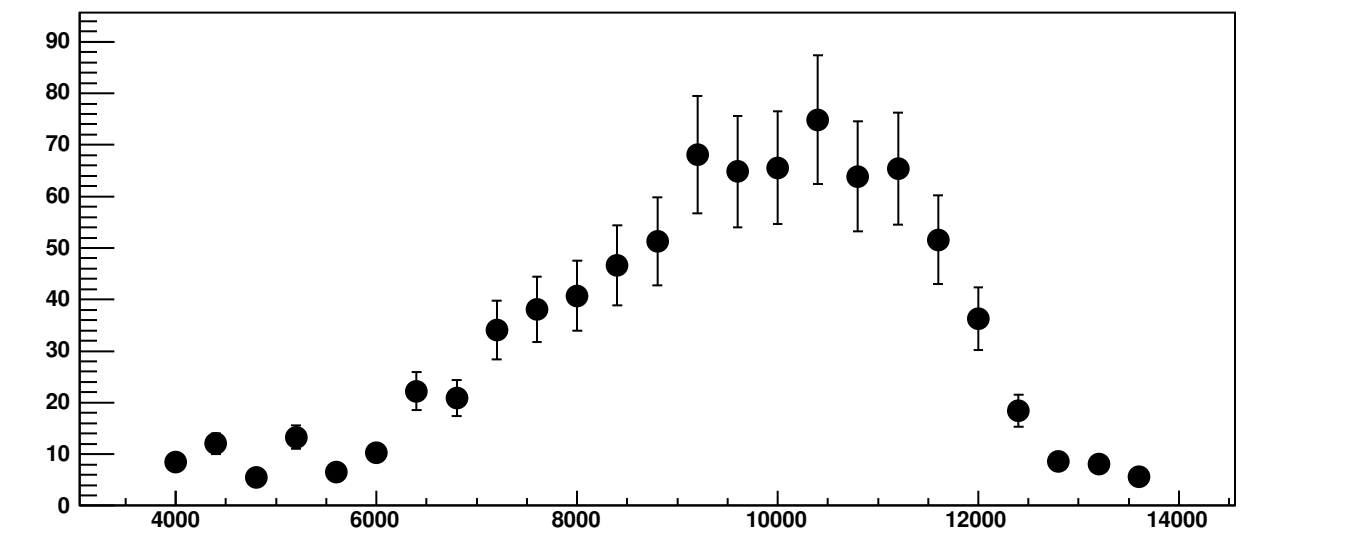

Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
4000
6000
8000
10000
12000
14000

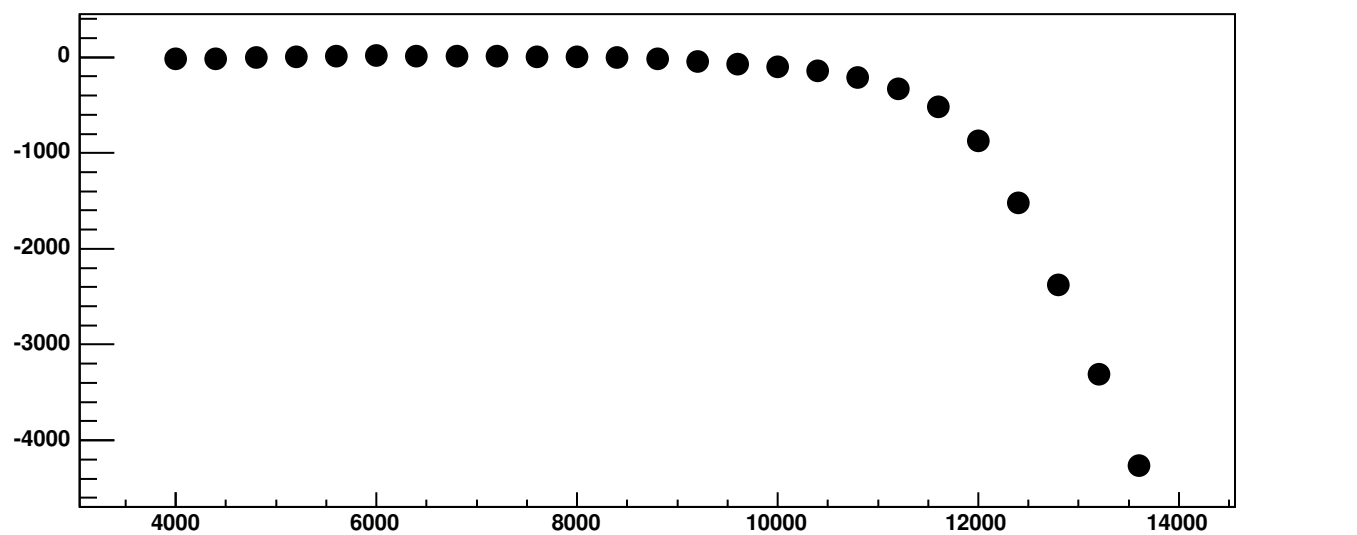
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

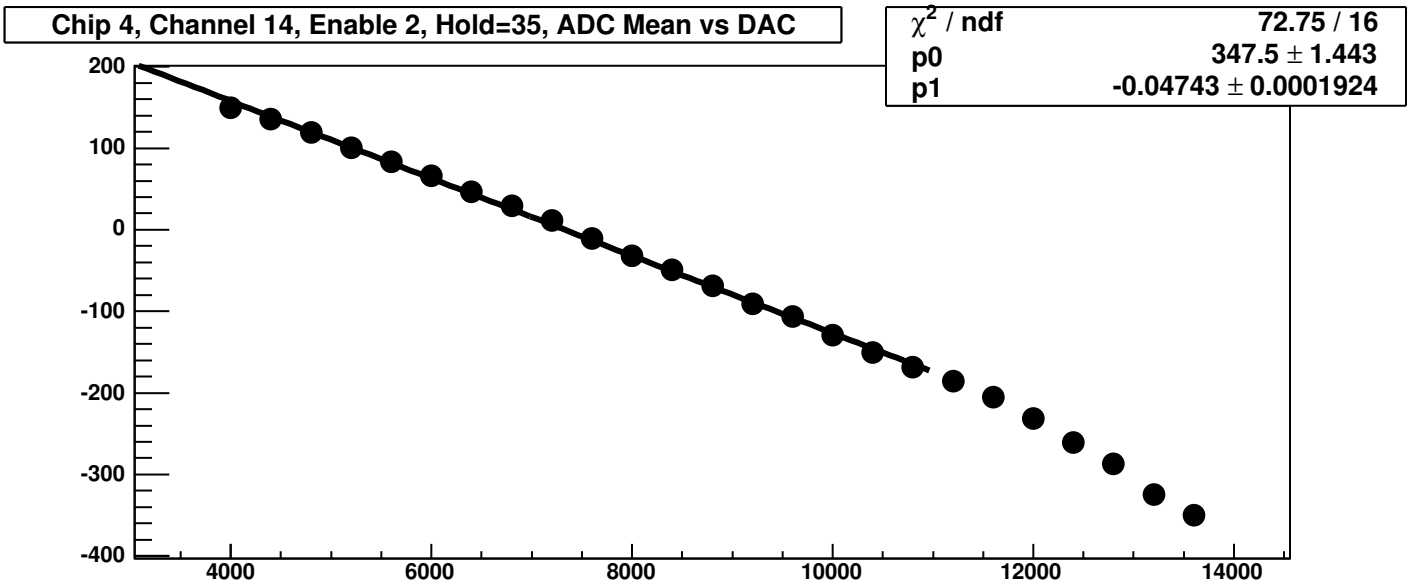


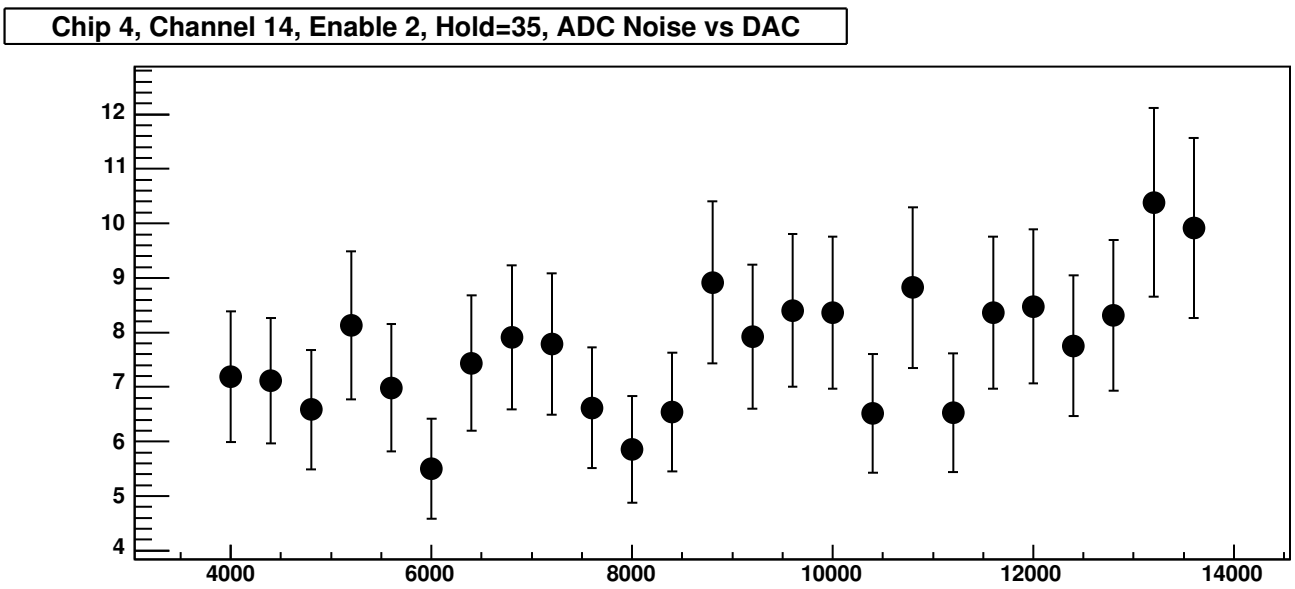
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC

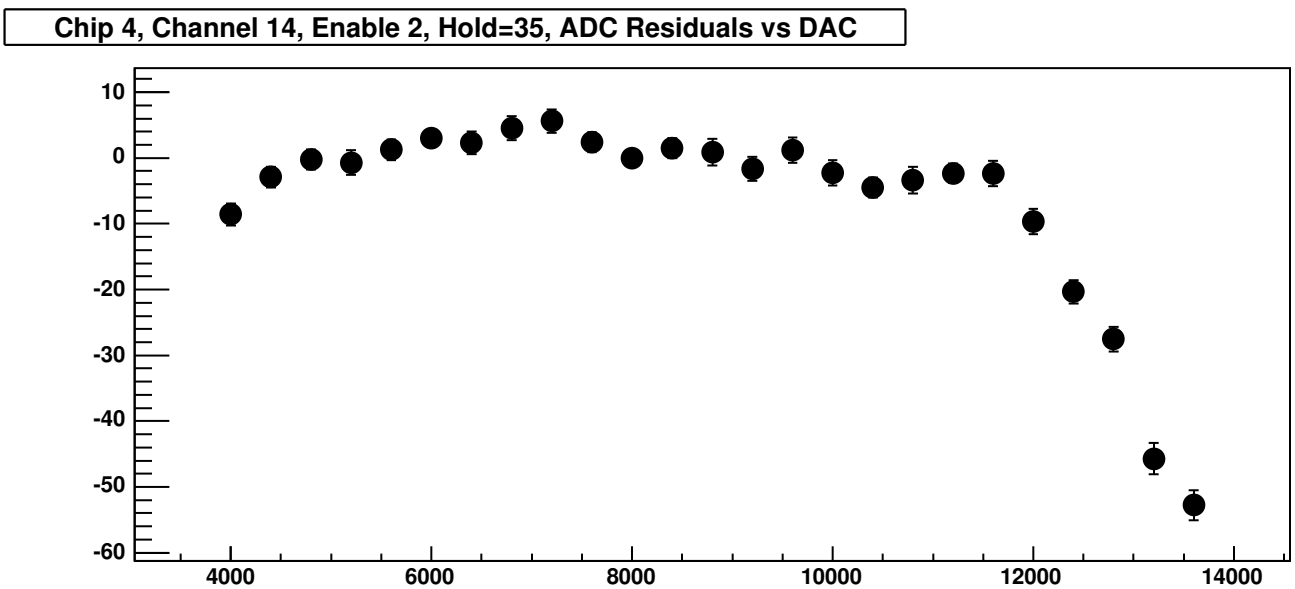


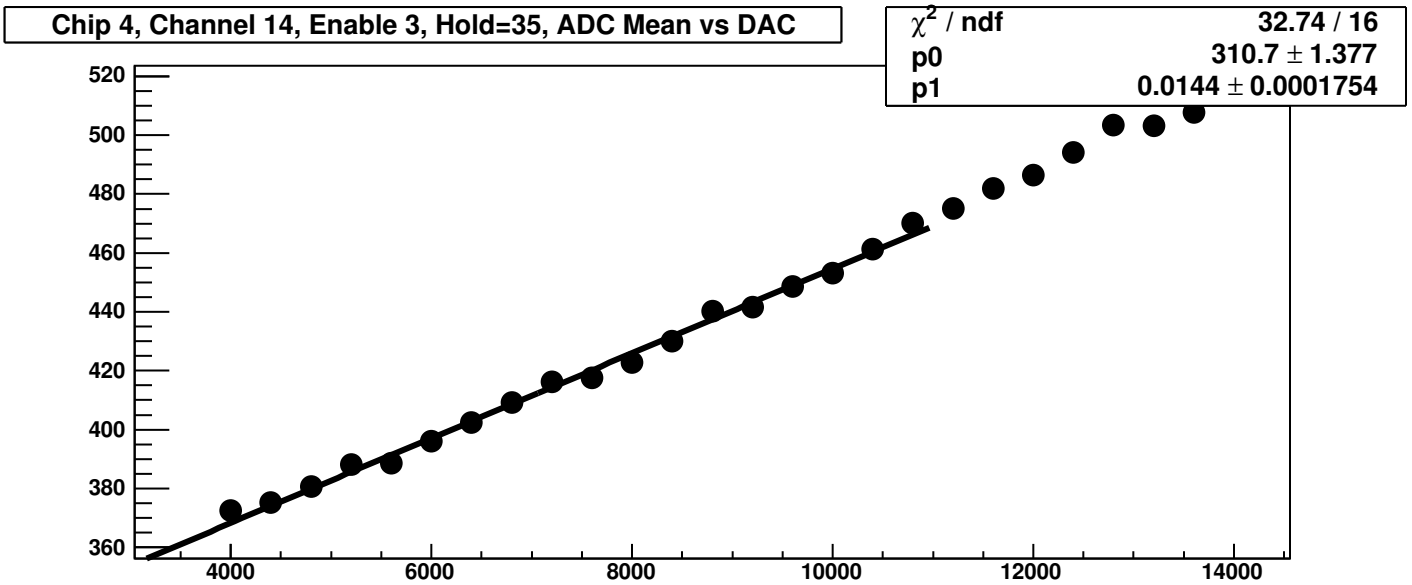
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

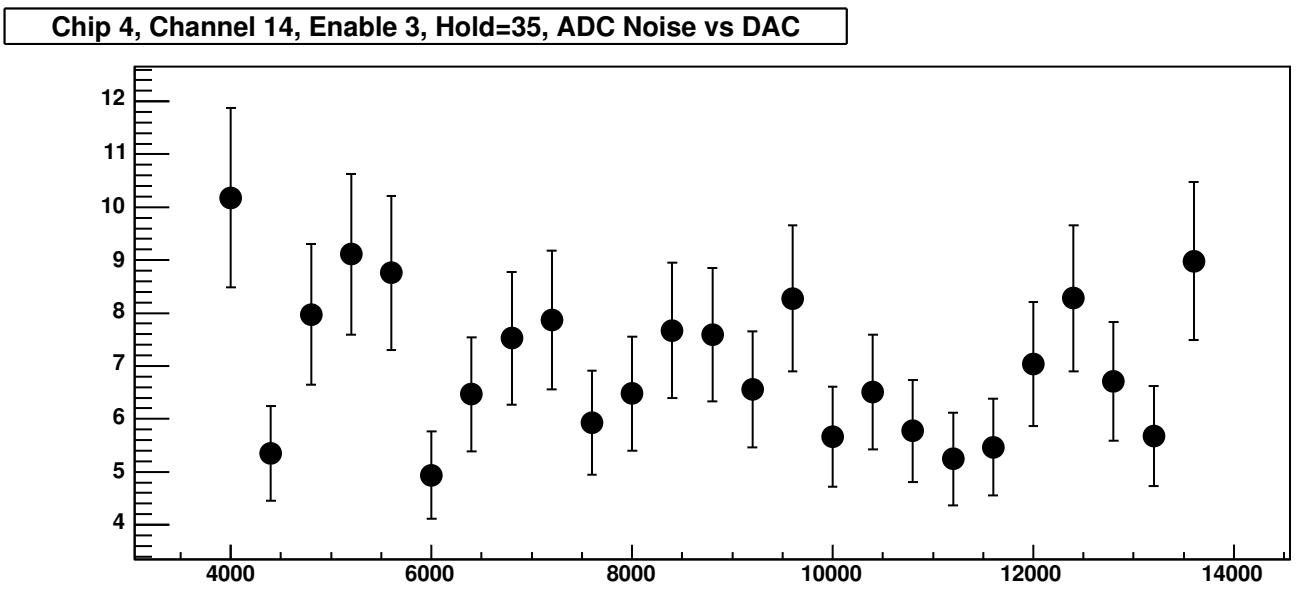


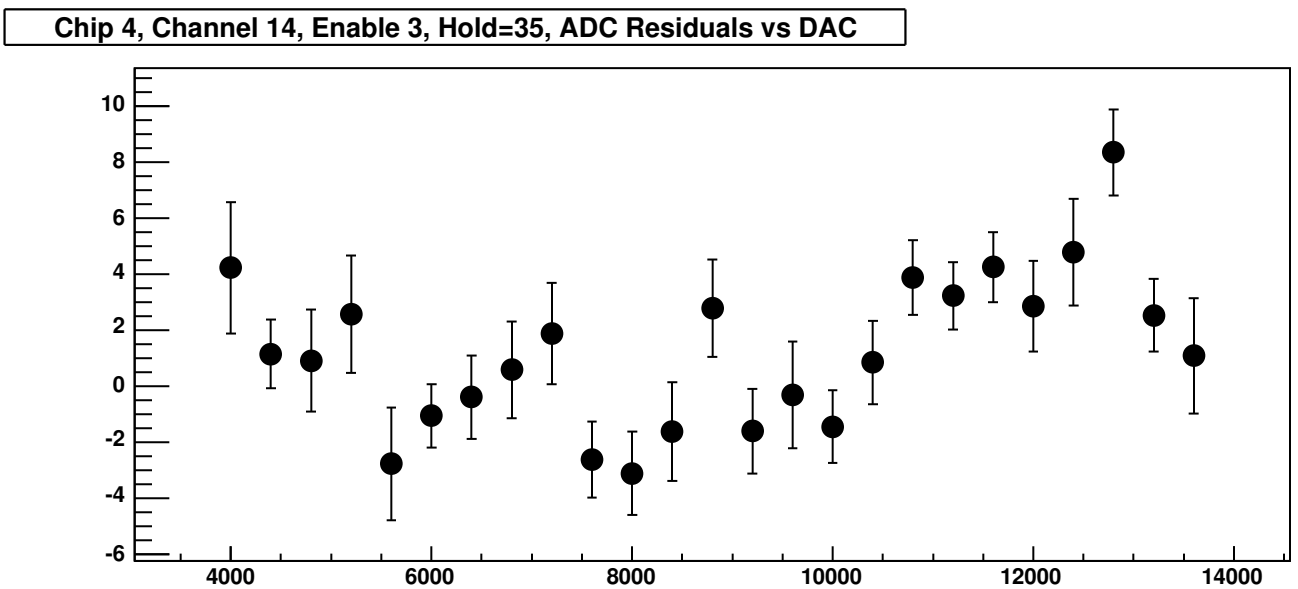

Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 72.75 / 16
p0 347.5 ± 1.443
p1 -0.04743 ± 0.0001924
200
100
0
-100
-200
-300
-400
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

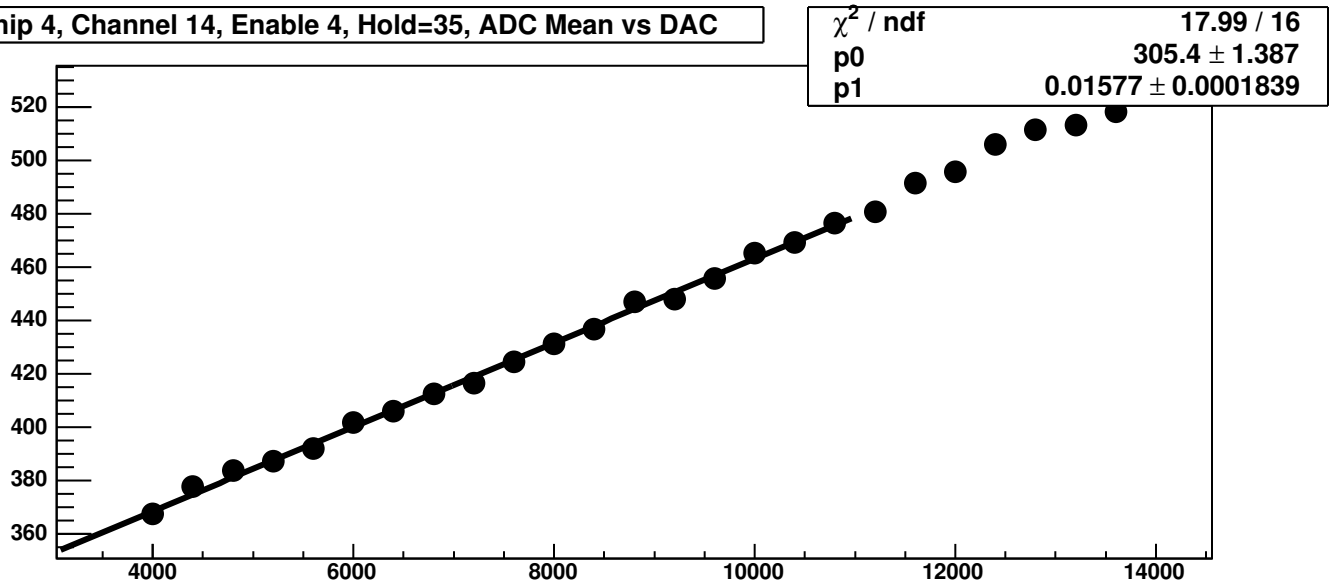

Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.74 / 16
p0 310.7 ± 1.377
p1 0.0144 ± 0.0001754
520
500
480
460
440
420
400
380
360
4000
6000
8000
10000
12000
14000

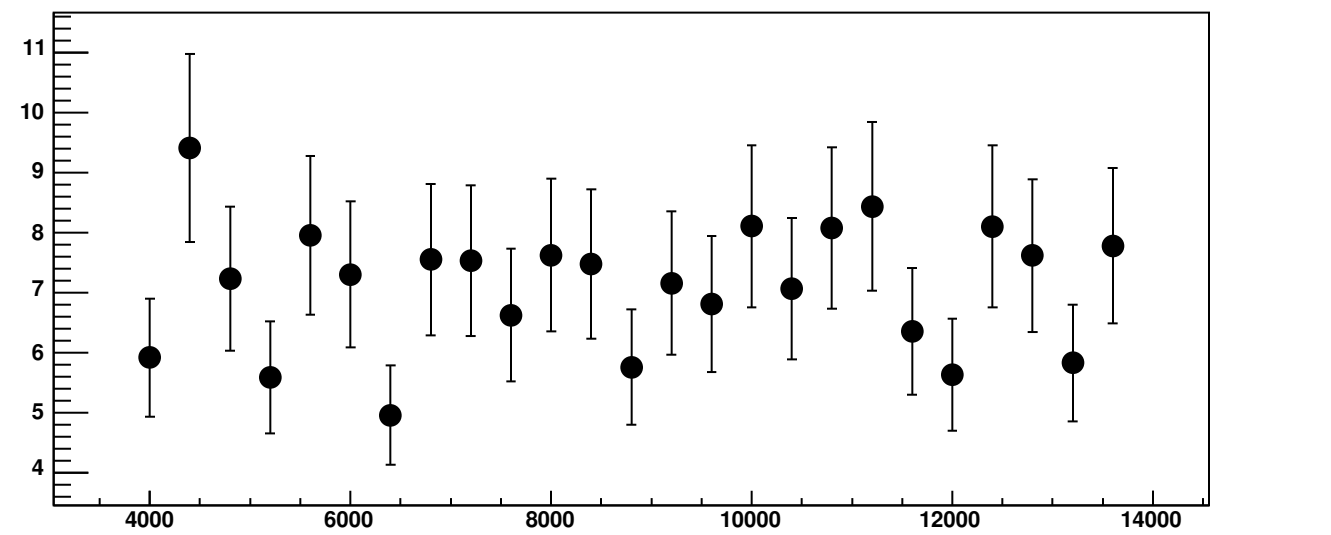

Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

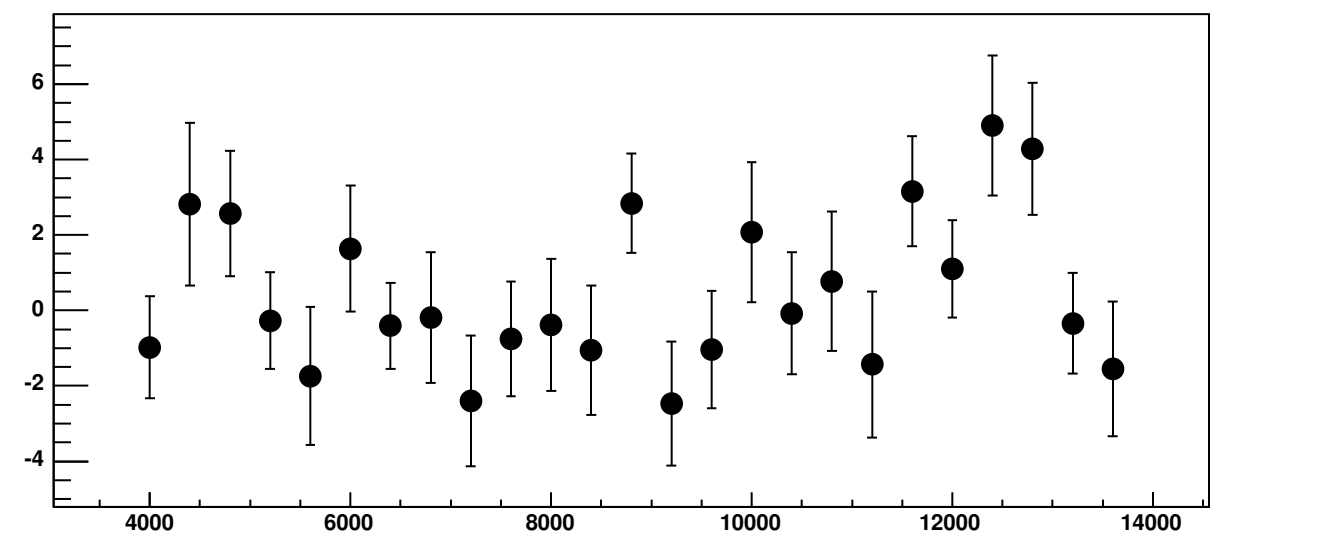
Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

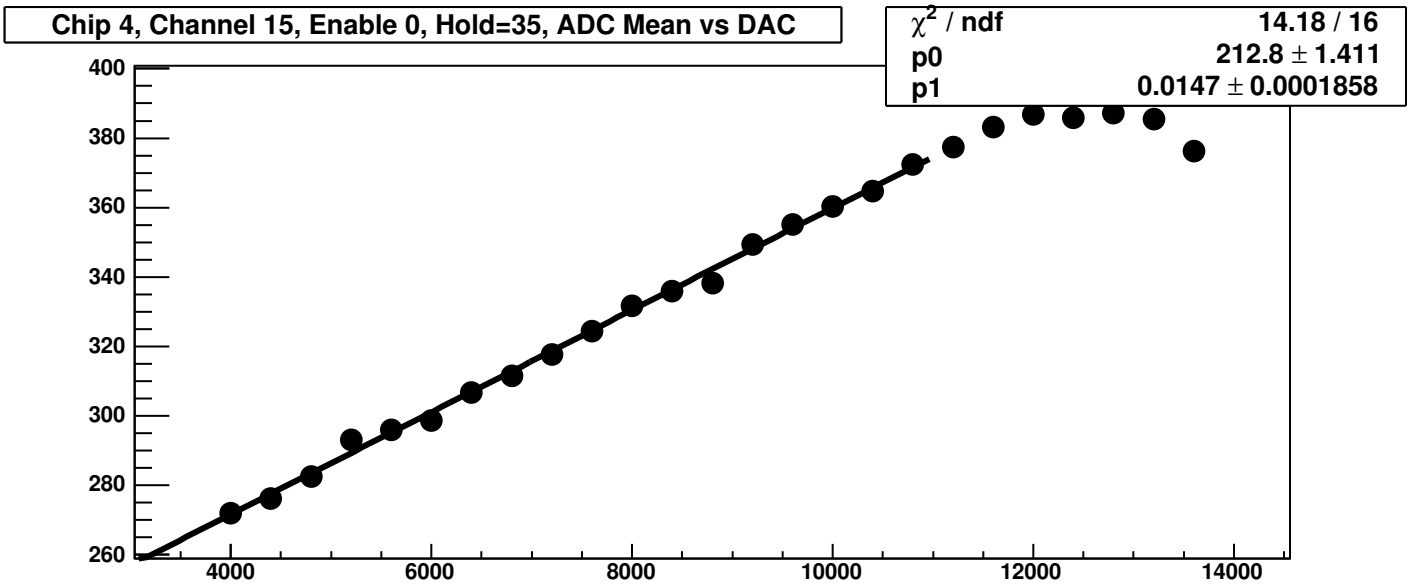


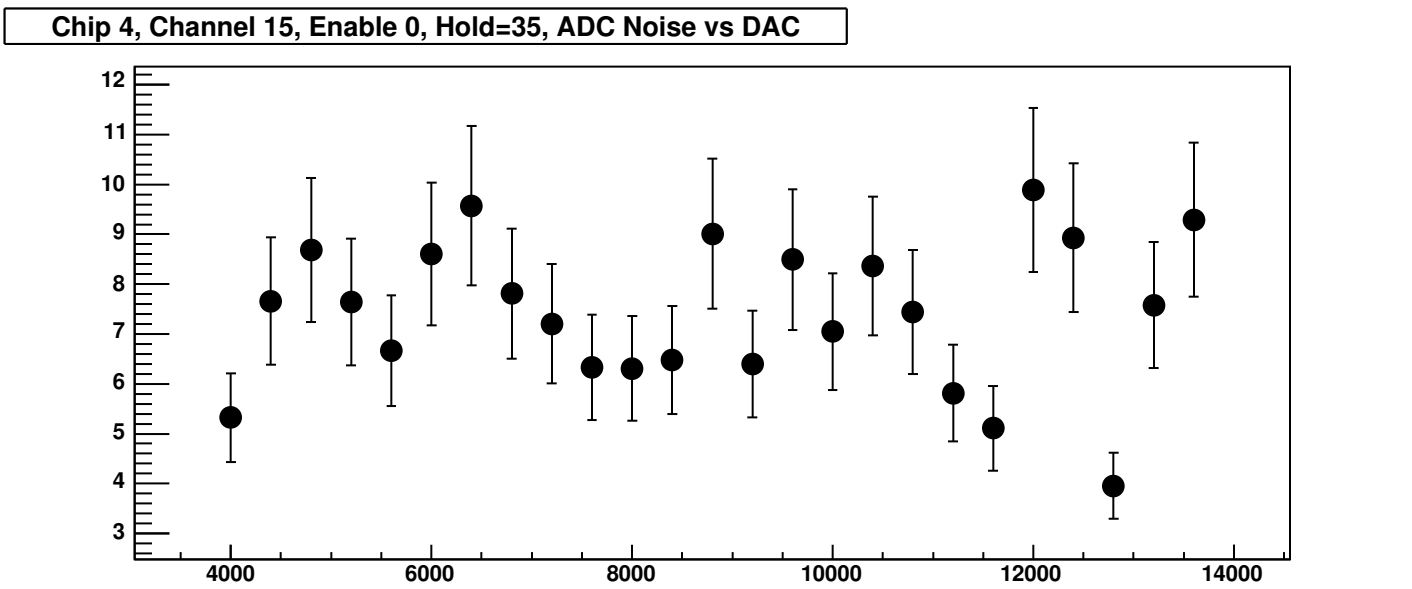
Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

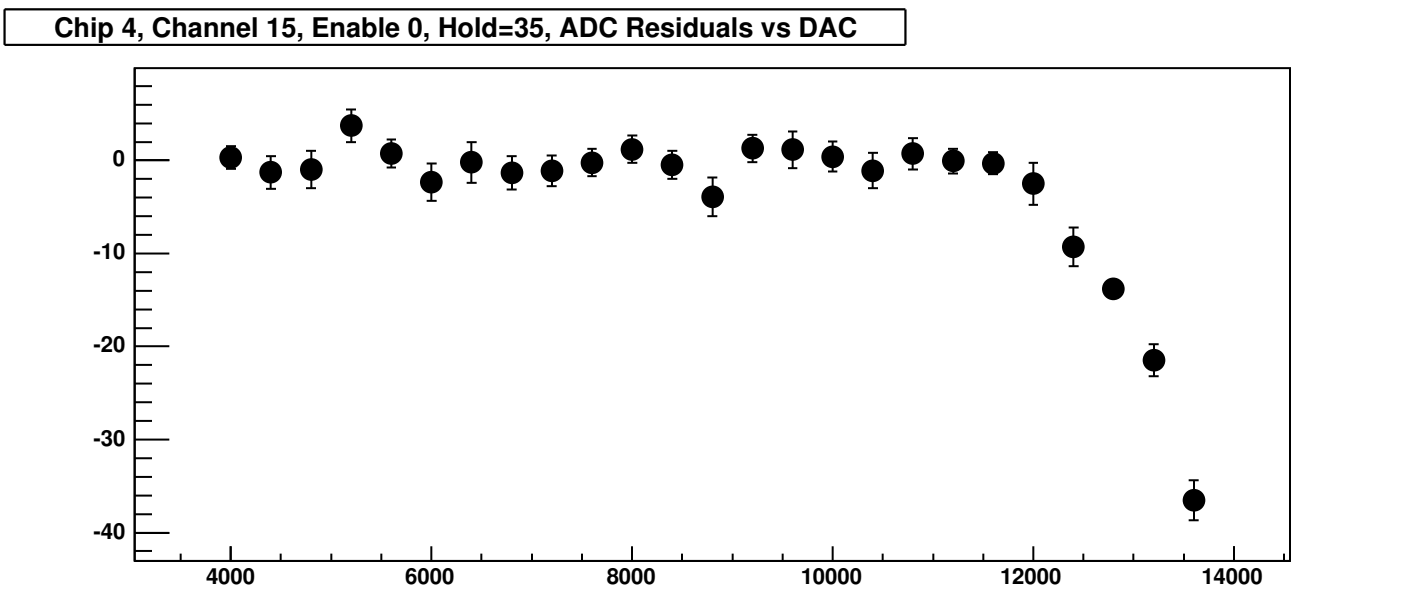


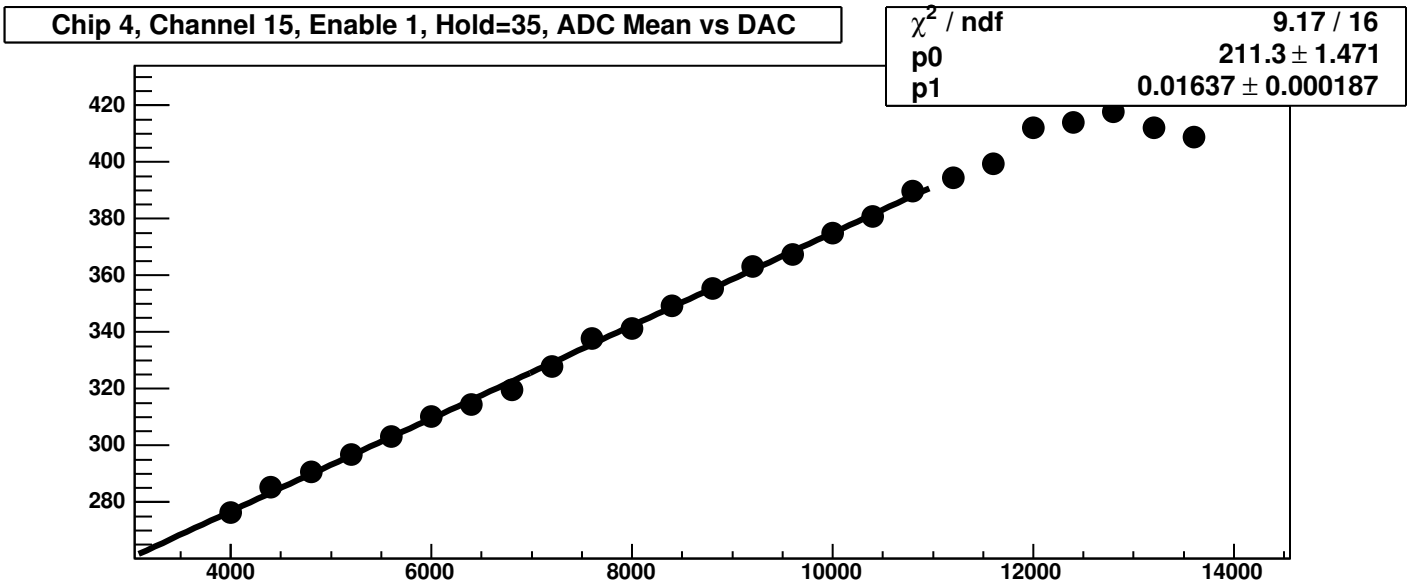
Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

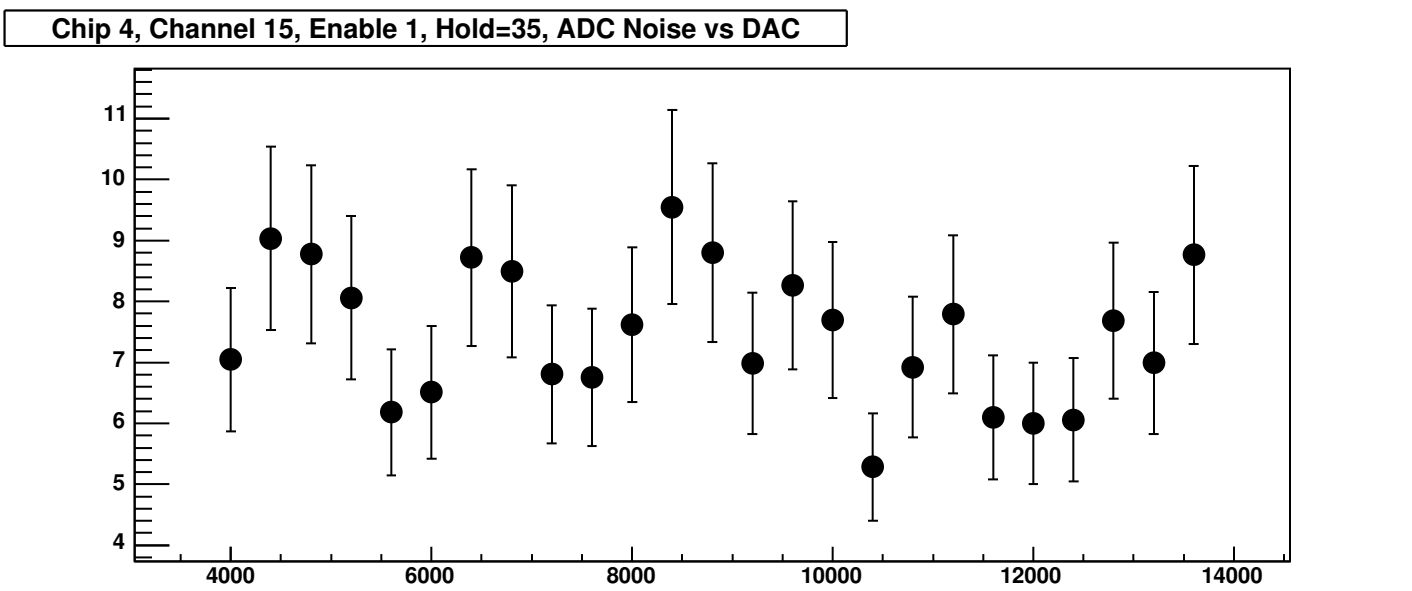


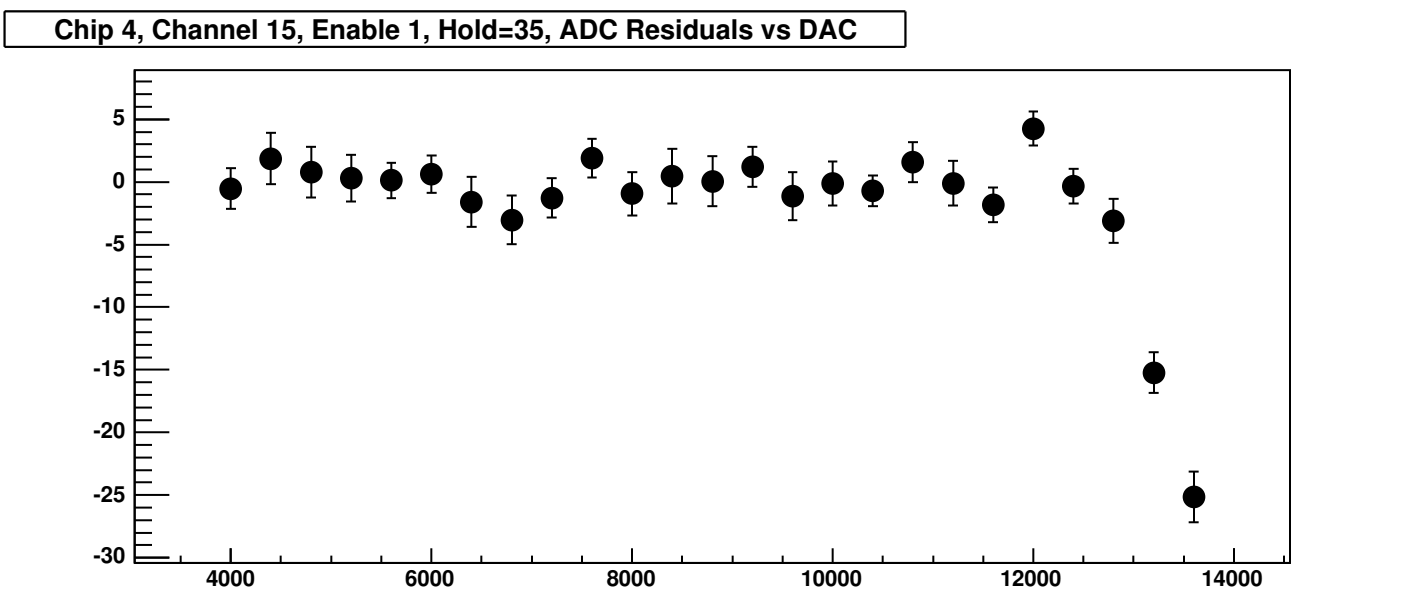

Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 14.18 / 16
p0 212.8 ± 1.411
p1 0.0147 ± 0.0001858
400
380
360
340
320
300
280
260
4000
6000
8000
10000
12000
14000

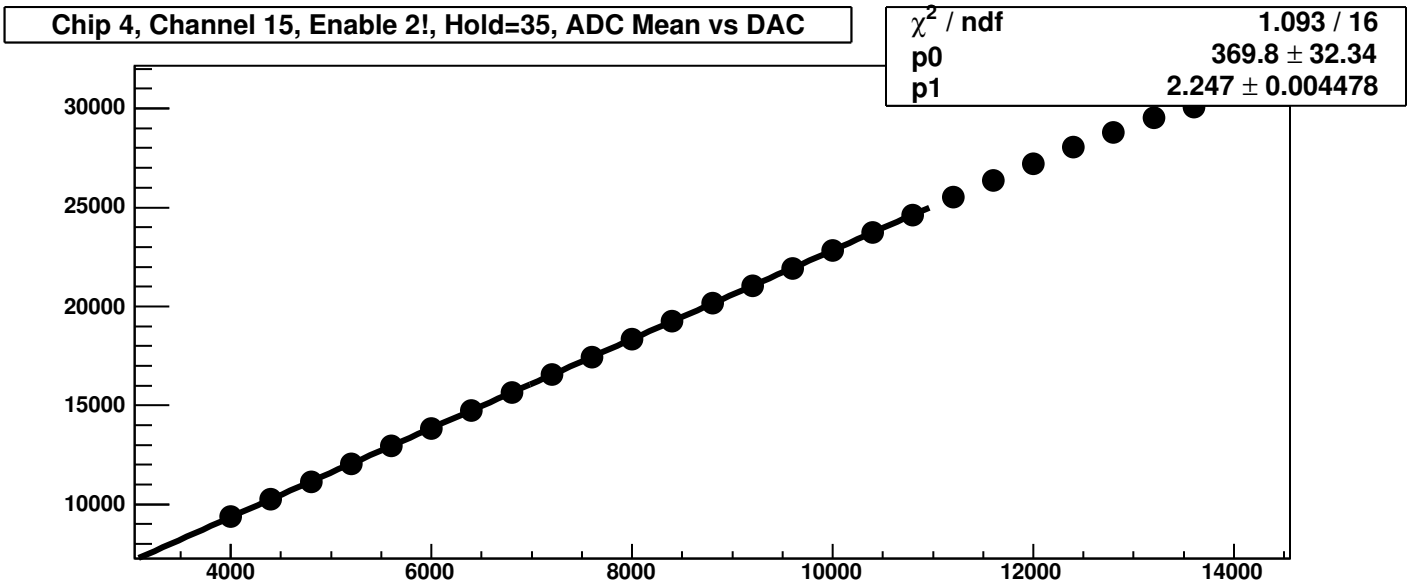

Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

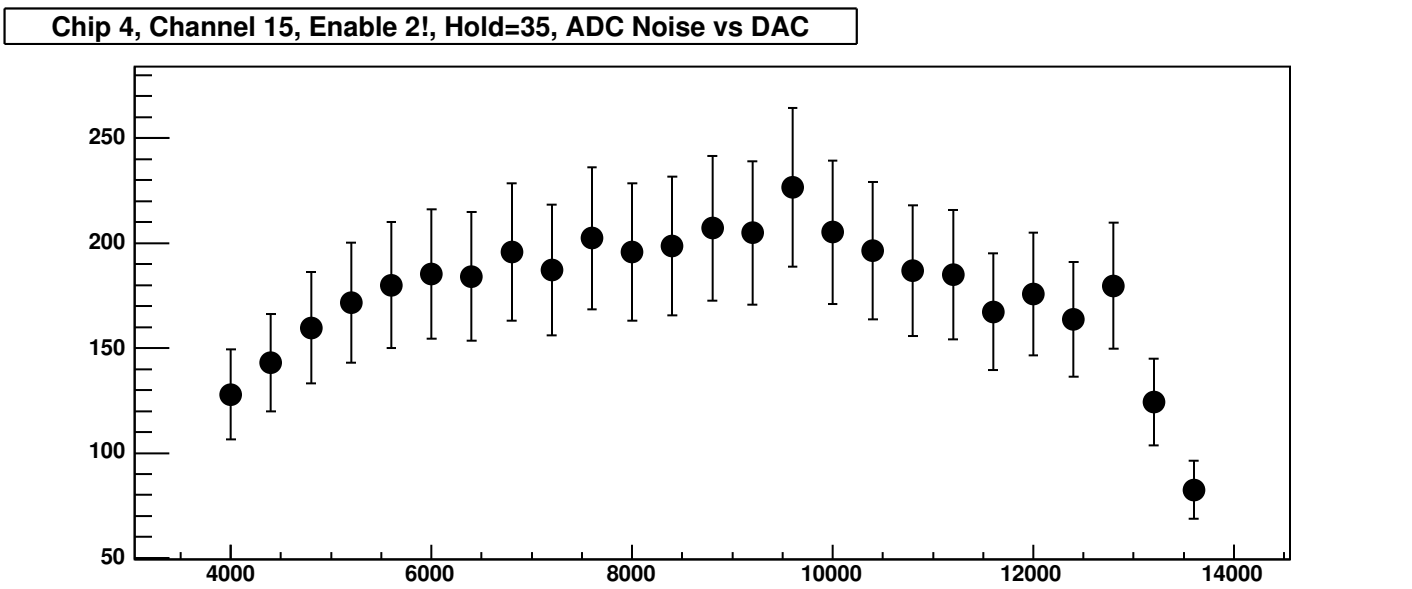

Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
0
-10
-20
-30
-40
4000
6000
8000
10000
12000
14000

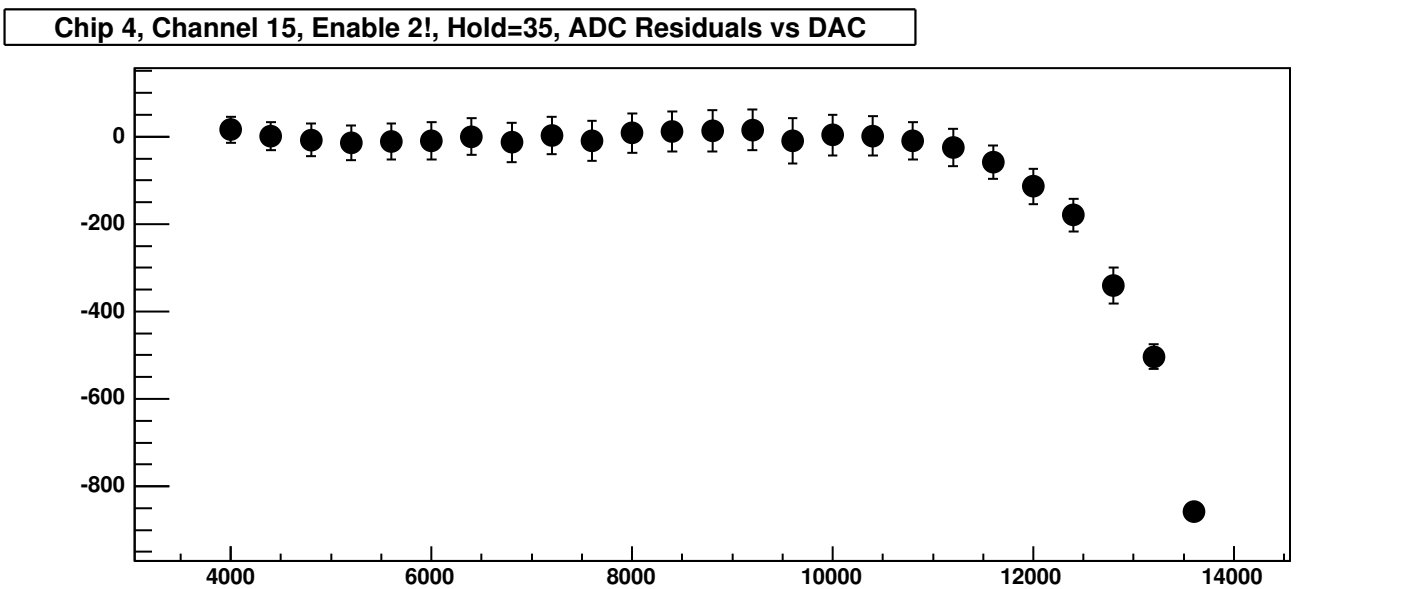

Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 9.17 / 16
p0 211.3 ± 1.471
p1 0.01637 ± 0.000187
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

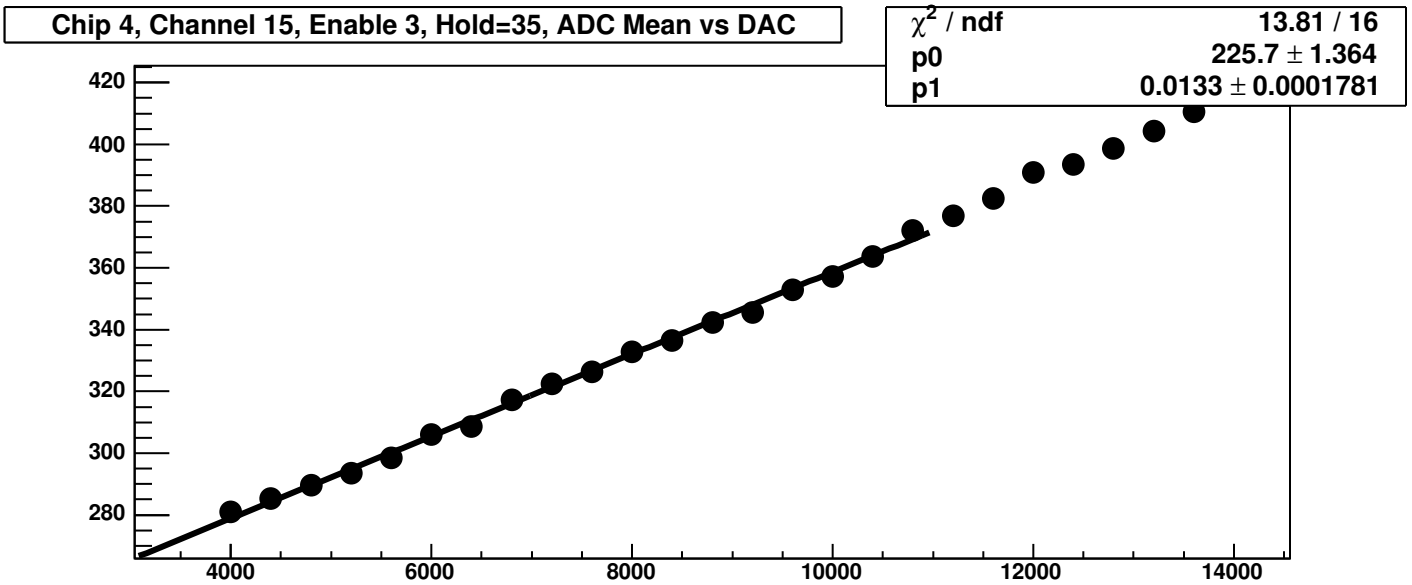

Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

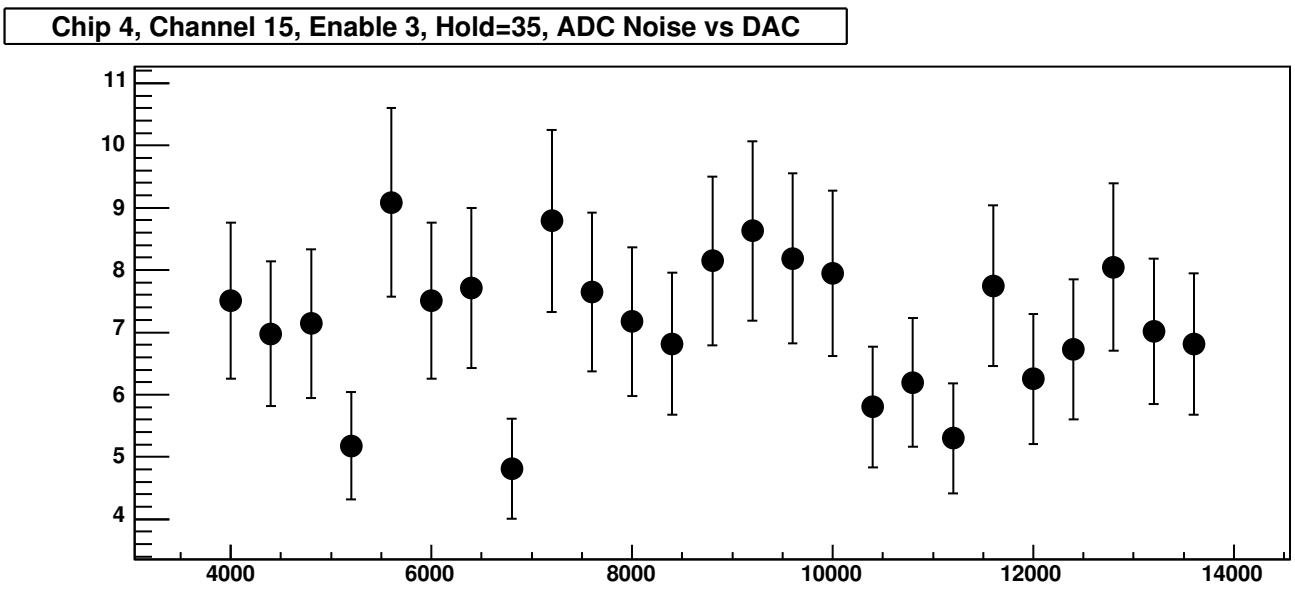

Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

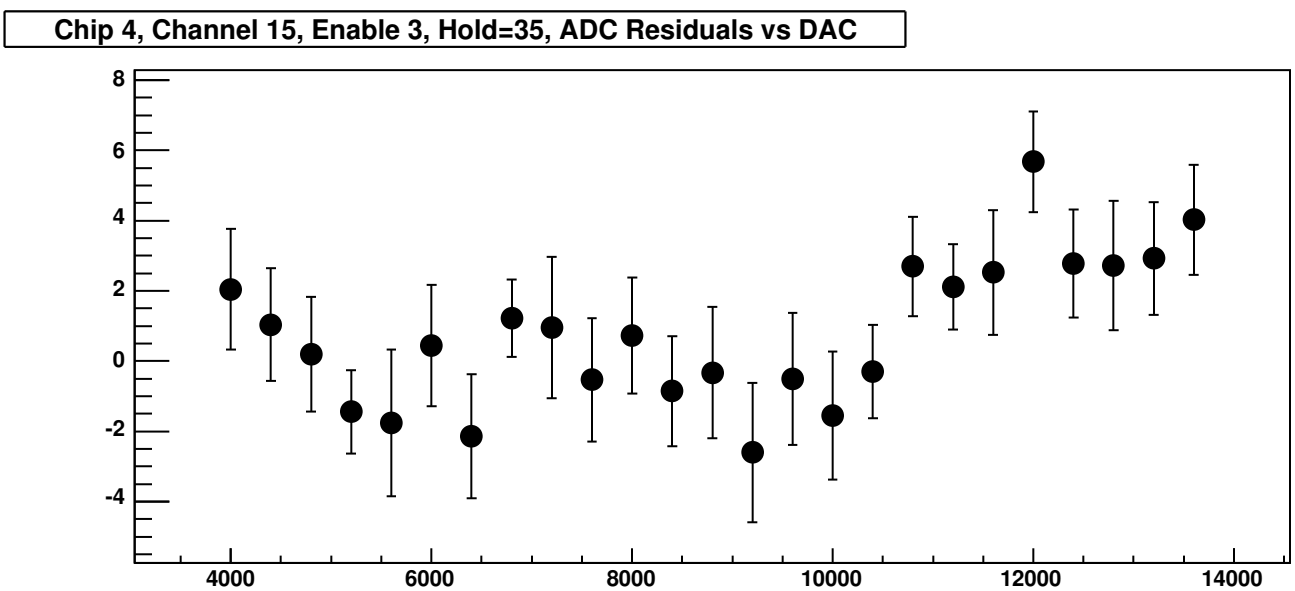

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.093 / 16
p0 369.8 ± 32.34
p1 2.247 ± 0.004478
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

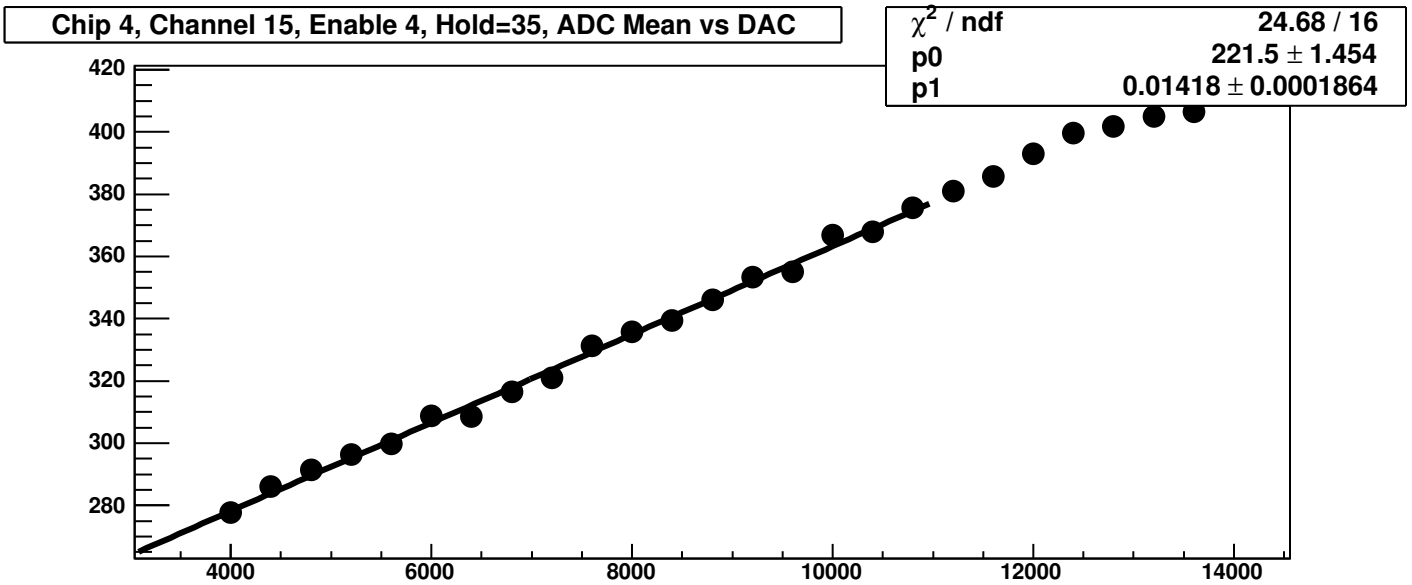

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
4000
6000
8000
10000
12000
14000

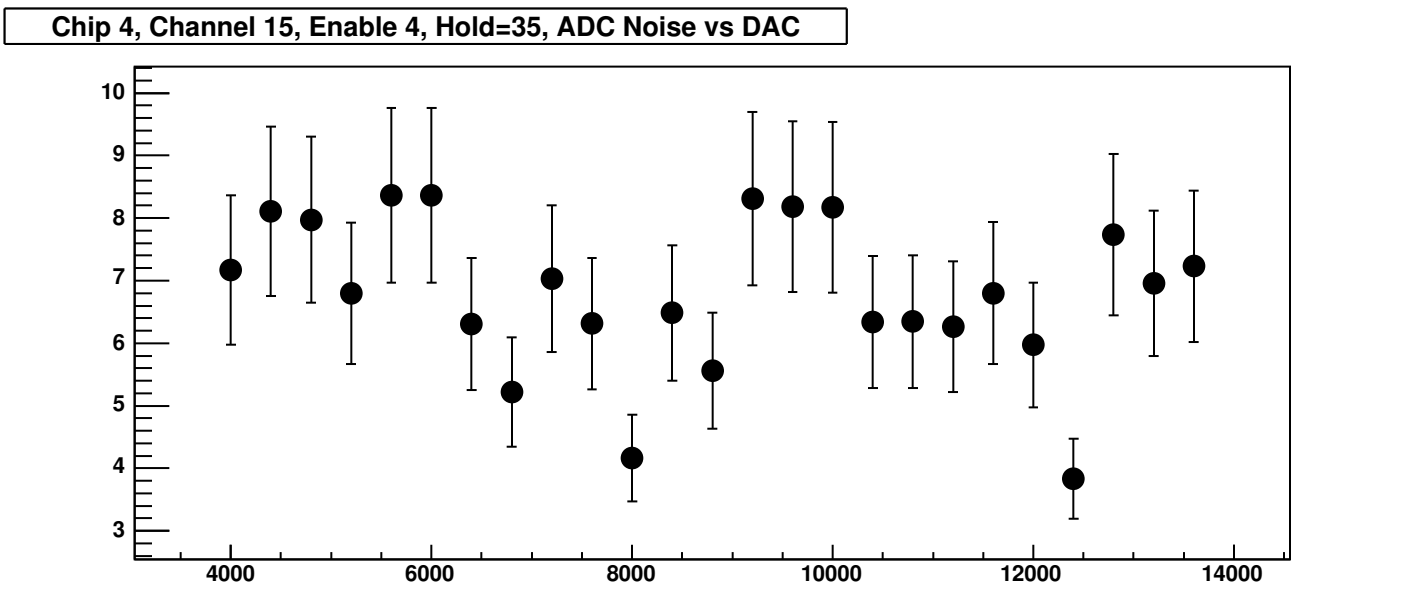

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
4000
6000
8000
10000
12000
14000

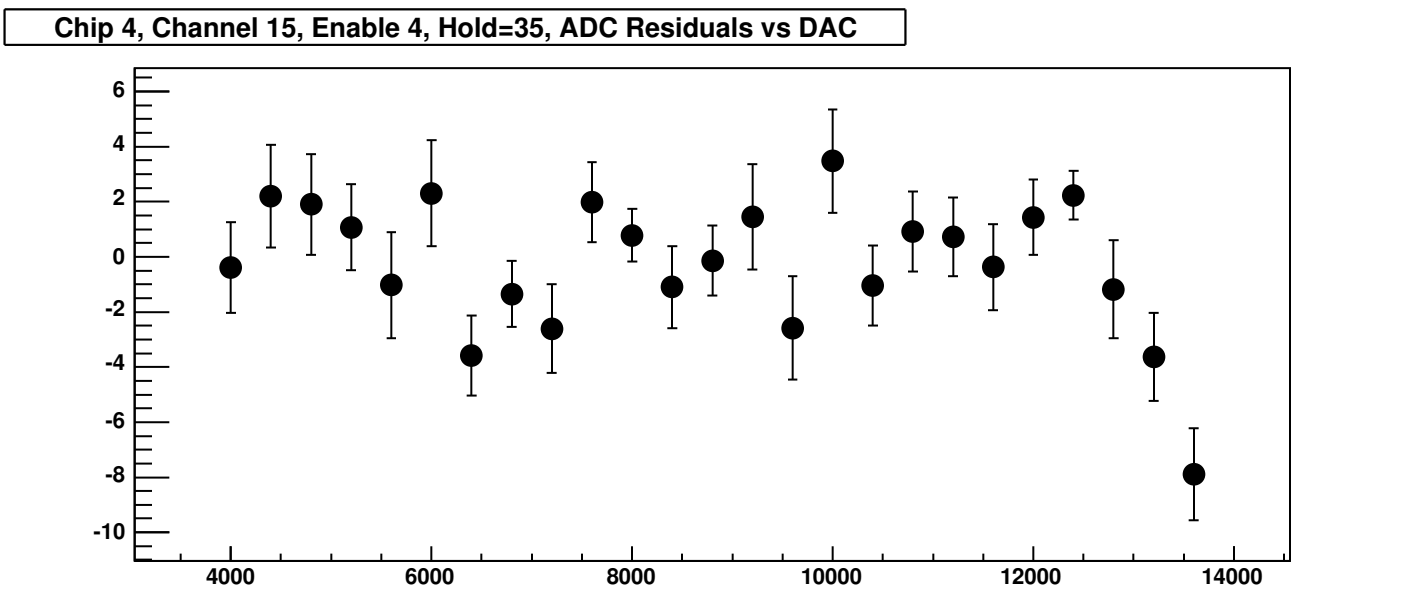

Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 13.81 / 16
p0 225.7 ± 1.364
p1 0.0133 ± 0.0001781
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

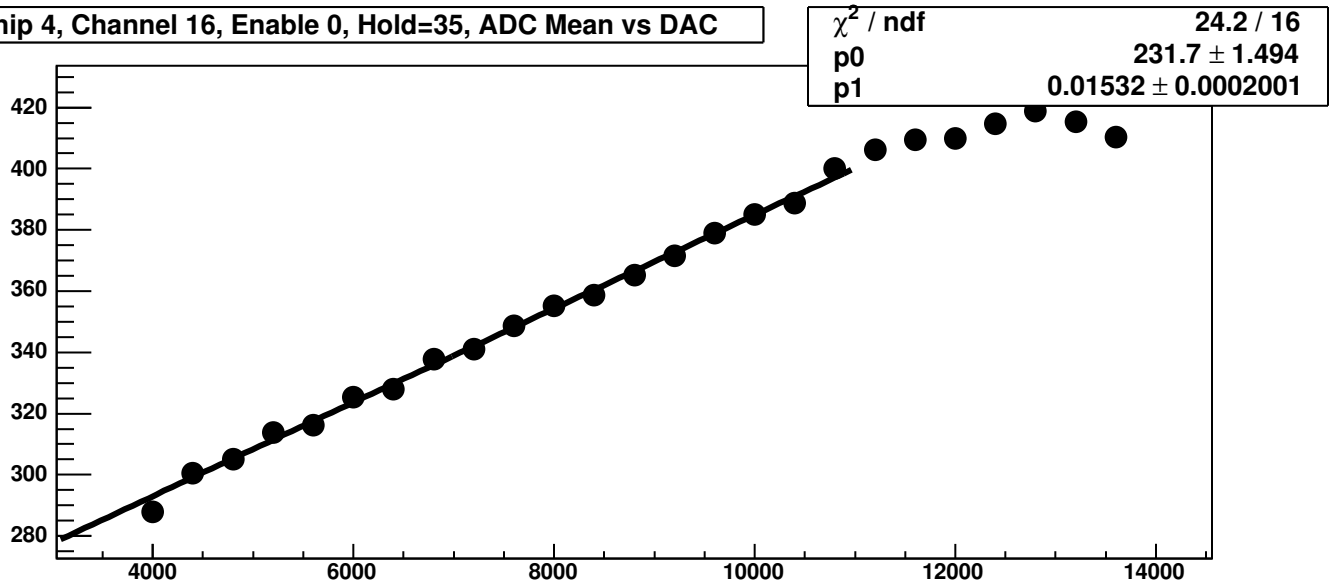

Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.68 / 16
p0 221.5 ± 1.454
p1 0.01418 ± 0.0001864
420
400
380
360
340
320
300
280
4000
6000
8000
10000
12000
14000

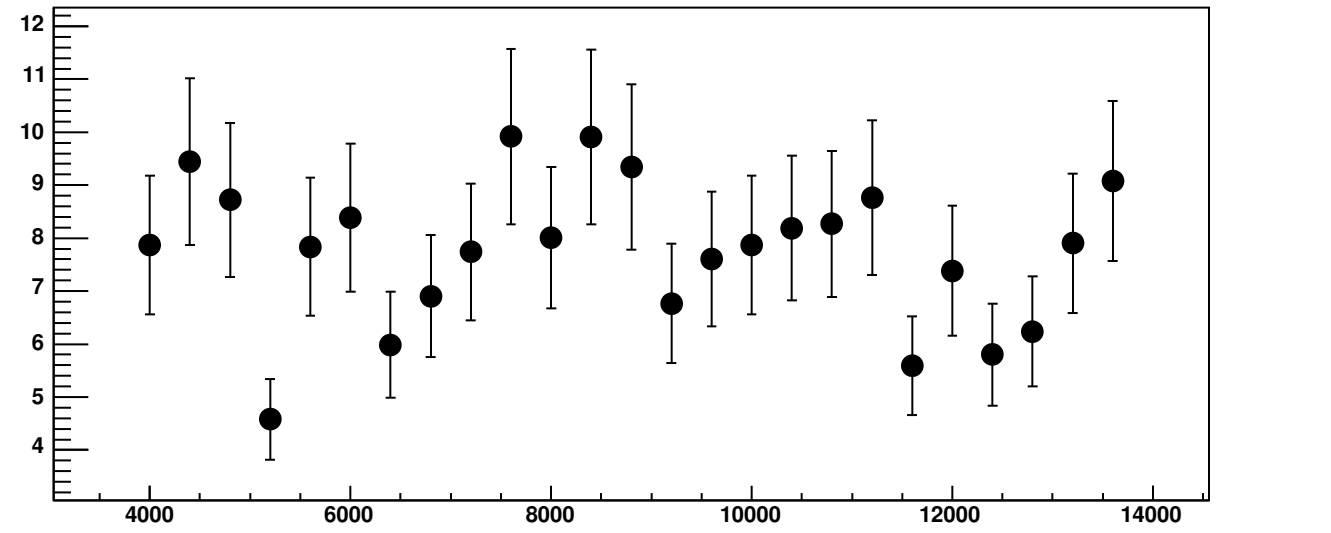

Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

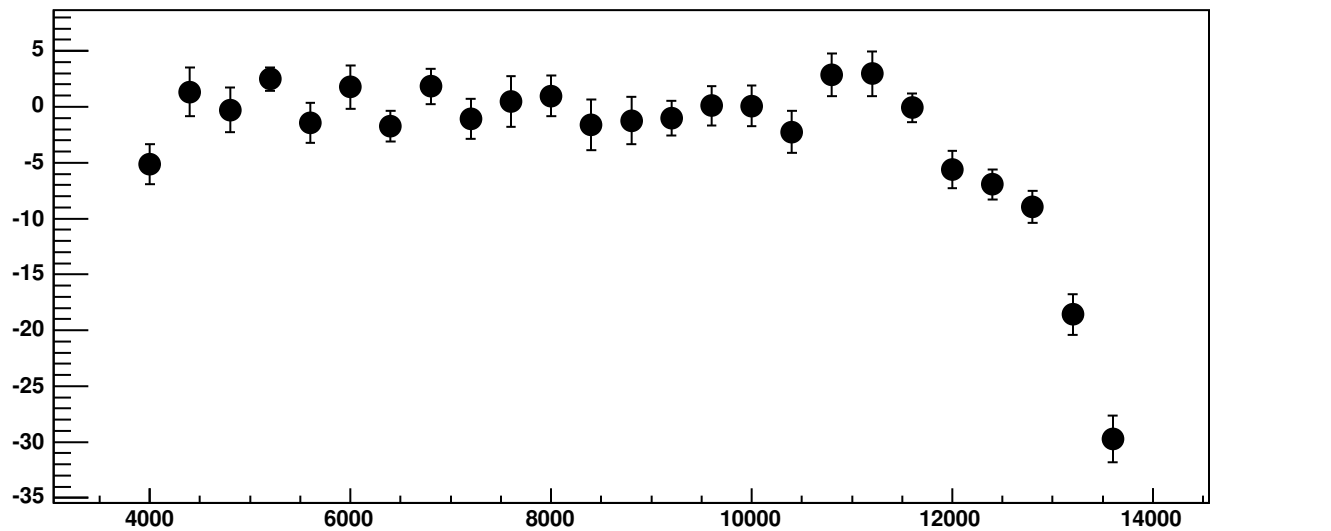
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

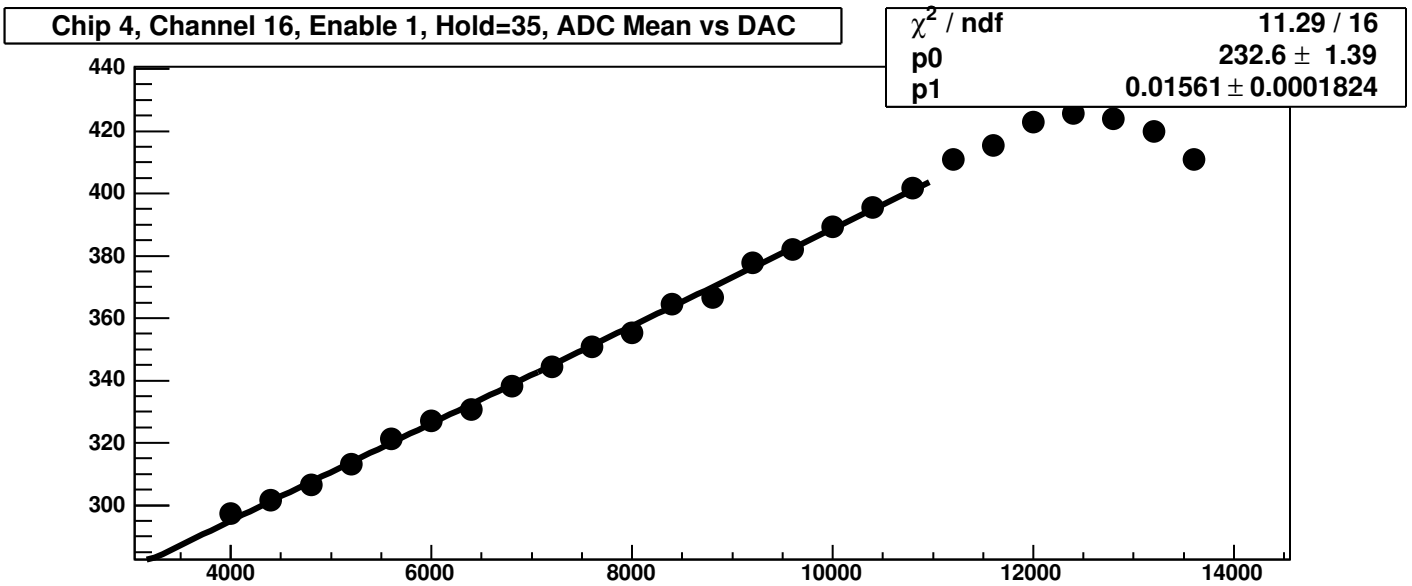


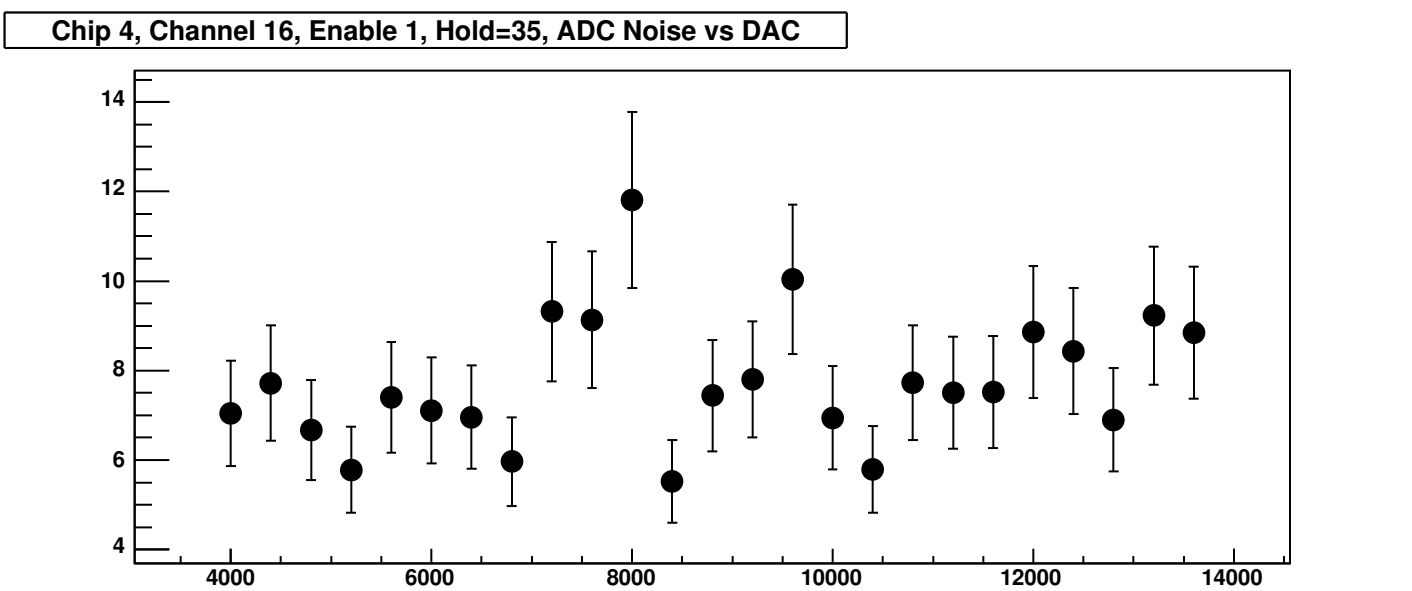
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

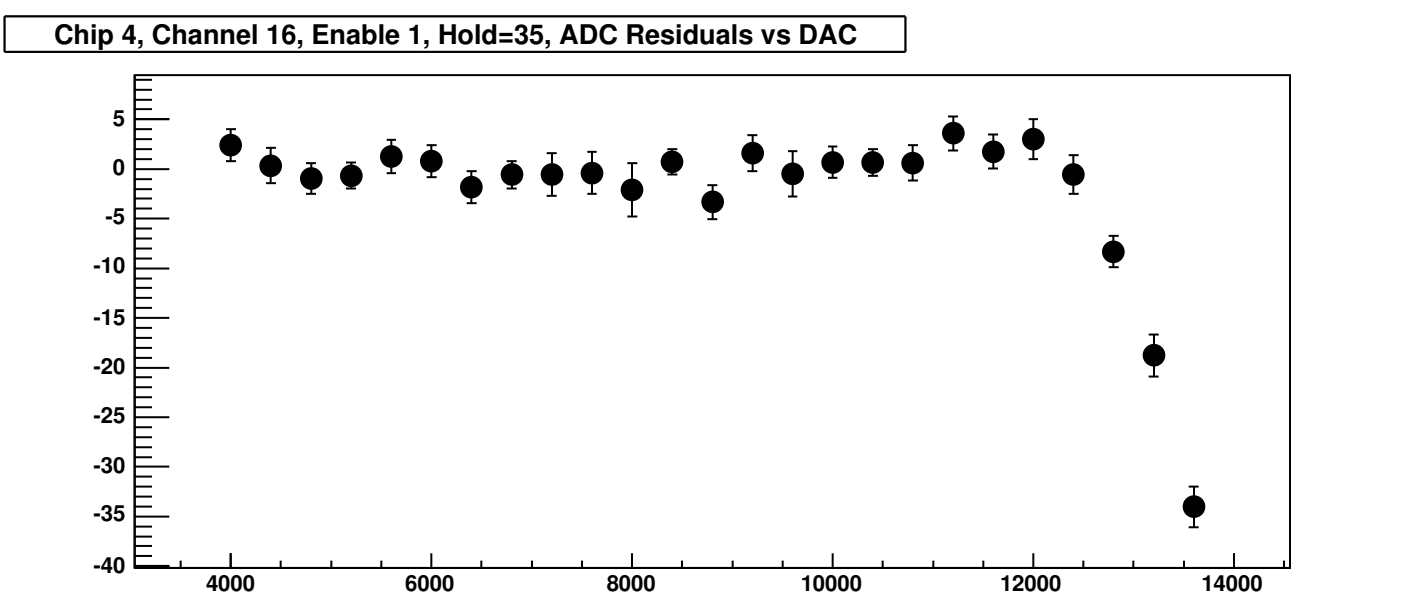


Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



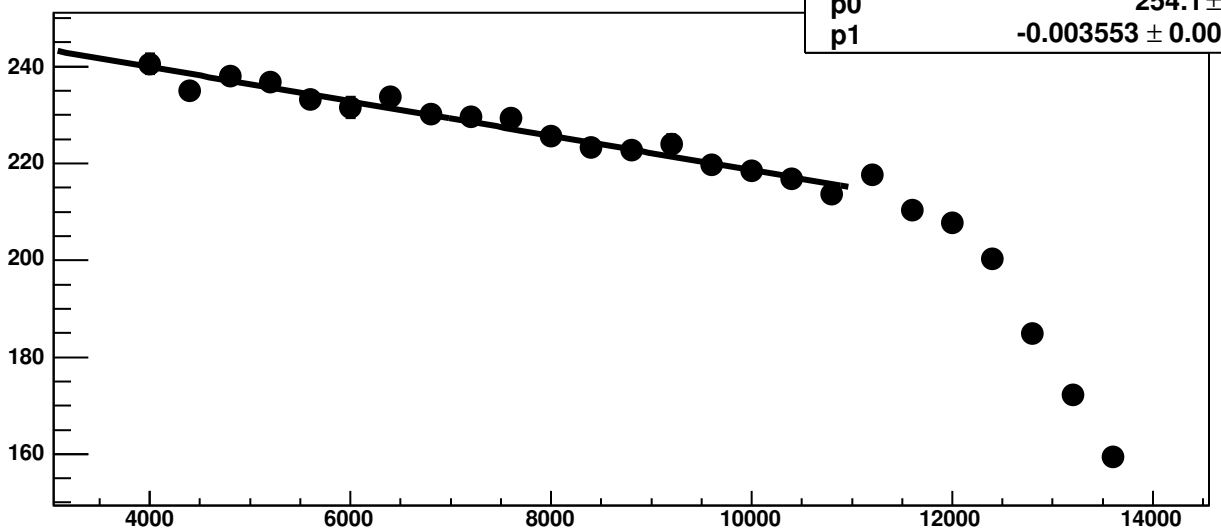

Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 11.29 / 16
p0 232.6 ± 1.39
p1 0.01561 ± 0.0001824
440
420
400
380
360
340
320
300
4000
6000
8000
10000
12000
14000


Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

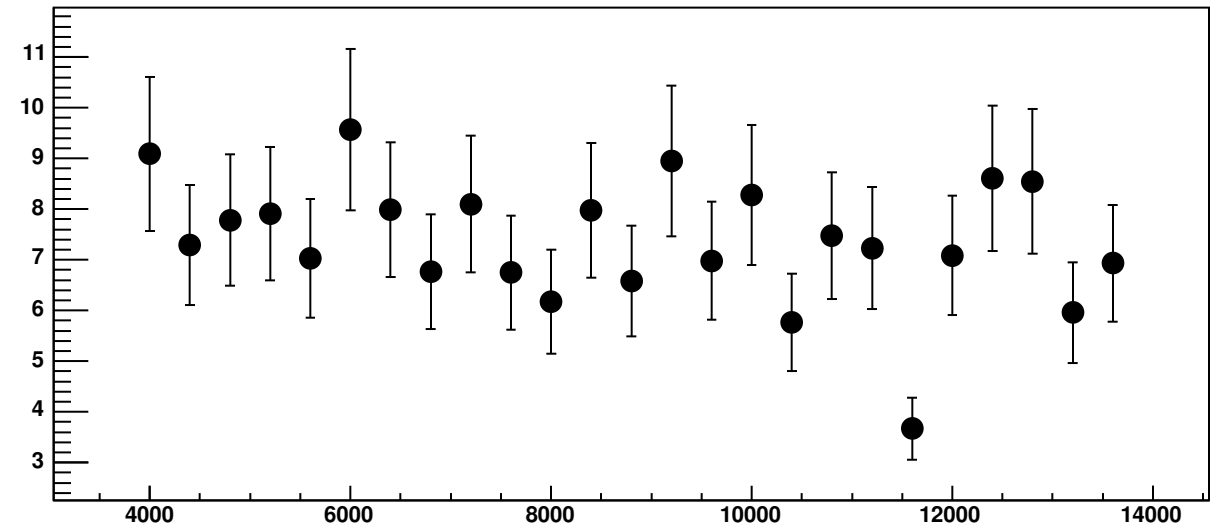

Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
-40
4000
6000
8000
10000
12000
14000

Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

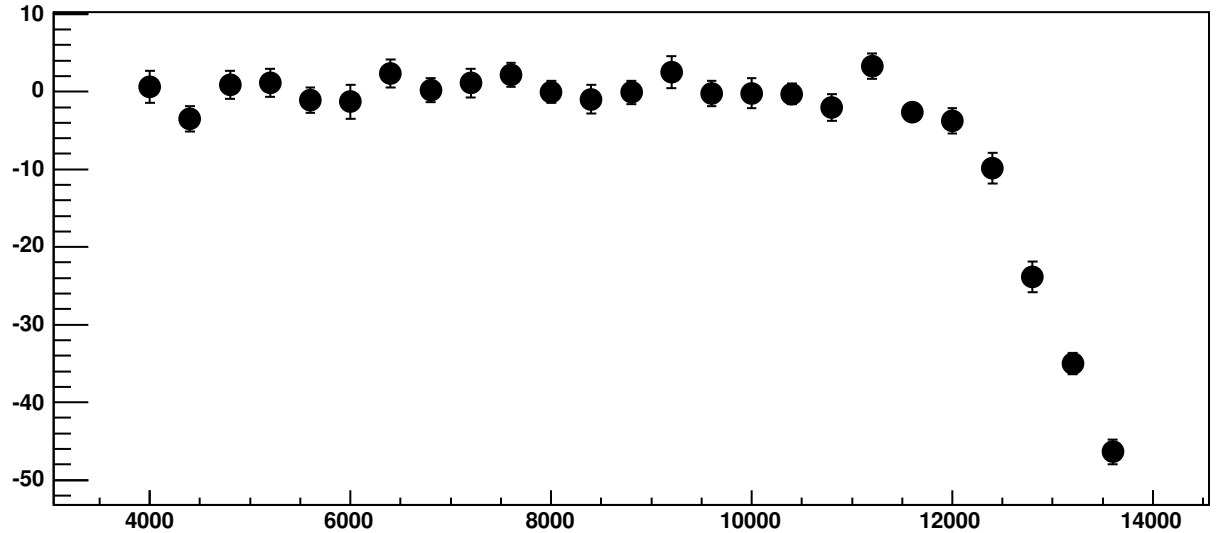
χ^2 / ndf	13.47 / 16
p0	254.1 ± 1.527
p1	-0.003553 ± 0.0001943



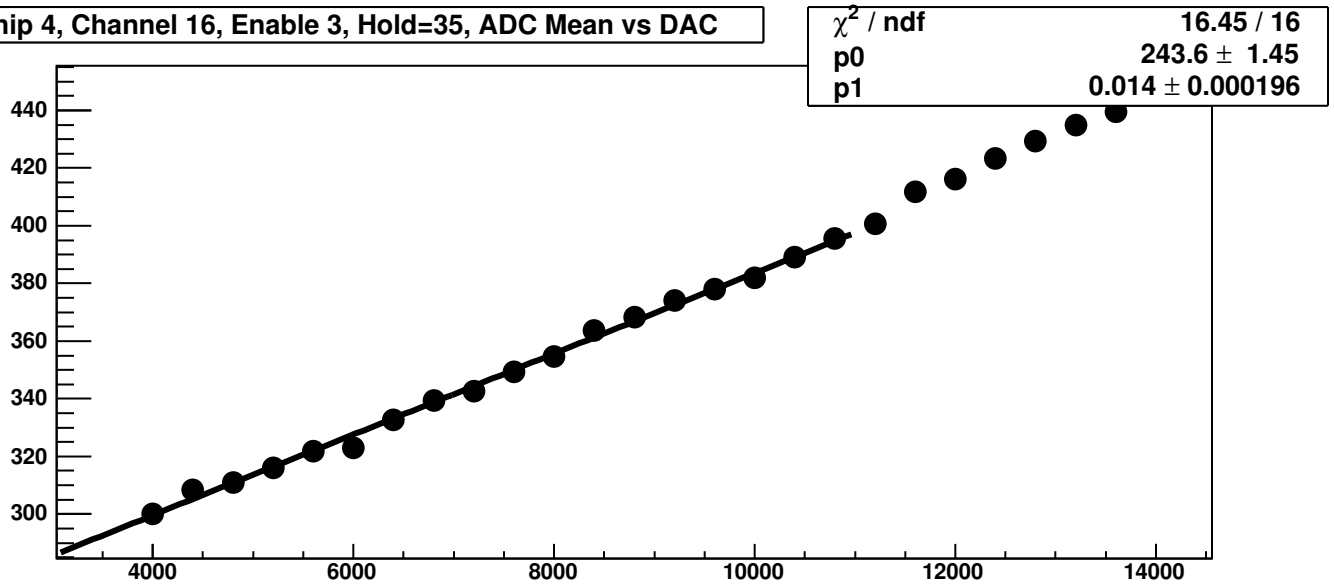
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



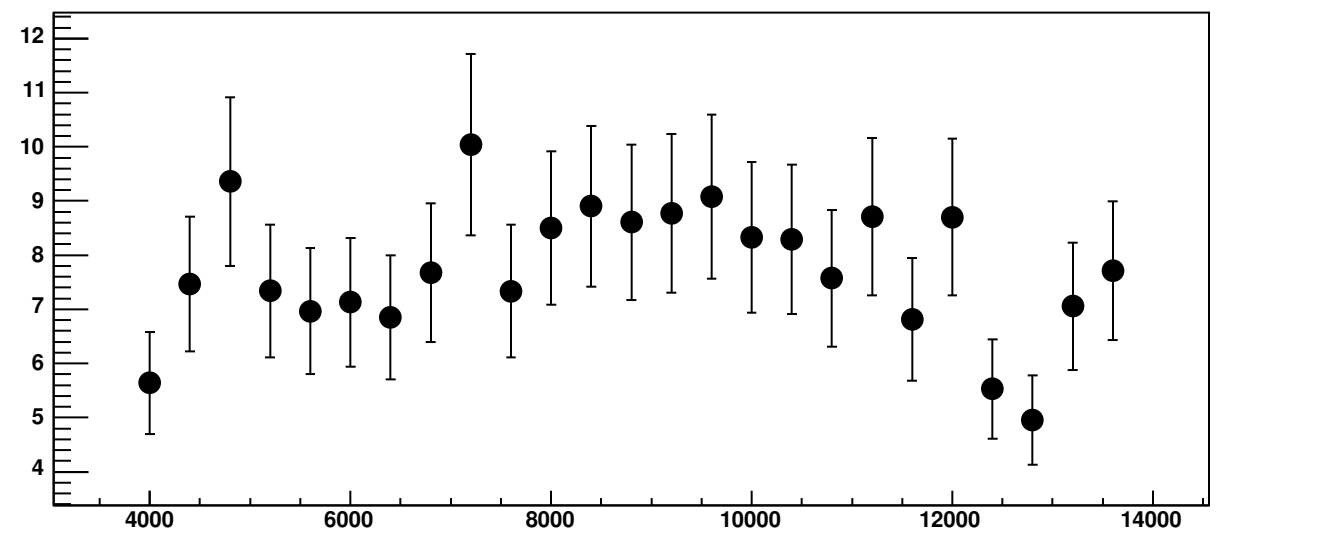
Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



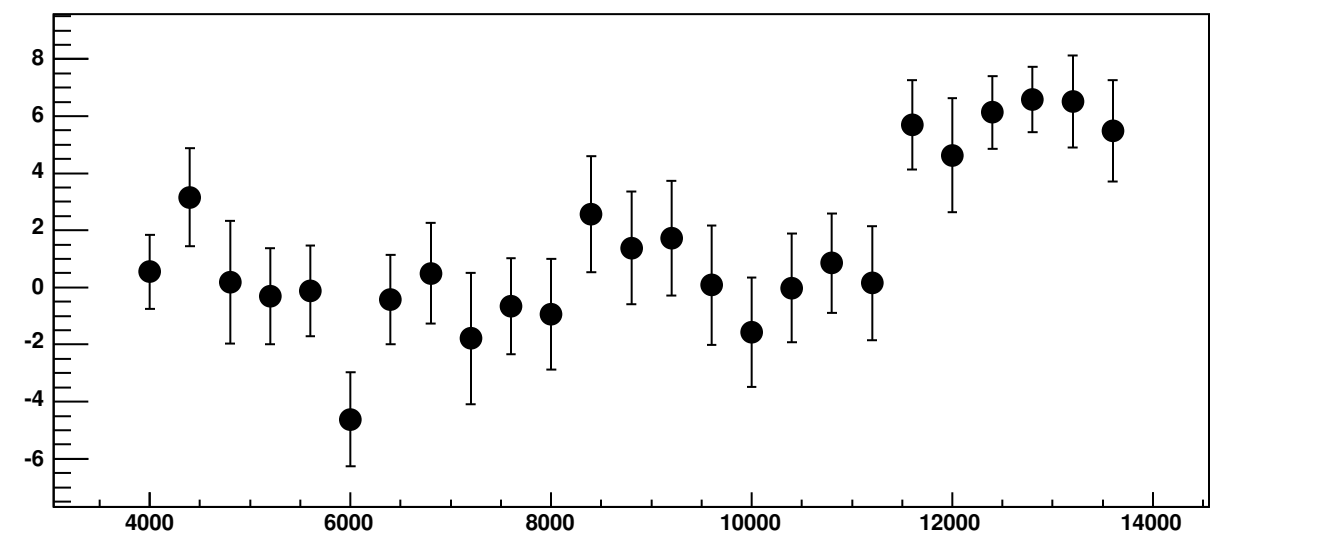
Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

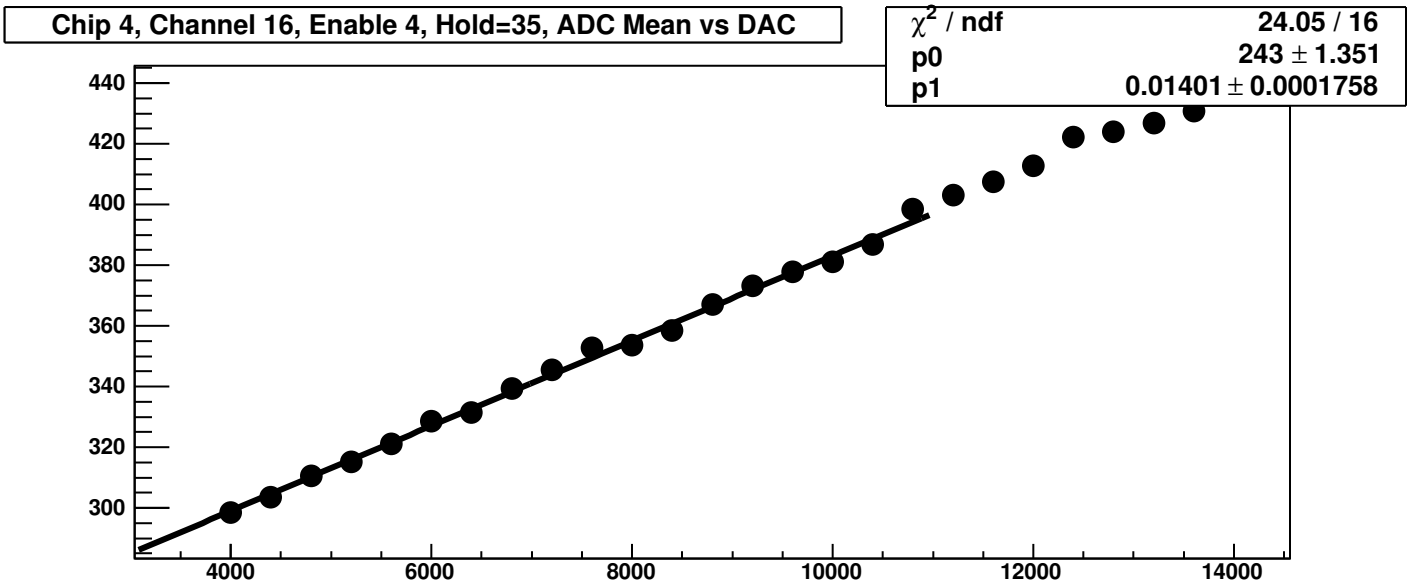


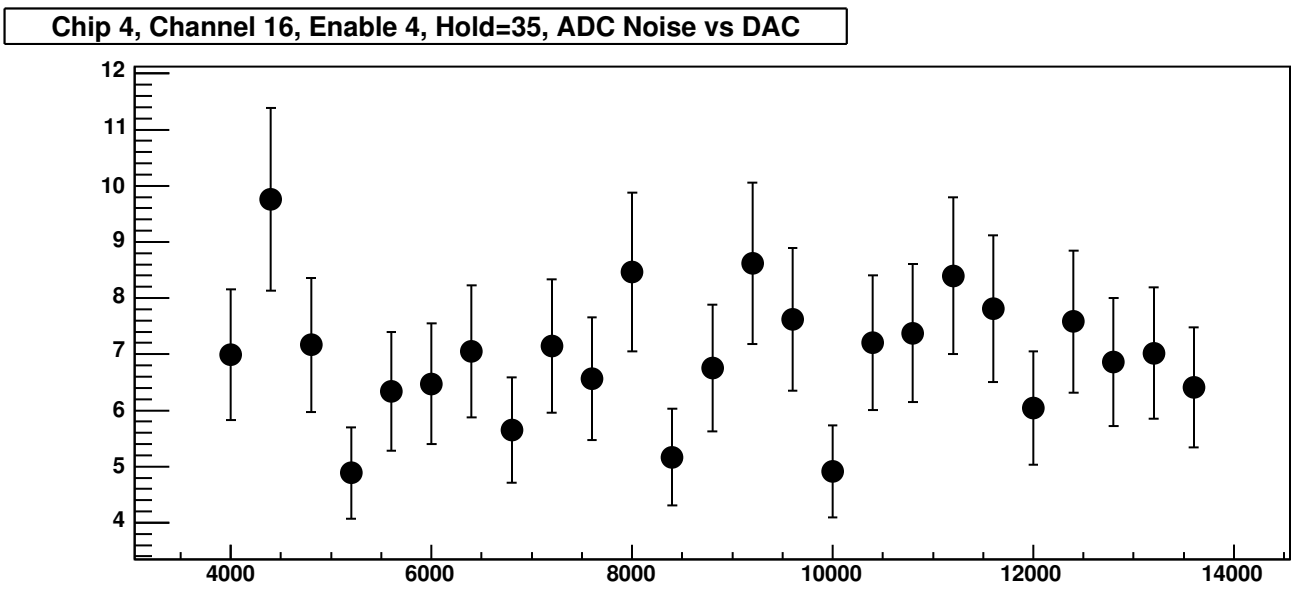
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

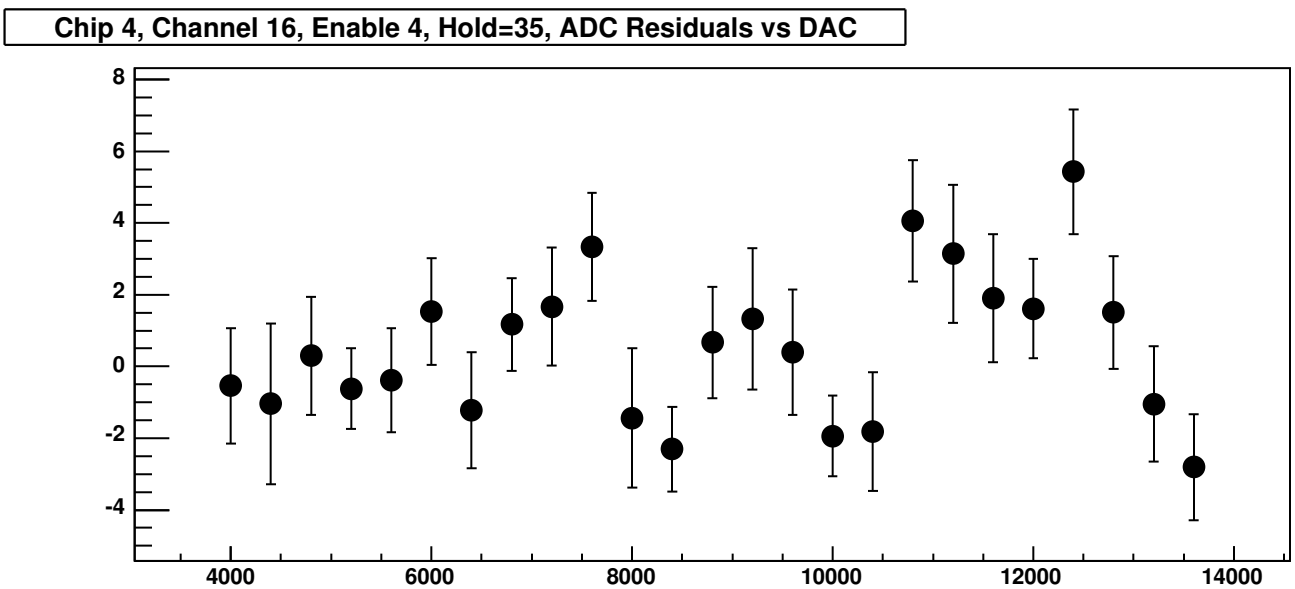


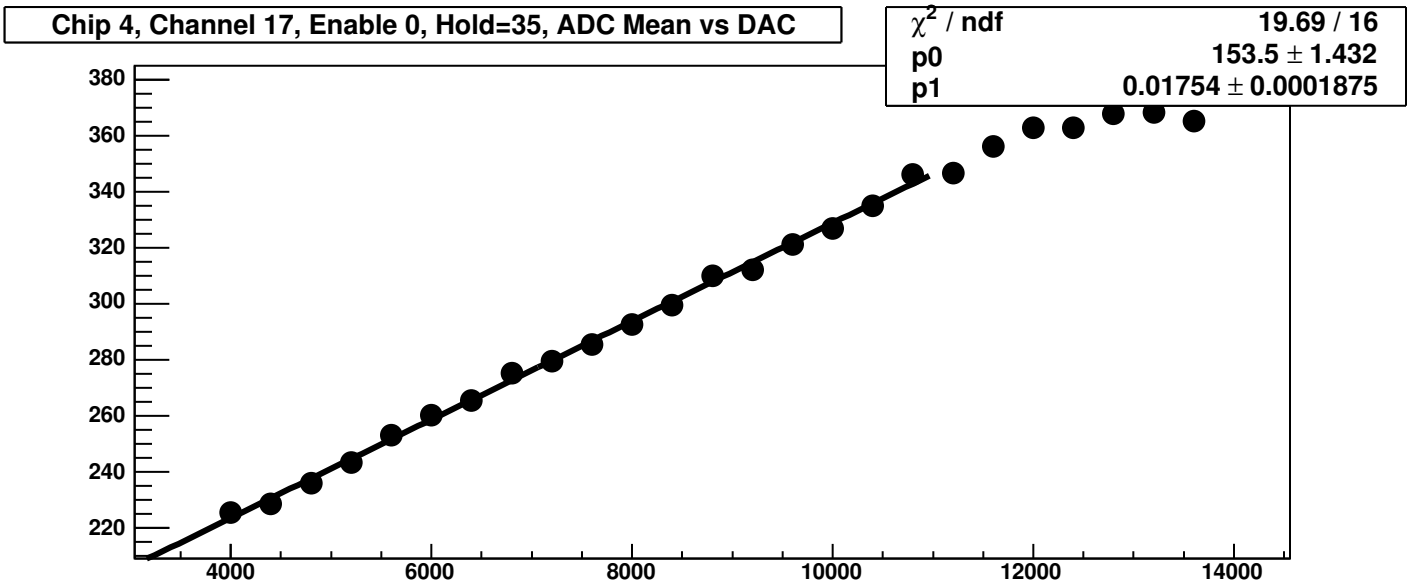
Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

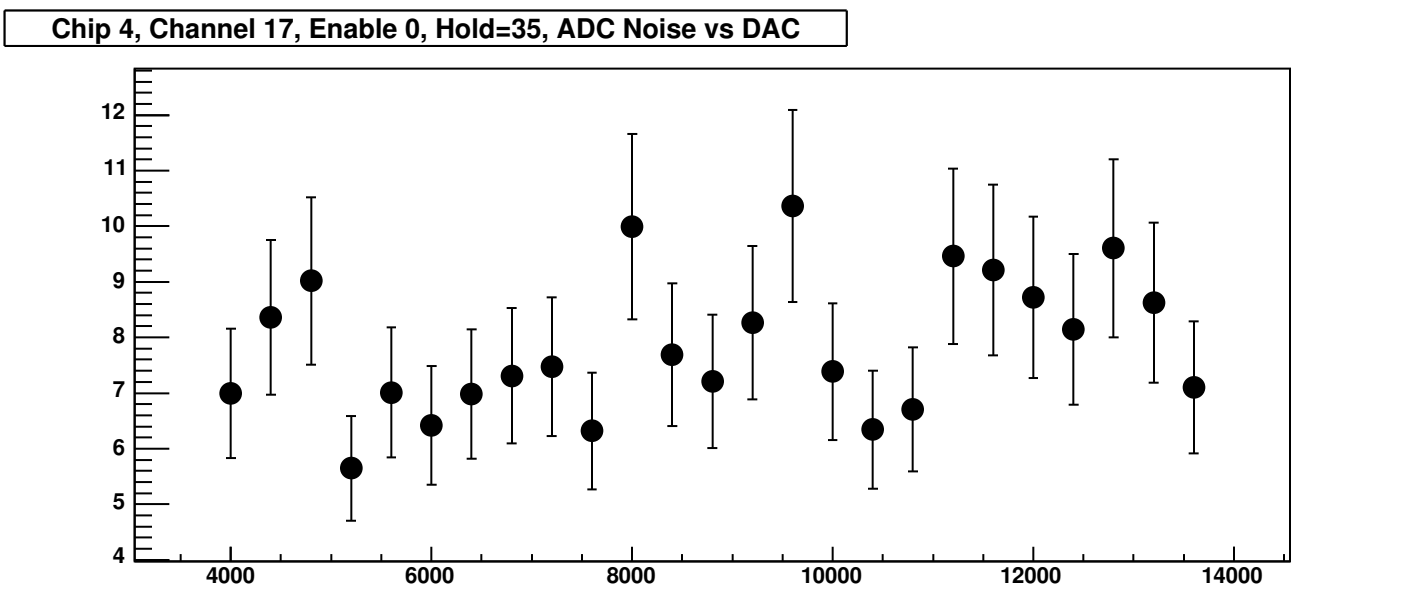


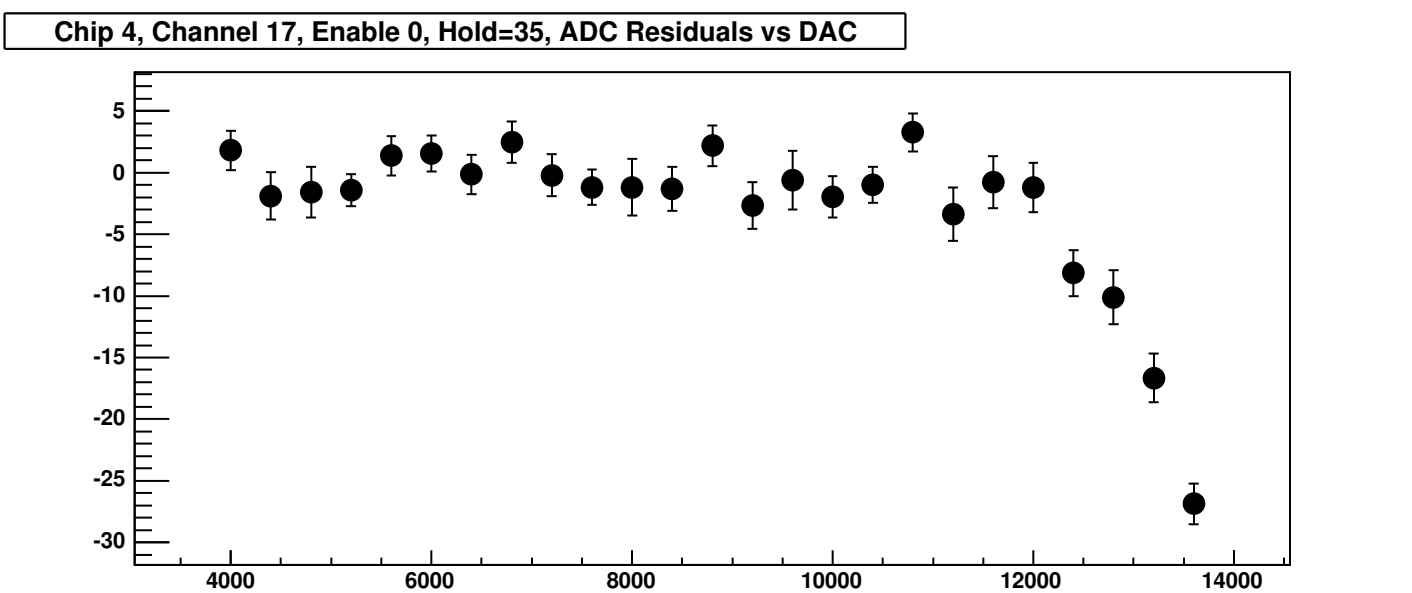

Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.05 / 16
p0 243 ± 1.351
p1 0.01401 ± 0.0001758
440
420
400
380
360
340
320
300
4000
6000
8000
10000
12000
14000

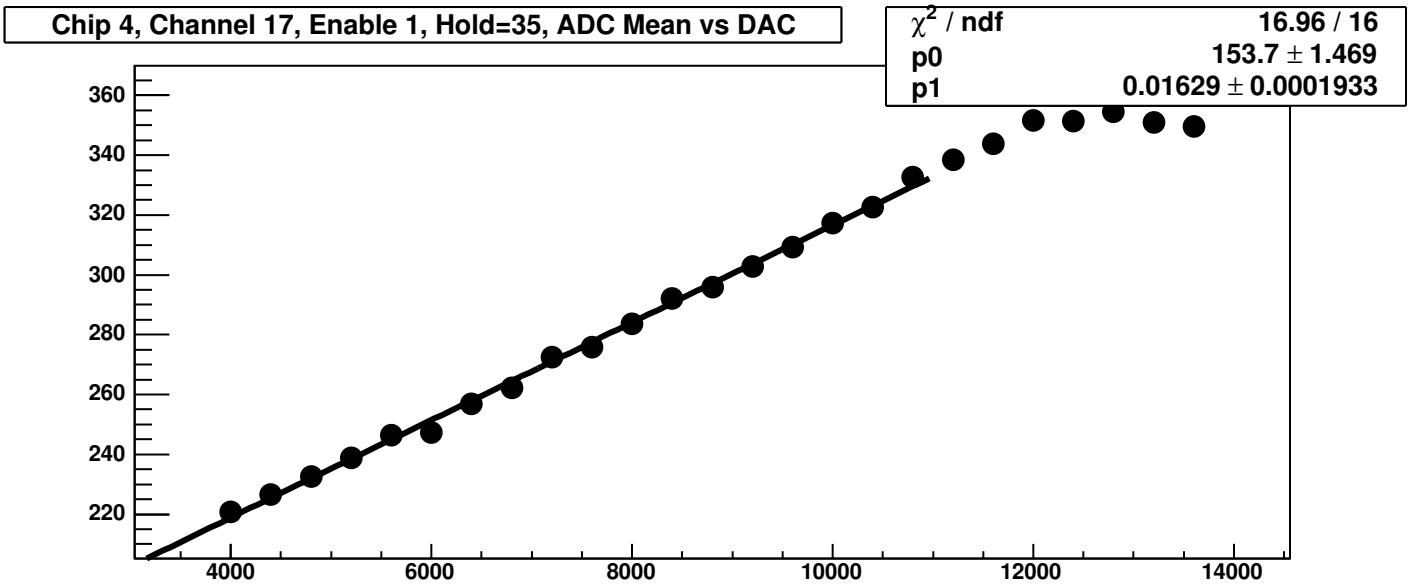

Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

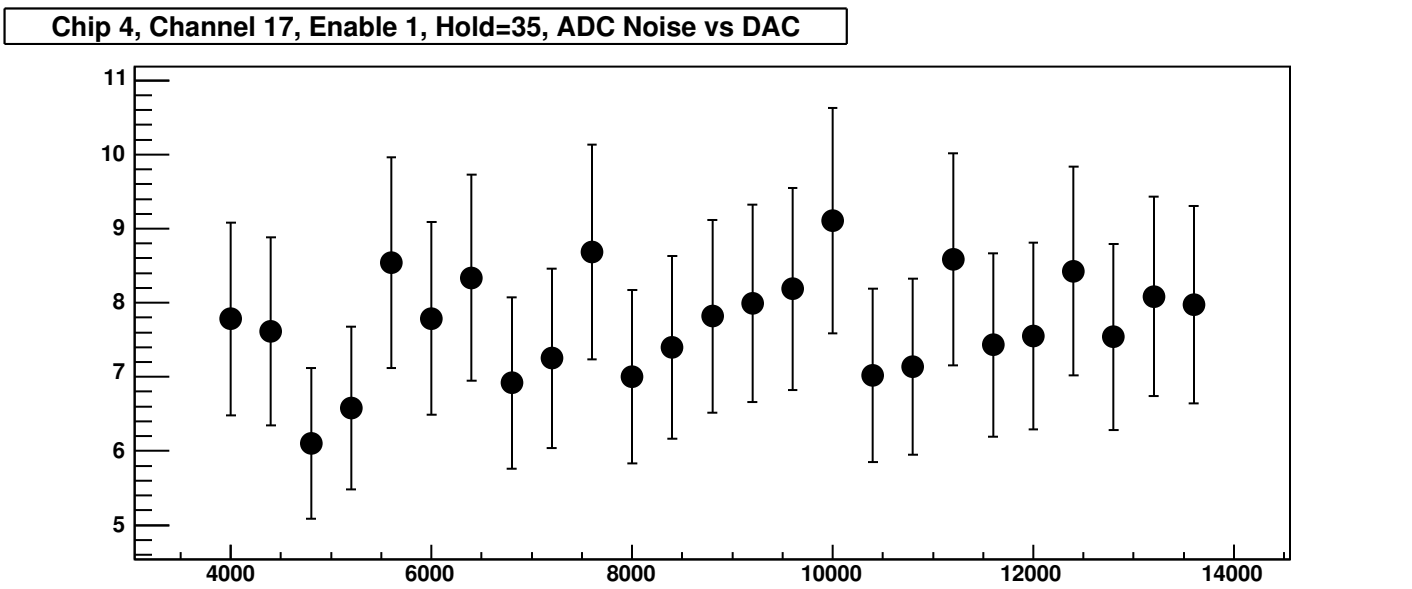

Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

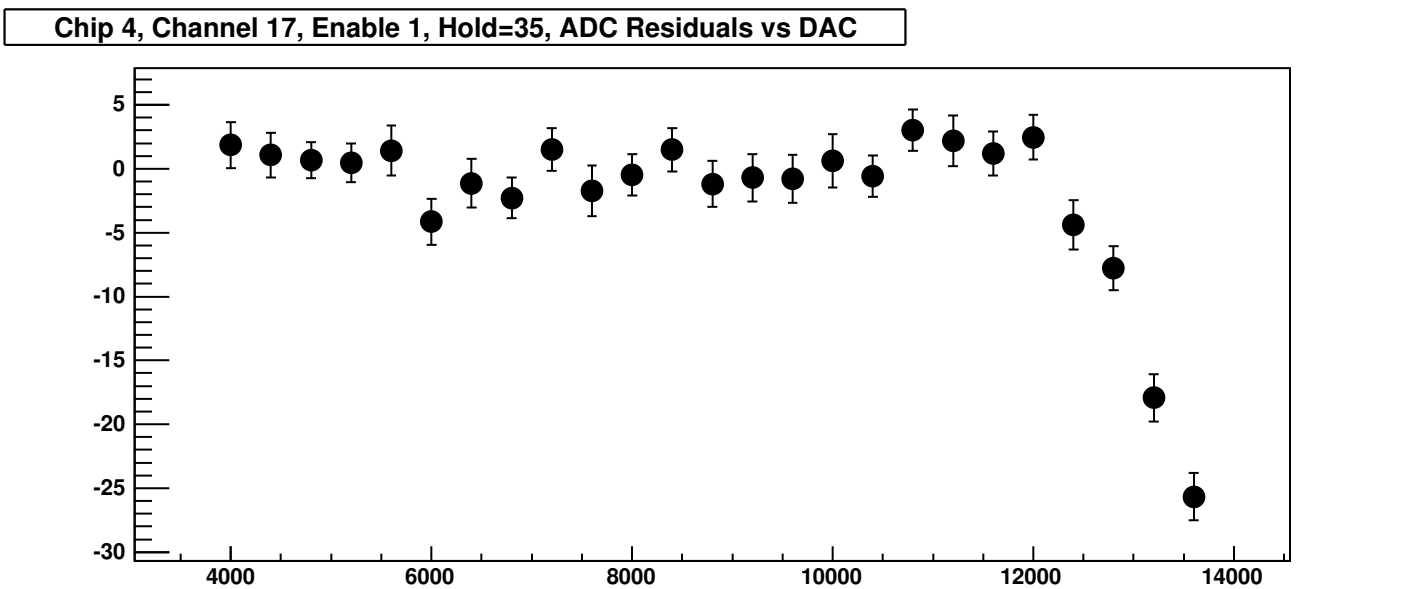

Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 19.69 / 16
p0 153.5 ± 1.432
p1 0.01754 ± 0.0001875
380
360
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000

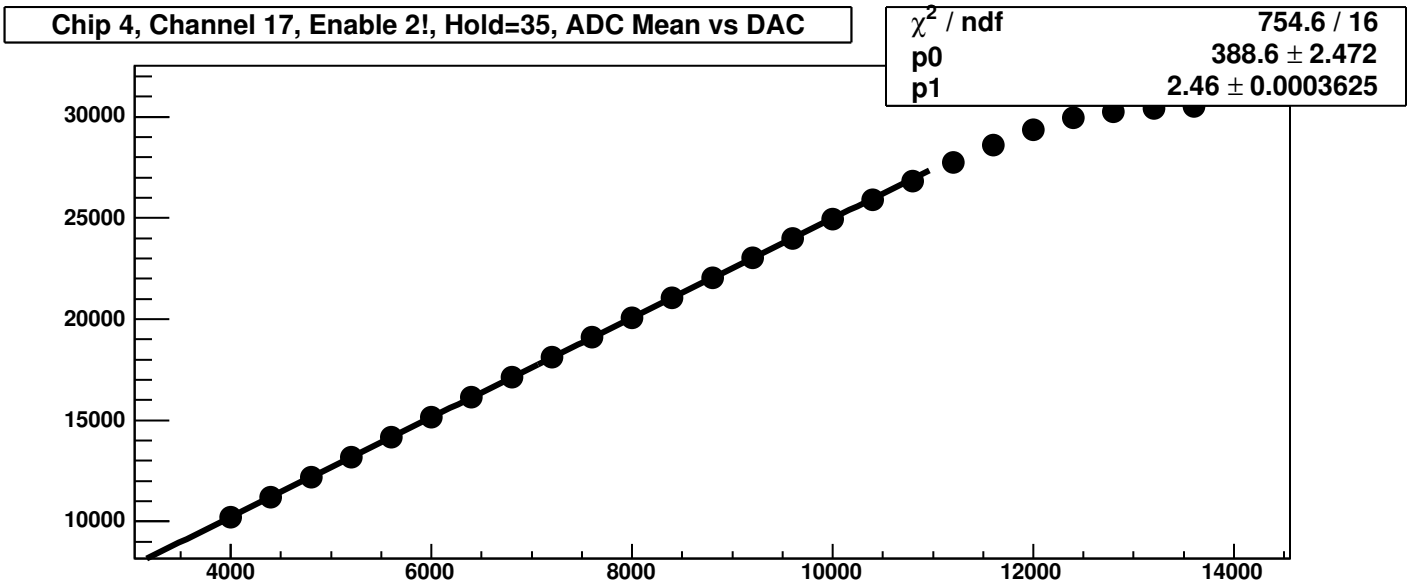

Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

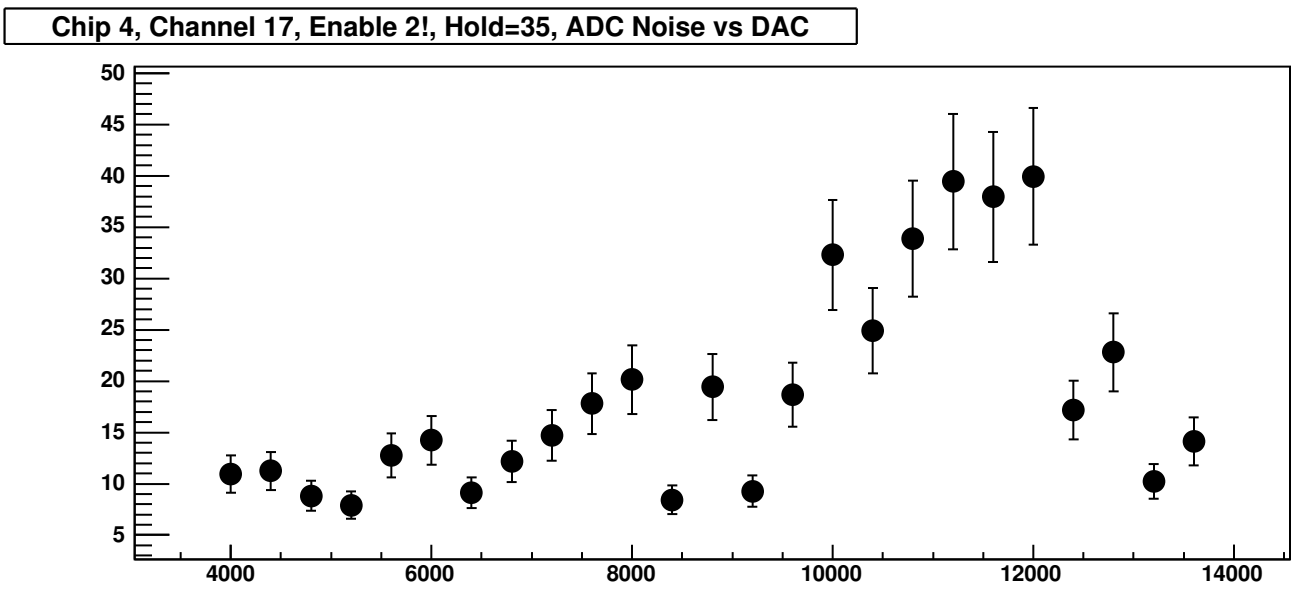

Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

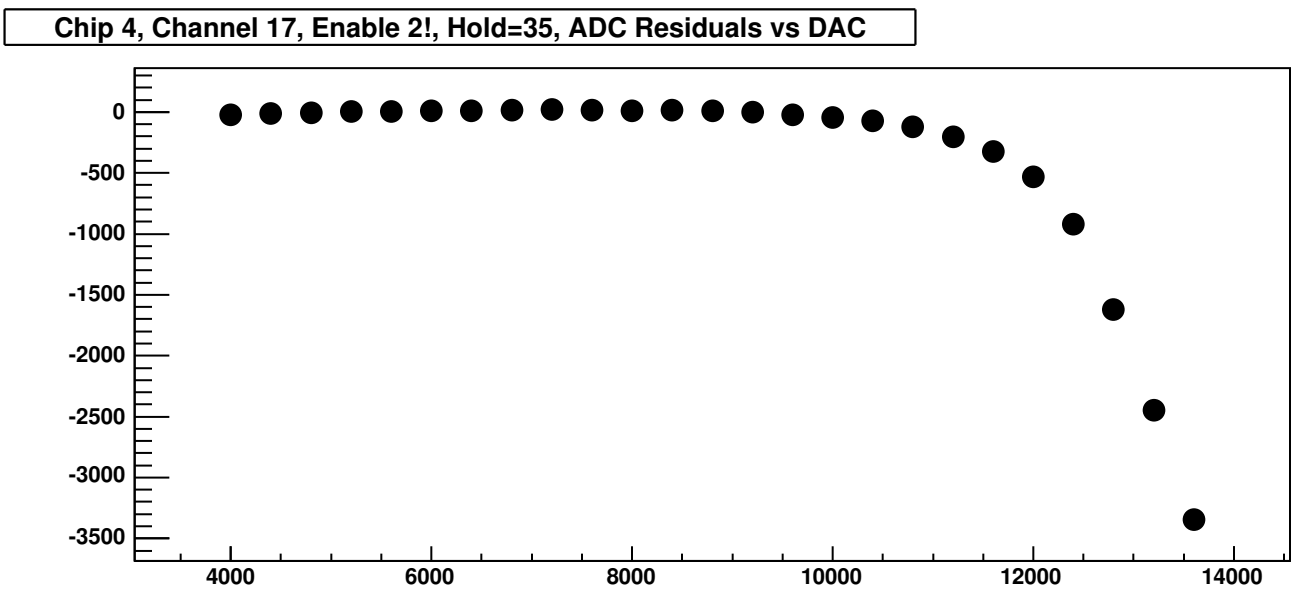

Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.96 / 16
p0 153.7 ± 1.469
p1 0.01629 ± 0.0001933
360
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000

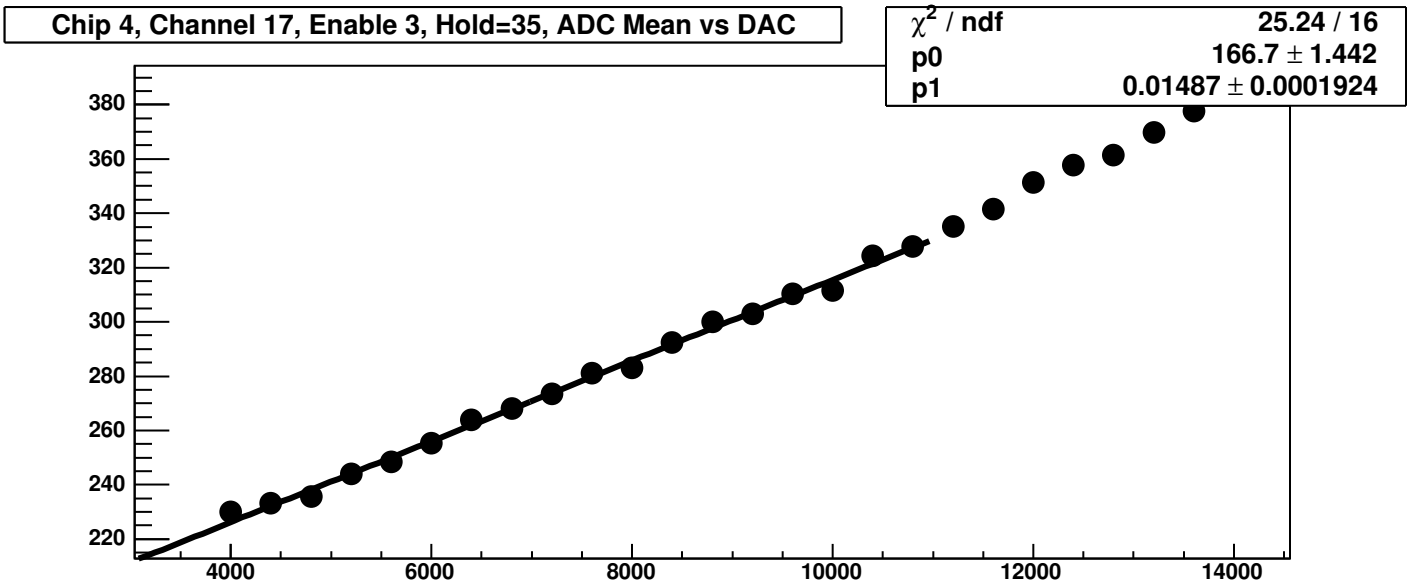

Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

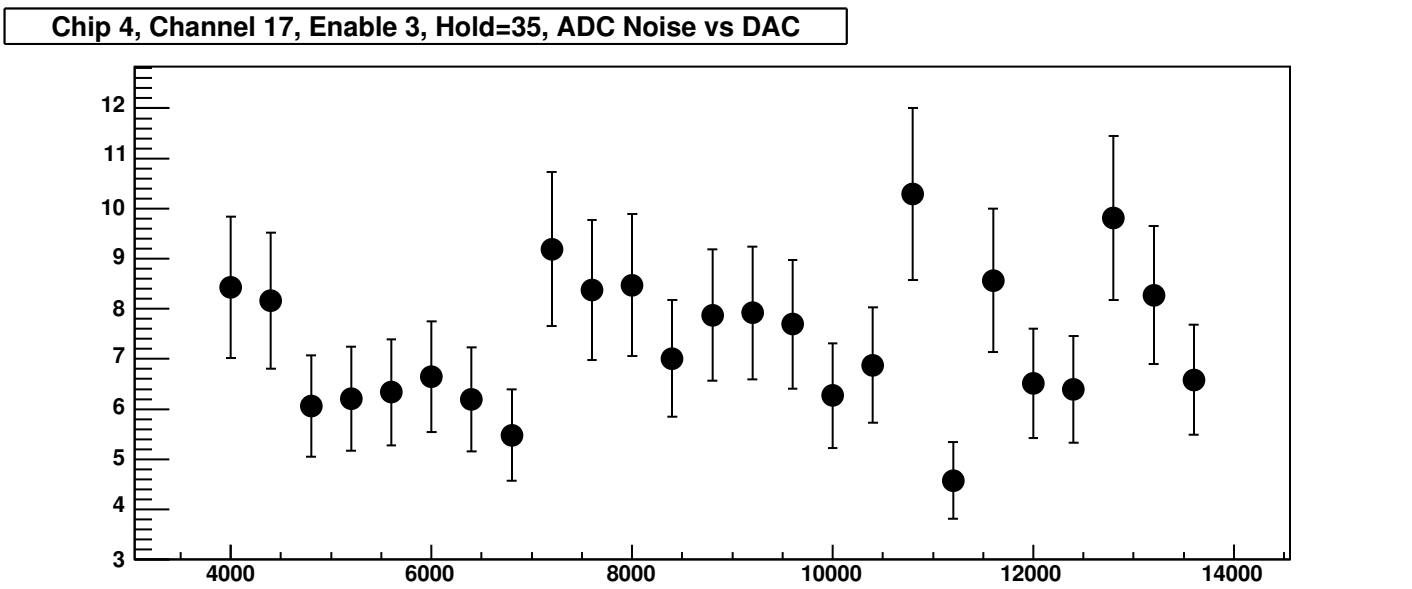

Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
4000
6000
8000
10000
12000
14000

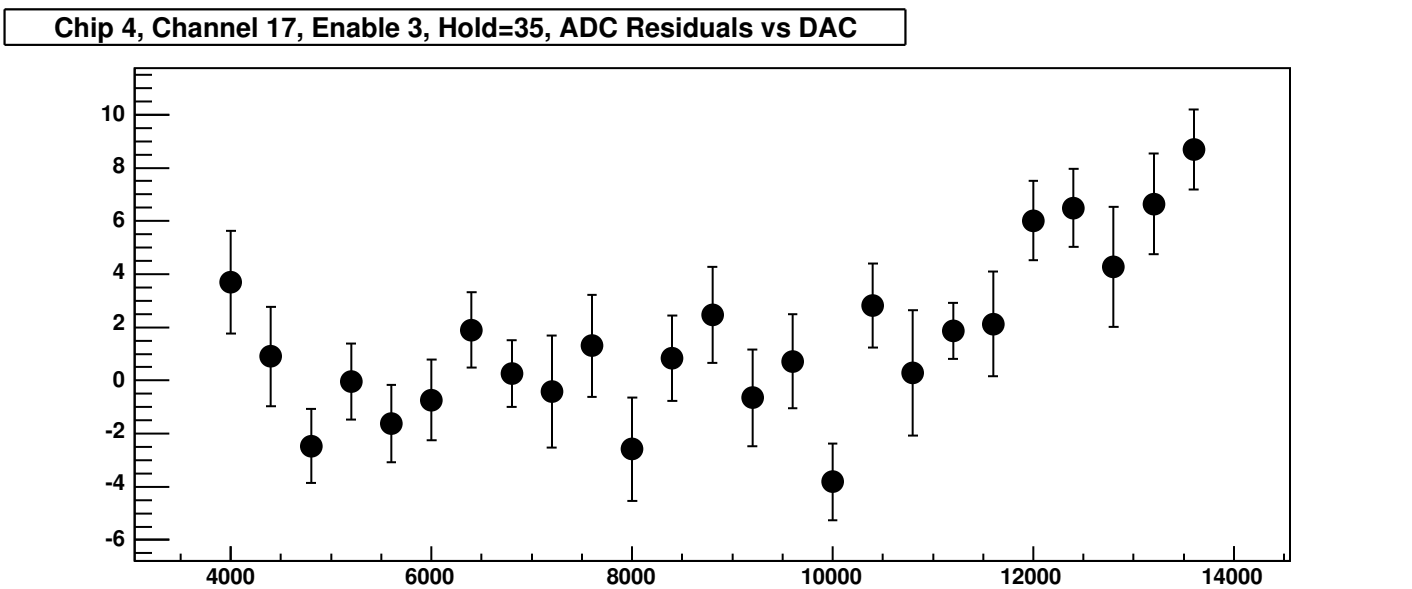

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 754.6 / 16
p0 388.6 ± 2.472
p1 2.46 ± 0.0003625
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

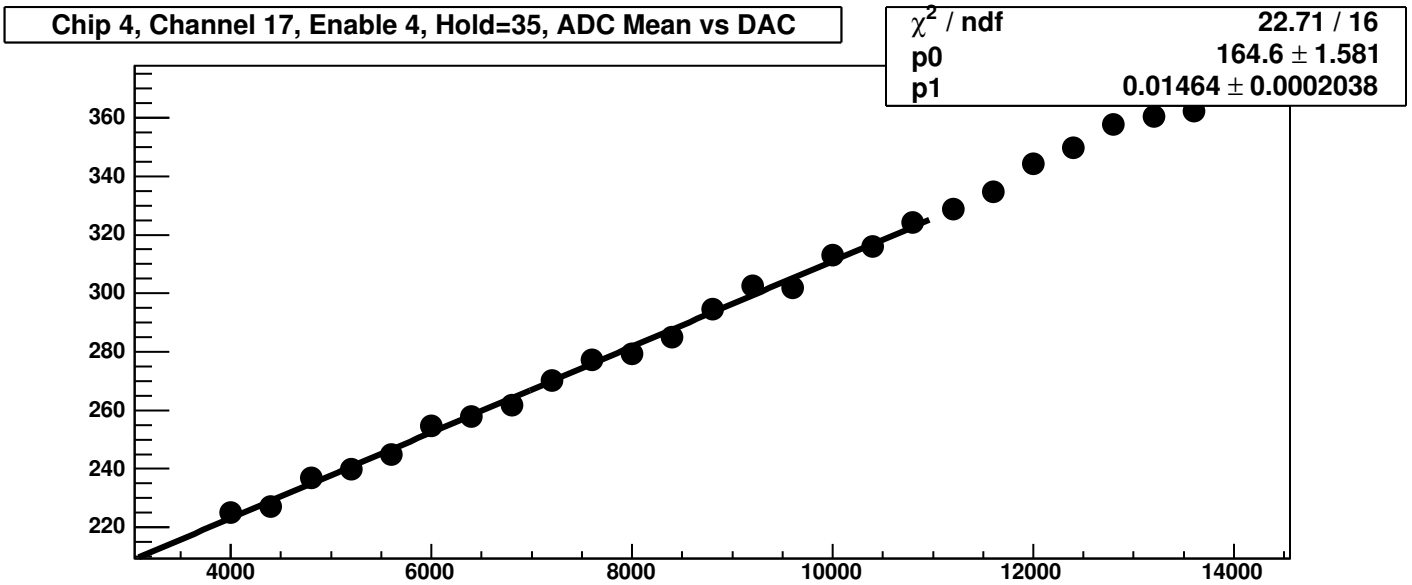

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000

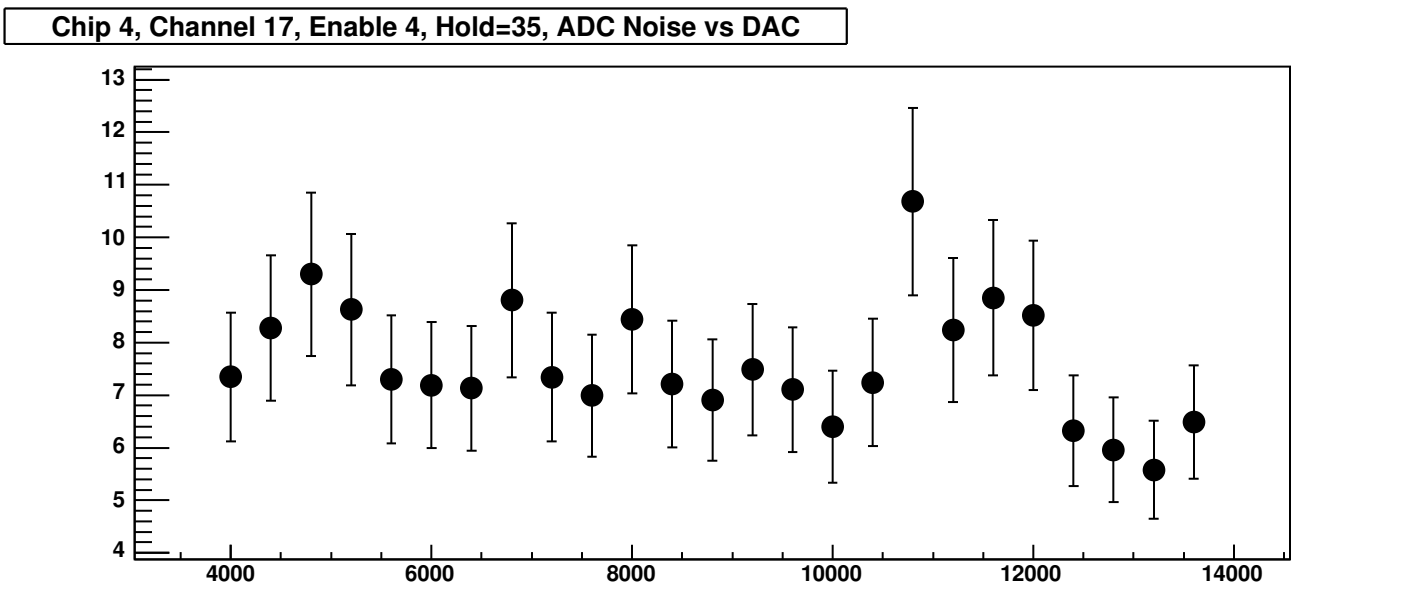

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

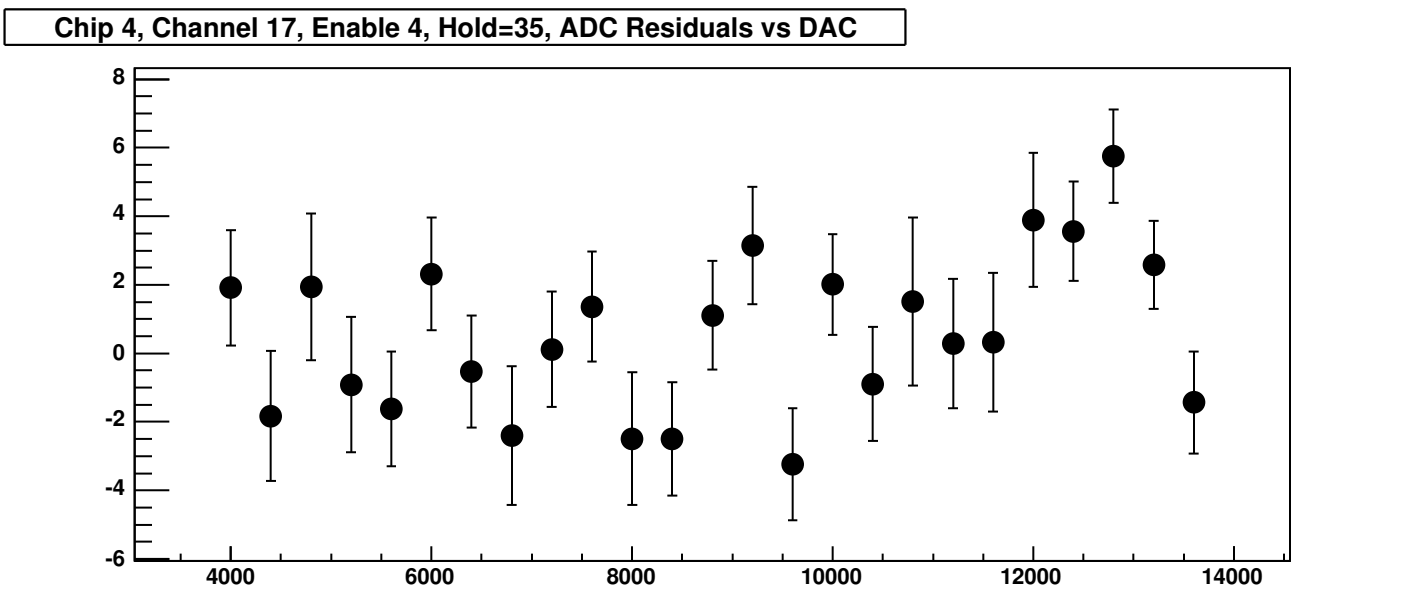

Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.24 / 16
p0 166.7 ± 1.442
p1 0.01487 ± 0.0001924
380
360
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000

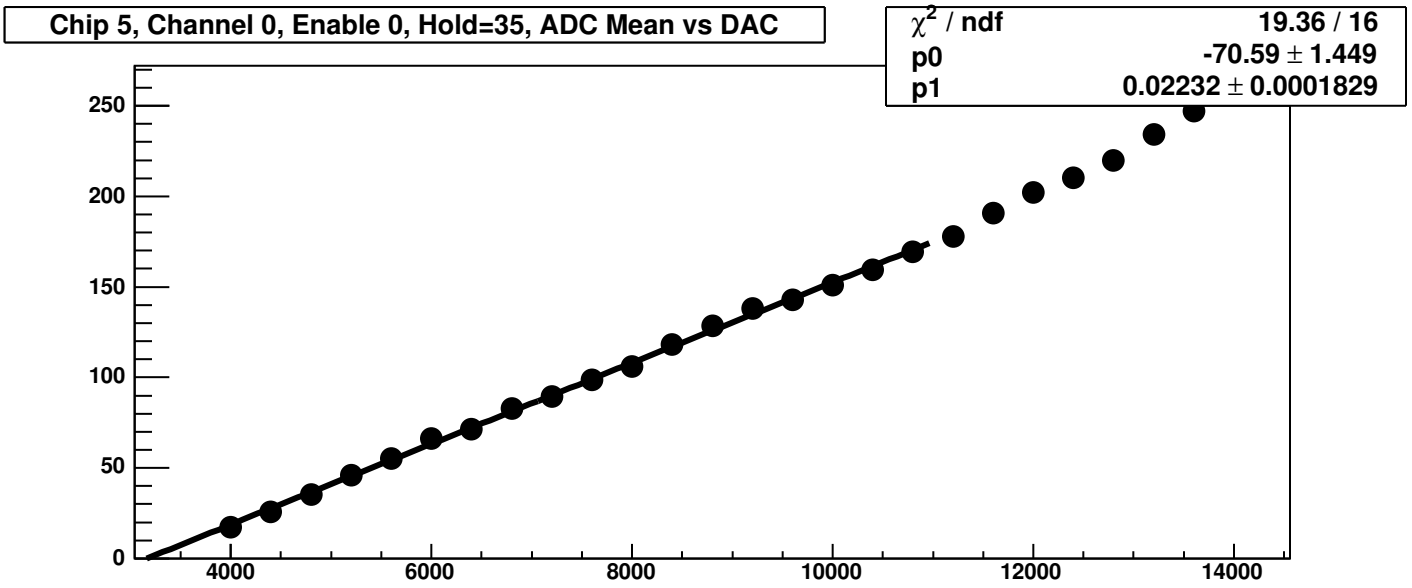

Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

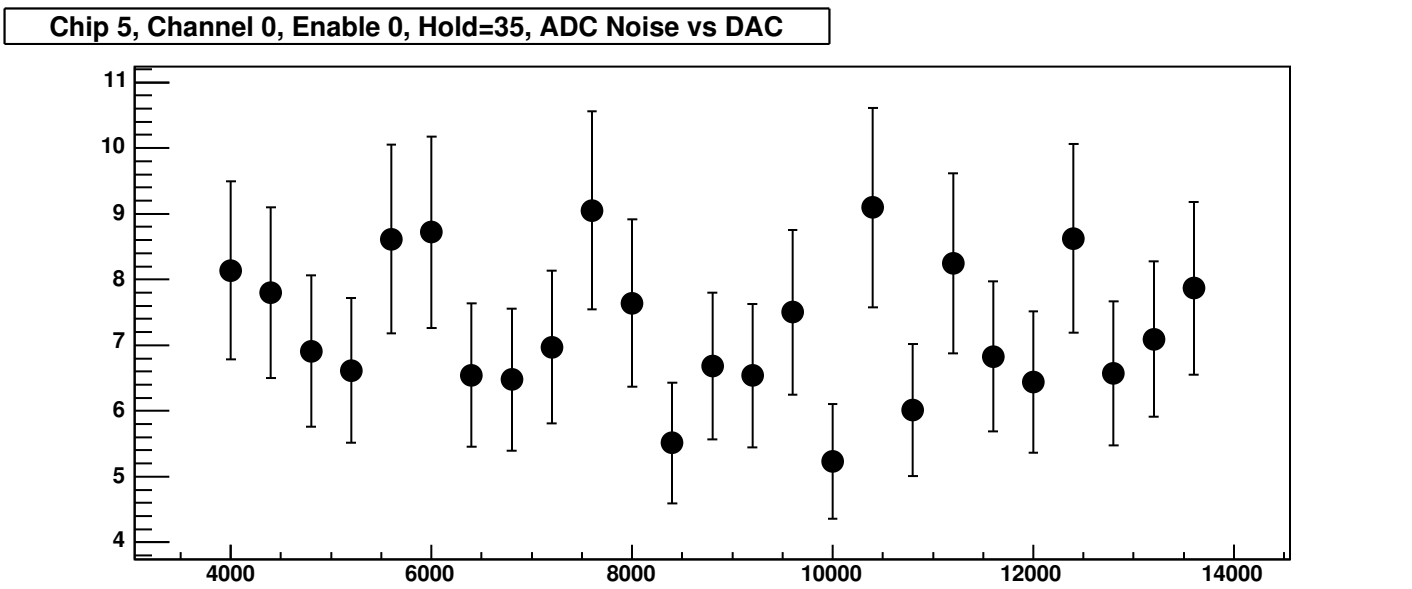

Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

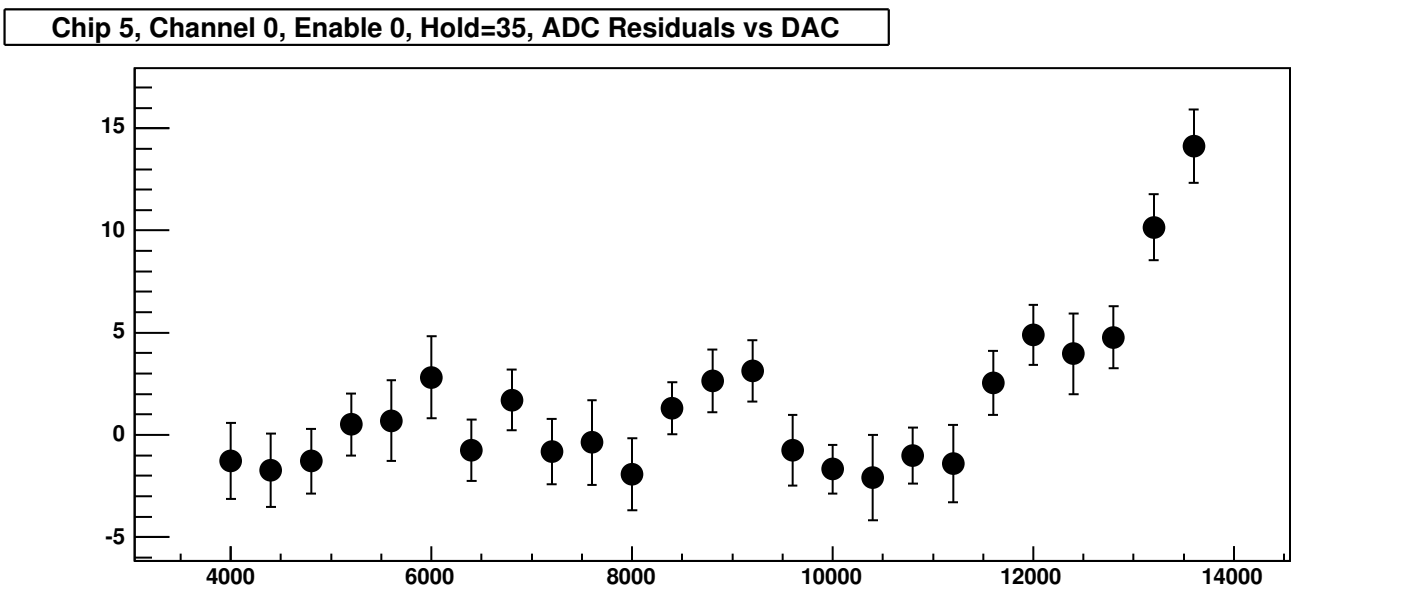

Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.71 / 16
p0 164.6 ± 1.581
p1 0.01464 ± 0.0002038
360
340
320
300
280
260
240
220
4000
6000
8000
10000
12000
14000

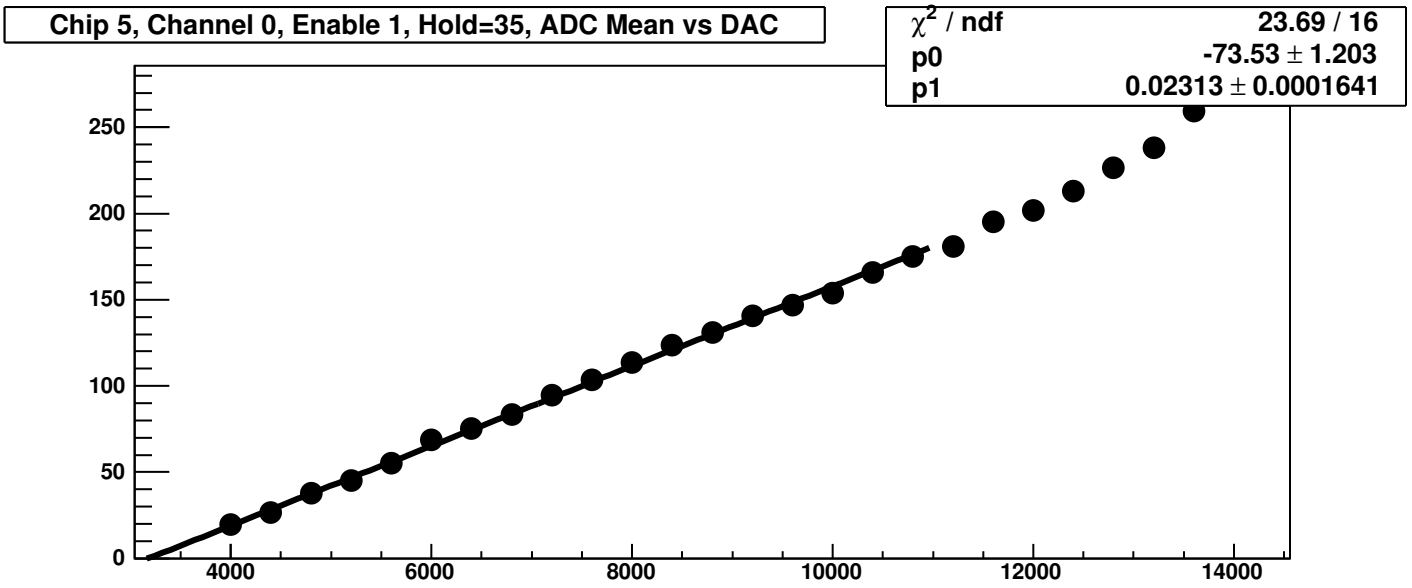

Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

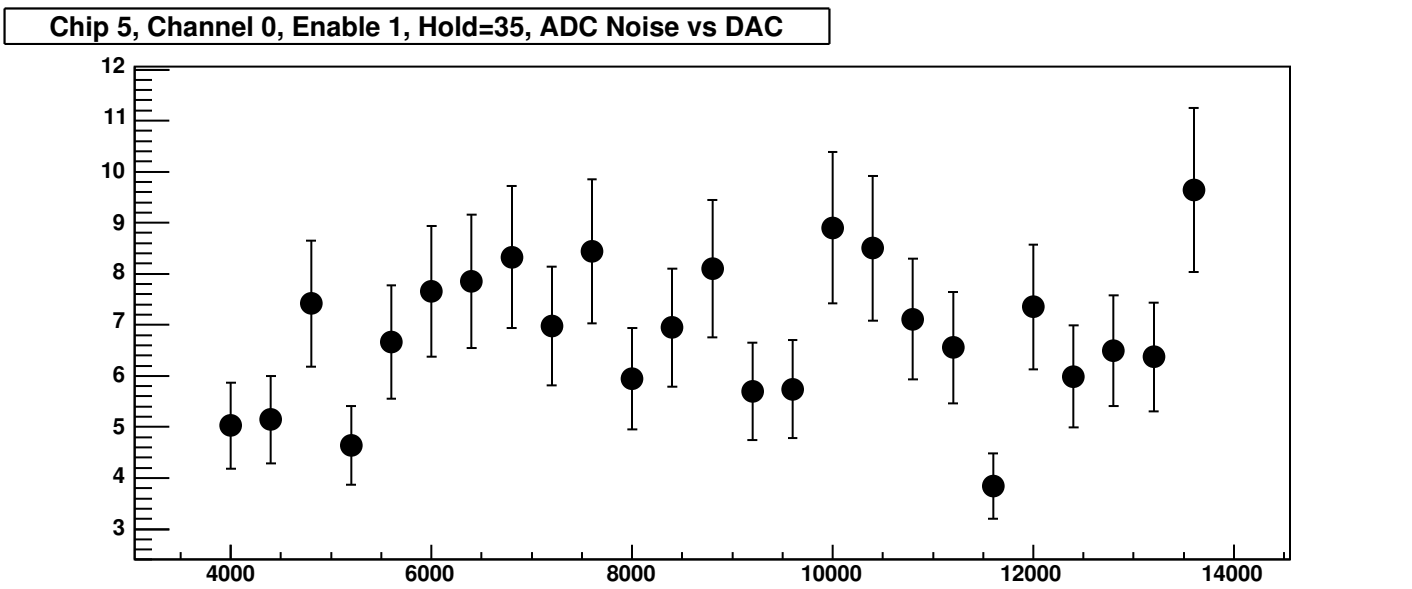

Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

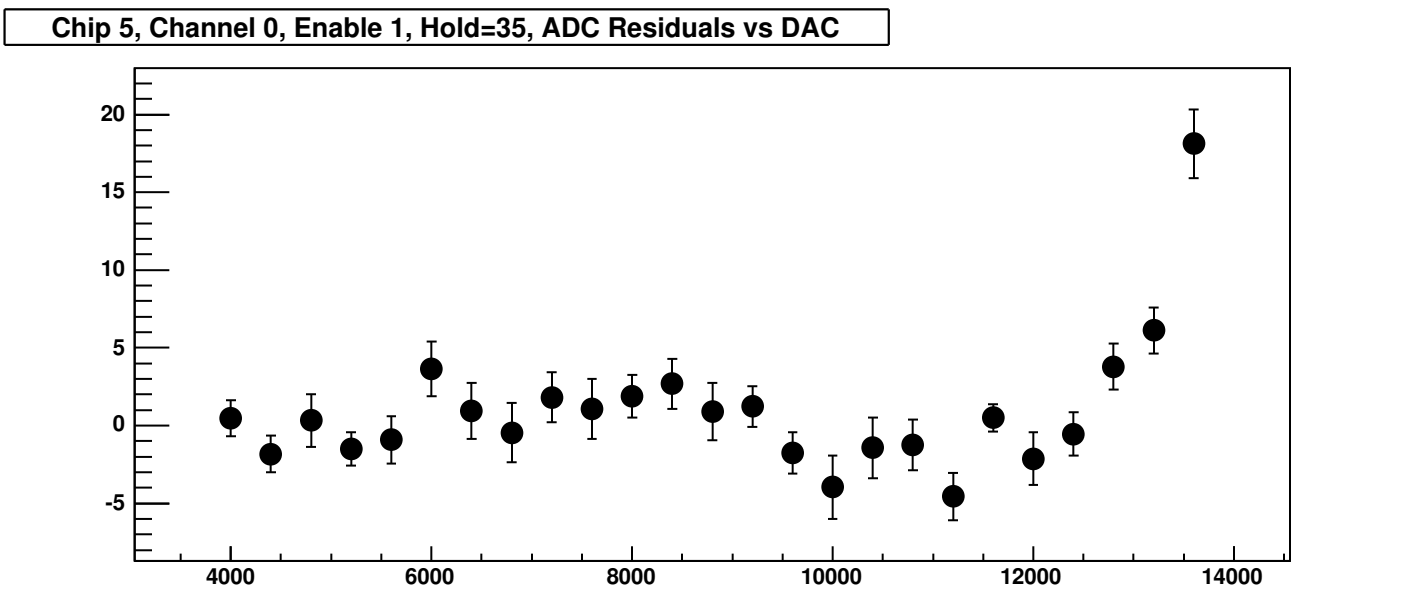

Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 19.36 / 16
p0 -70.59 ± 1.449
p1 0.02232 ± 0.0001829
250
200
150
100
50
0
4000
6000
8000
10000
12000
14000

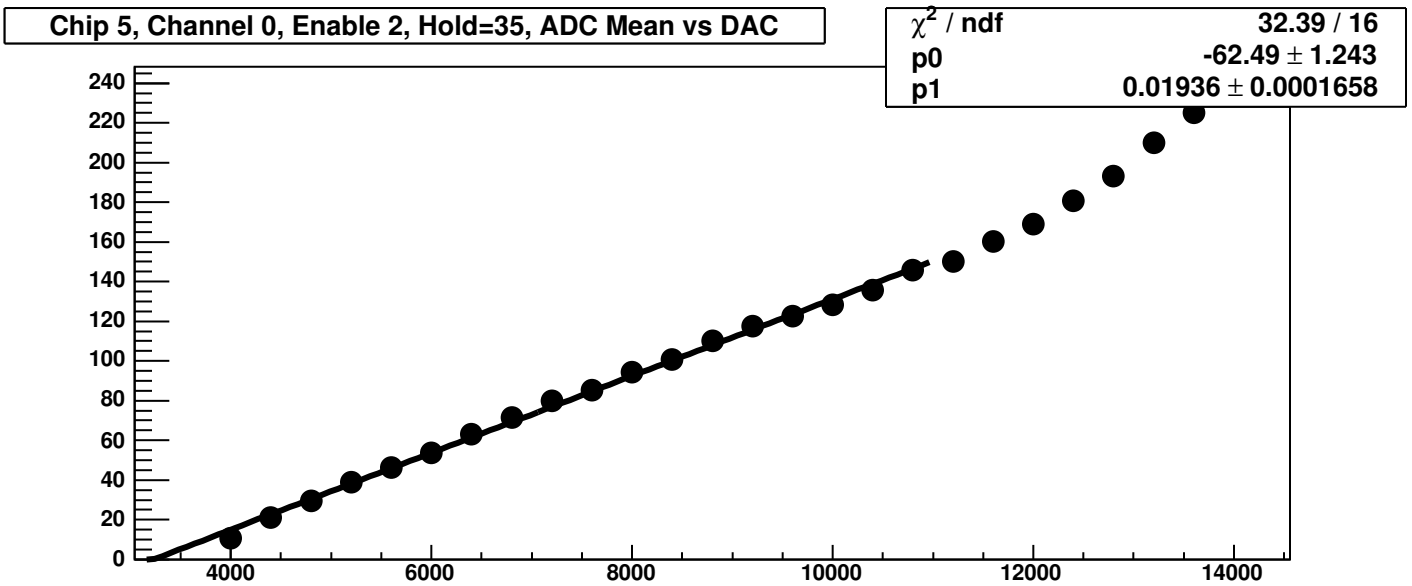

Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

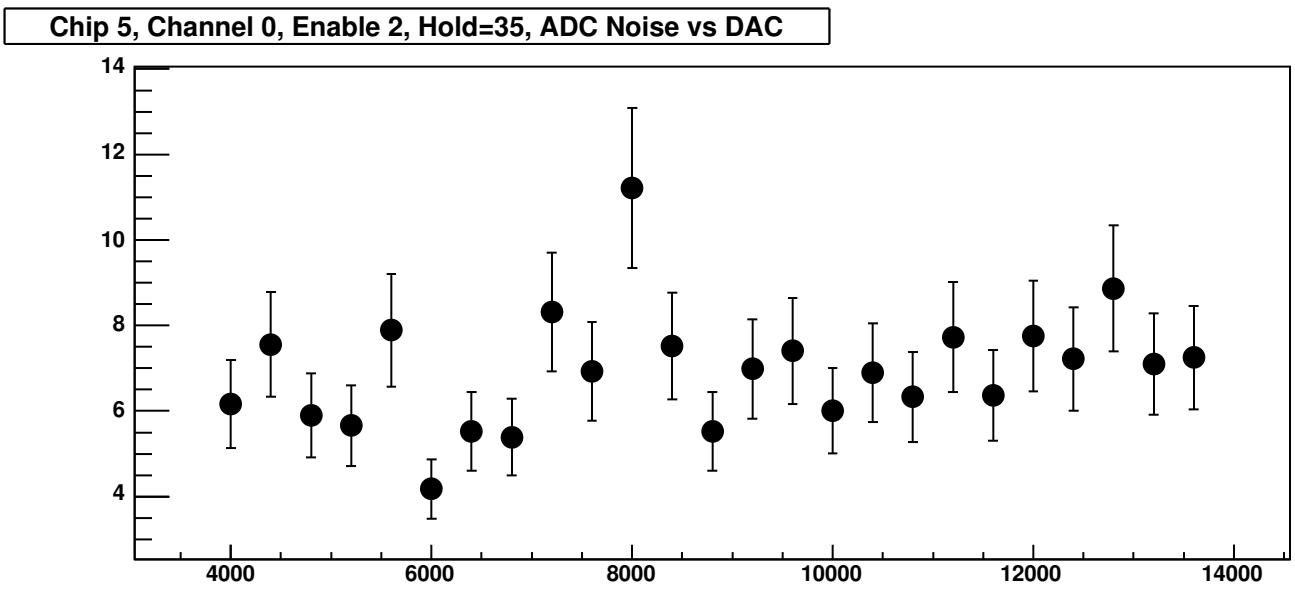

Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

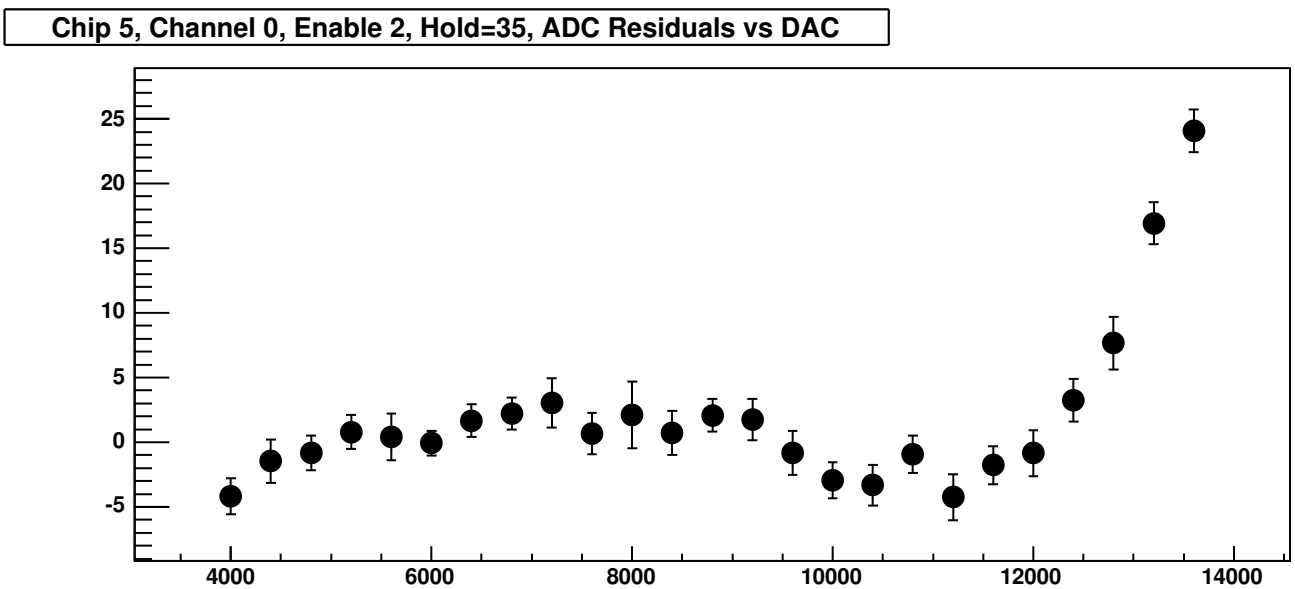

Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.69 / 16
p0 -73.53 ± 1.203
p1 0.02313 ± 0.0001641
250
200
150
100
50
0
4000
6000
8000
10000
12000
14000

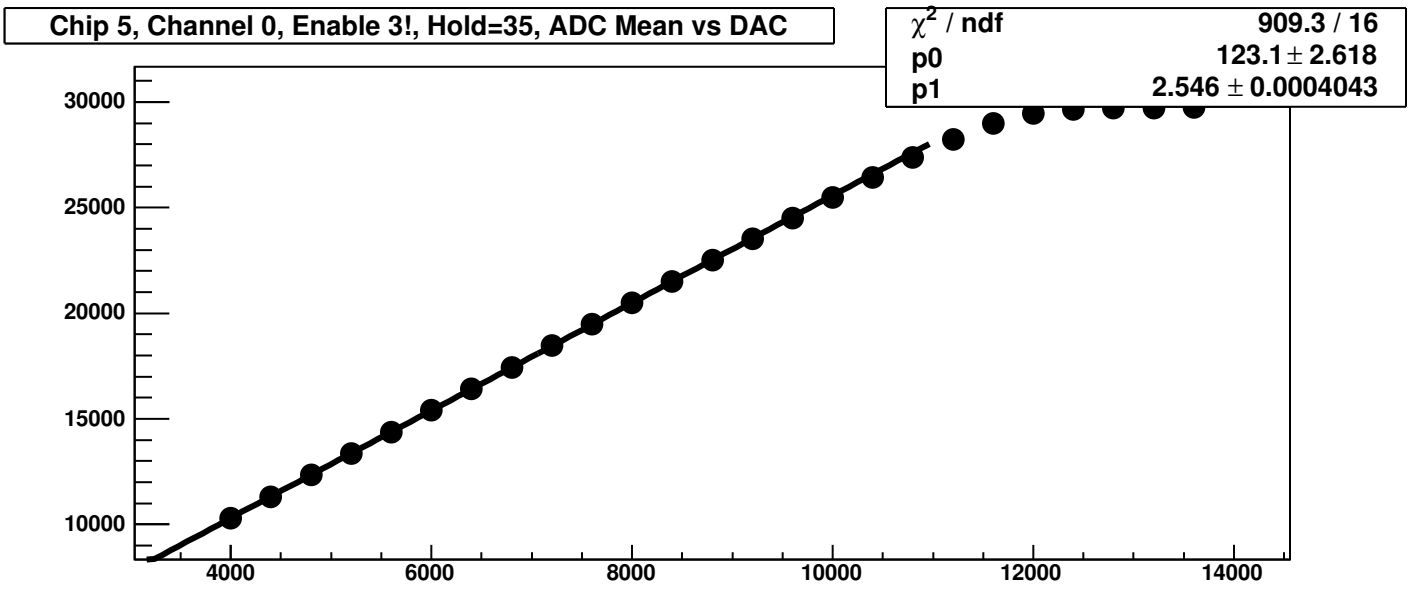

Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

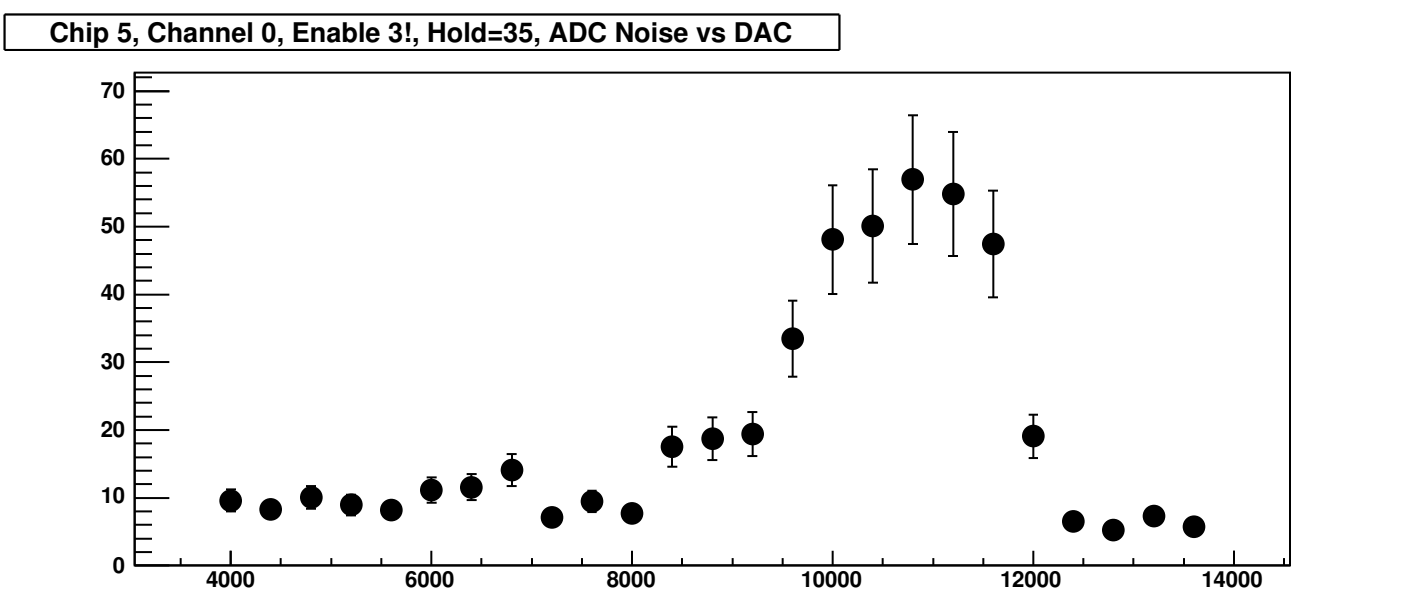

Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

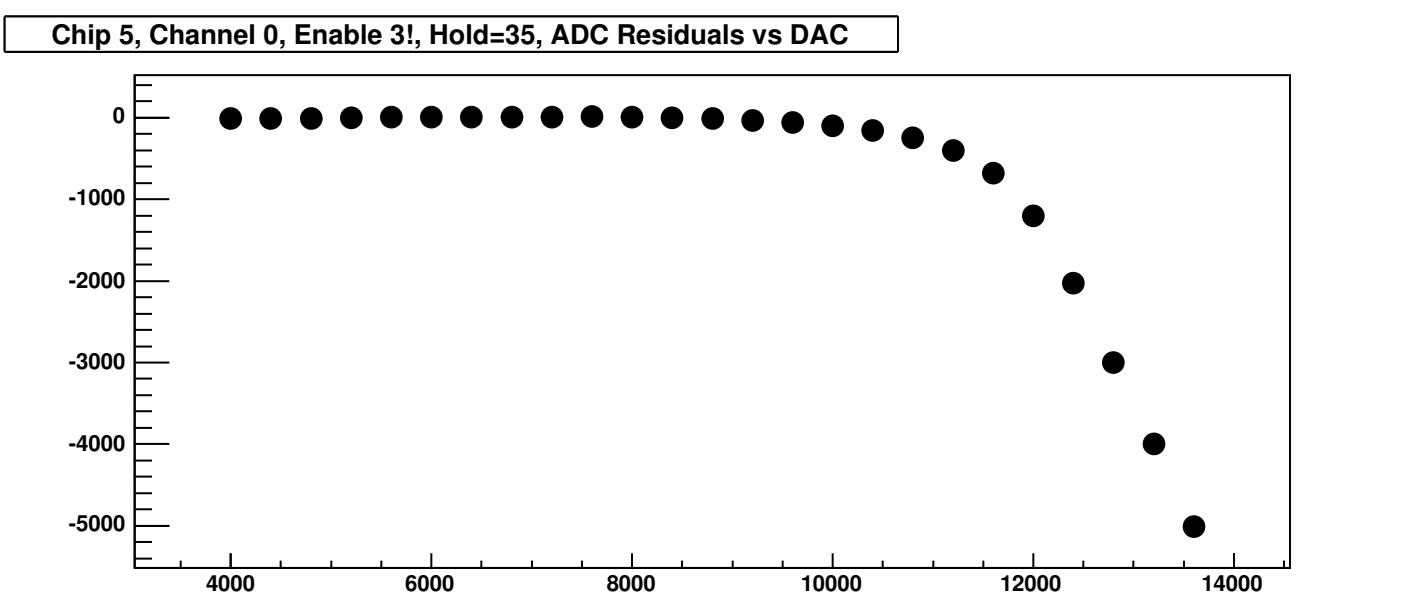

Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.39 / 16
p0 -62.49 ± 1.243
p1 0.01936 ± 0.0001658
240
220
200
180
160
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

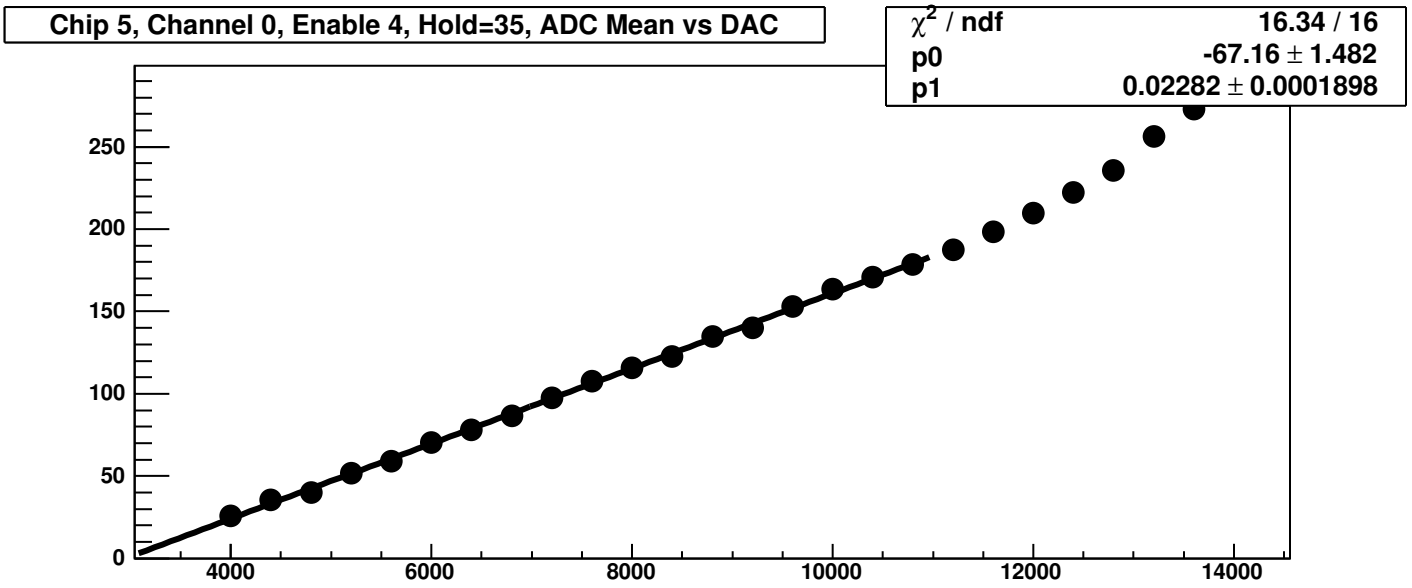

Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

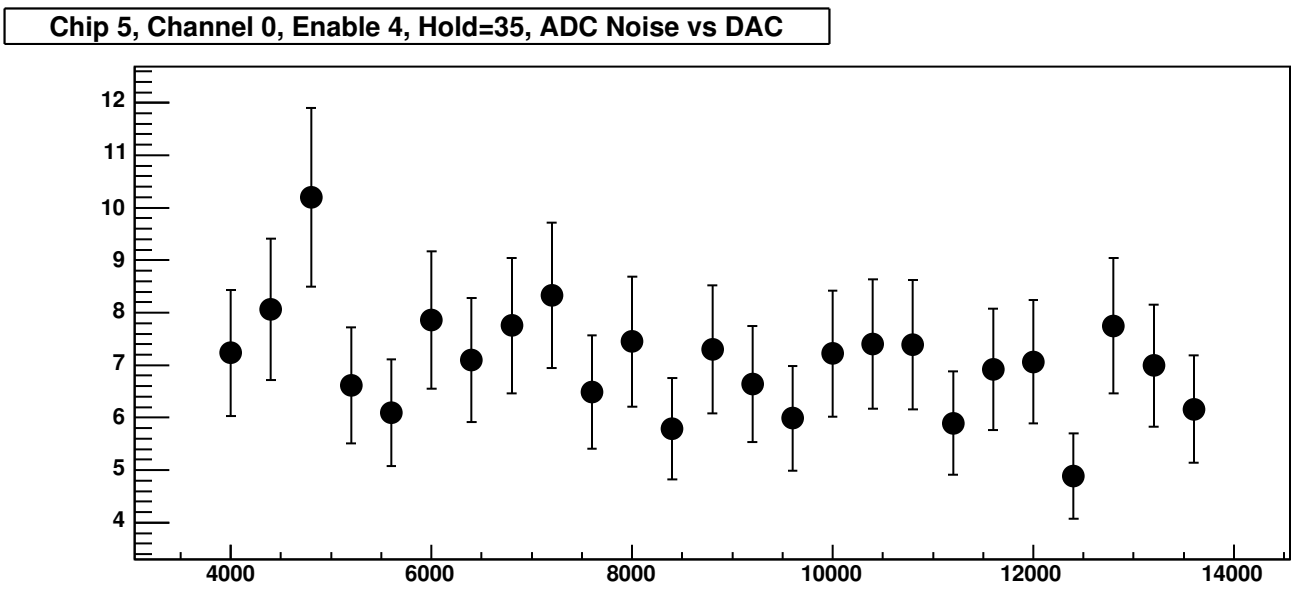

Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

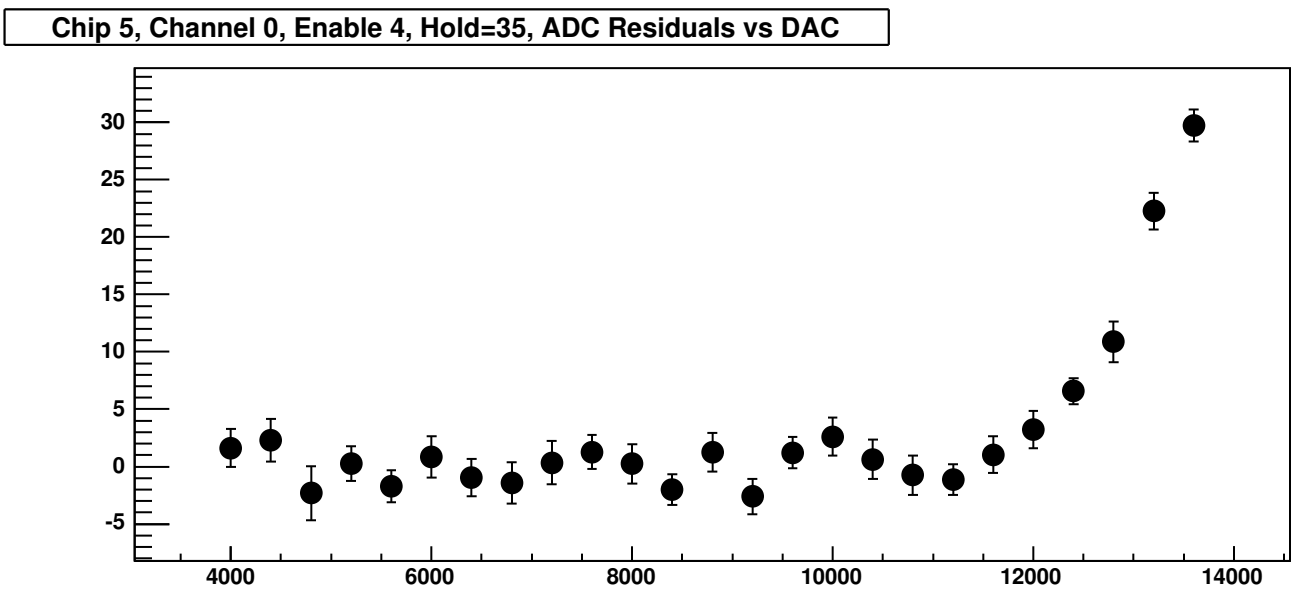

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 909.3 / 16
p0 123.1 ± 2.618
p1 2.546 ± 0.0004043
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

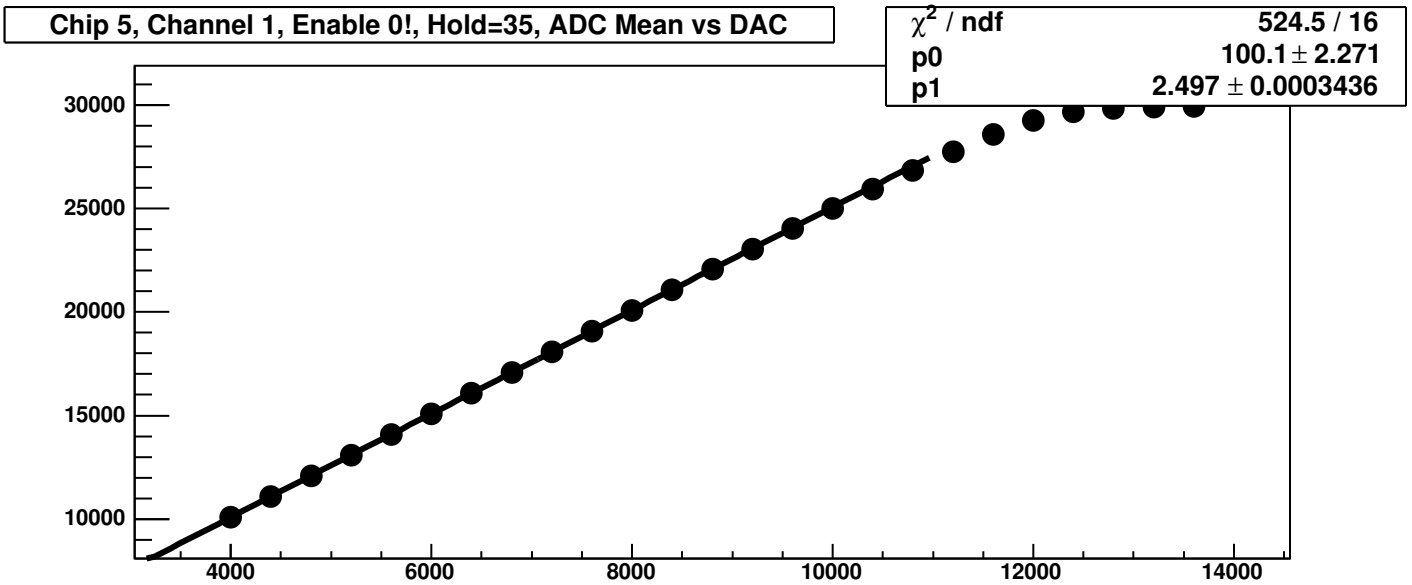

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

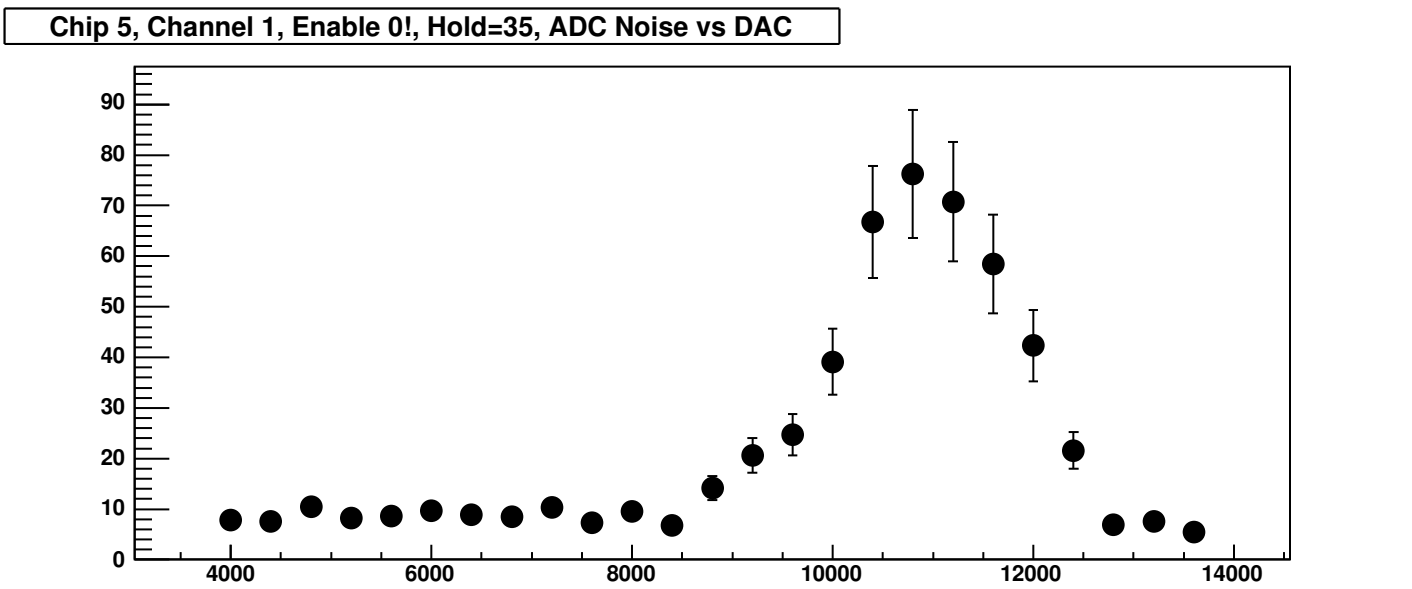

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

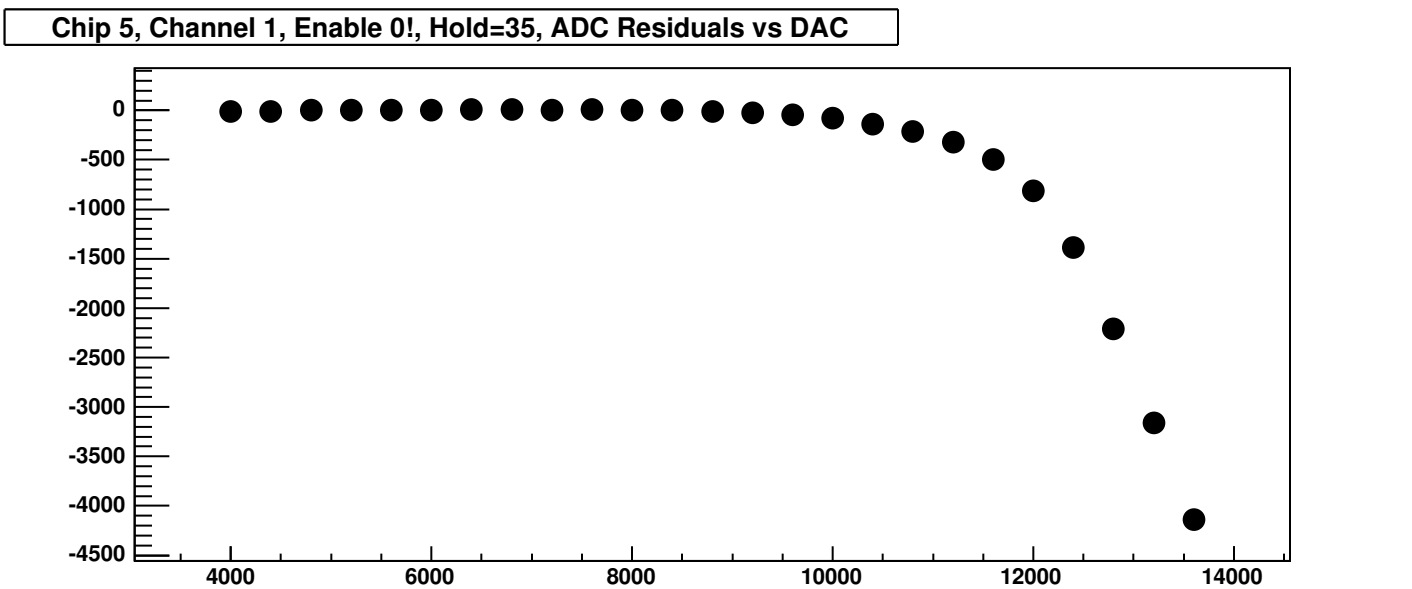

Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.34 / 16
p0 -67.16 ± 1.482
p1 0.02282 ± 0.0001898
0
50
100
150
200
250
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
4
5
6
7
8
9
10
11
12
4000
6000
8000
10000
12000
14000

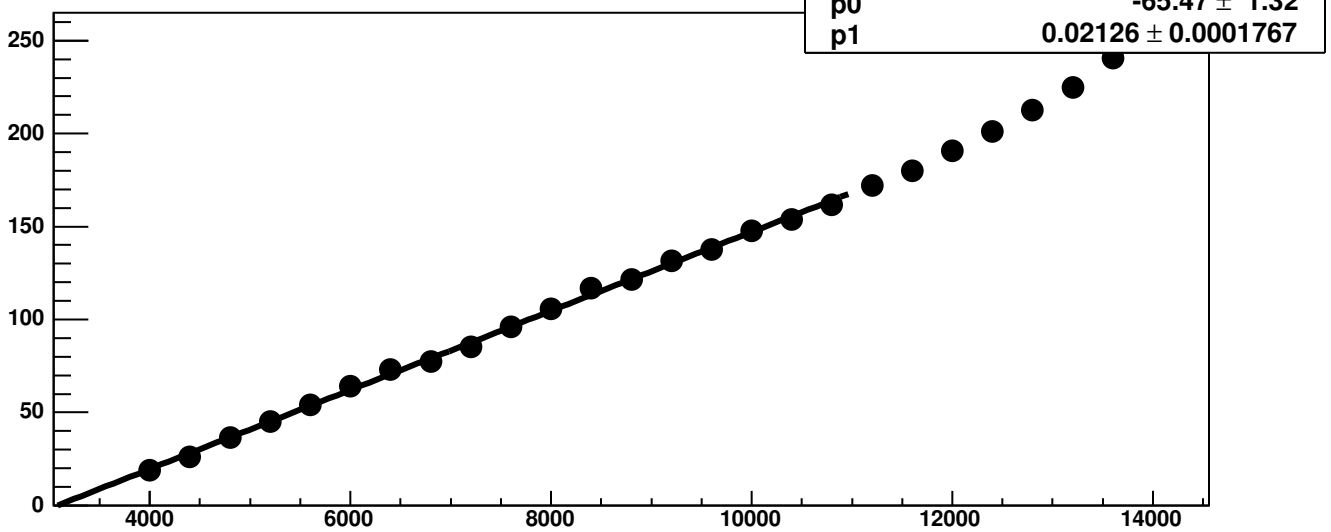

Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
-5
0
5
10
15
20
25
30
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 524.5 / 16
p0 100.1 ± 2.271
p1 2.497 ± 0.0003436
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

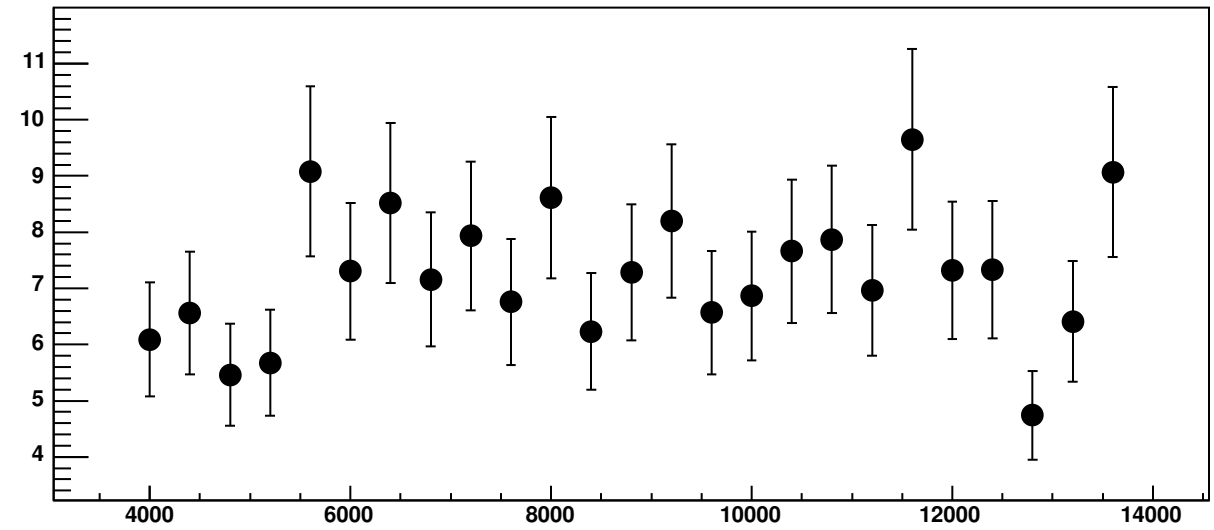

Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000

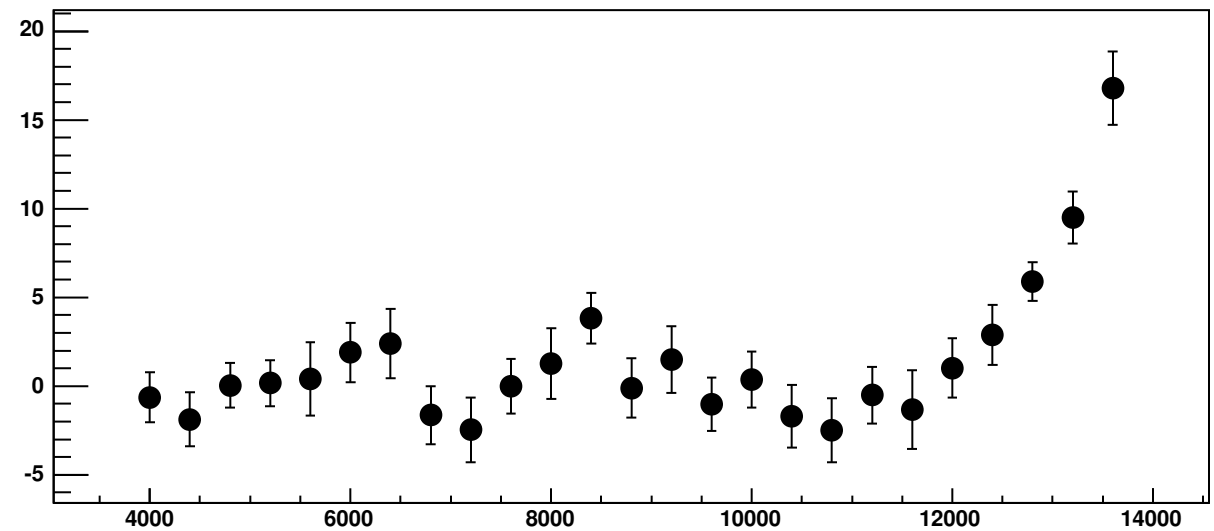
Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



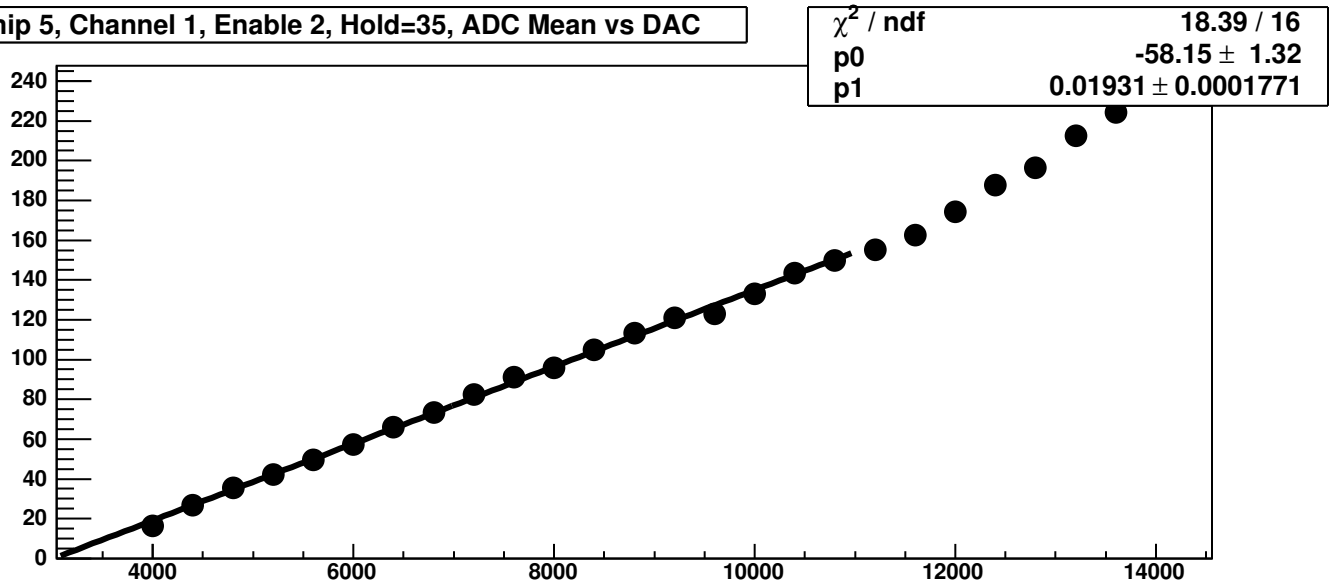
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



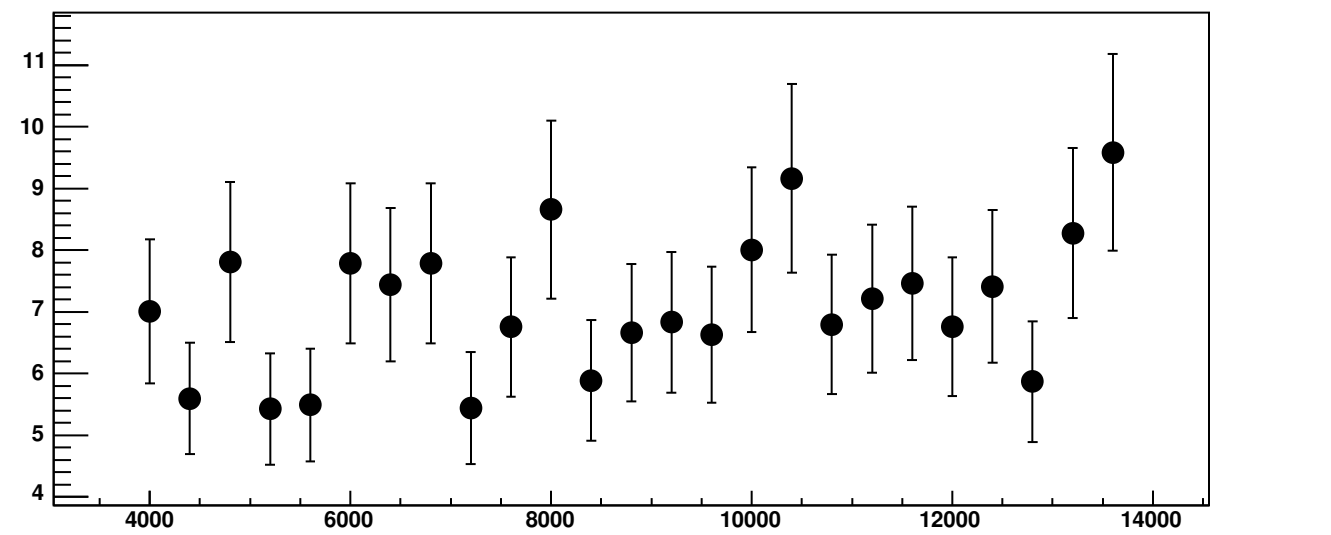
Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



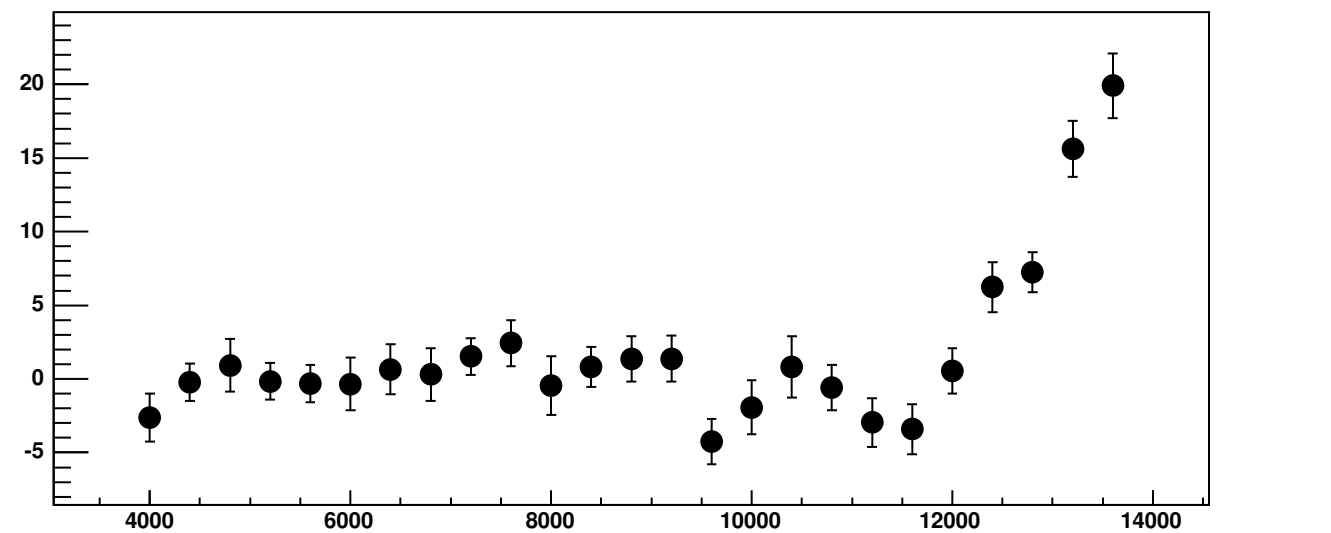
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

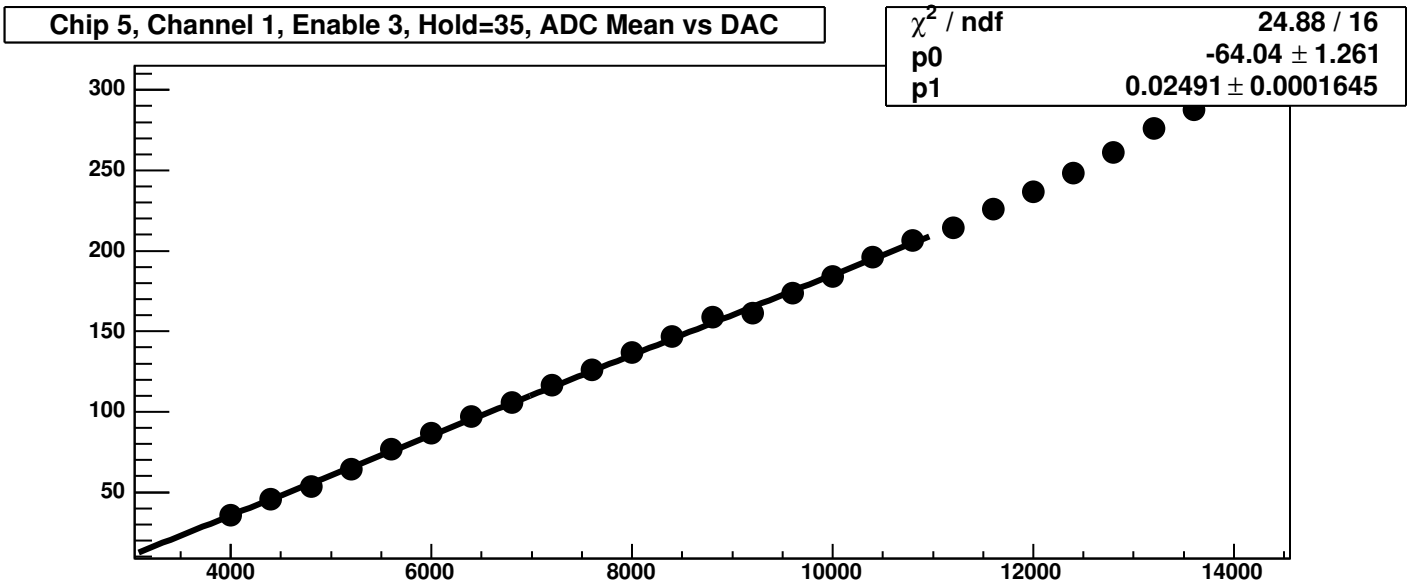


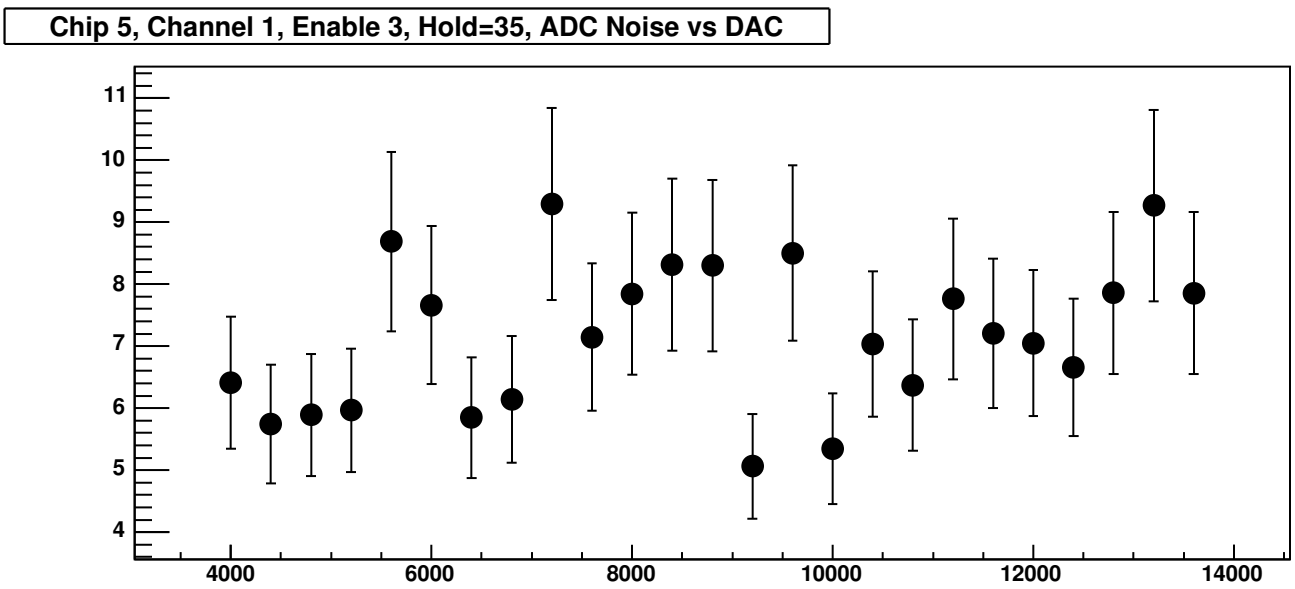
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

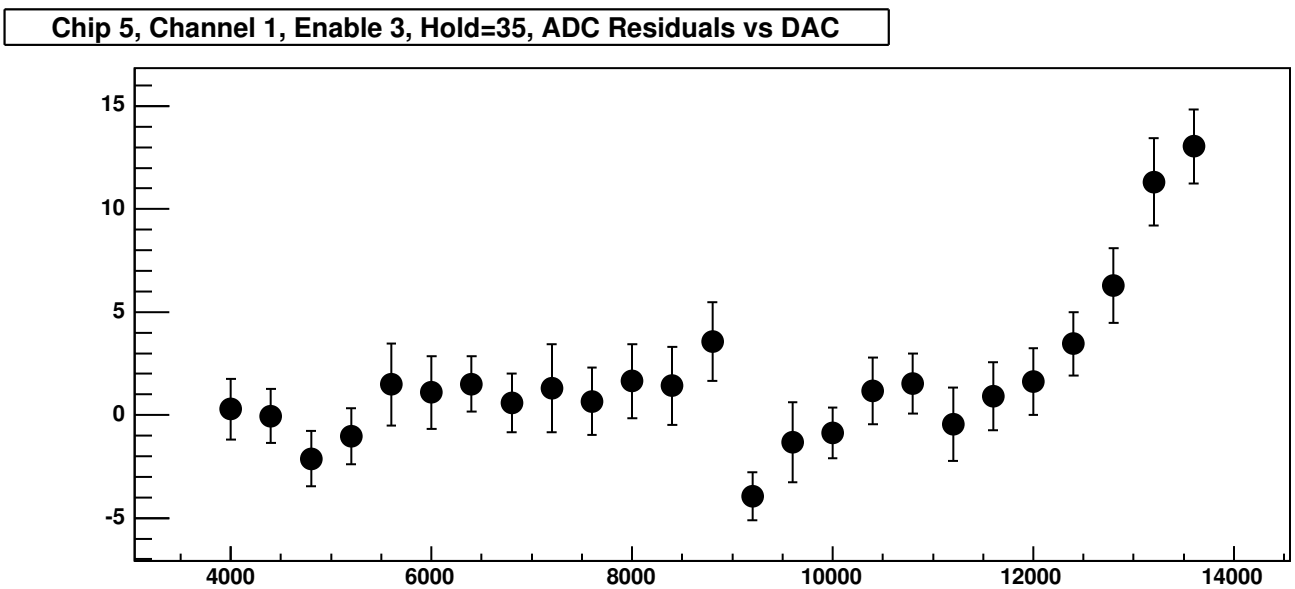


Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

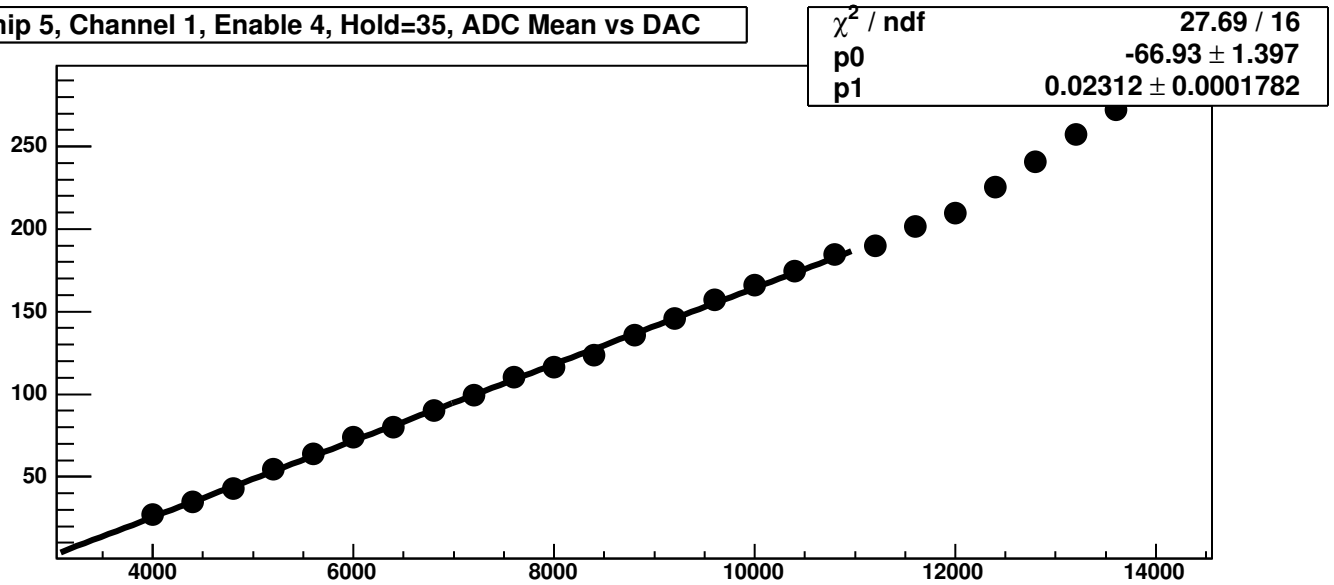



Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.88 / 16
p0 -64.04 ± 1.261
p1 0.02491 ± 0.0001645
300
250
200
150
100
50
4000
6000
8000
10000
12000
14000

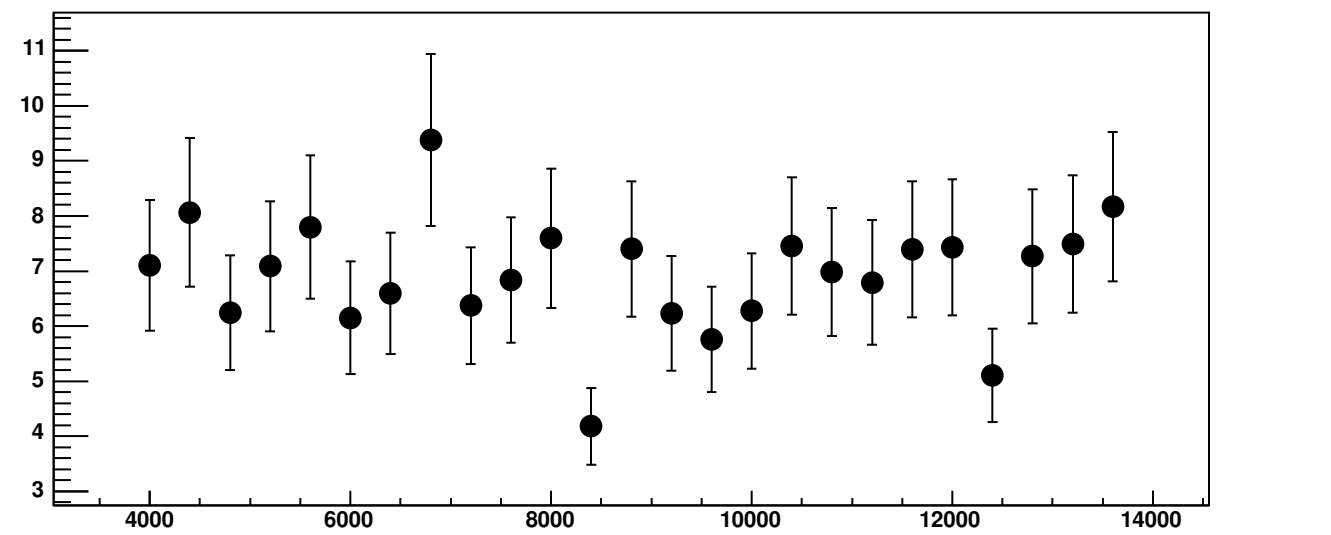

Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

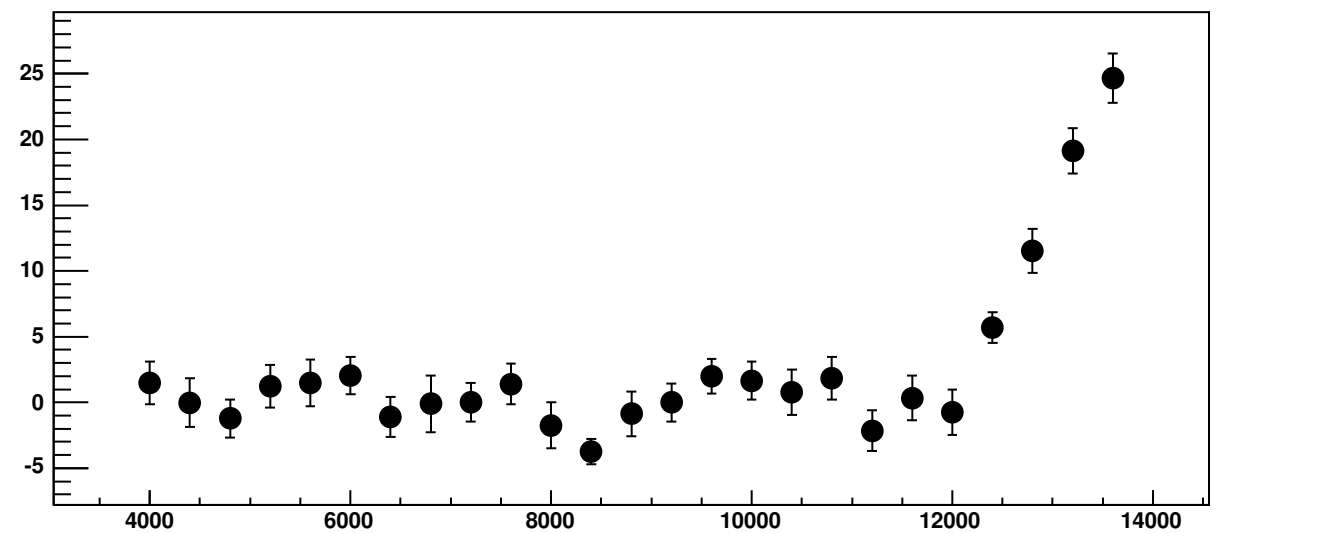
Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

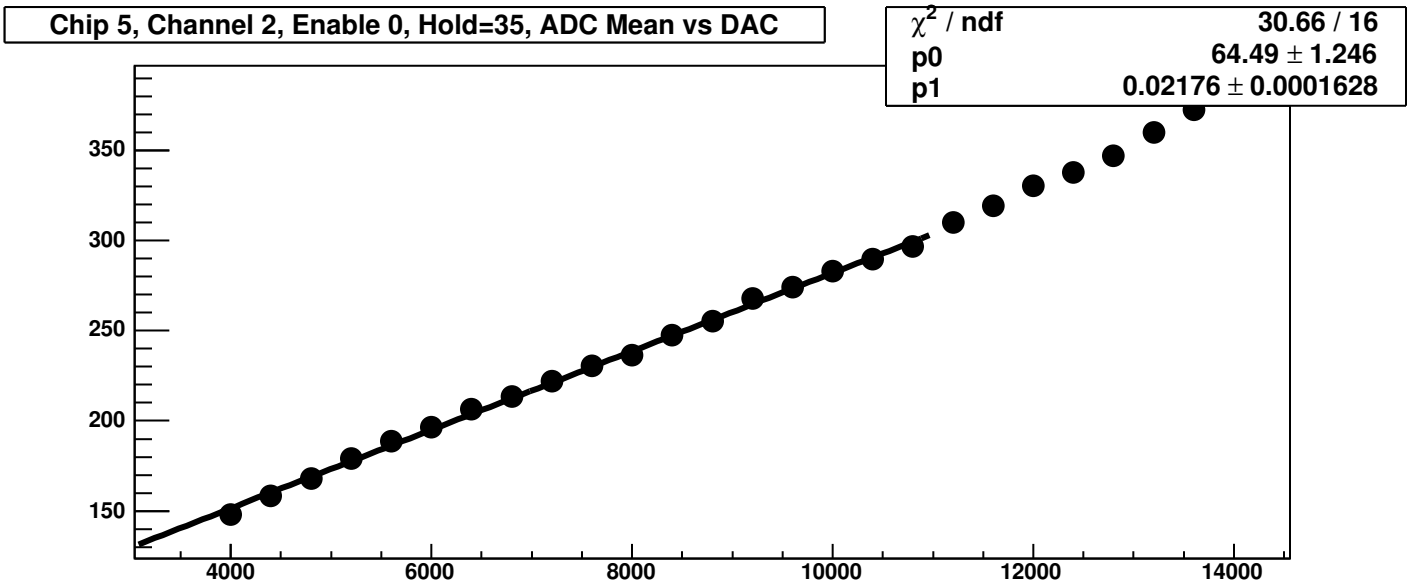


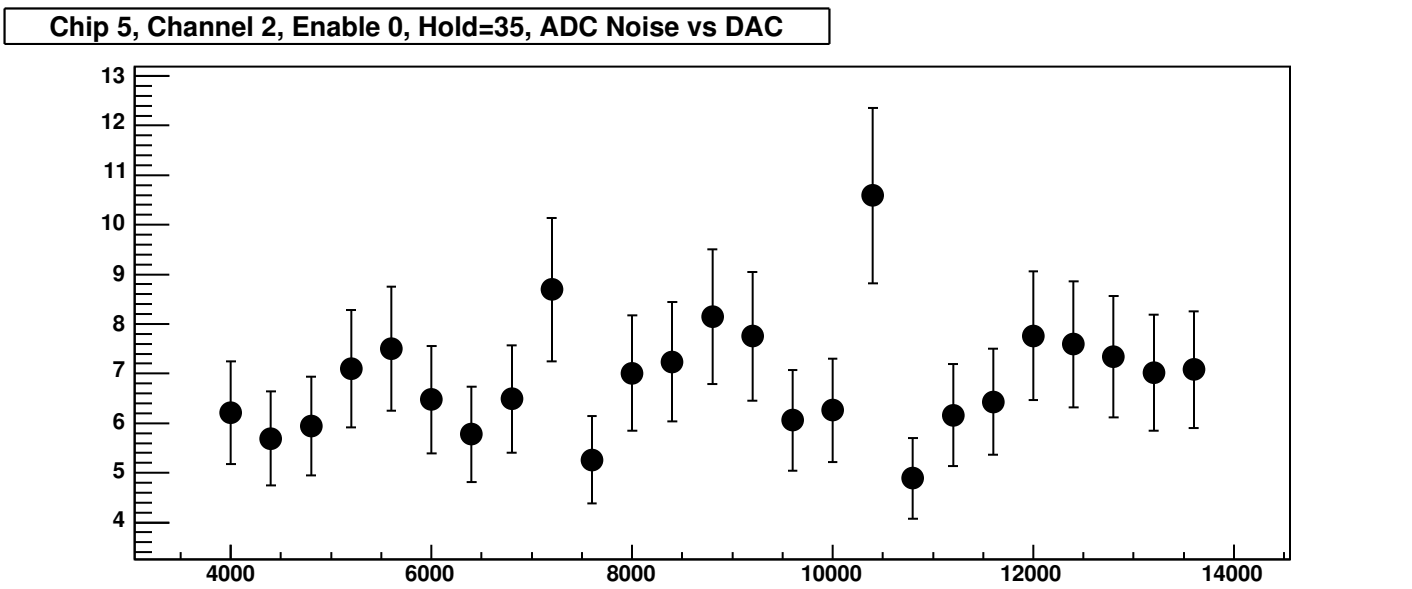
Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

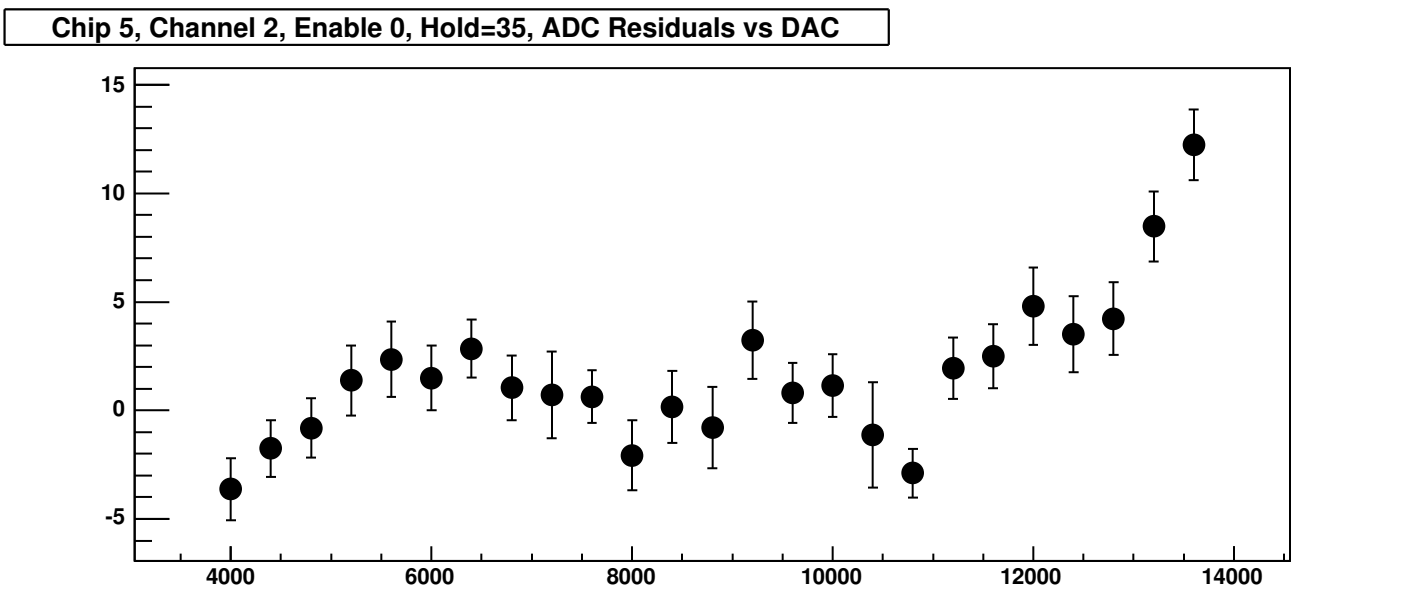


Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

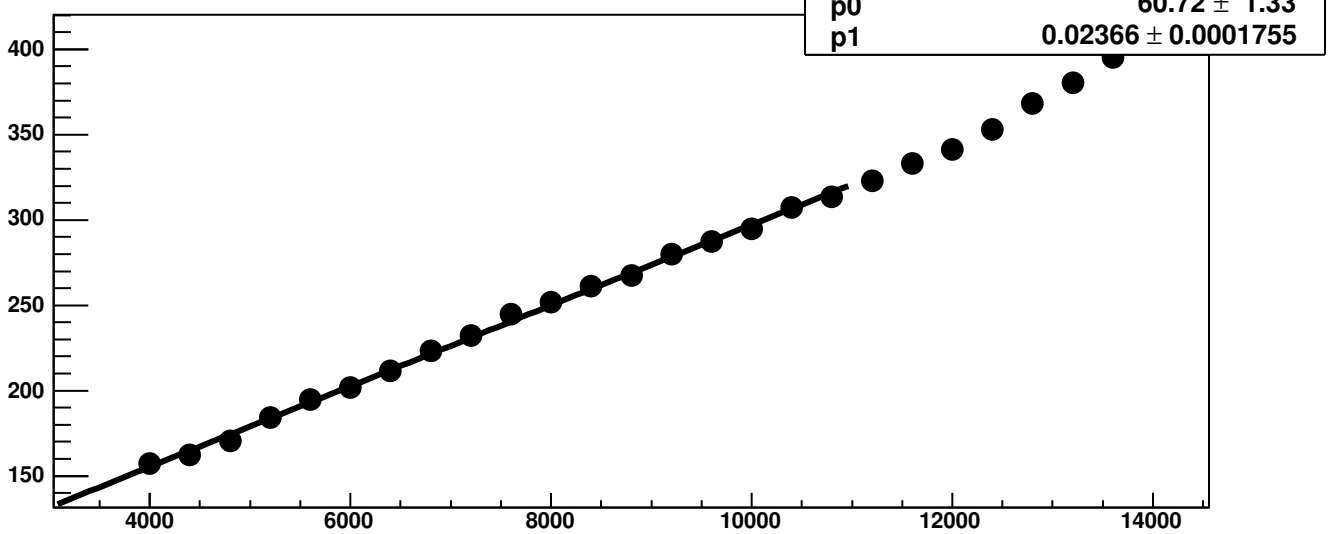



Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 30.66 / 16
p0 64.49 ± 1.246
p1 0.02176 ± 0.0001628
350
300
250
200
150
4000
6000
8000
10000
12000
14000

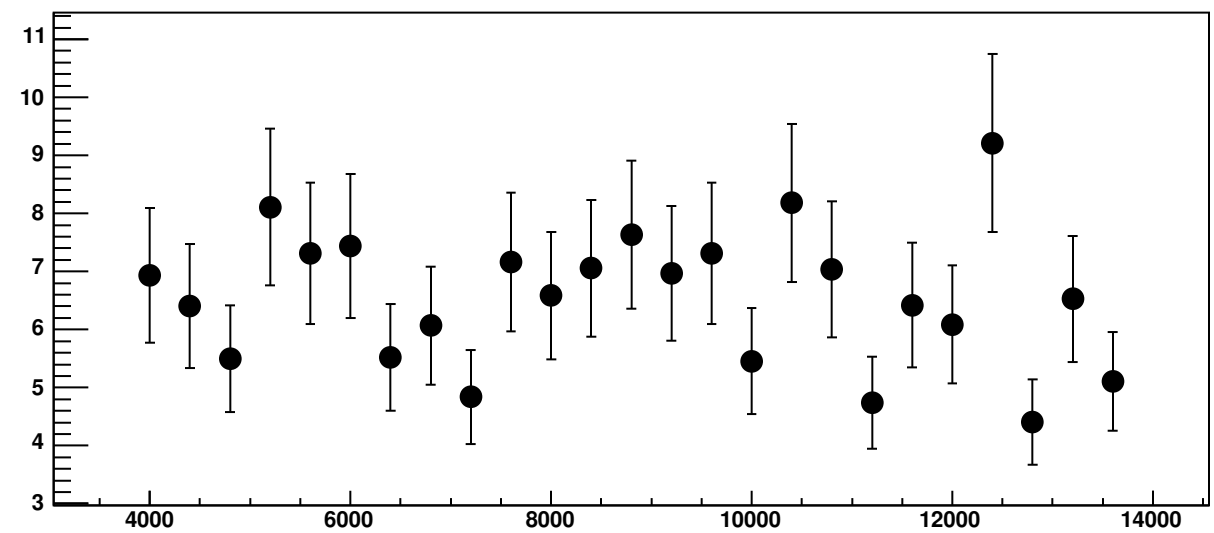

Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

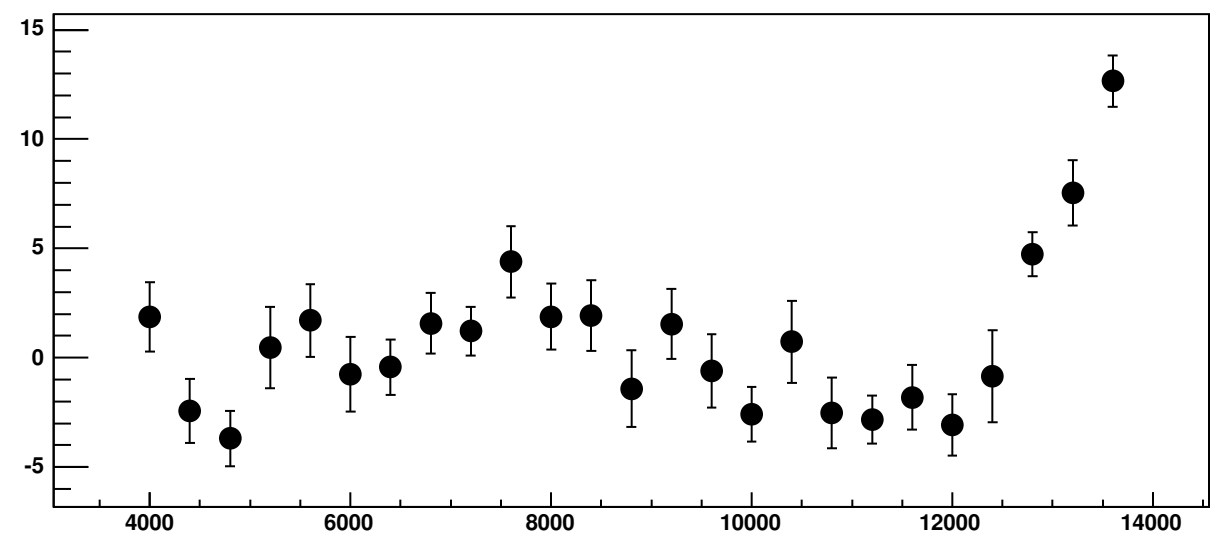
Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

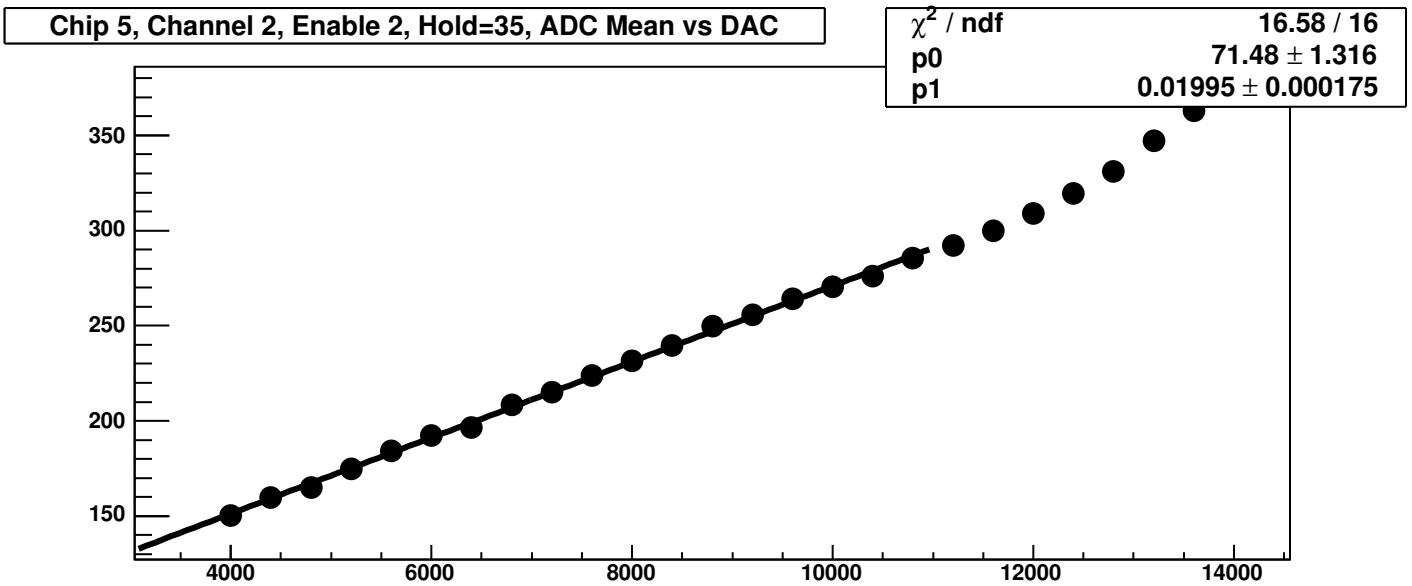


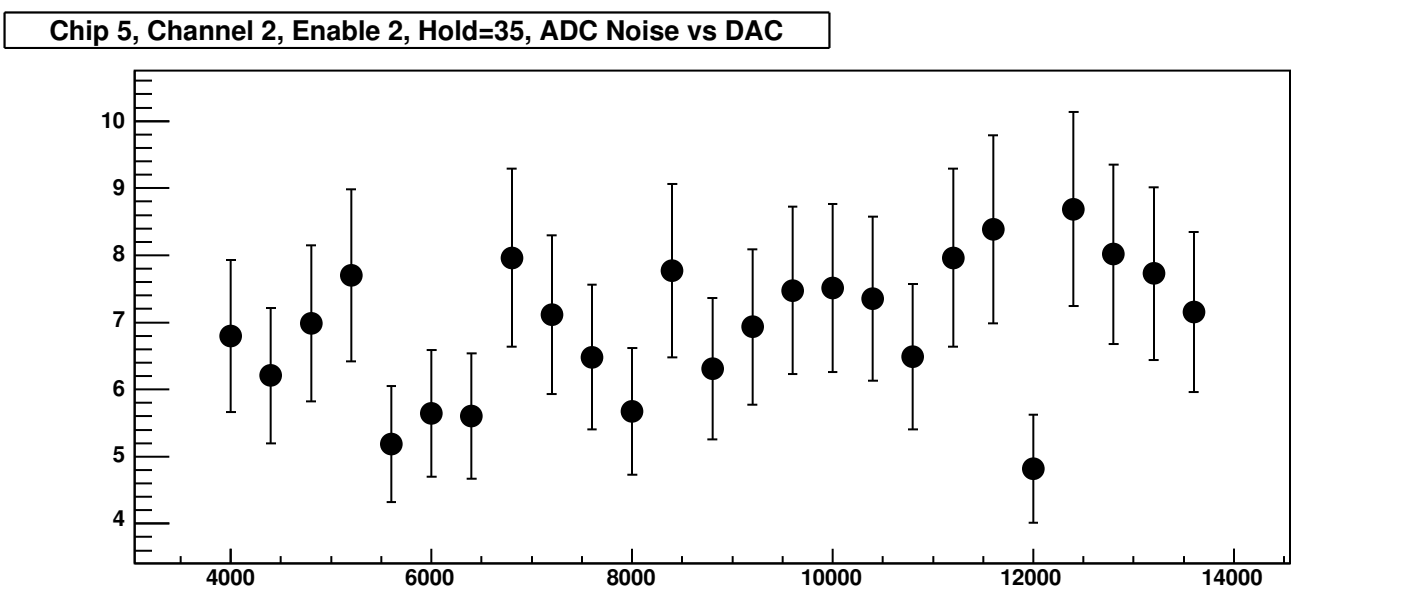
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

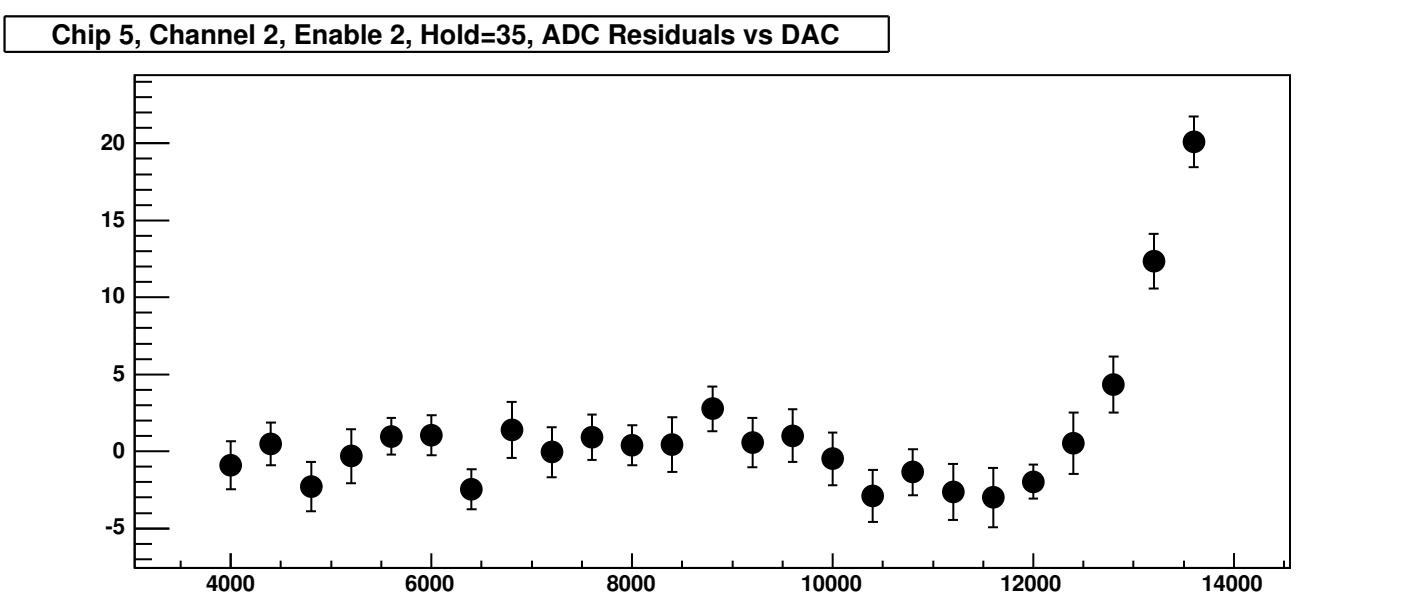


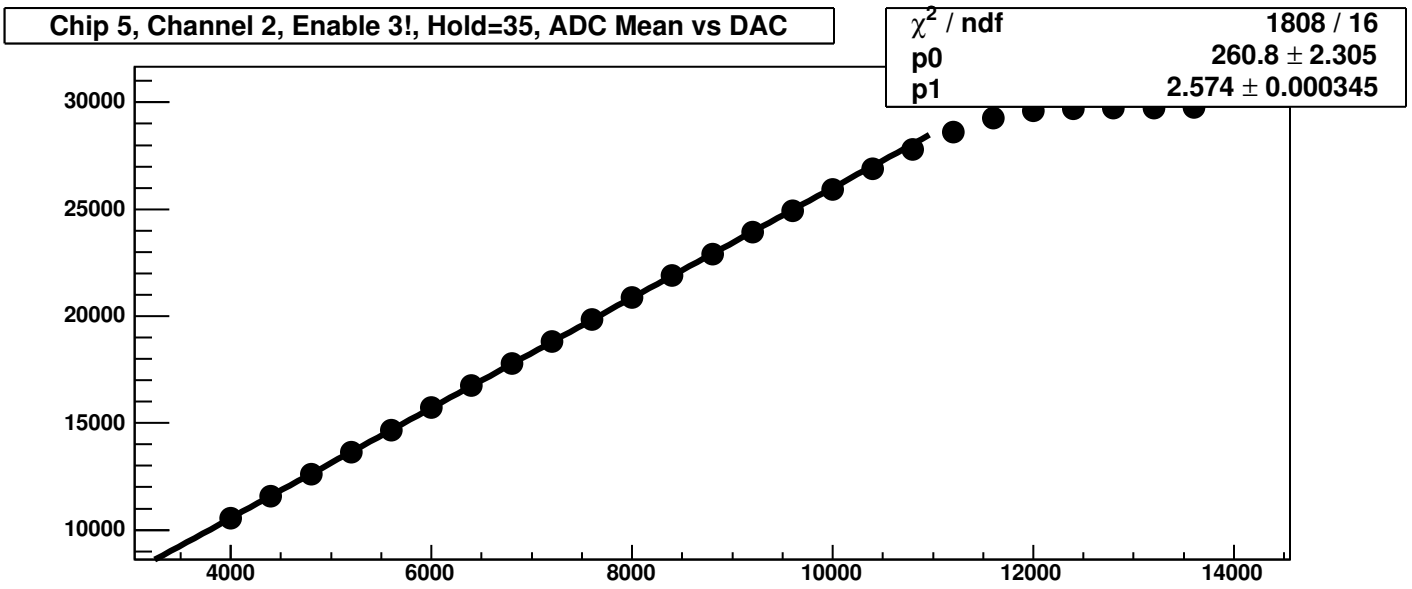
Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

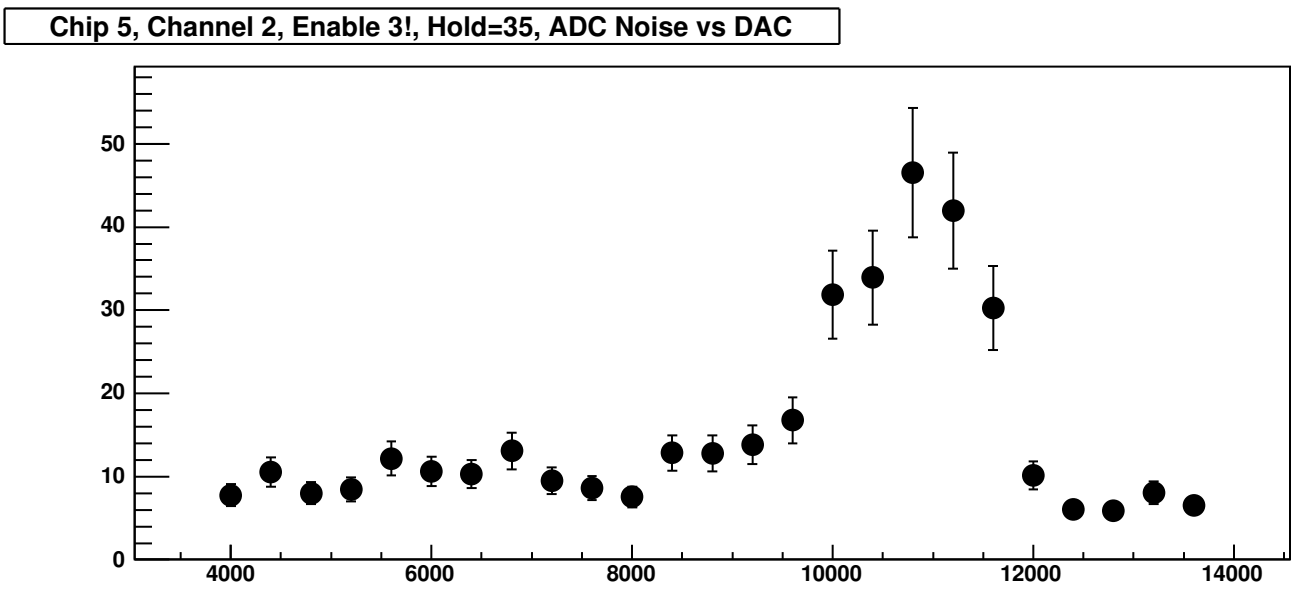


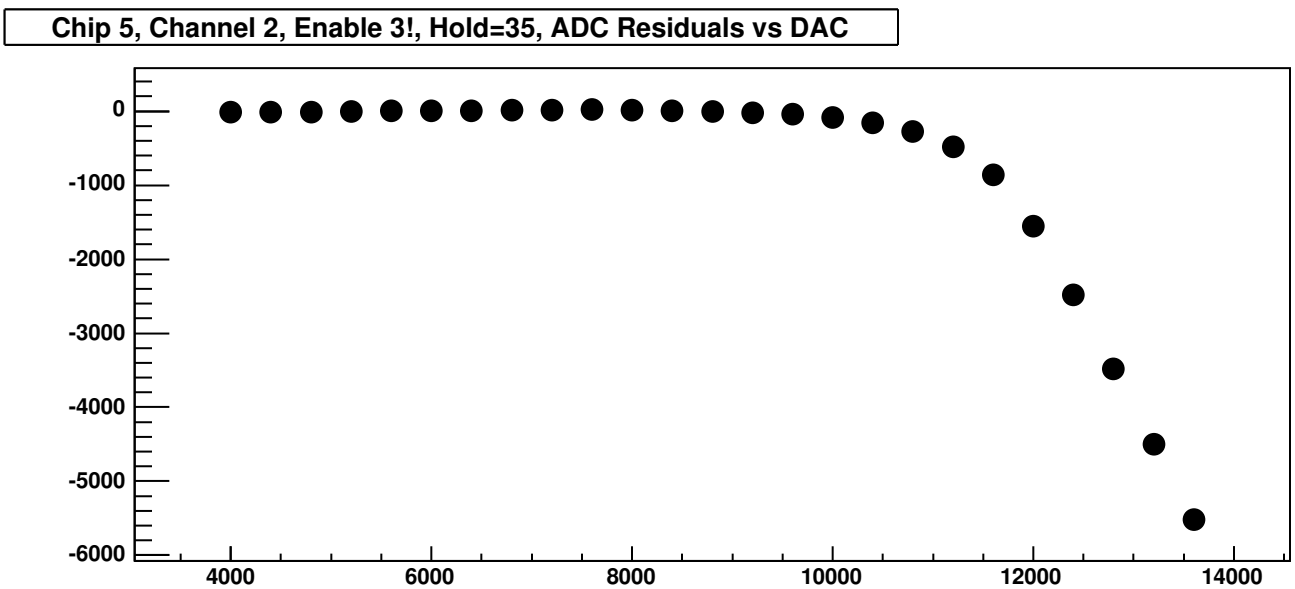

Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 16.58 / 16
p0 71.48 ± 1.316
p1 0.01995 ± 0.000175
350
300
250
200
150
4000
6000
8000
10000
12000
14000

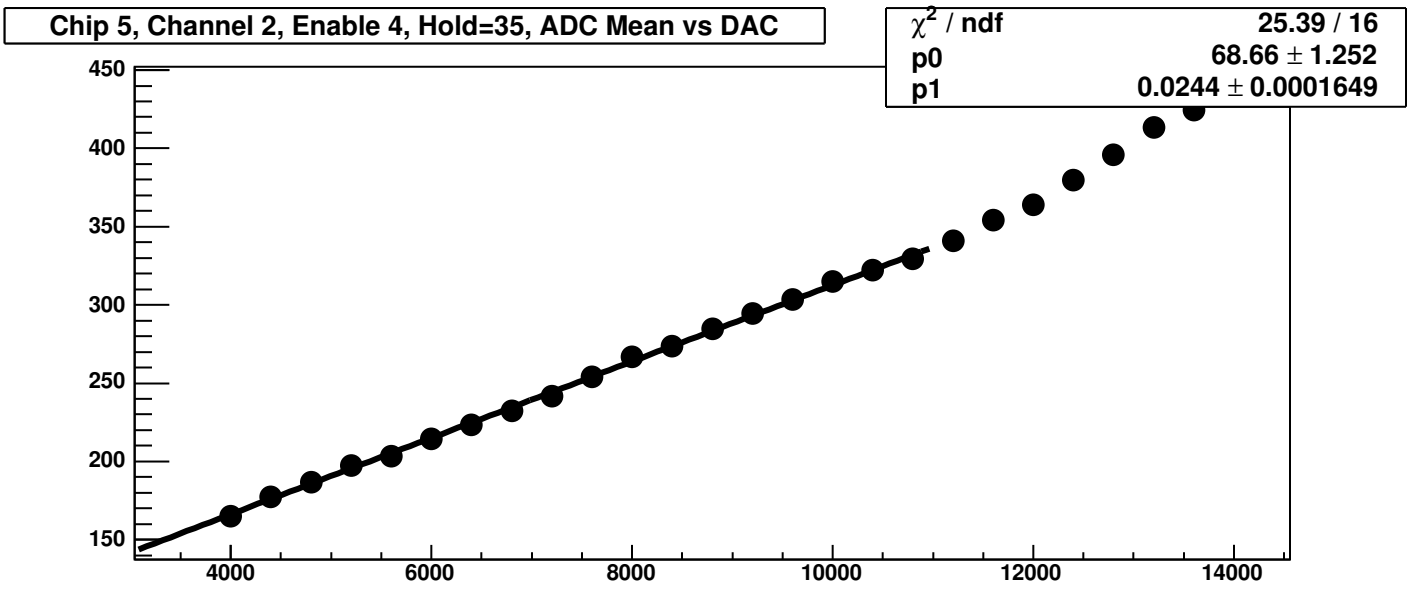

Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

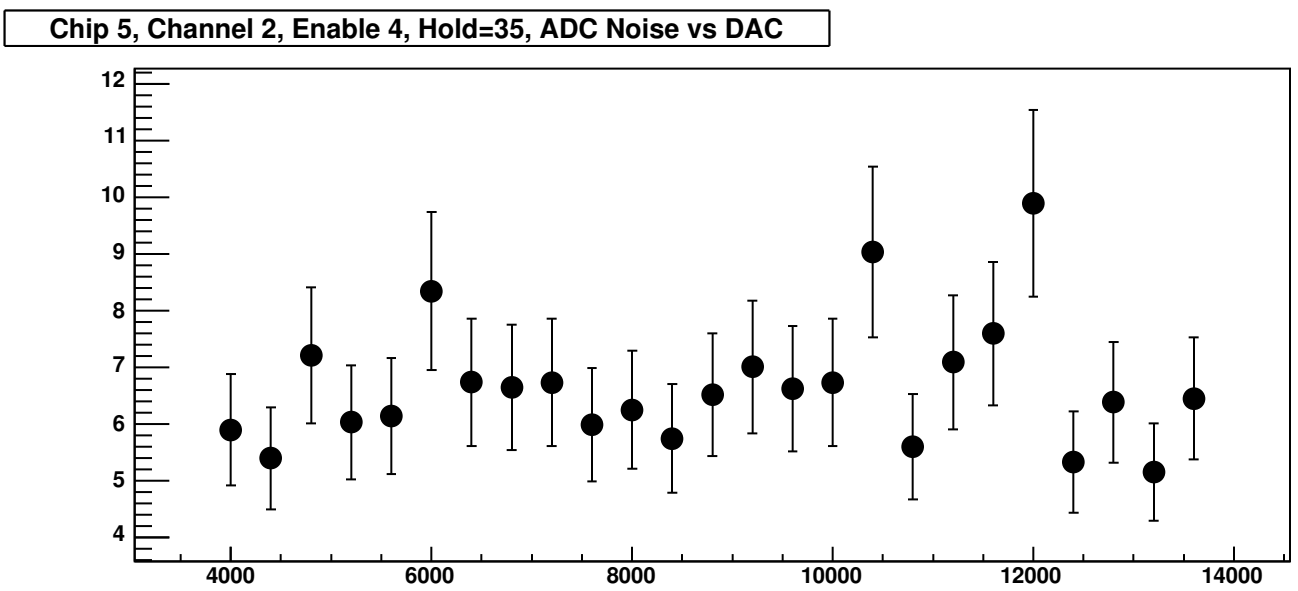

Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

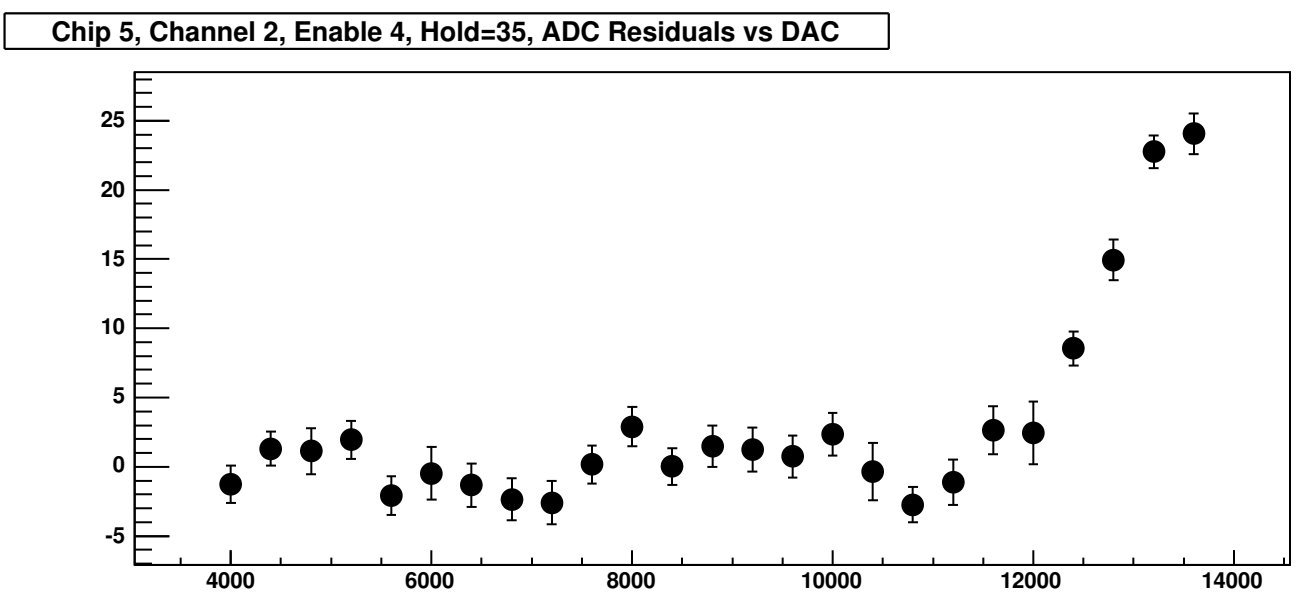

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1808 / 16
p0 260.8 ± 2.305
p1 2.574 ± 0.000345
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

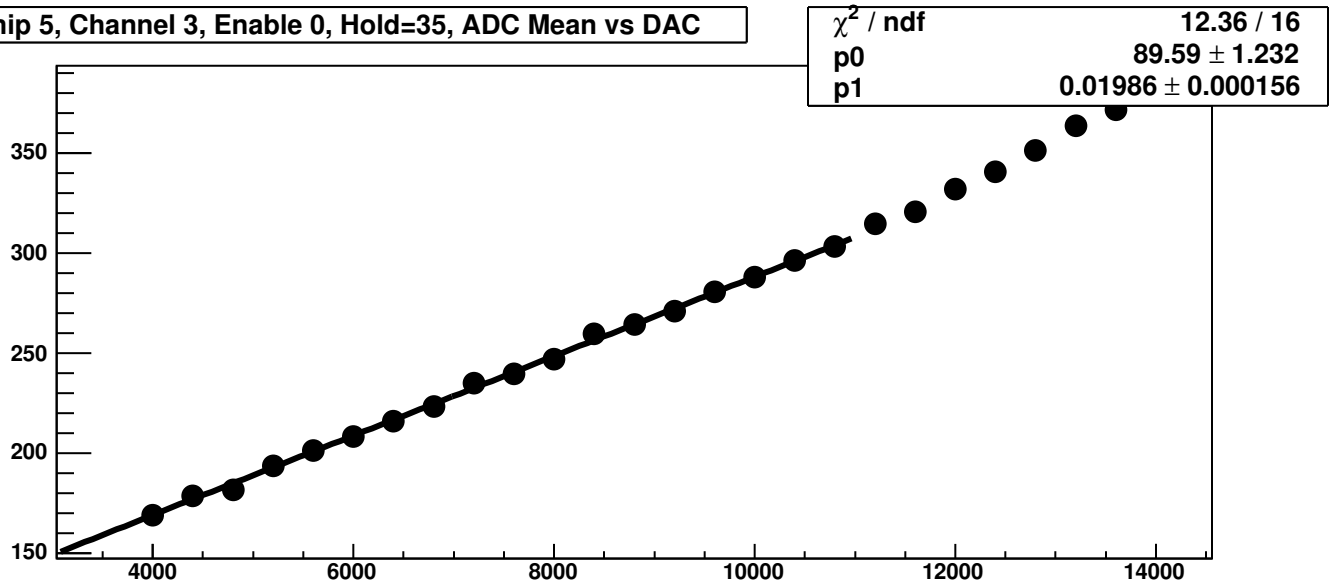

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
-6000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.39 / 16
p0 68.66 ± 1.252
p1 0.0244 ± 0.0001649
450
400
350
300
250
200
150
4000
6000
8000
10000
12000
14000

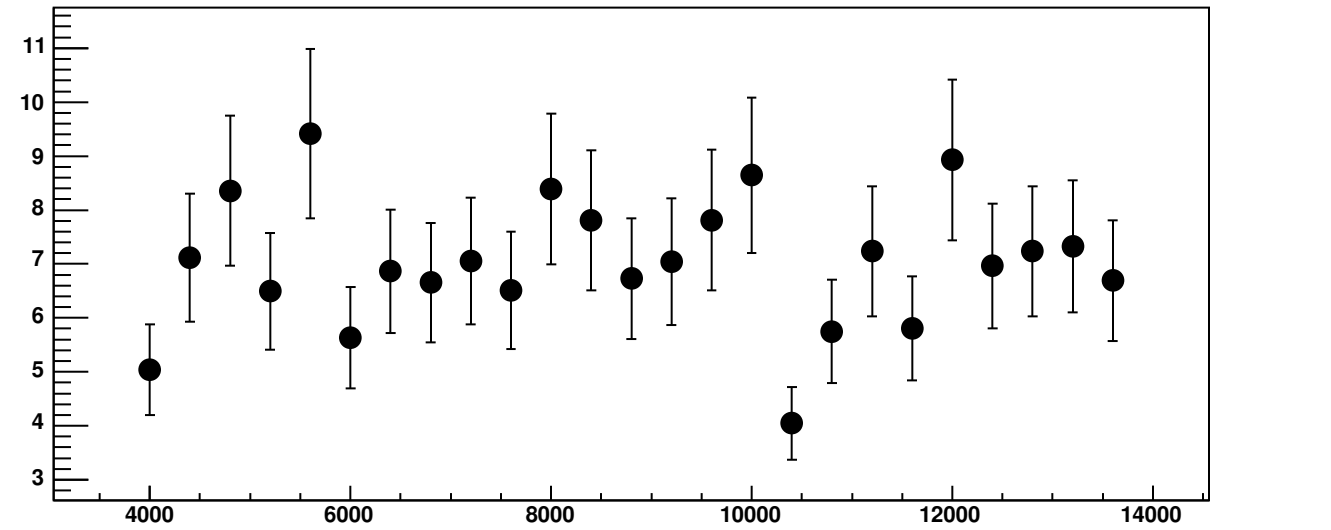

Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

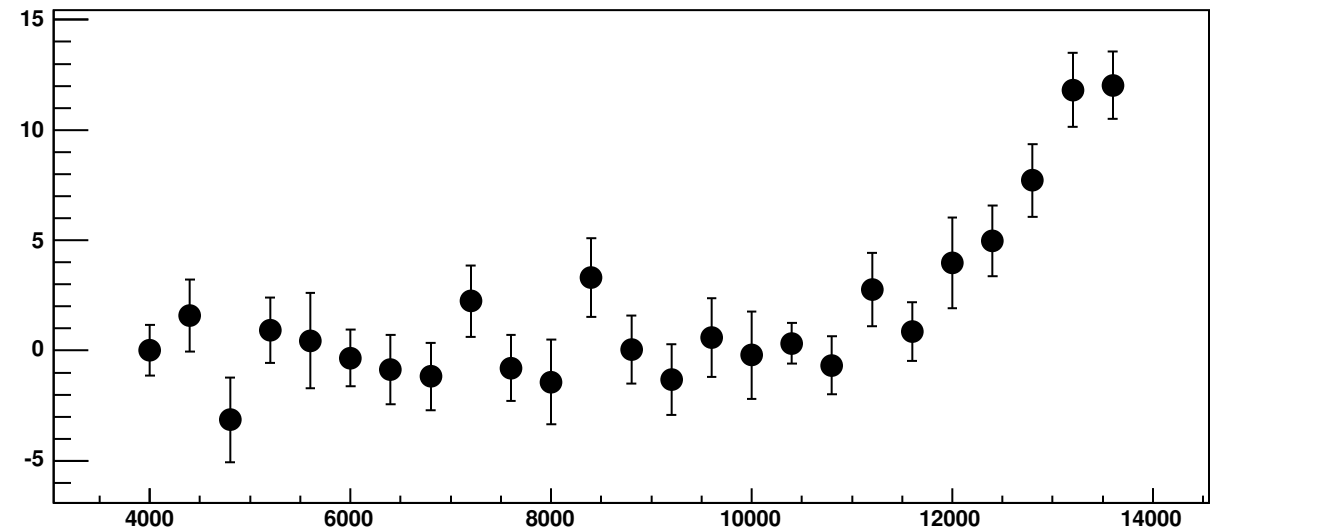
Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

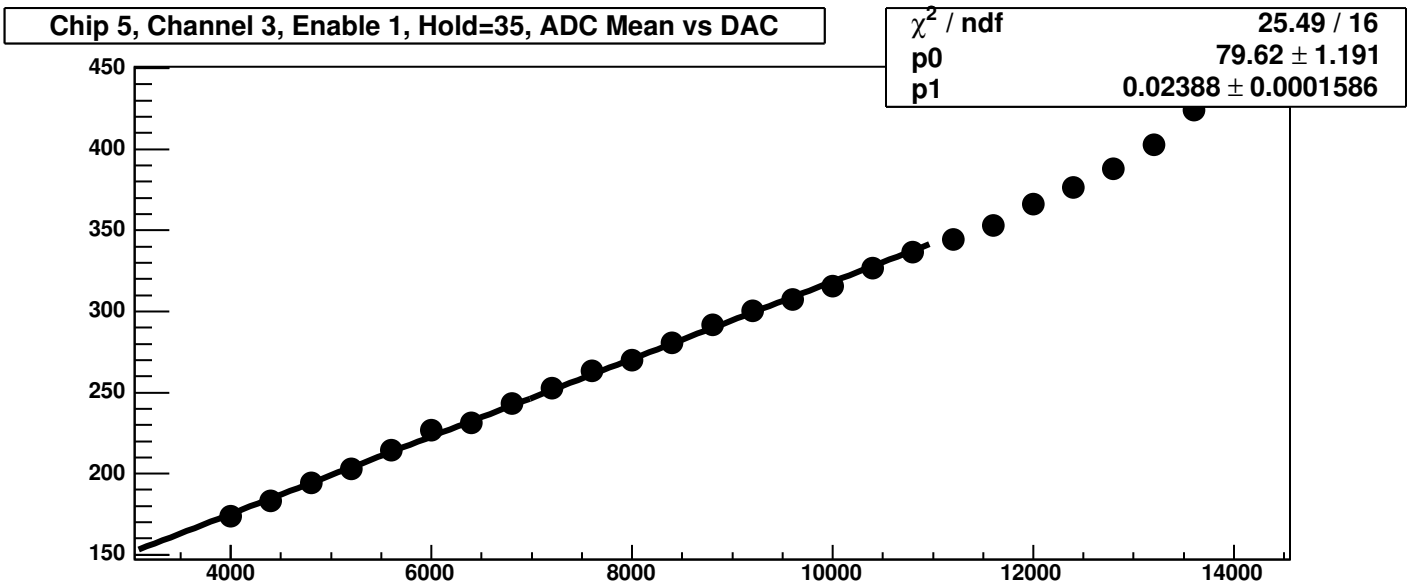


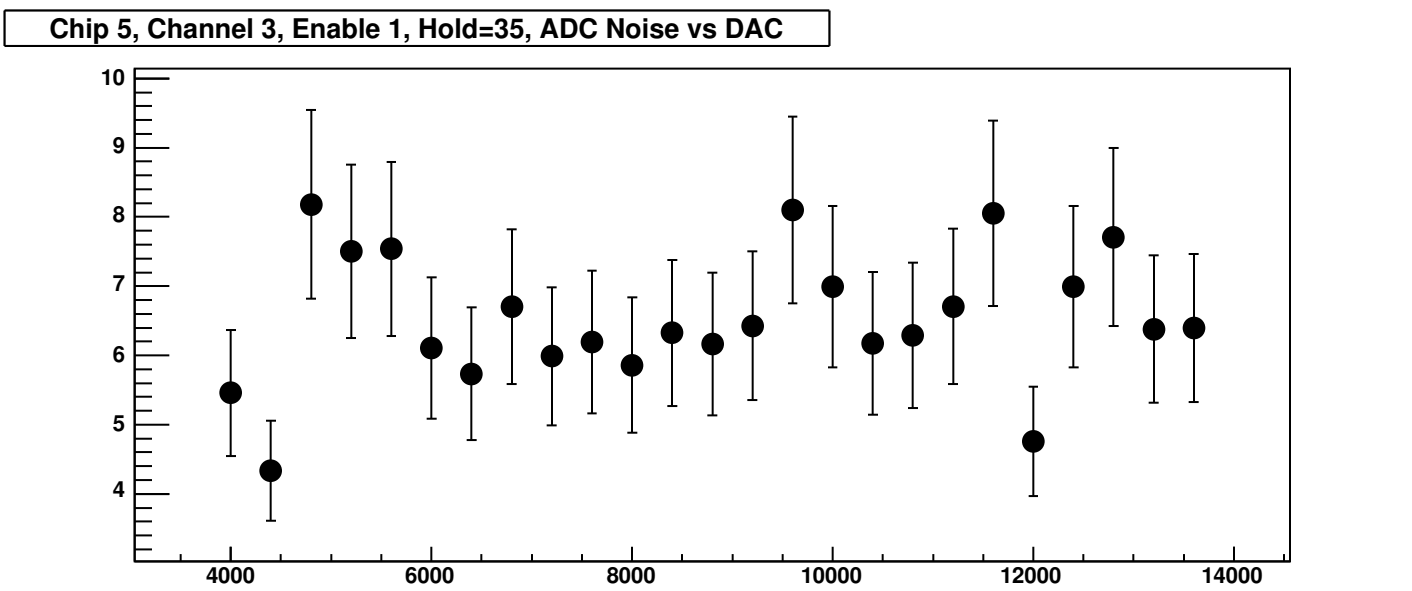
Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

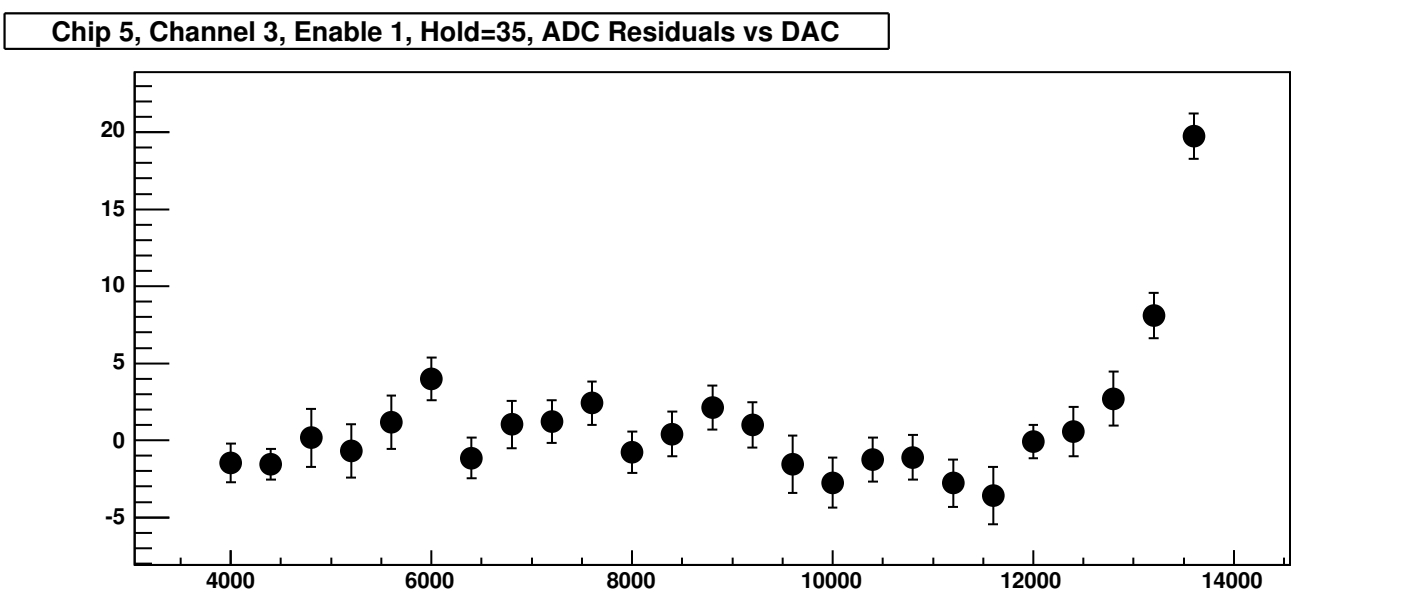


Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

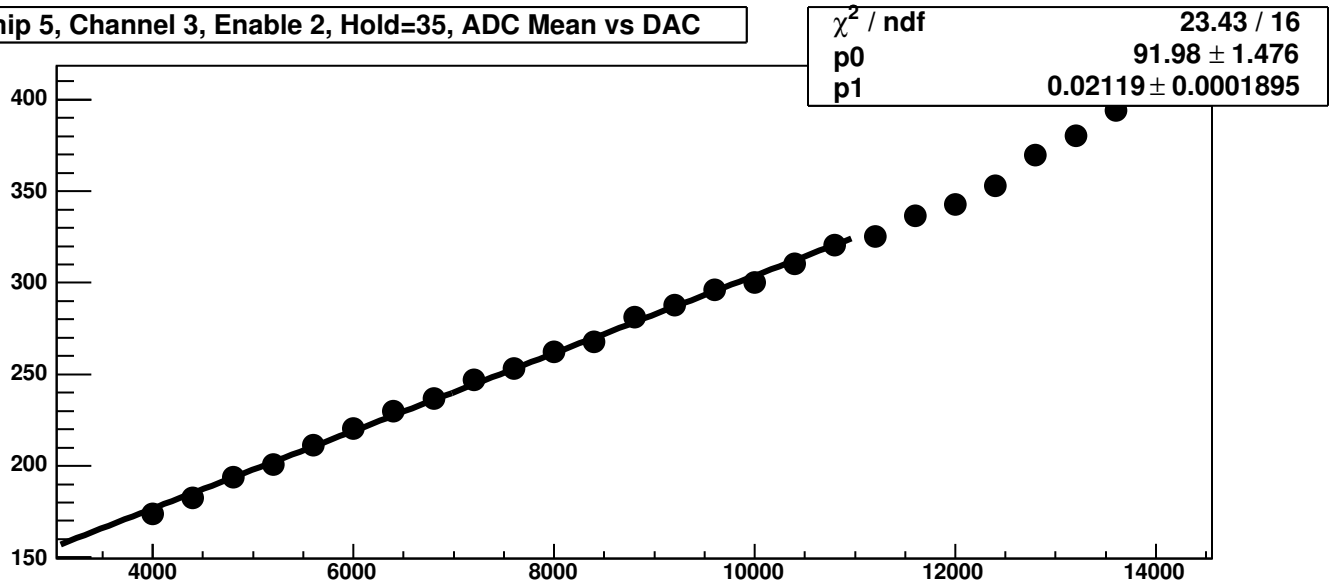



Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.49 / 16
p0 79.62 ± 1.191
p1 0.02388 ± 0.0001586
450
400
350
300
250
200
150
4000
6000
8000
10000
12000
14000

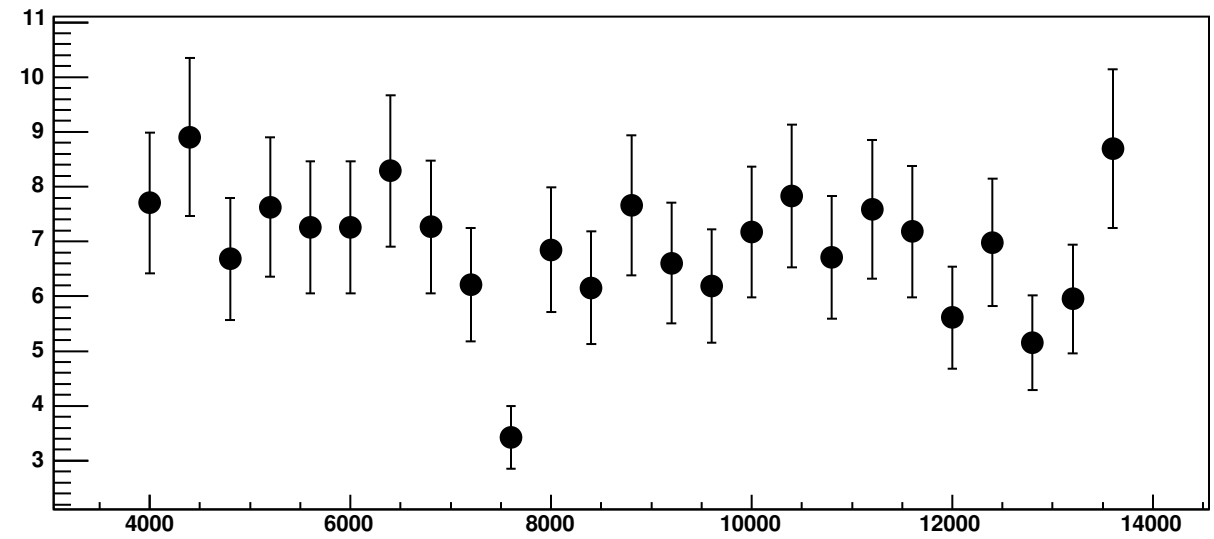

Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

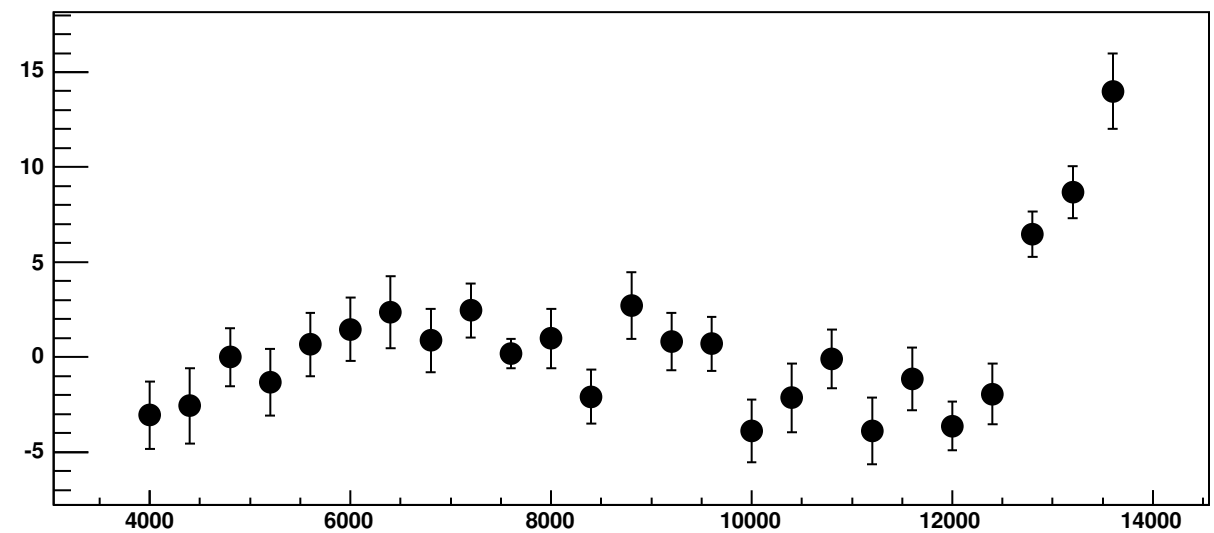
Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



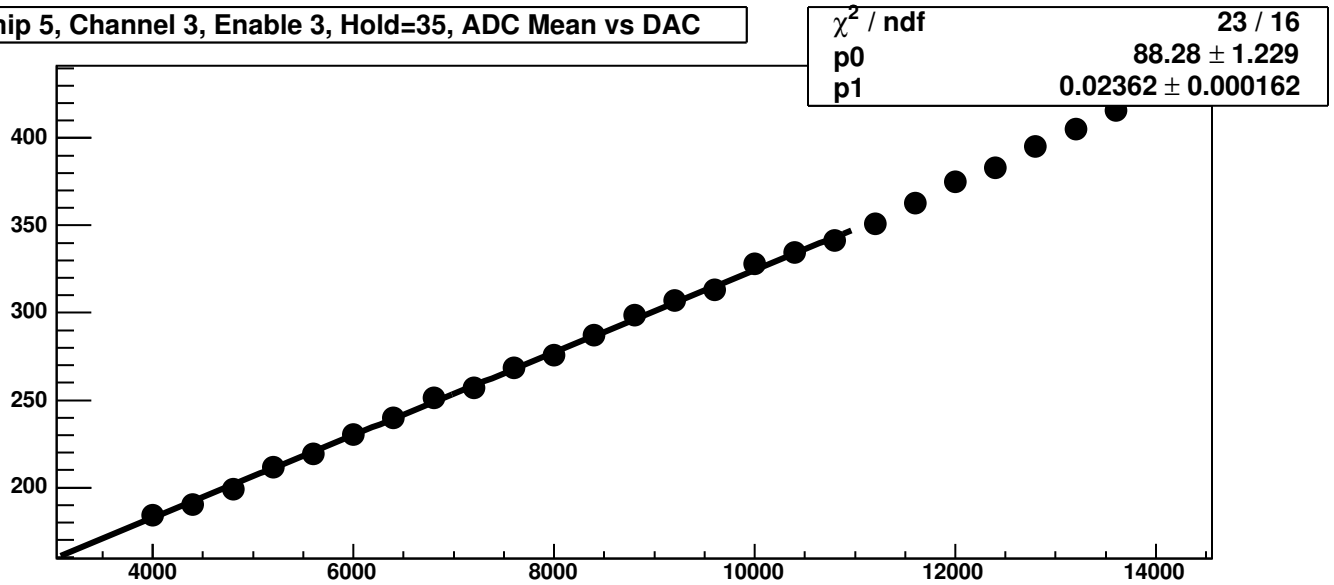
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



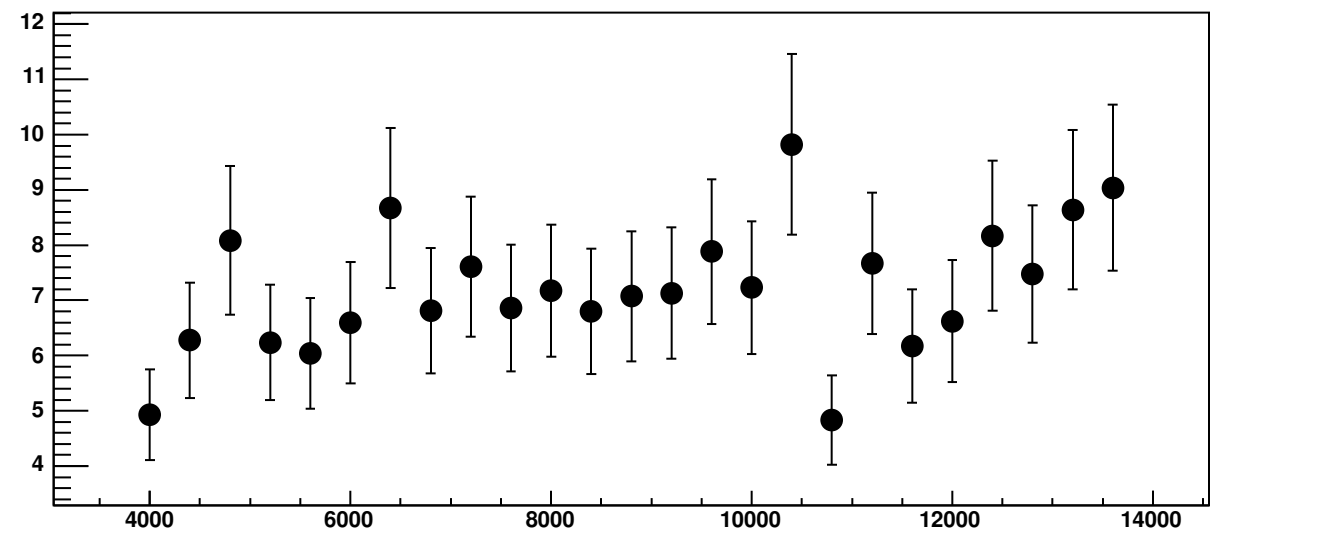
Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



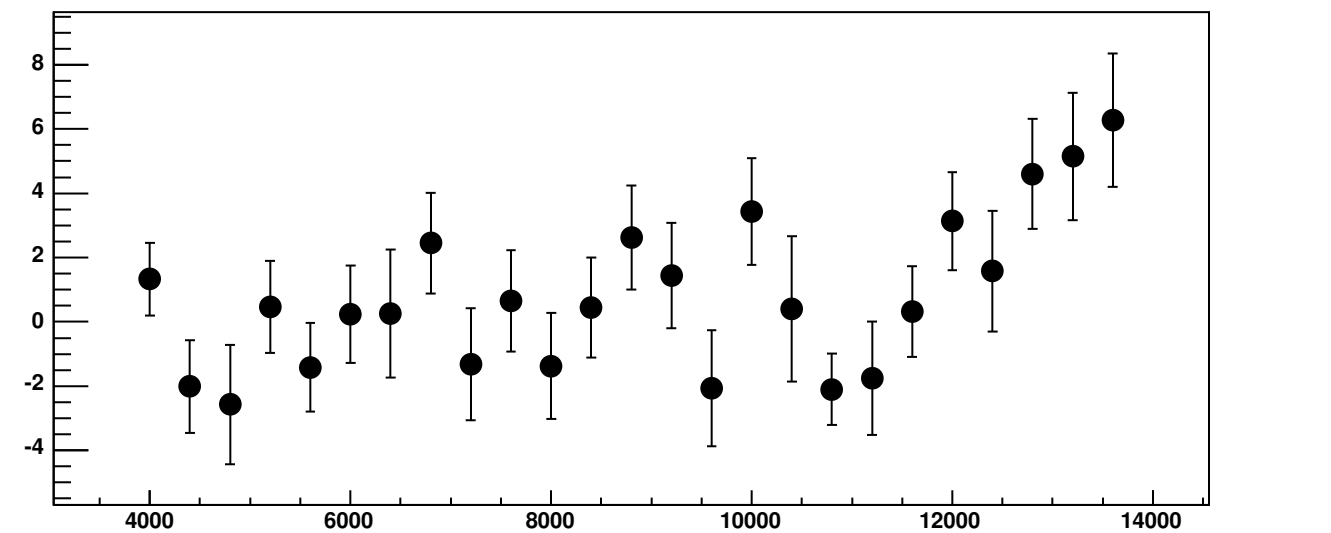
Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

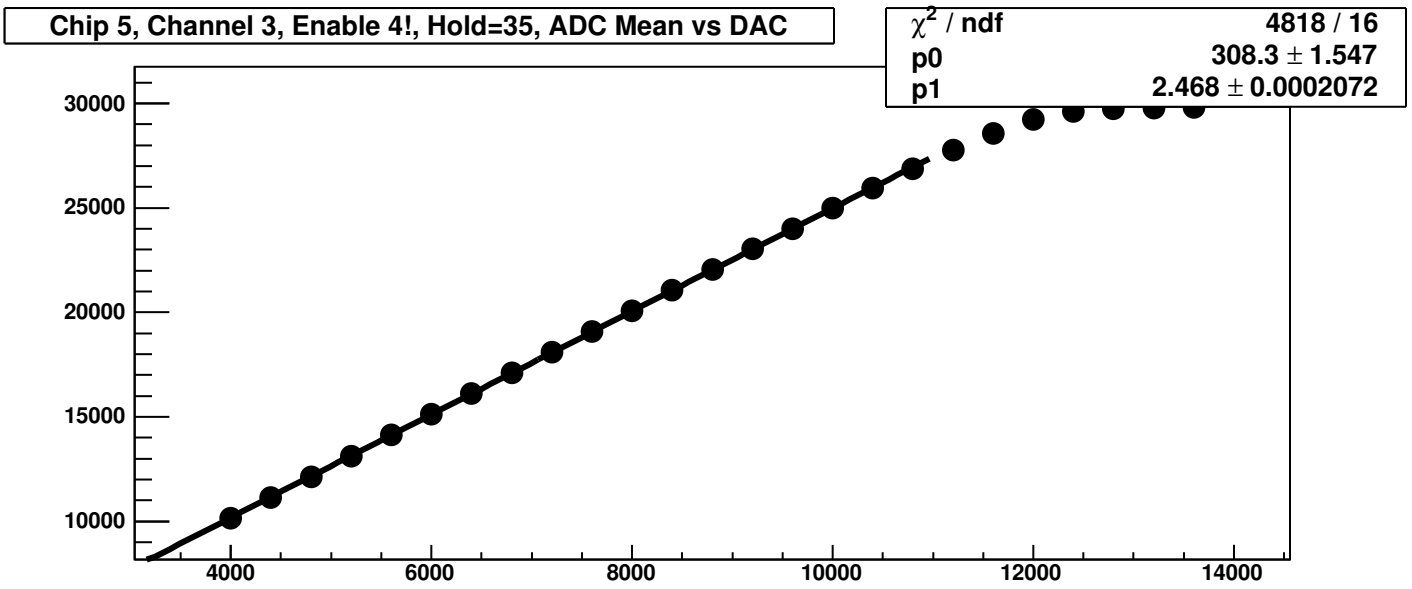


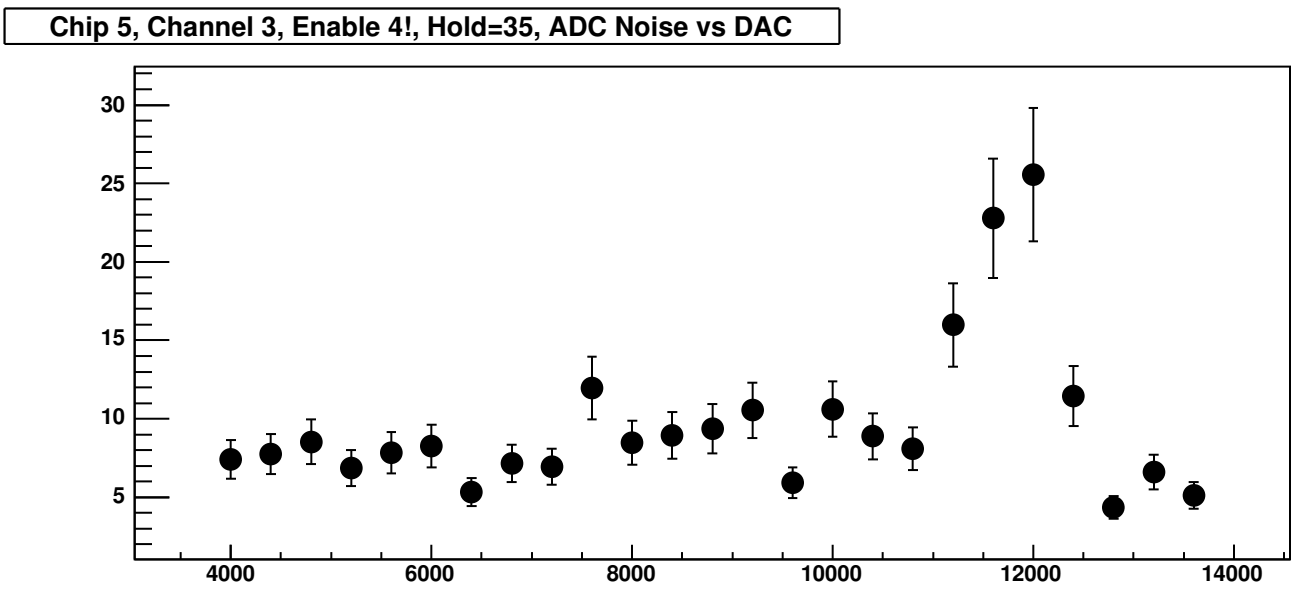
Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

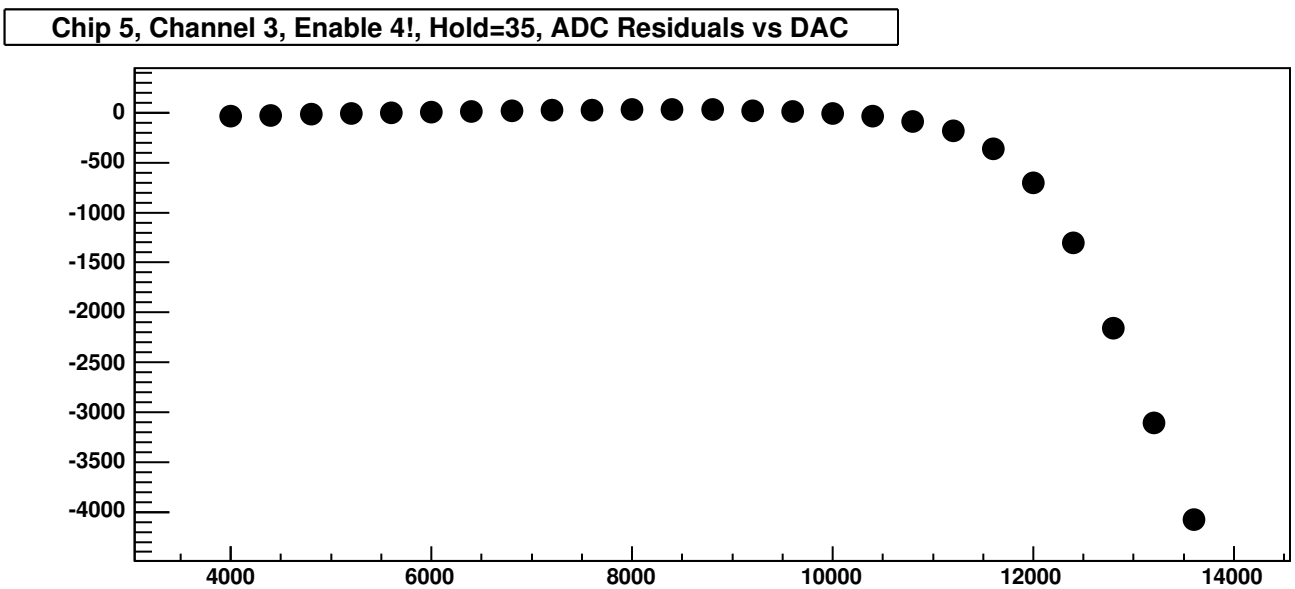


Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

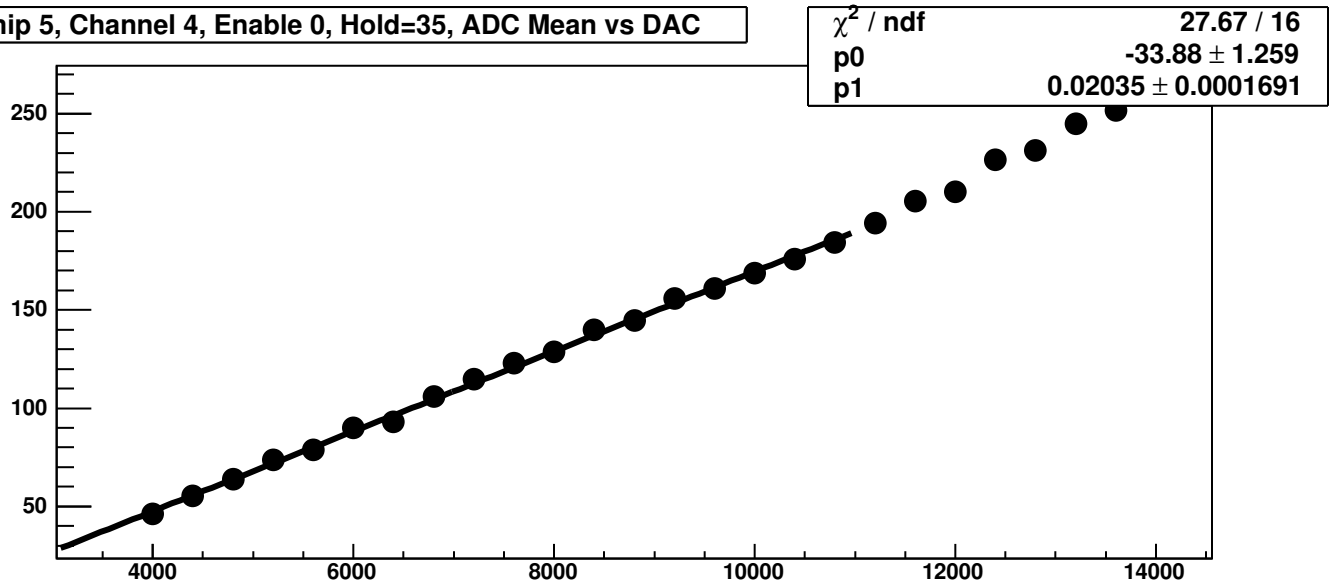



Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 4818 / 16
p0 308.3 ± 1.547
p1 2.468 ± 0.0002072
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

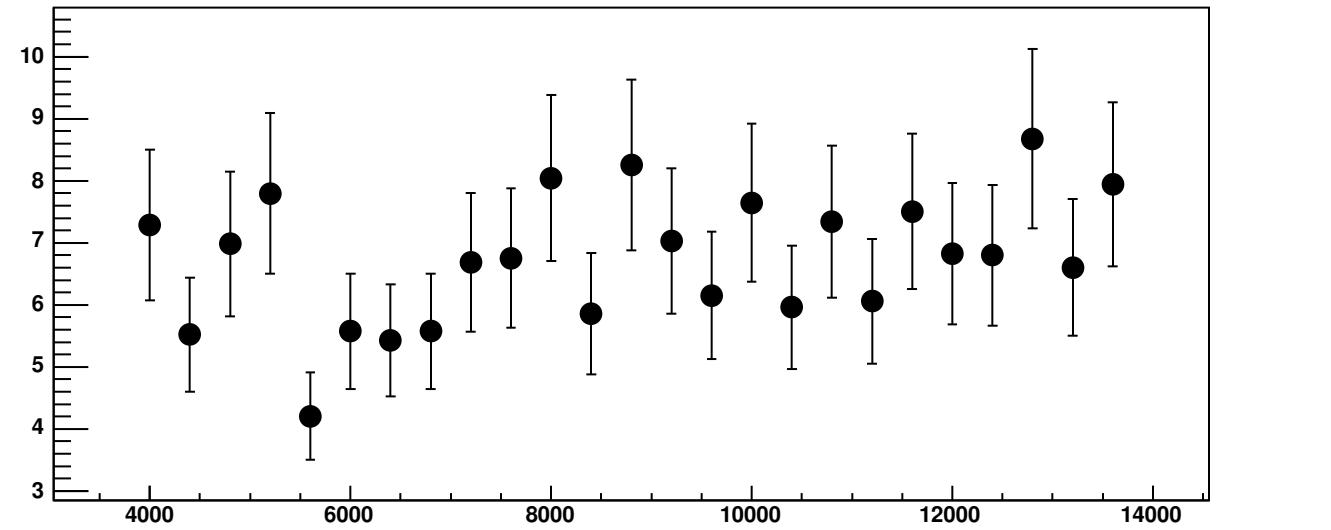

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

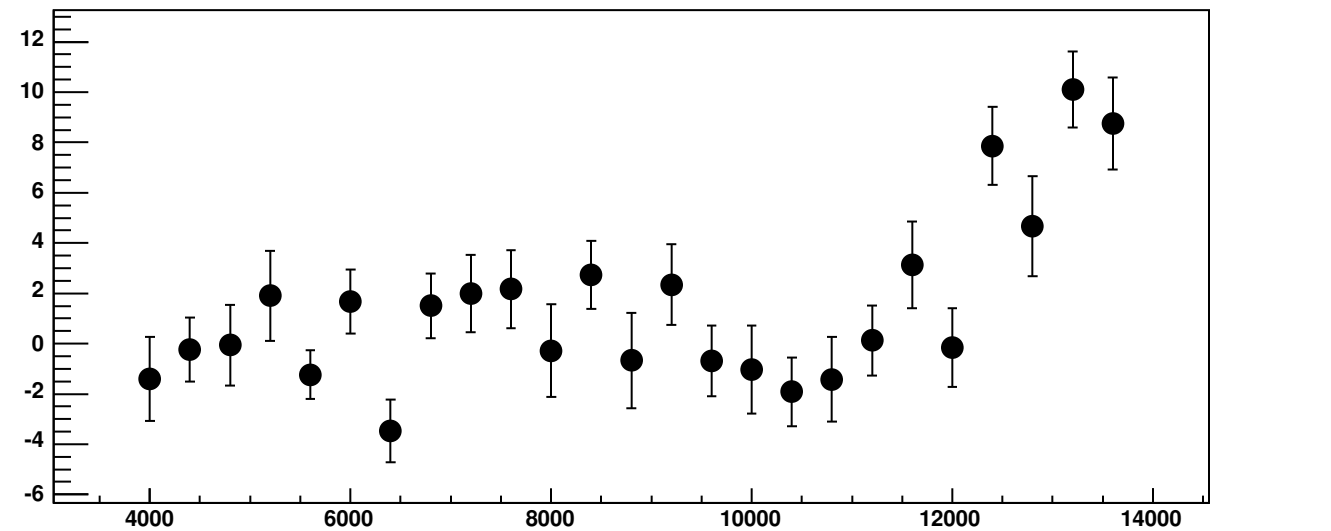
Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

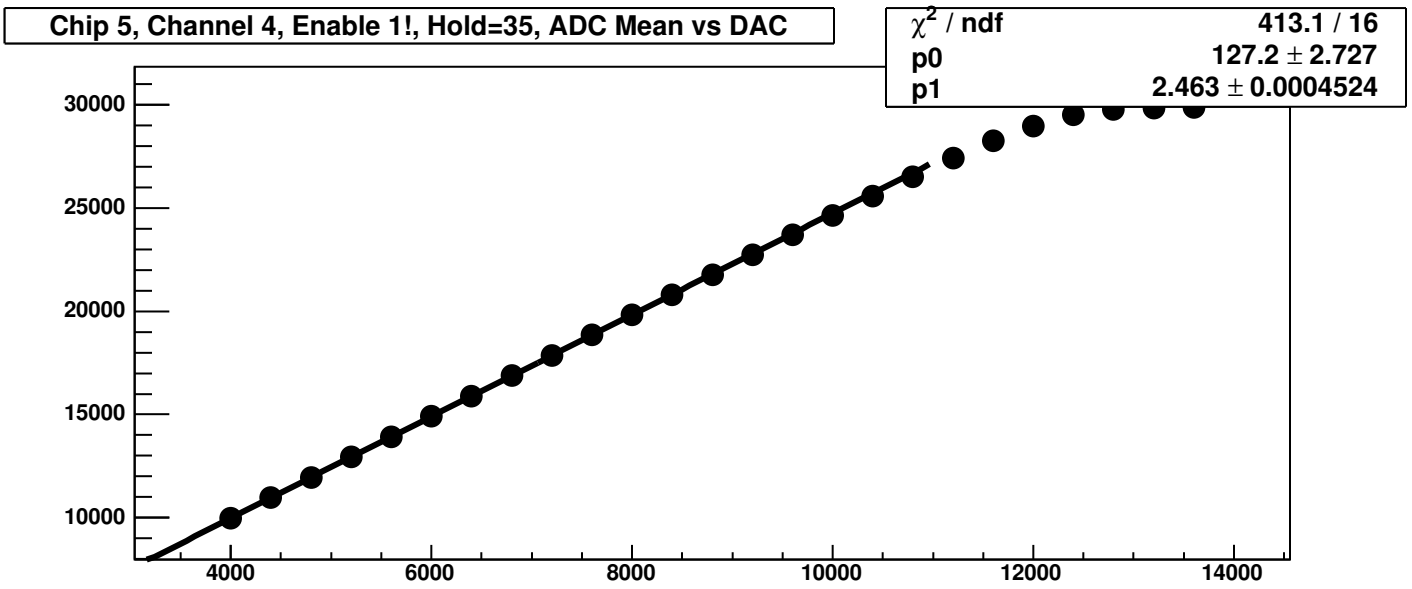


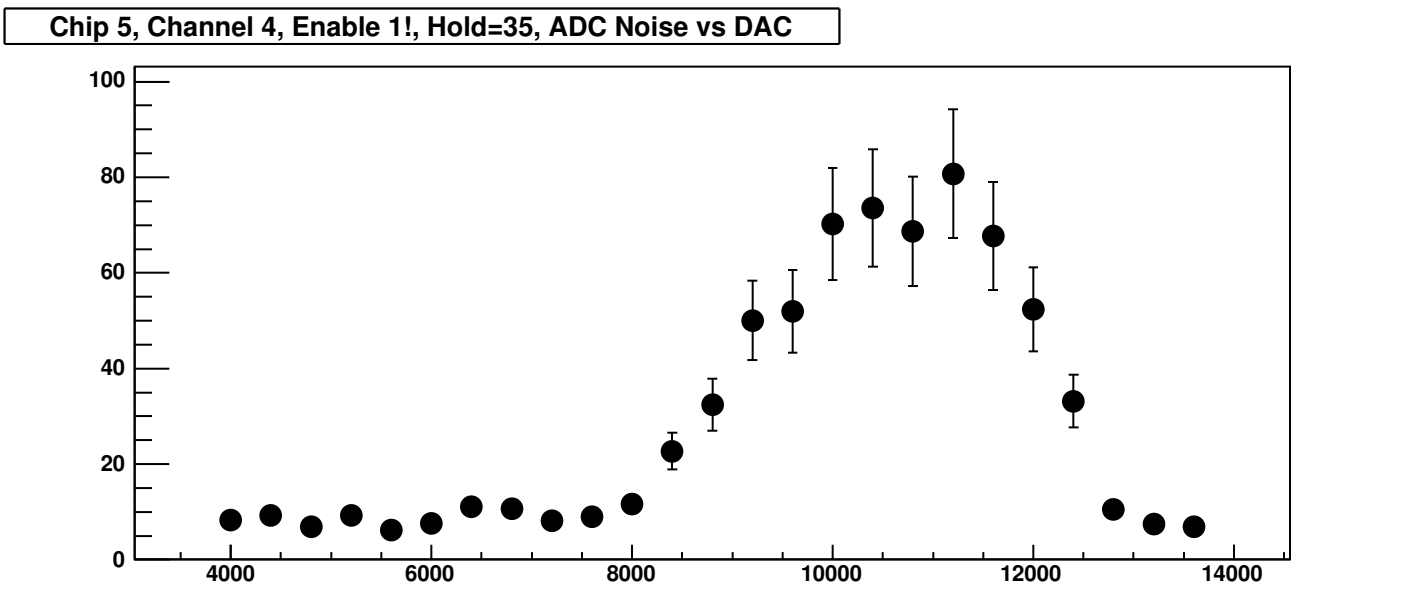
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

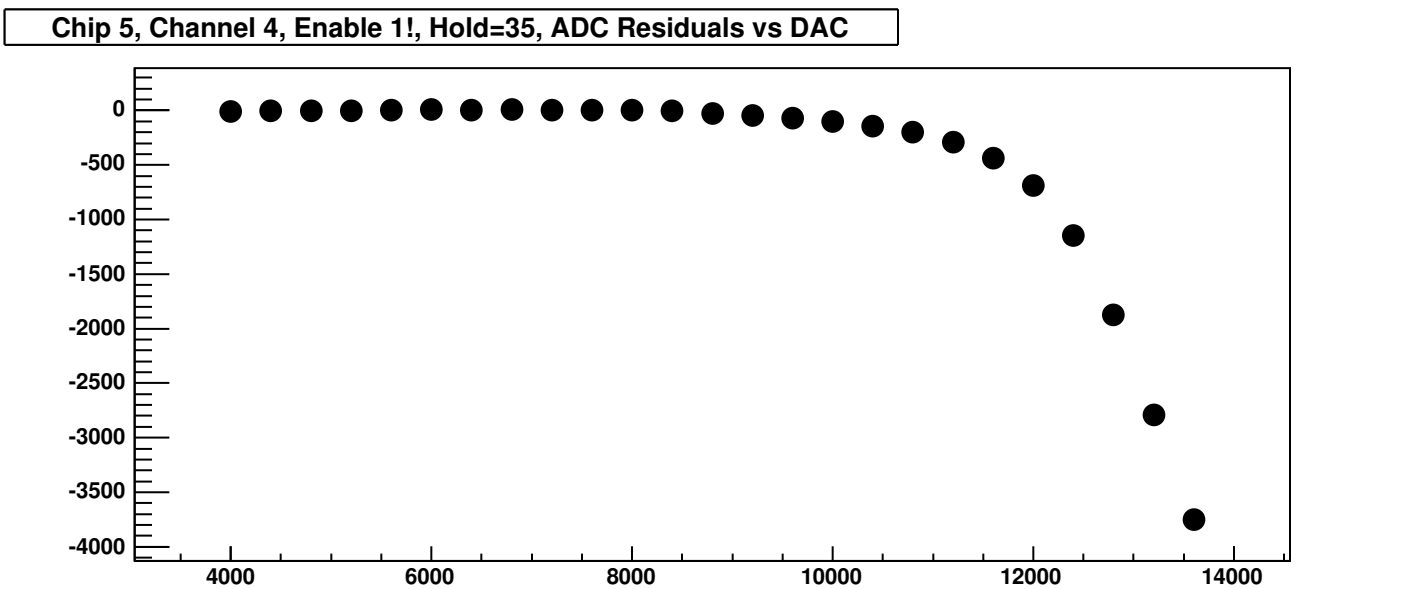


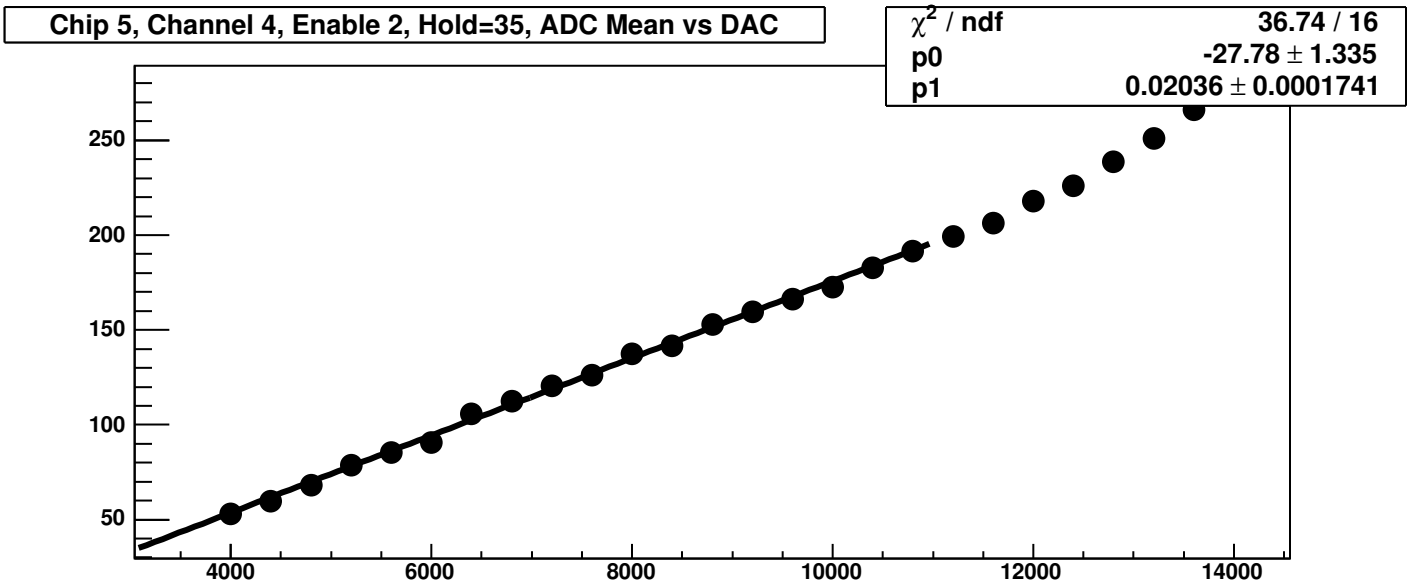
Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

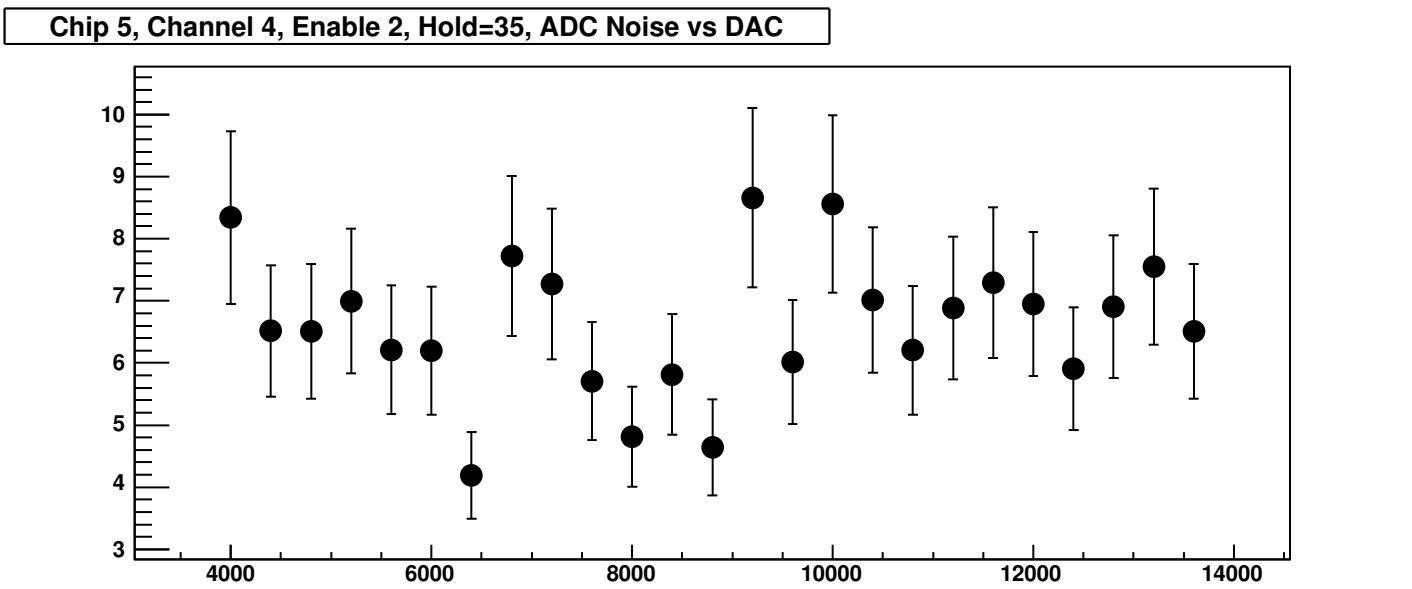


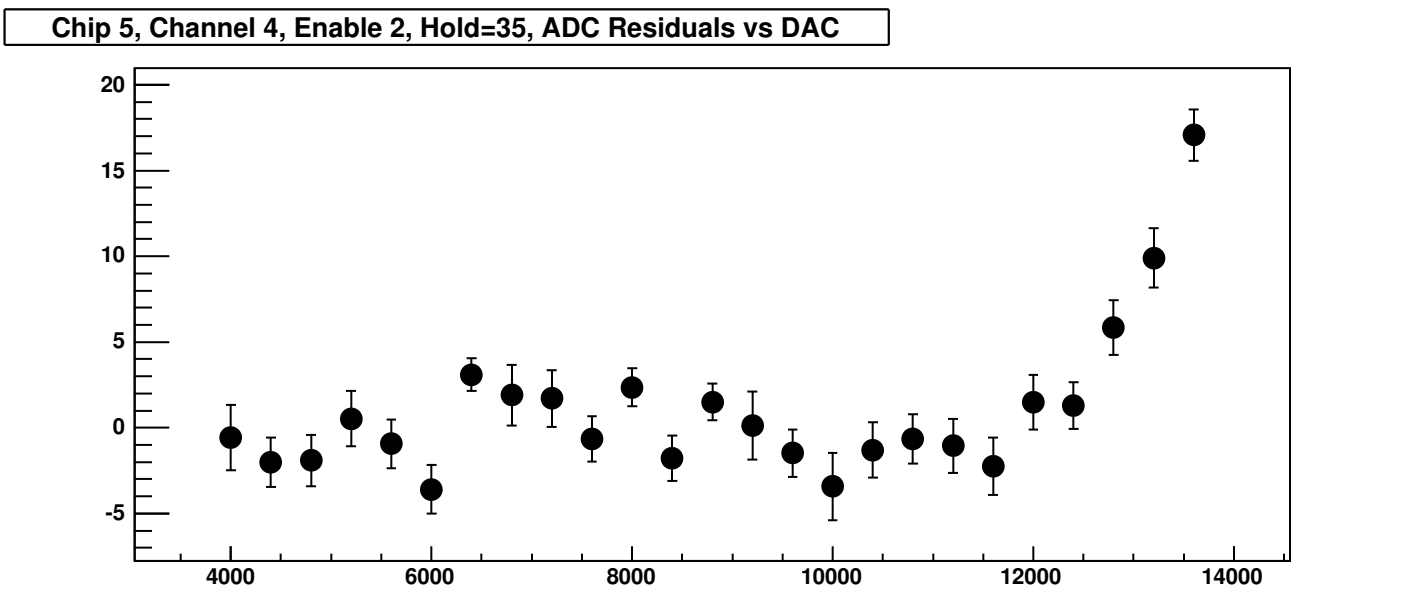

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 413.1 / 16
p0 127.2 ± 2.727
p1 2.463 ± 0.0004524
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

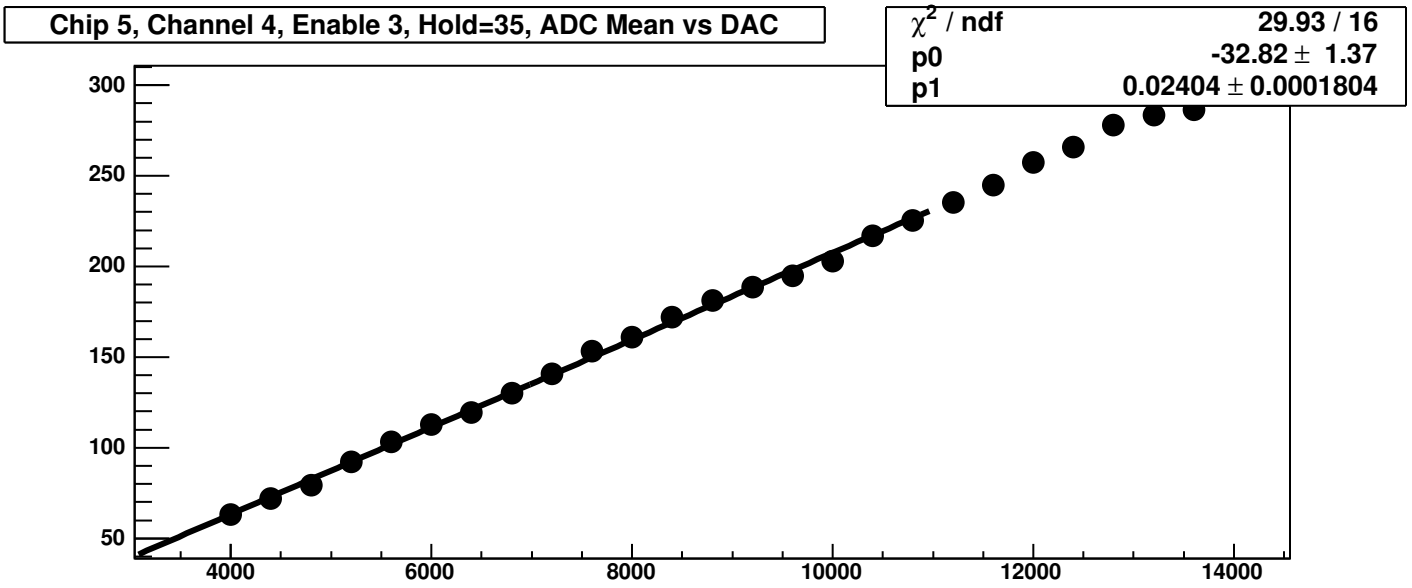

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

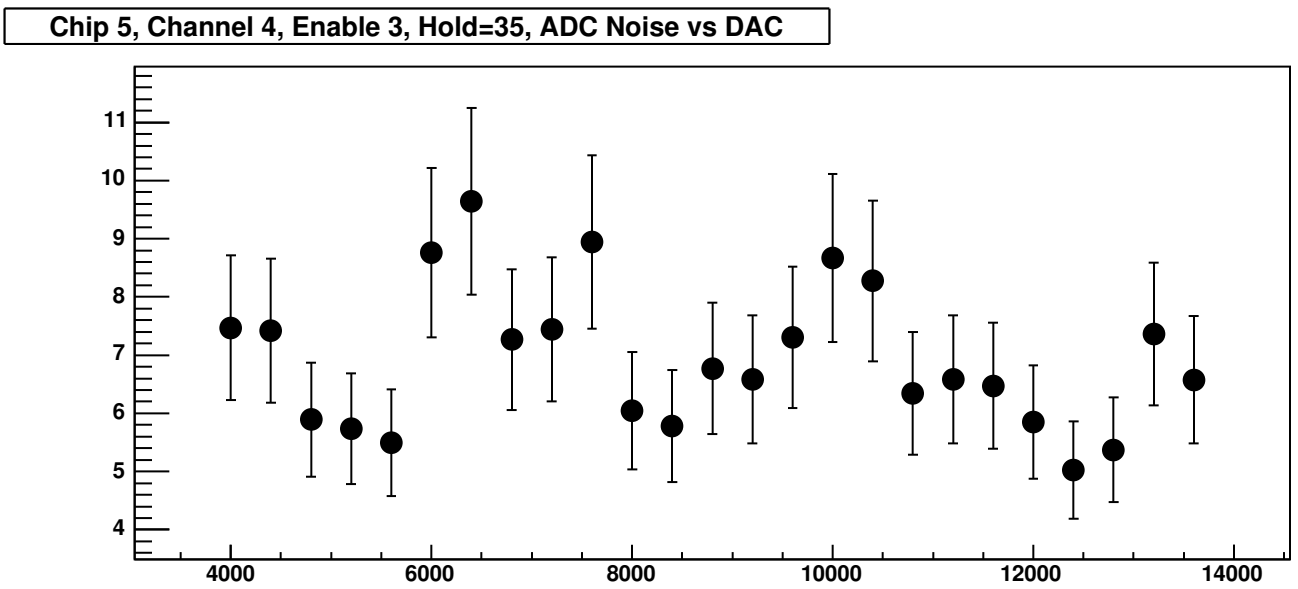

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
4000
6000
8000
10000
12000
14000

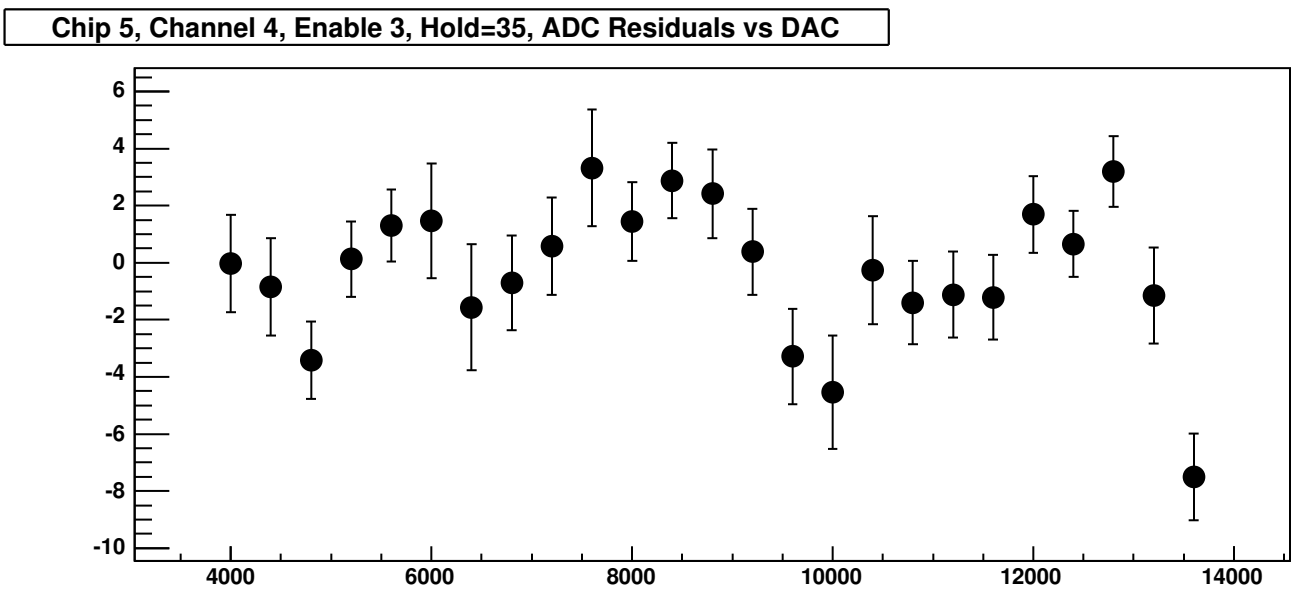

Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.74 / 16
p0 -27.78 ± 1.335
p1 0.02036 ± 0.0001741
250
200
150
100
50
4000
6000
8000
10000
12000
14000

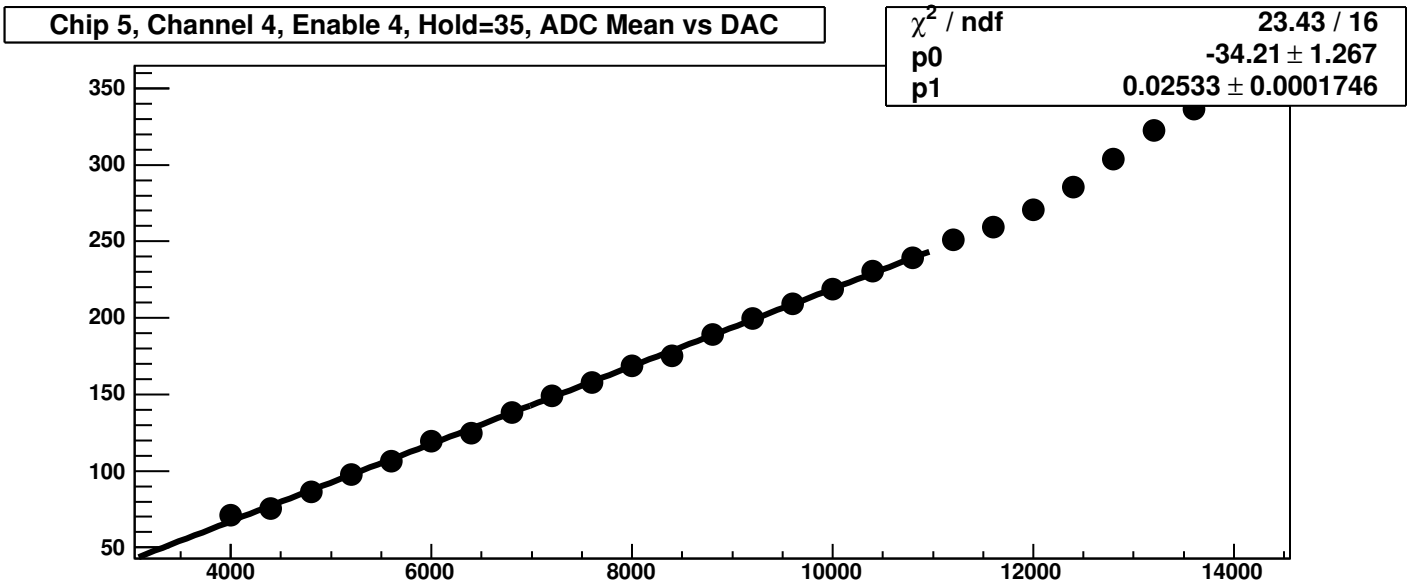

Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

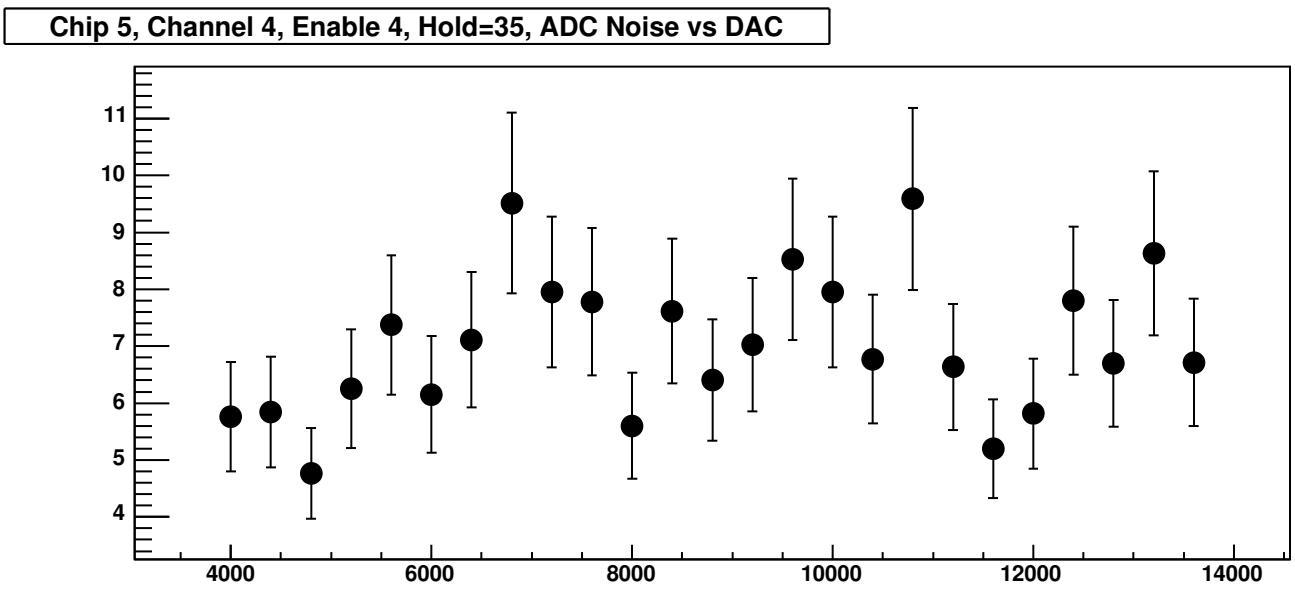

Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

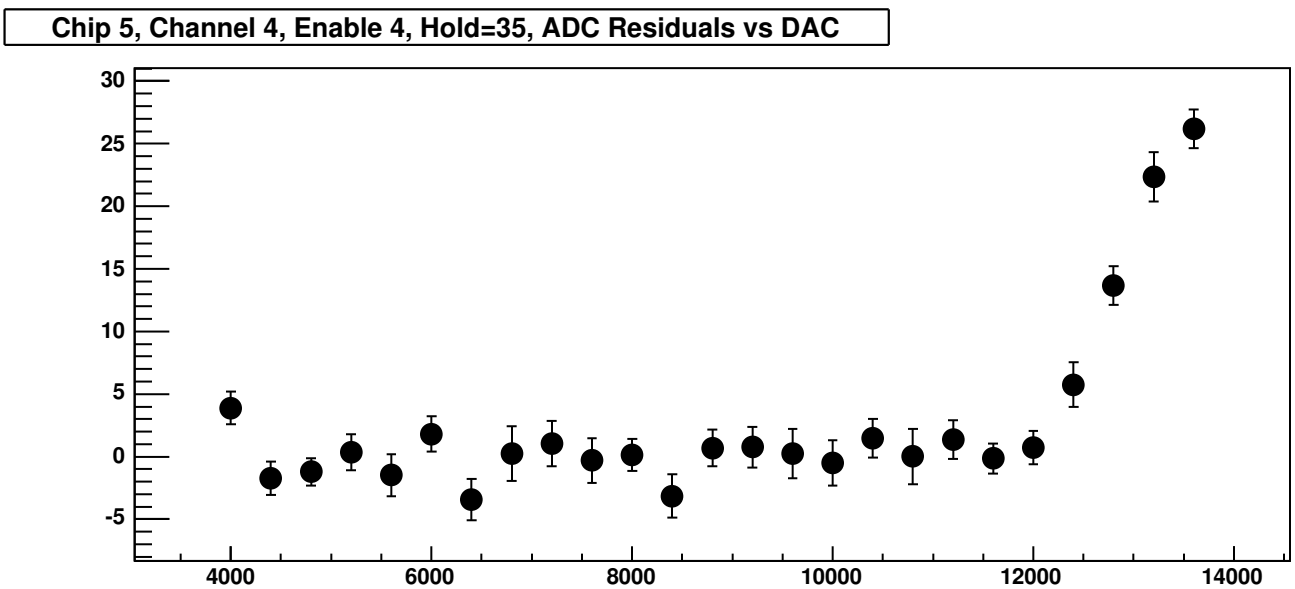

Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.93 / 16
p0 -32.82 ± 1.37
p1 0.02404 ± 0.0001804
300
250
200
150
100
50
4000
6000
8000
10000
12000
14000

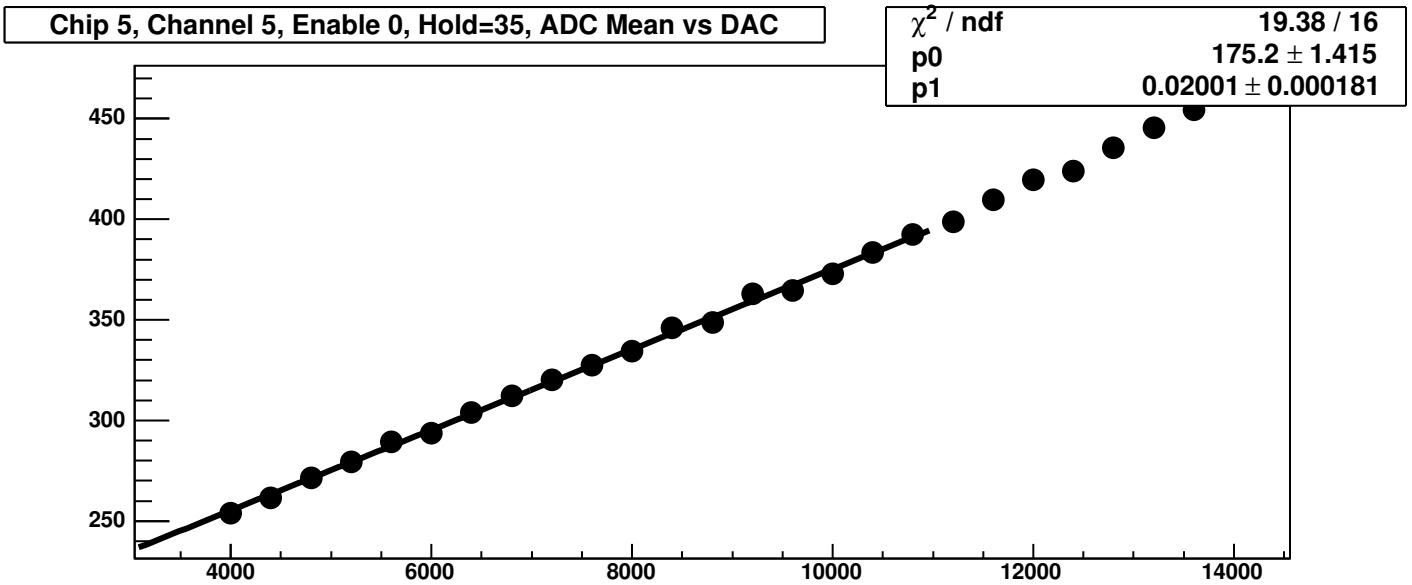

Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

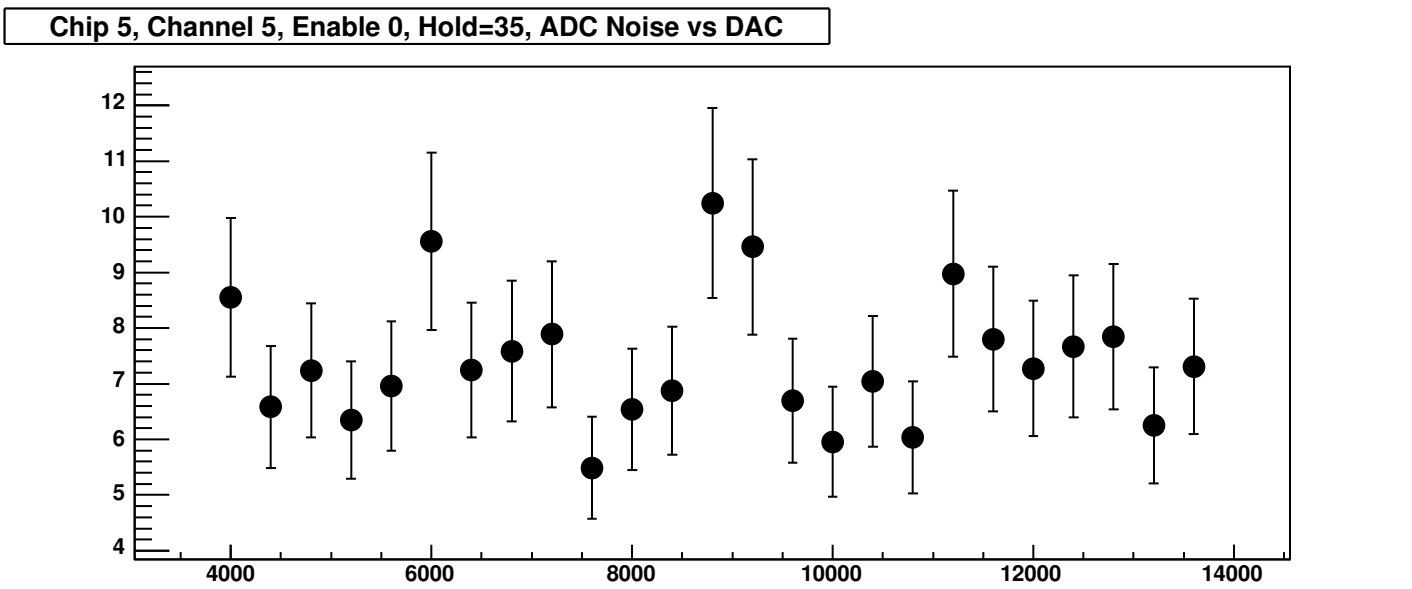

Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
4000
6000
8000
10000
12000
14000

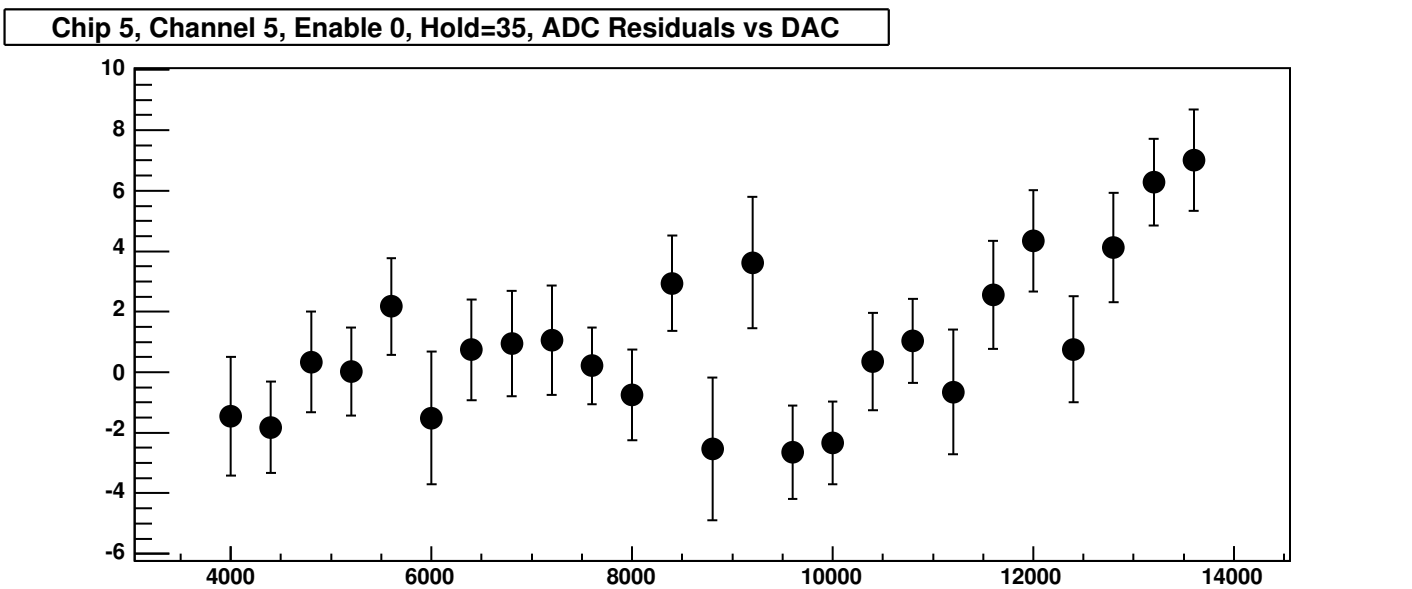

Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.43 / 16
p0 -34.21 ± 1.267
p1 0.02533 ± 0.0001746
350
300
250
200
150
100
50
4000
6000
8000
10000
12000
14000

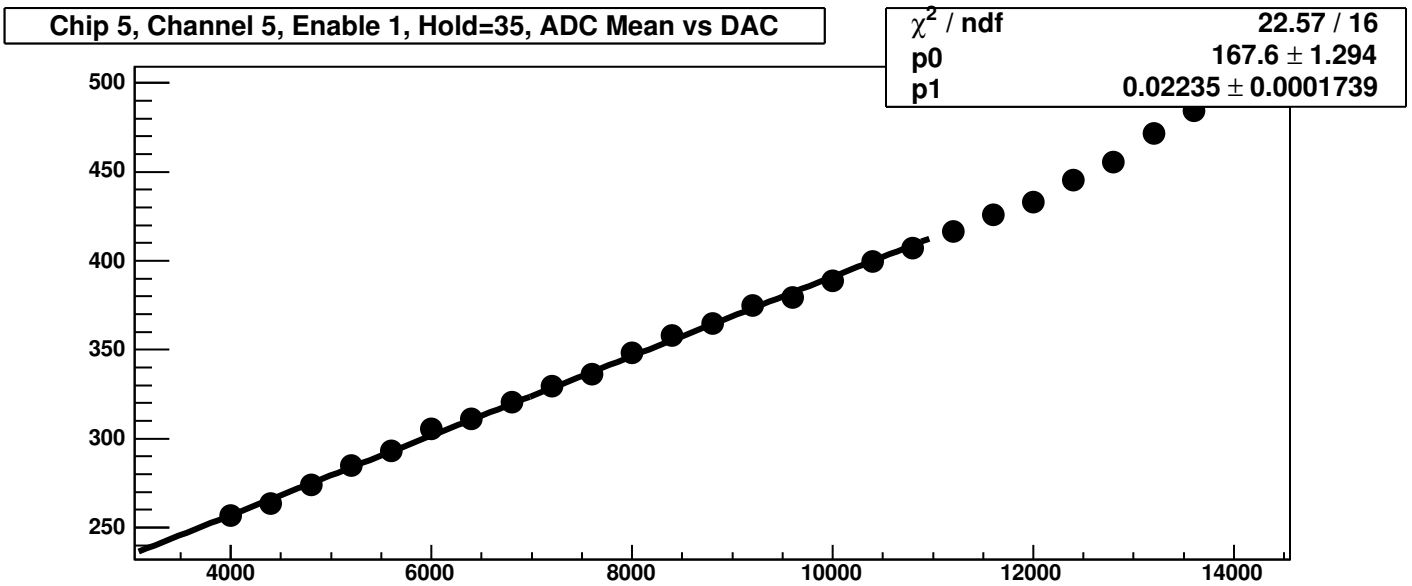

Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

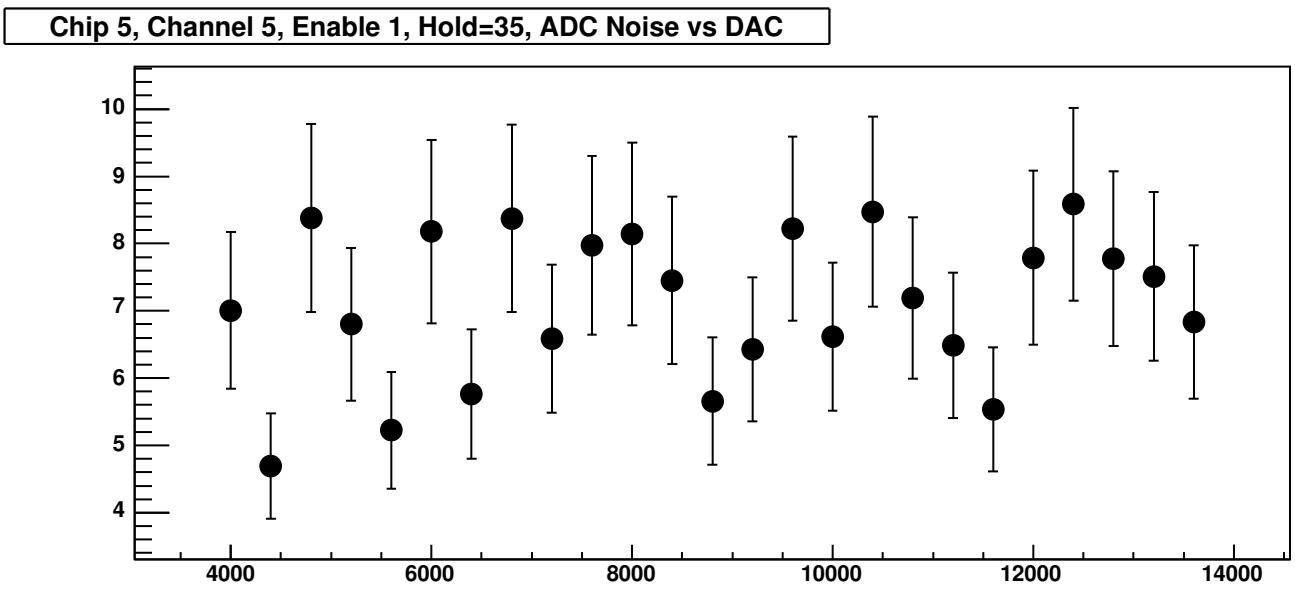

Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

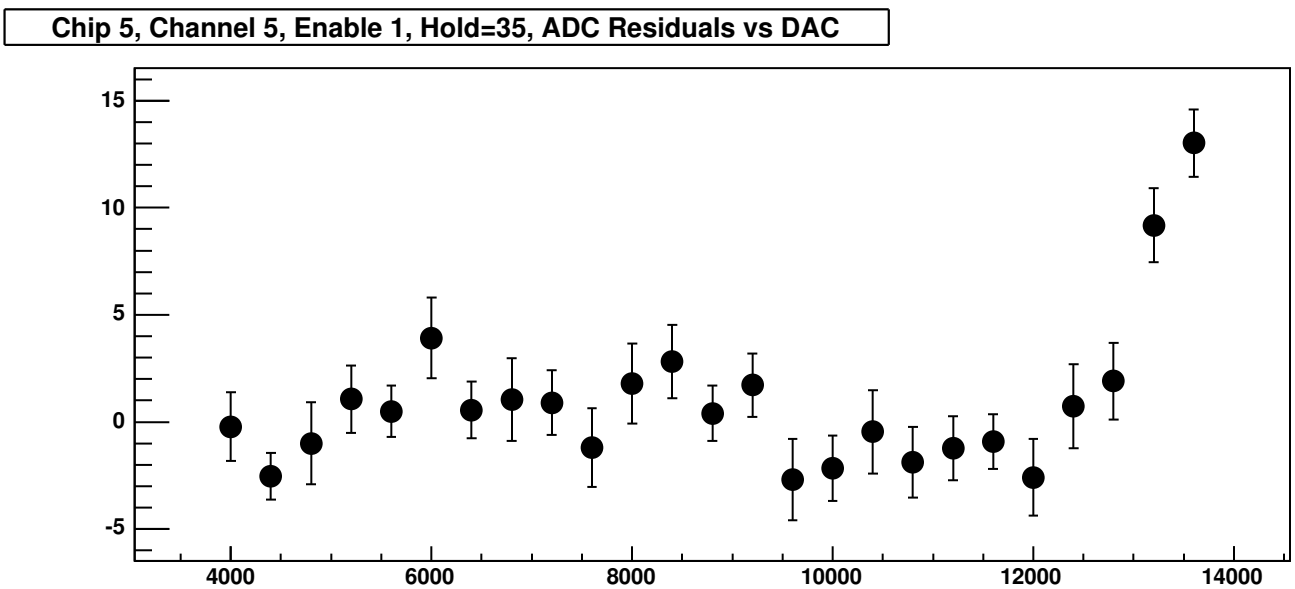

Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 19.38 / 16
p0 175.2 ± 1.415
p1 0.02001 ± 0.000181
450
400
350
300
250
4000
6000
8000
10000
12000
14000

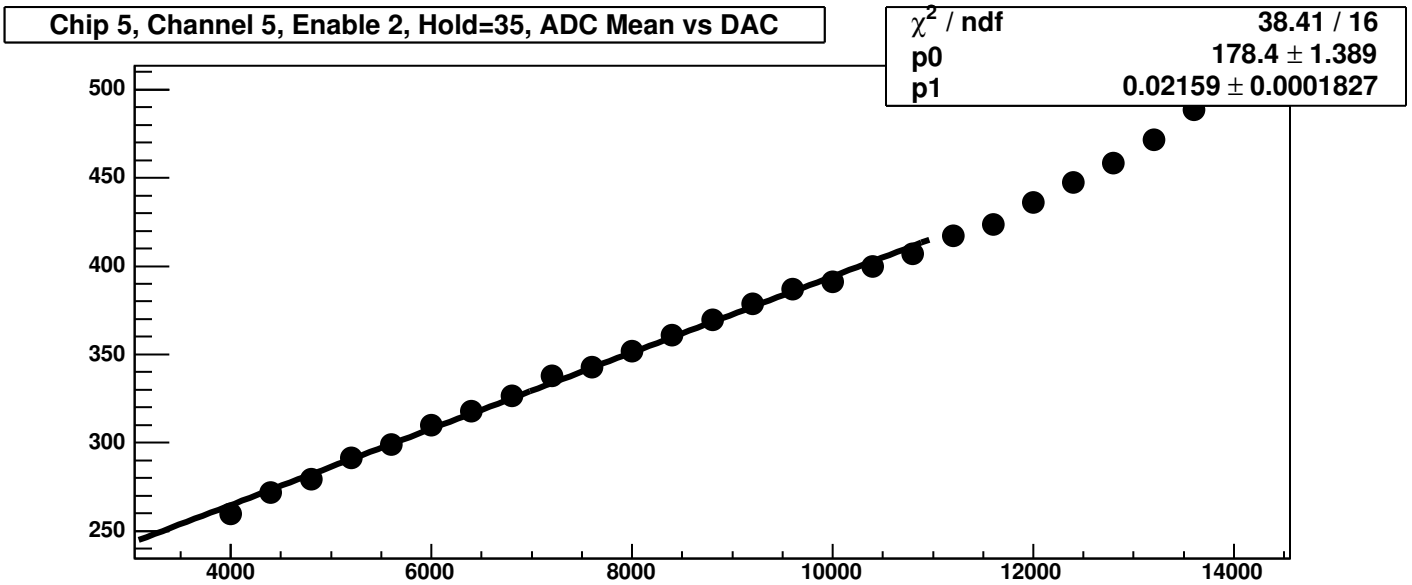

Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

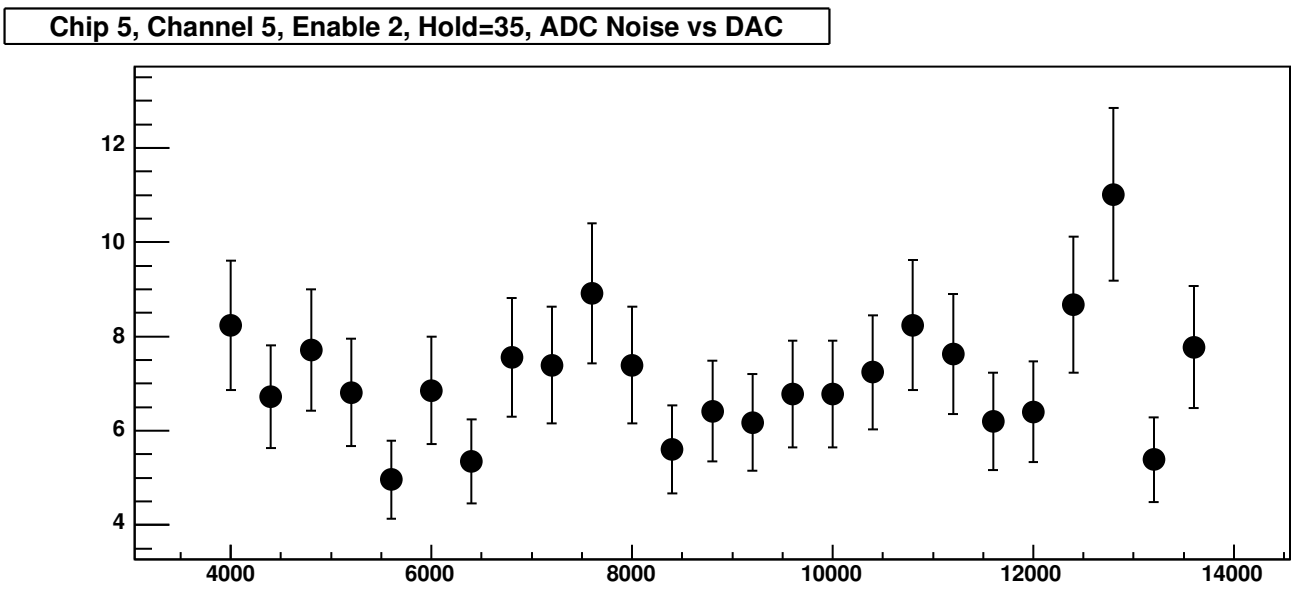

Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

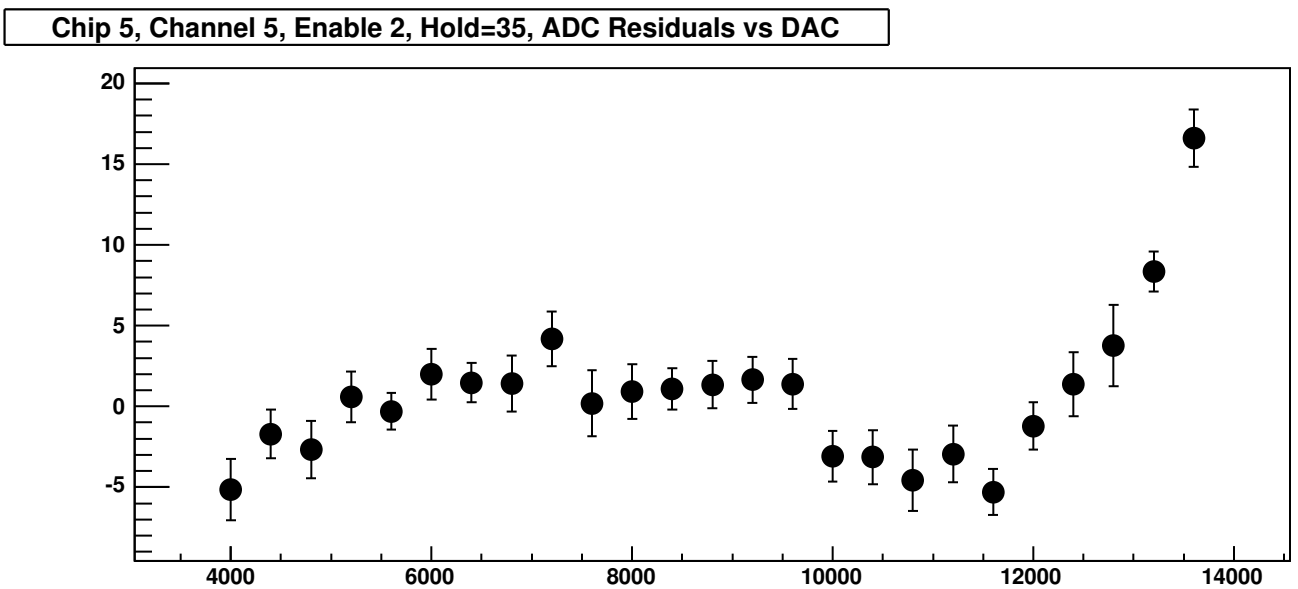

Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.57 / 16
p0 167.6 ± 1.294
p1 0.02235 ± 0.0001739
500
450
400
350
300
250
4000
6000
8000
10000
12000
14000


Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

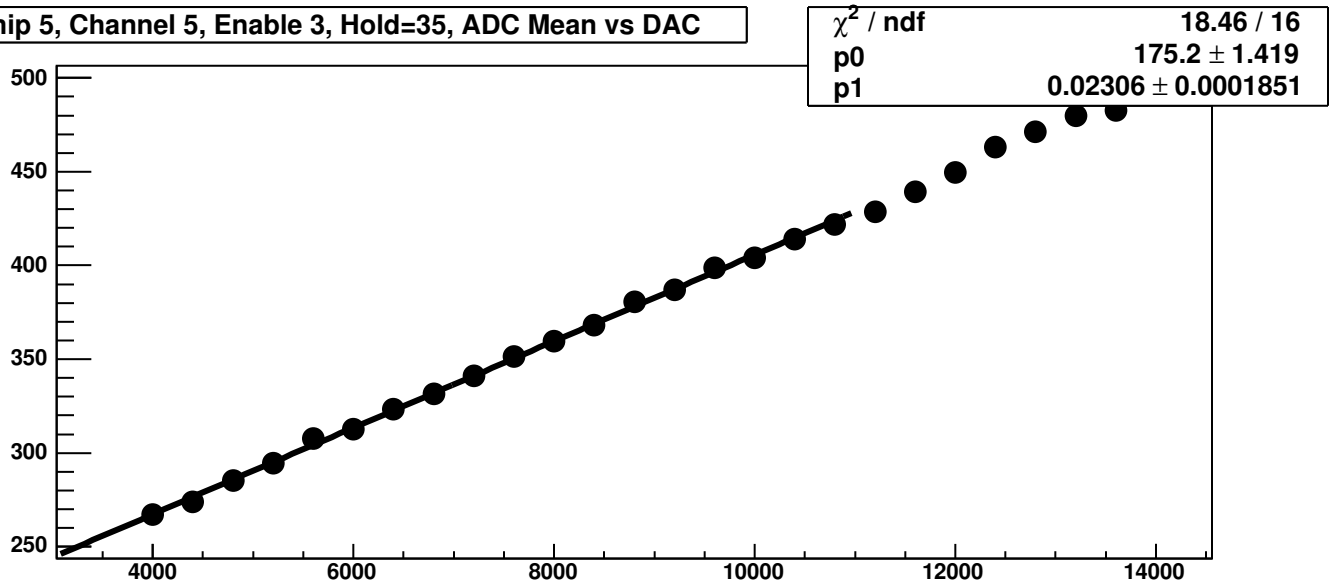

Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000


Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.41 / 16
p0 178.4 ± 1.389
p1 0.02159 ± 0.0001827
500
450
400
350
300
250
4000
6000
8000
10000
12000
14000

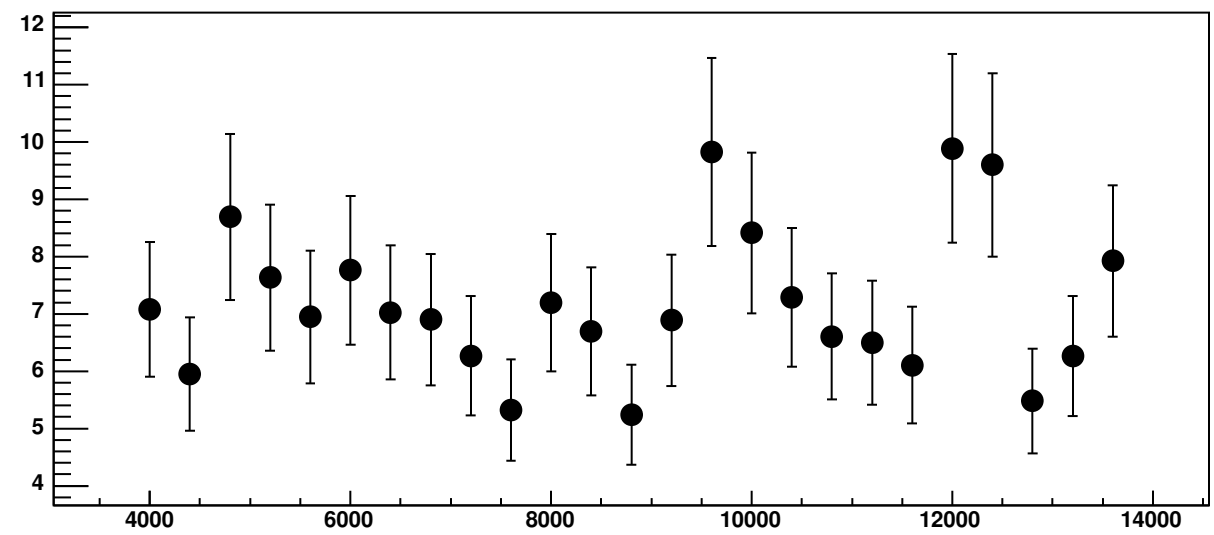

Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

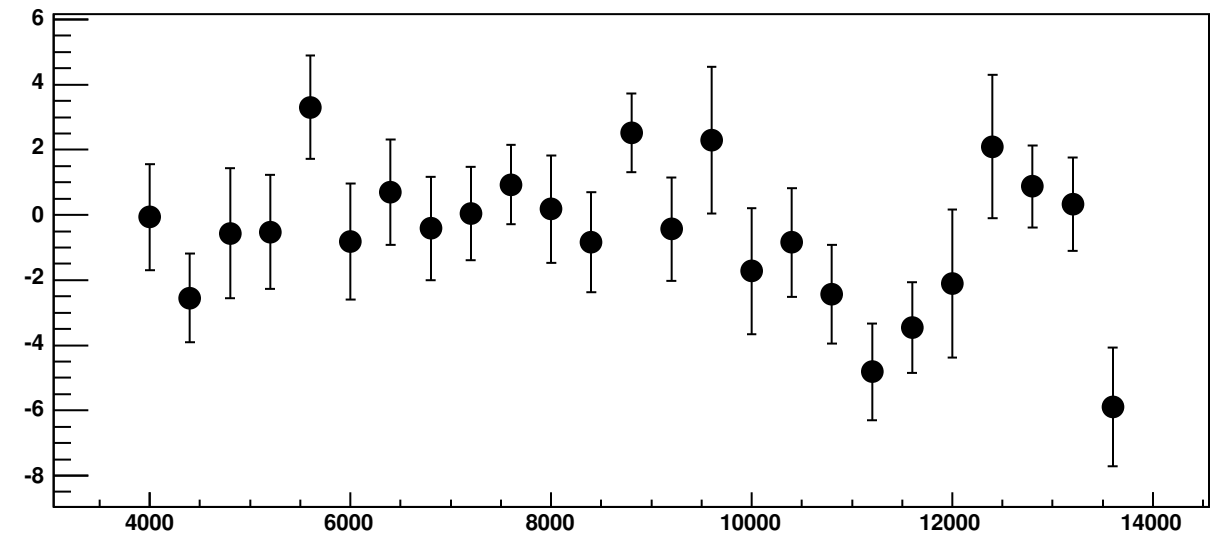
Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

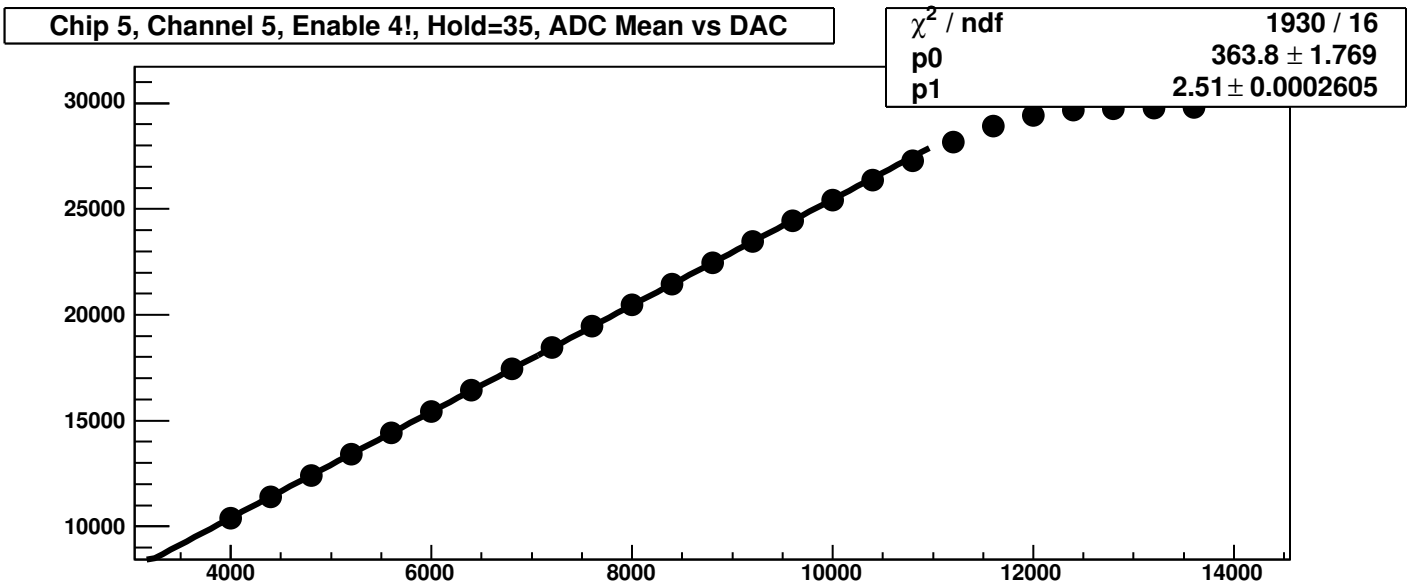


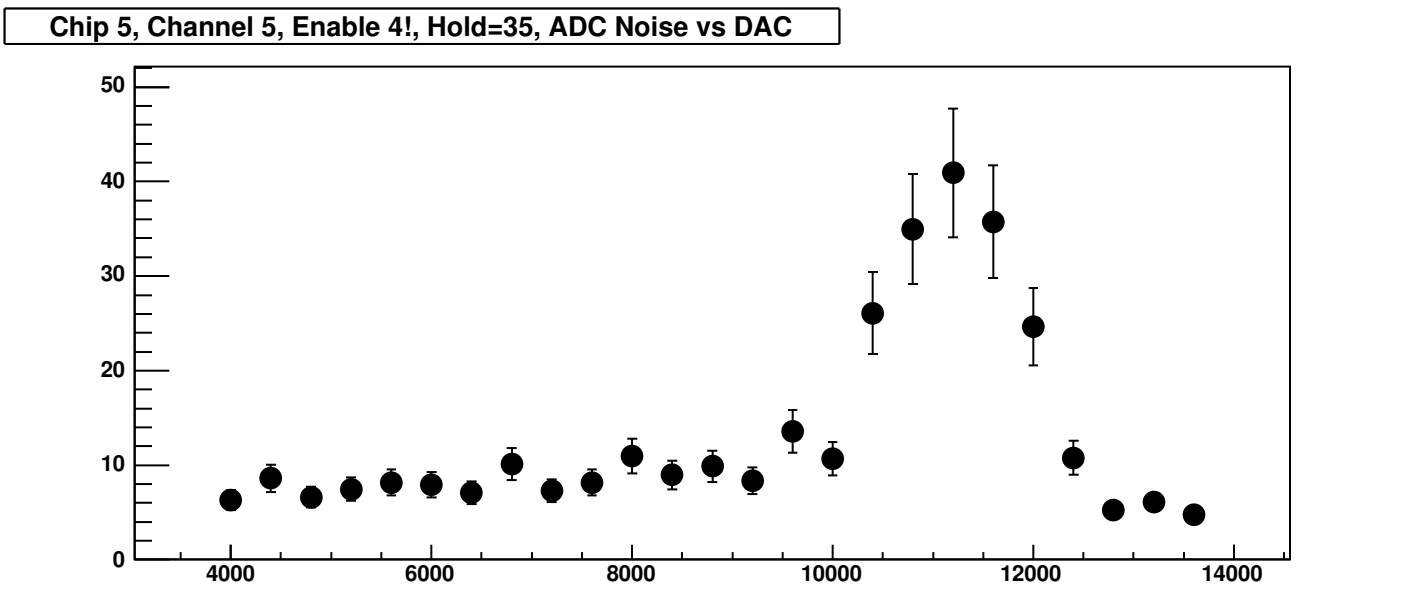
Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

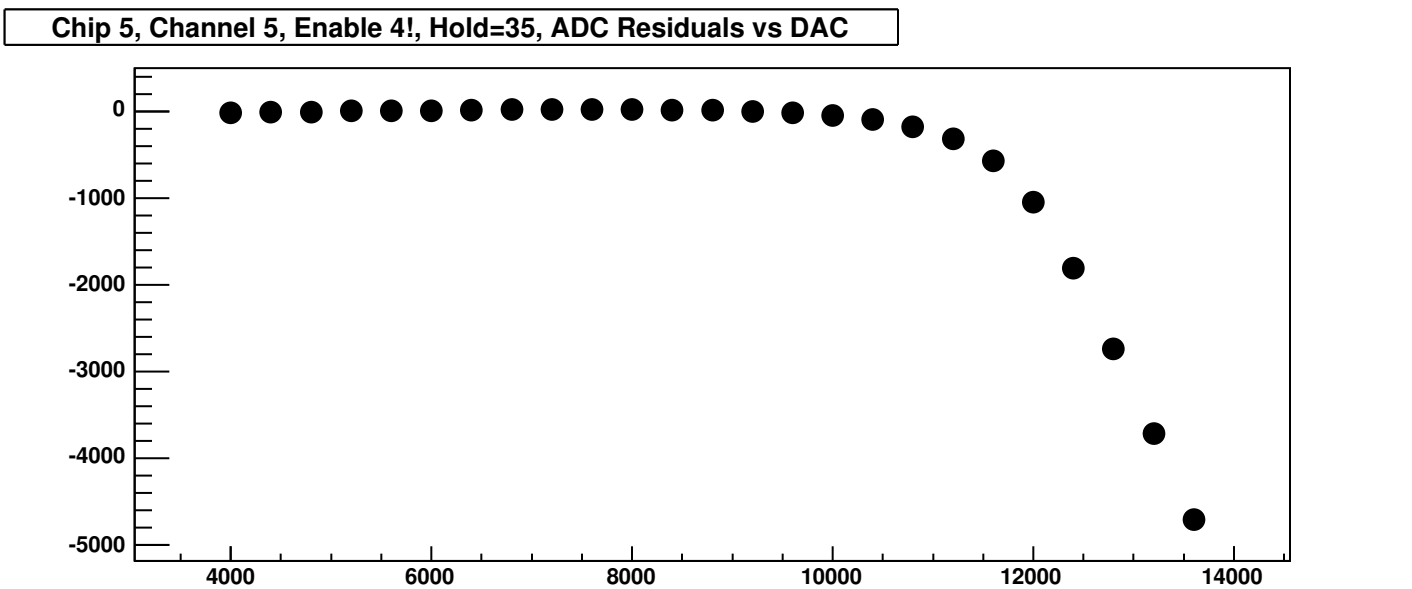


Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

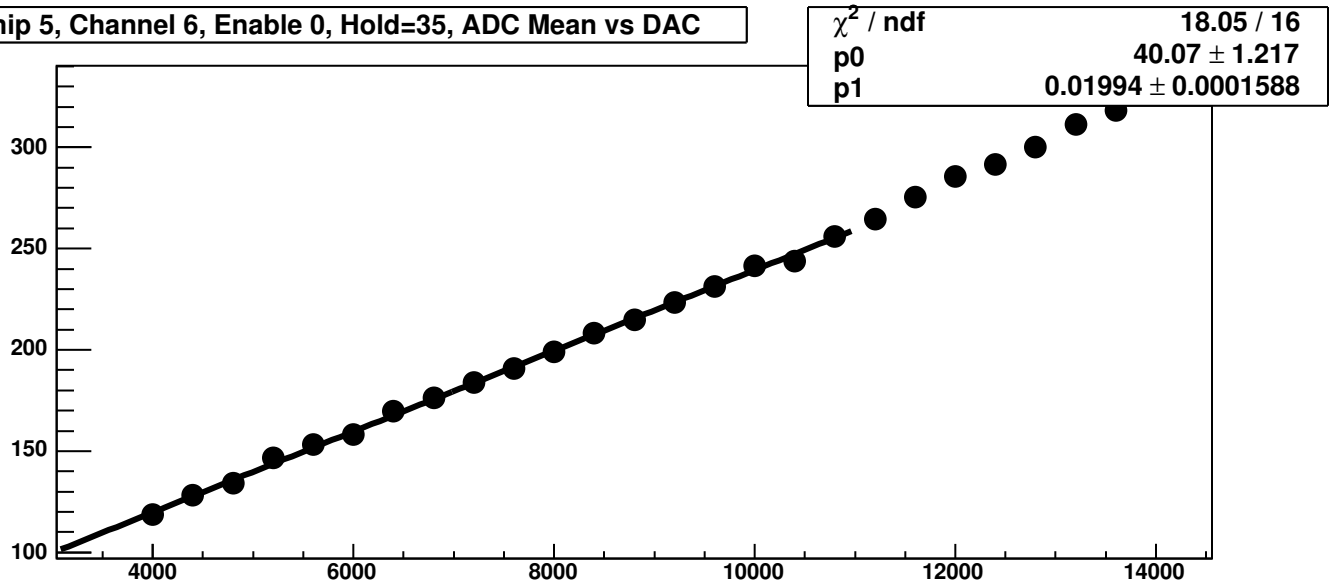



Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1930 / 16
p0 363.8 ± 1.769
p1 2.51 ± 0.0002605
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

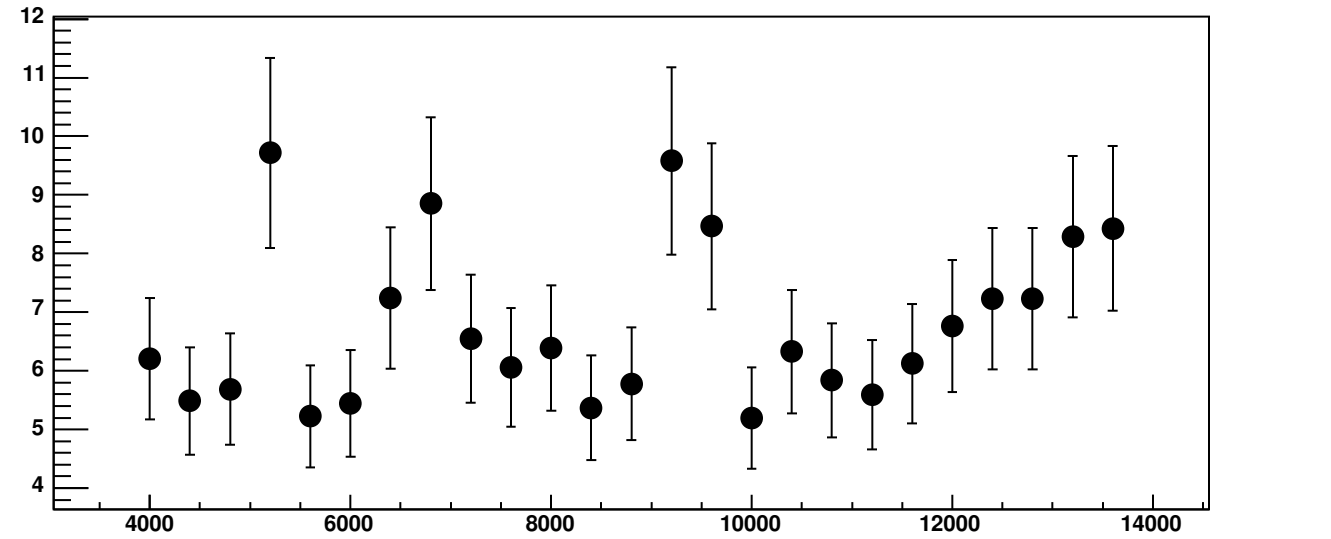

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000


Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000

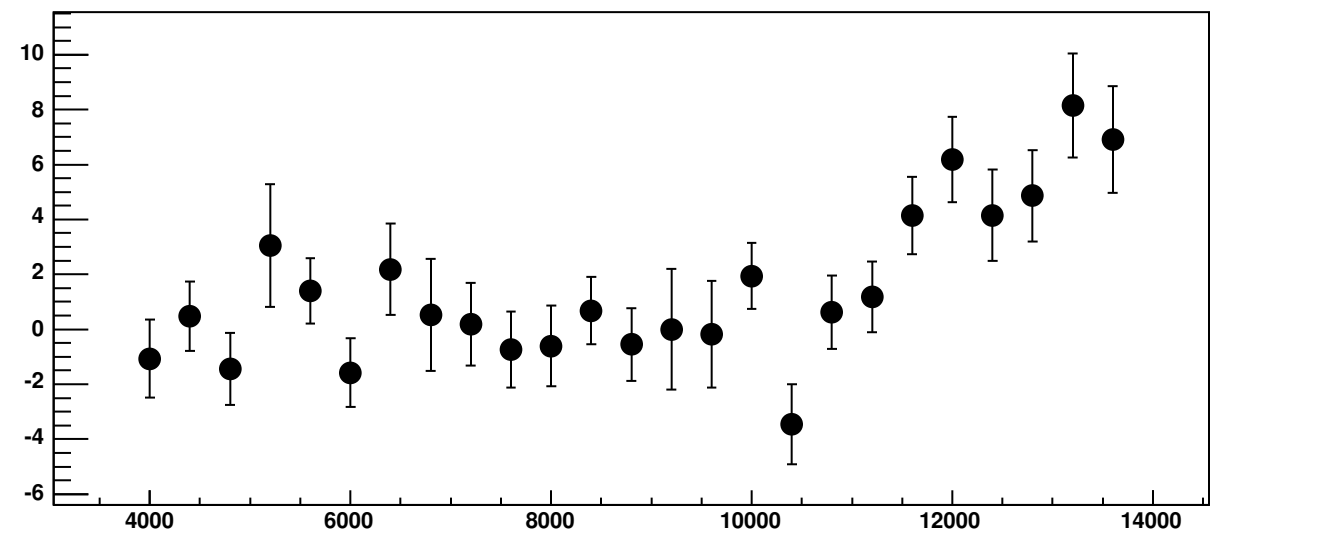
Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

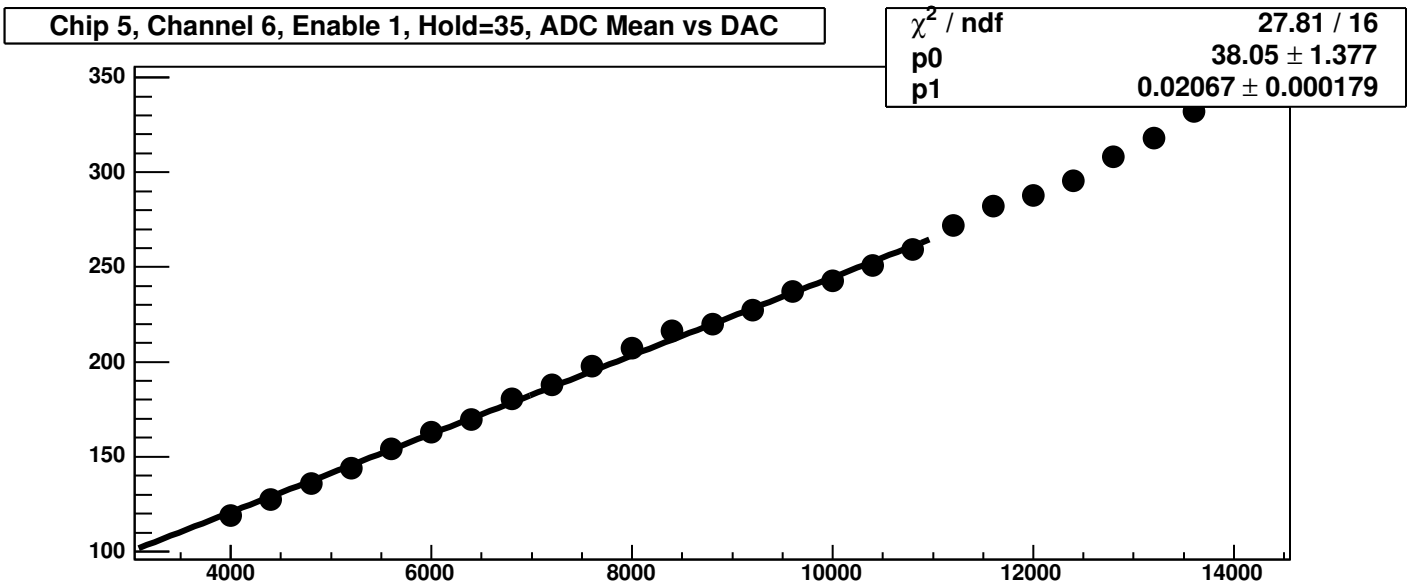


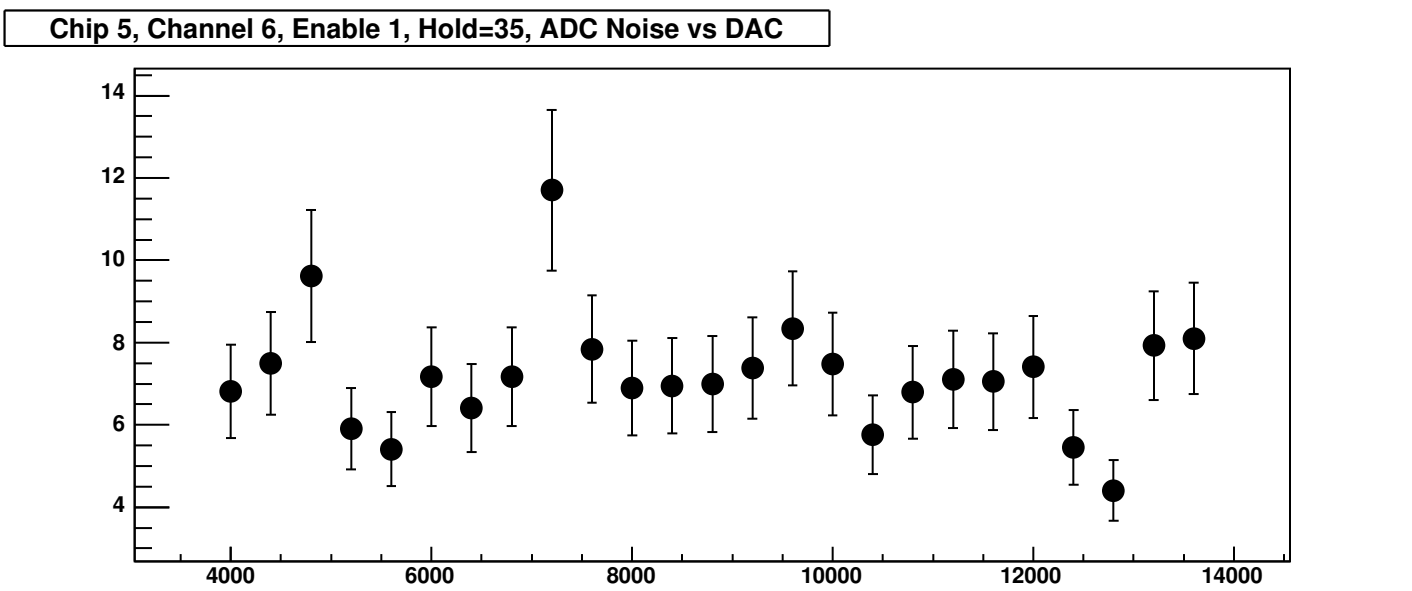
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

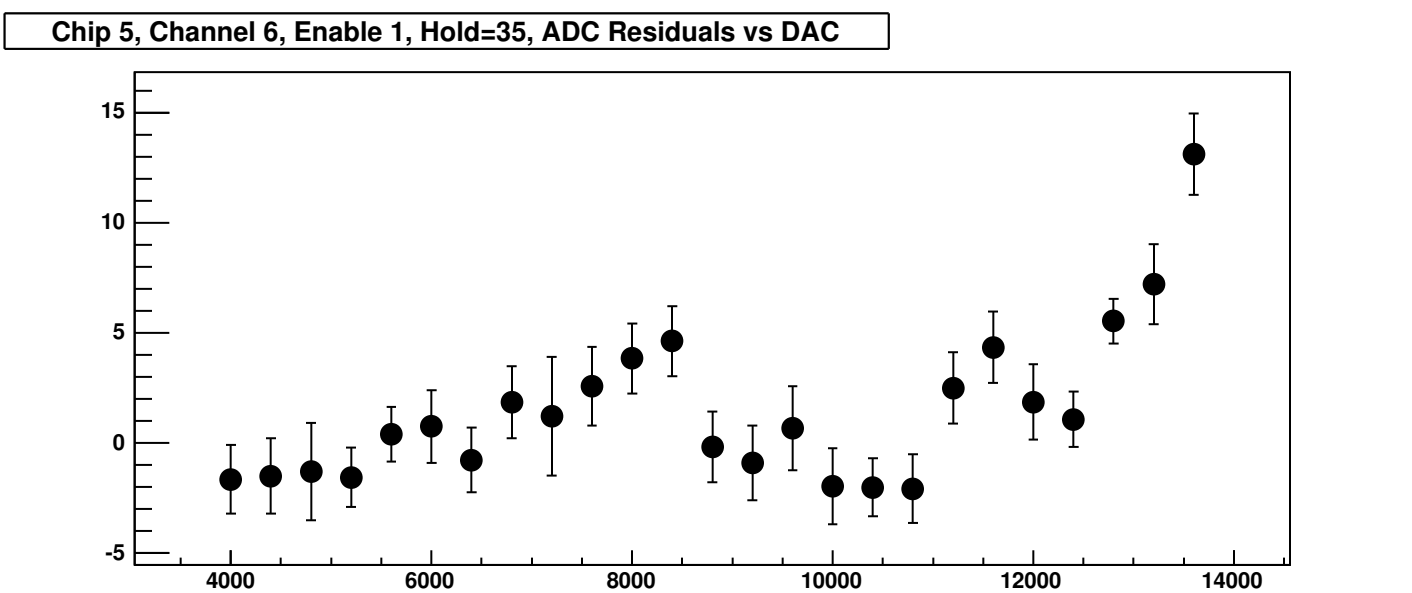


Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

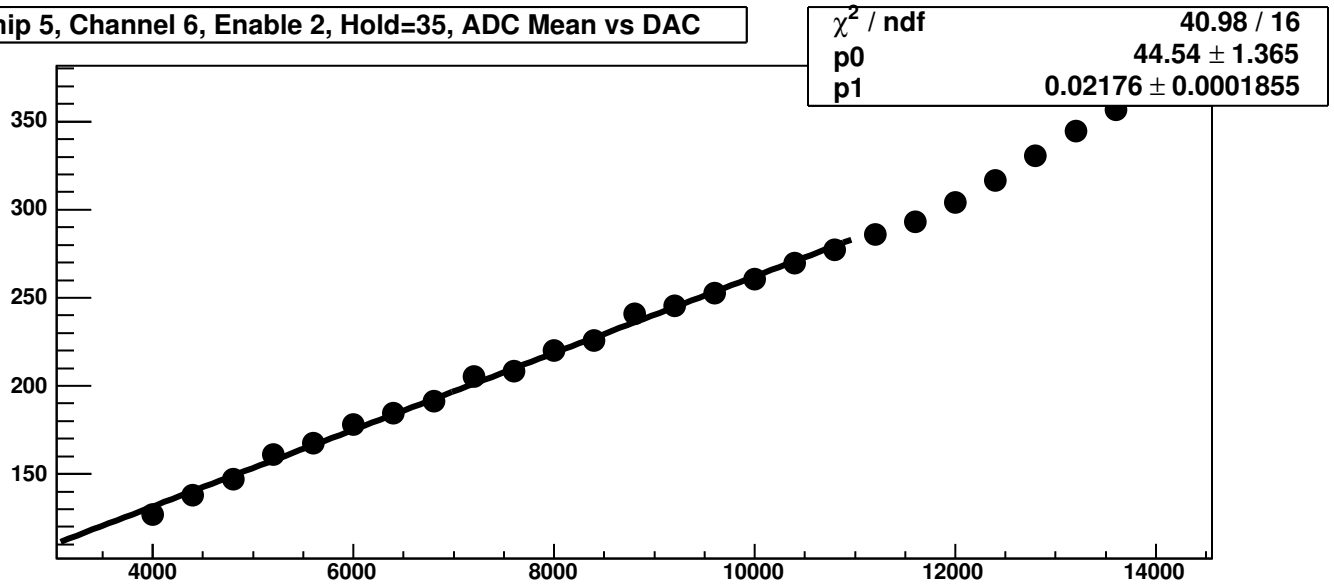



Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.81 / 16
p0 38.05 ± 1.377
p1 0.02067 ± 0.000179
350
300
250
200
150
100
4000
6000
8000
10000
12000
14000

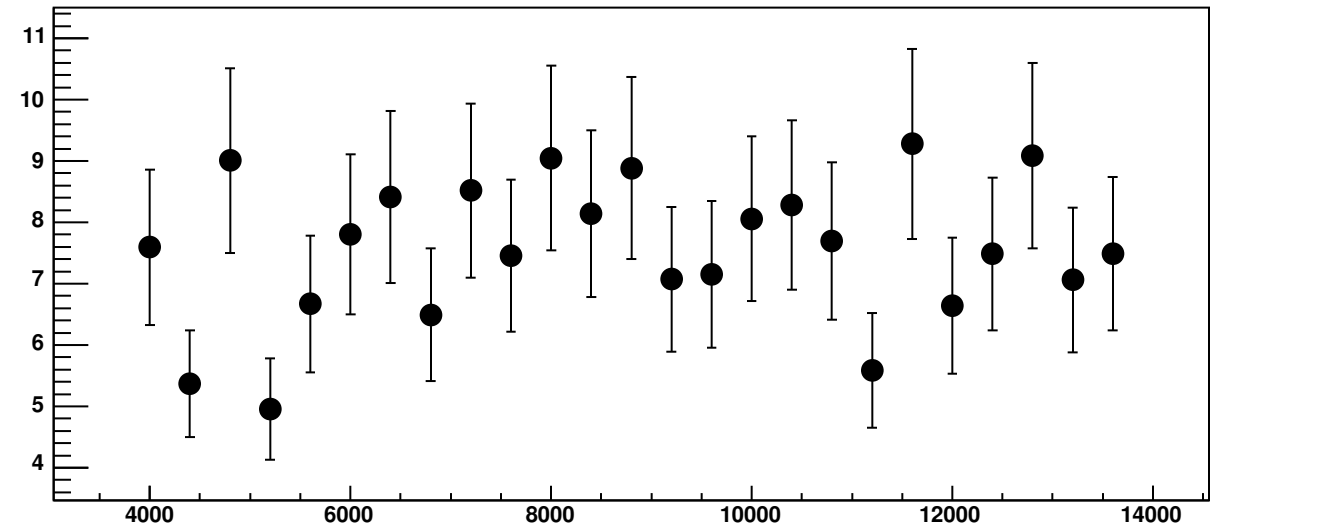

Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

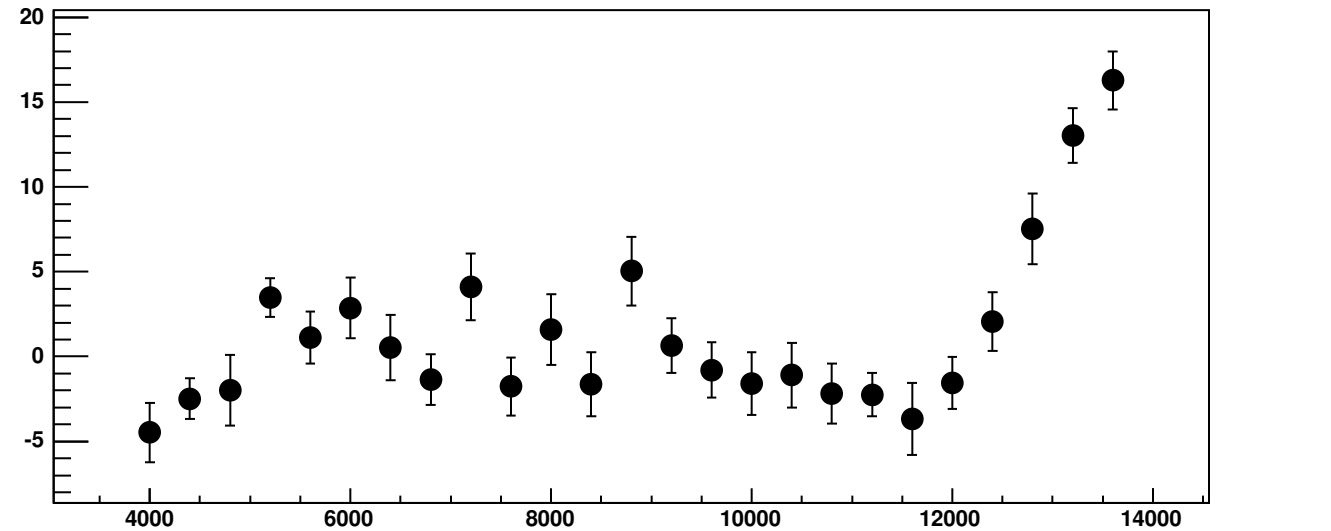
Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



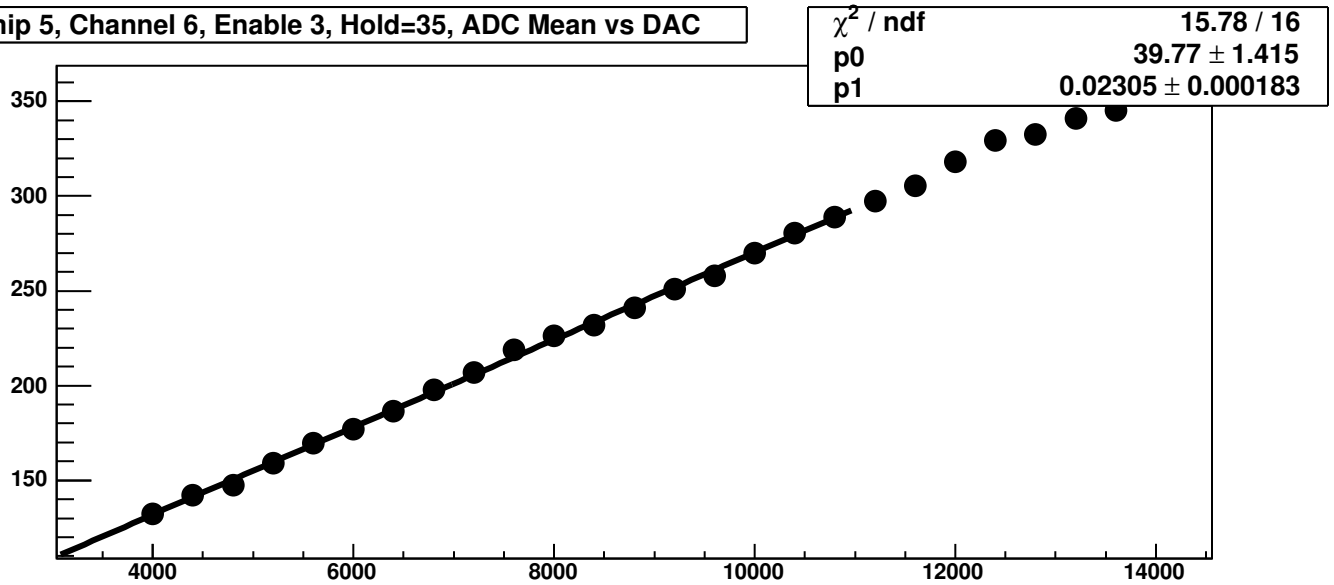
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



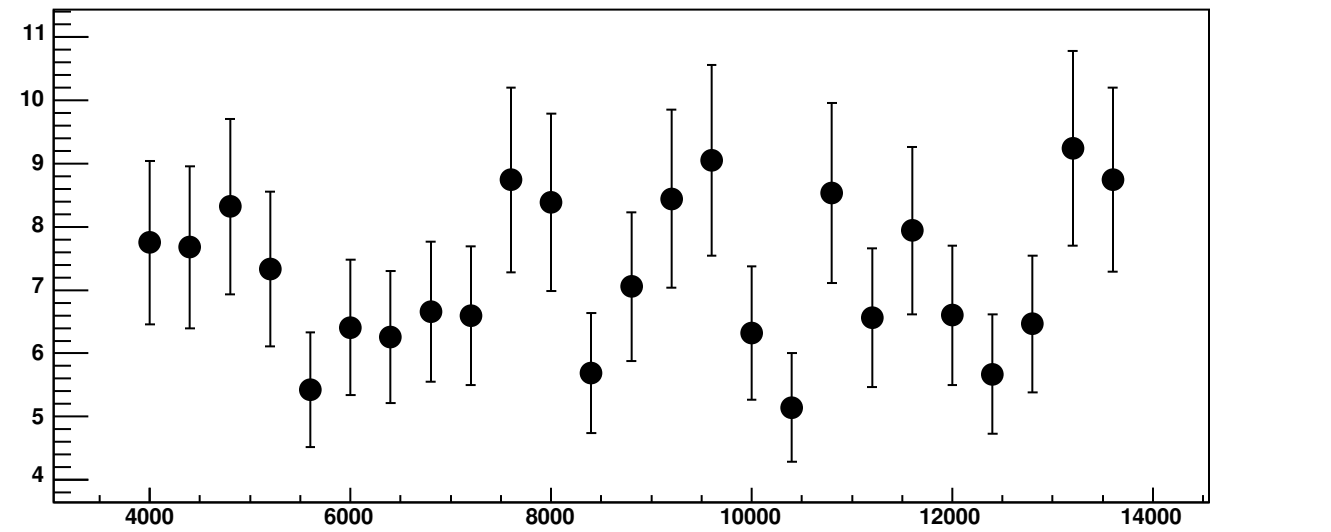
Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



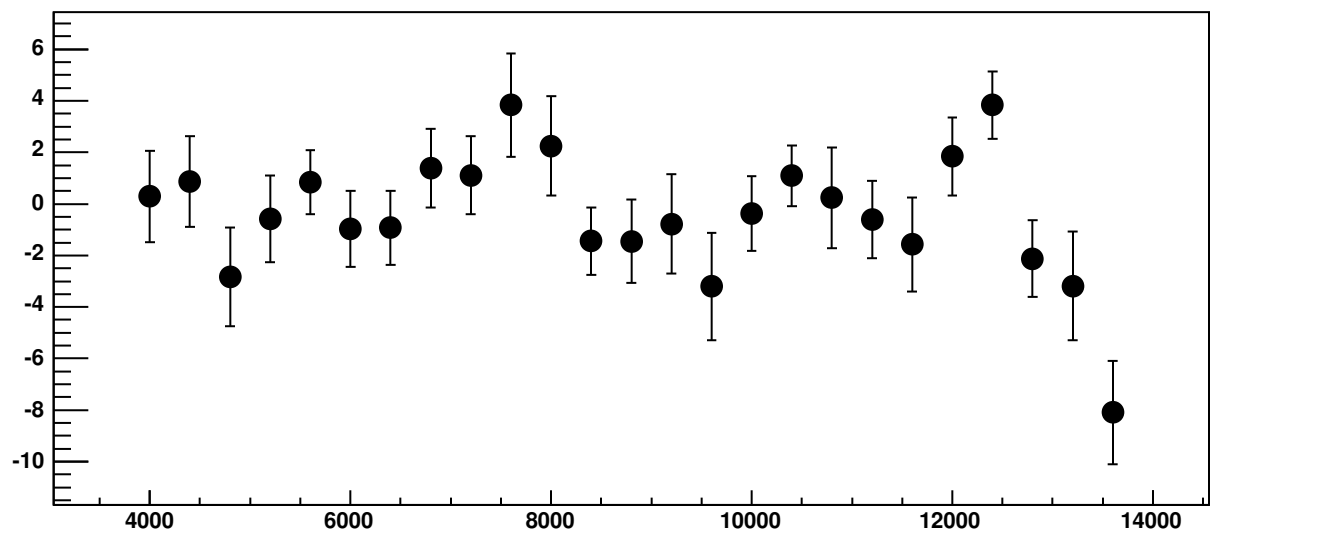
Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

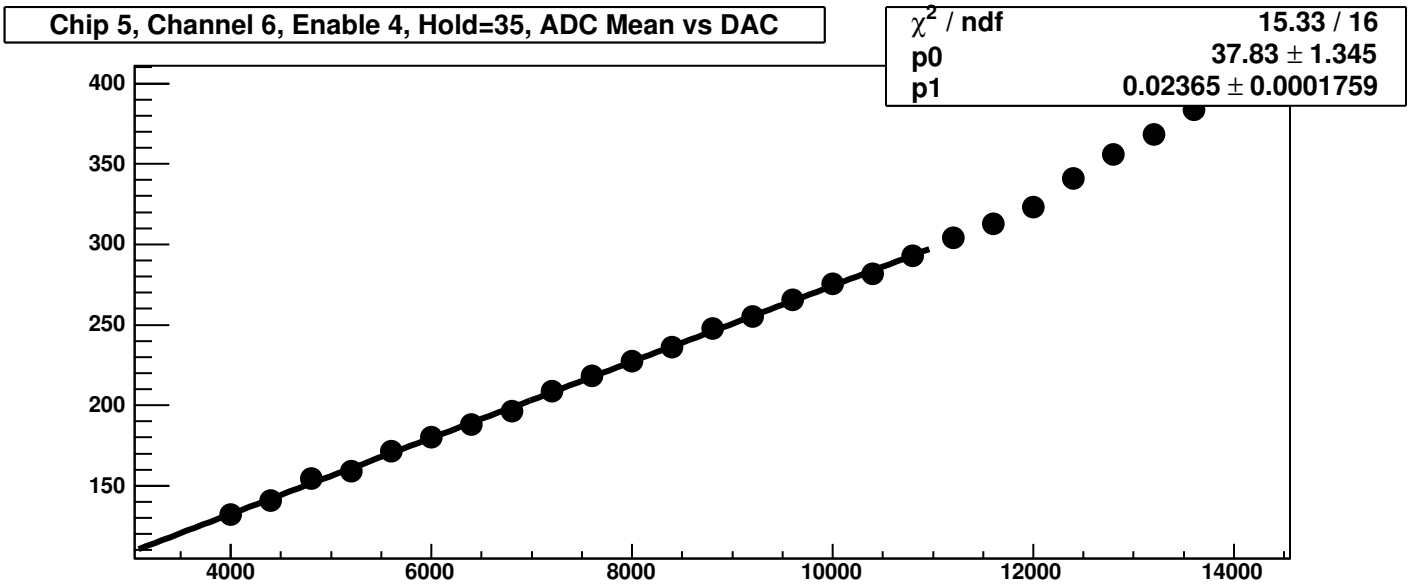


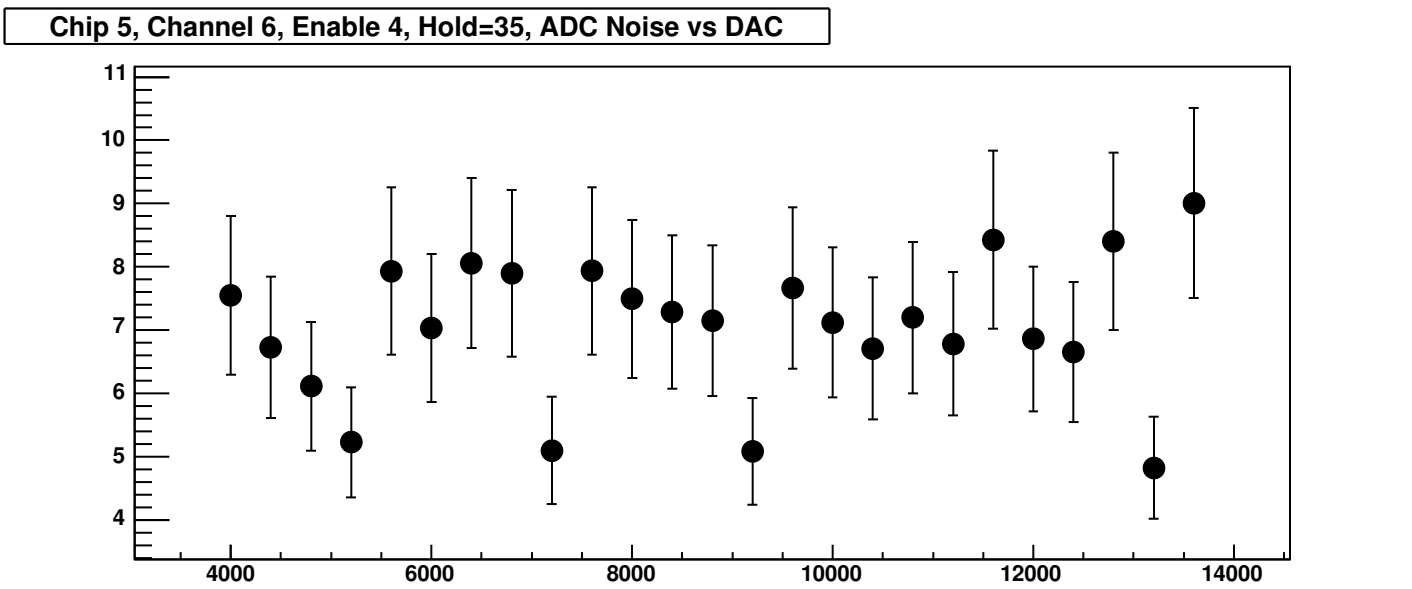
Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

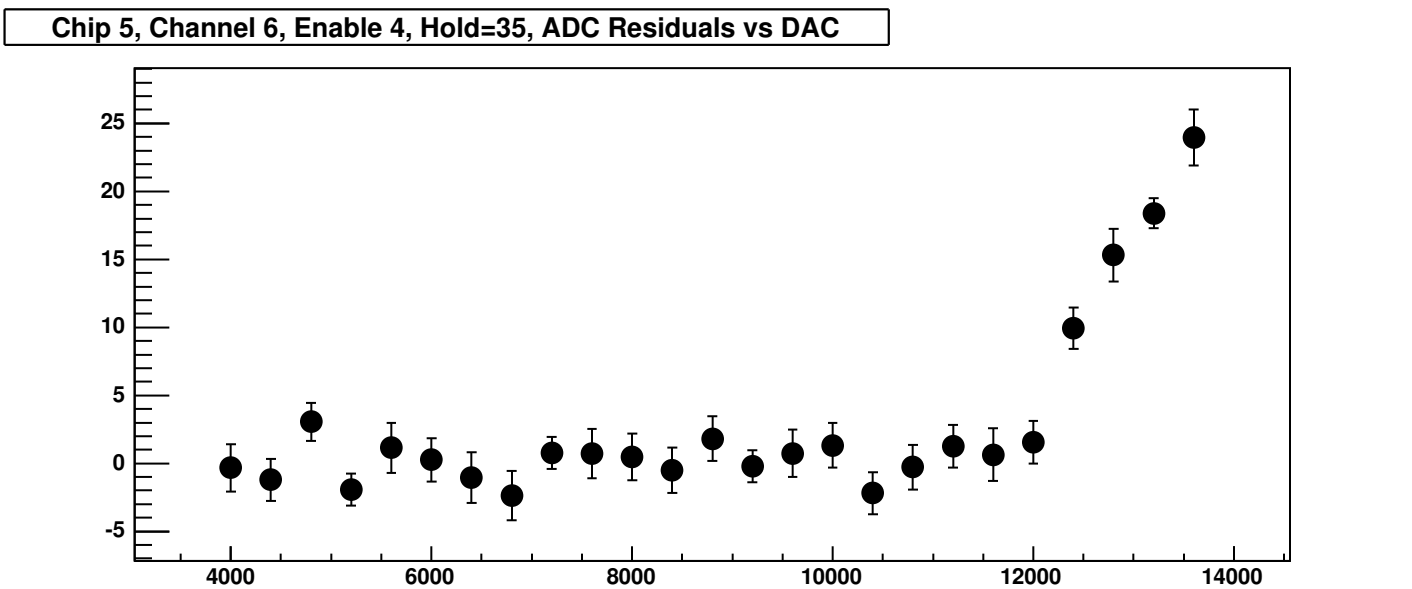


Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

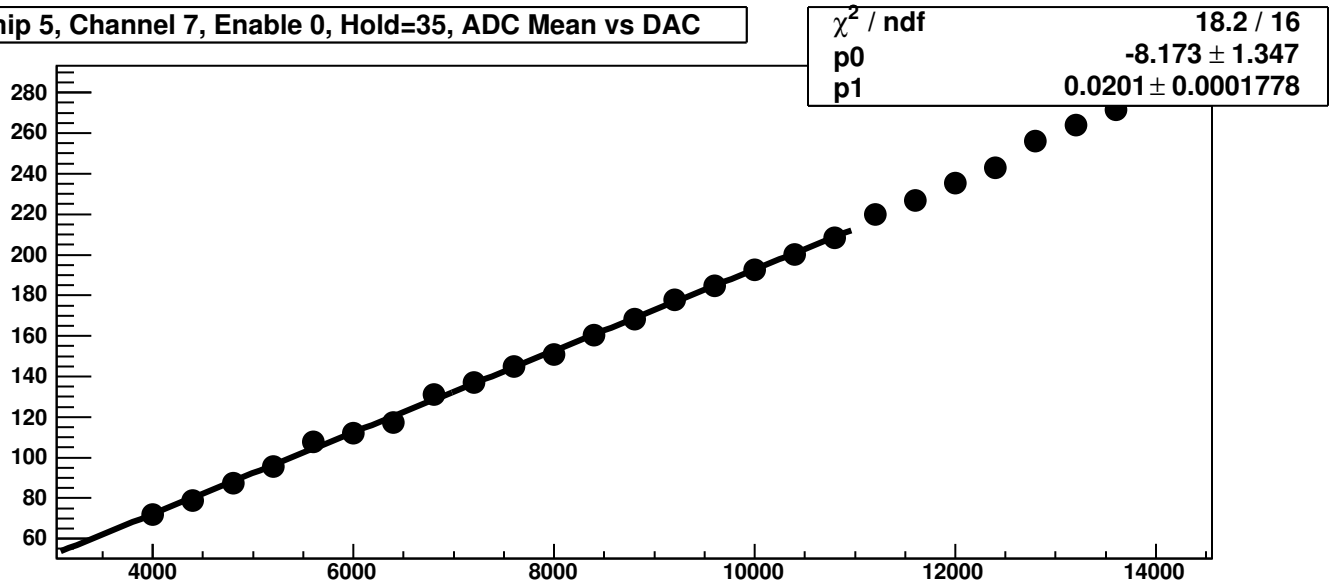



Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.33 / 16
p0 37.83 ± 1.345
p1 0.02365 ± 0.0001759
400
350
300
250
200
150
4000
6000
8000
10000
12000
14000

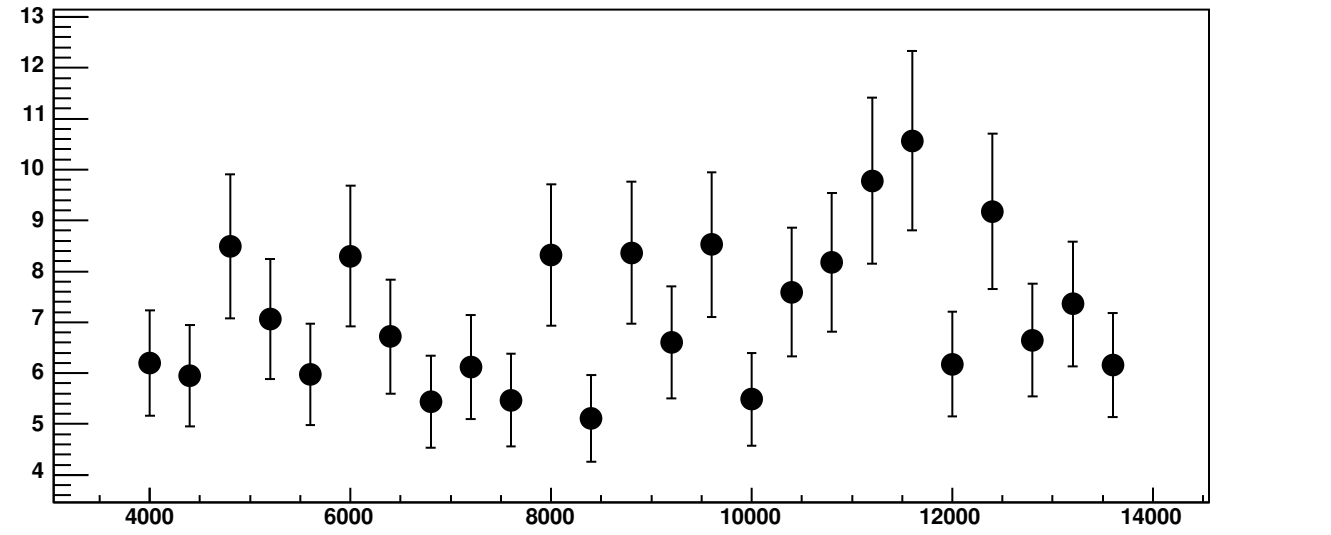

Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

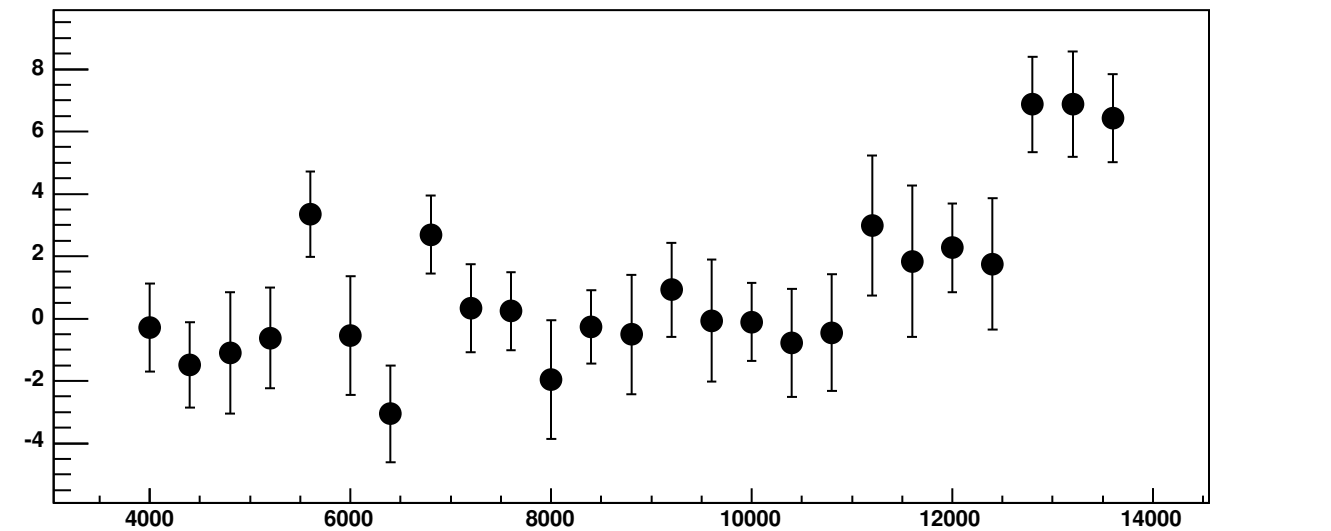
Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



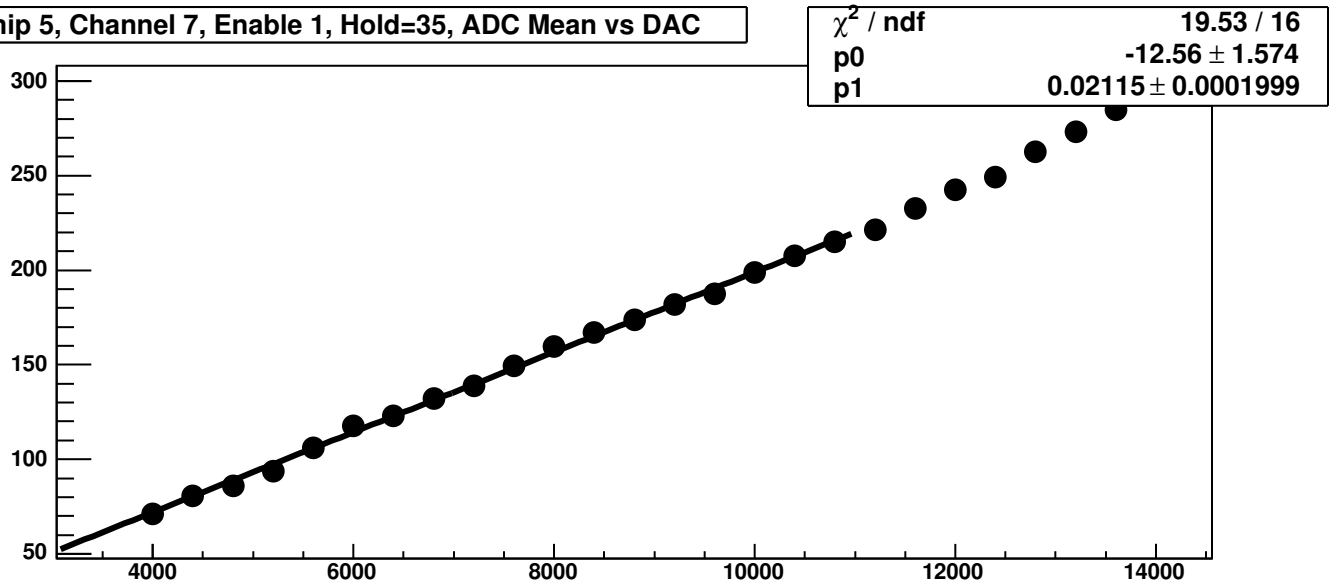
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



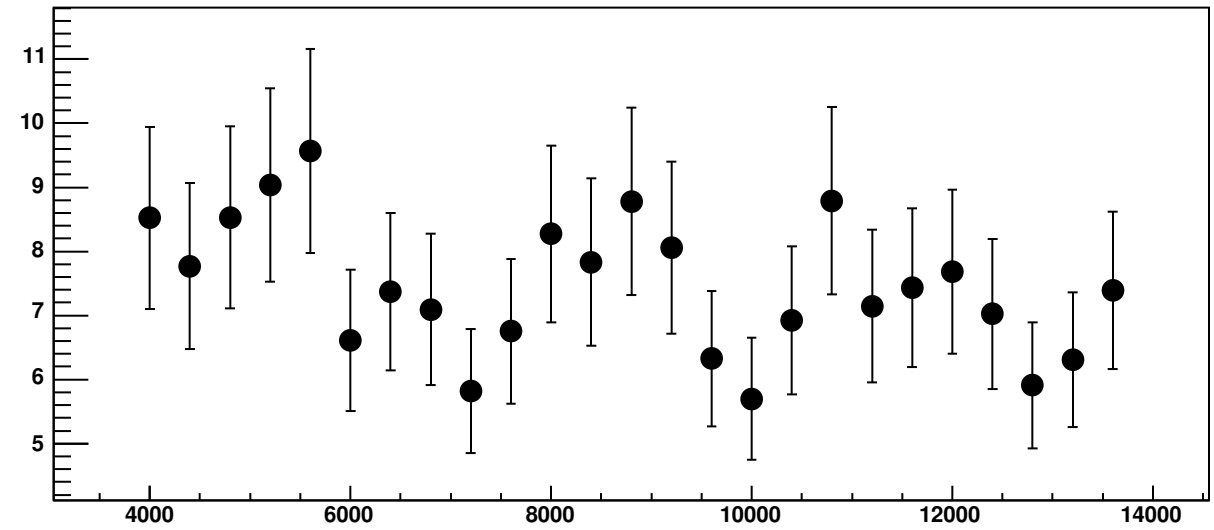
Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



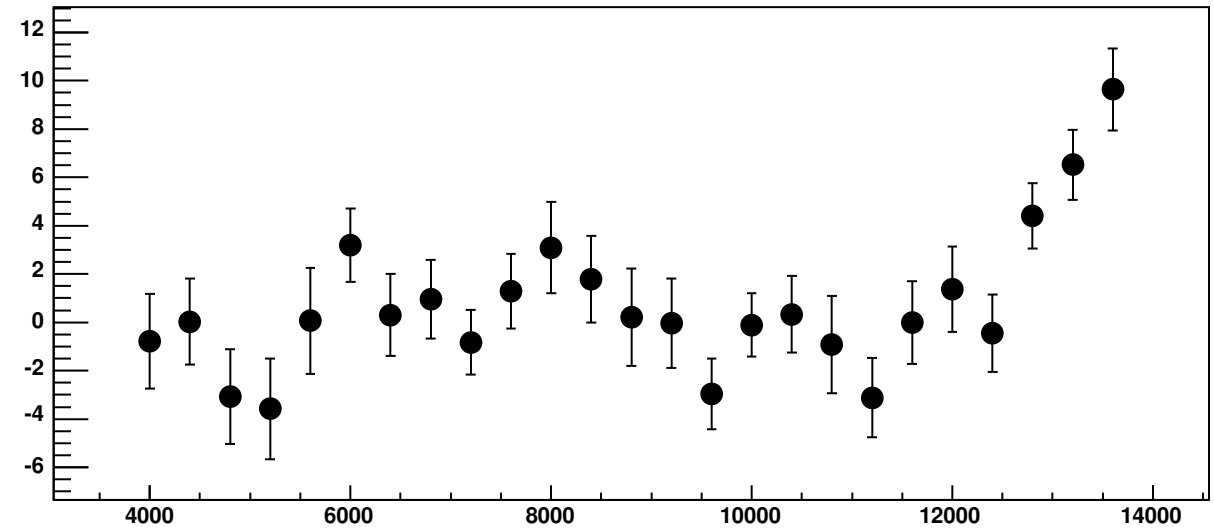
Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

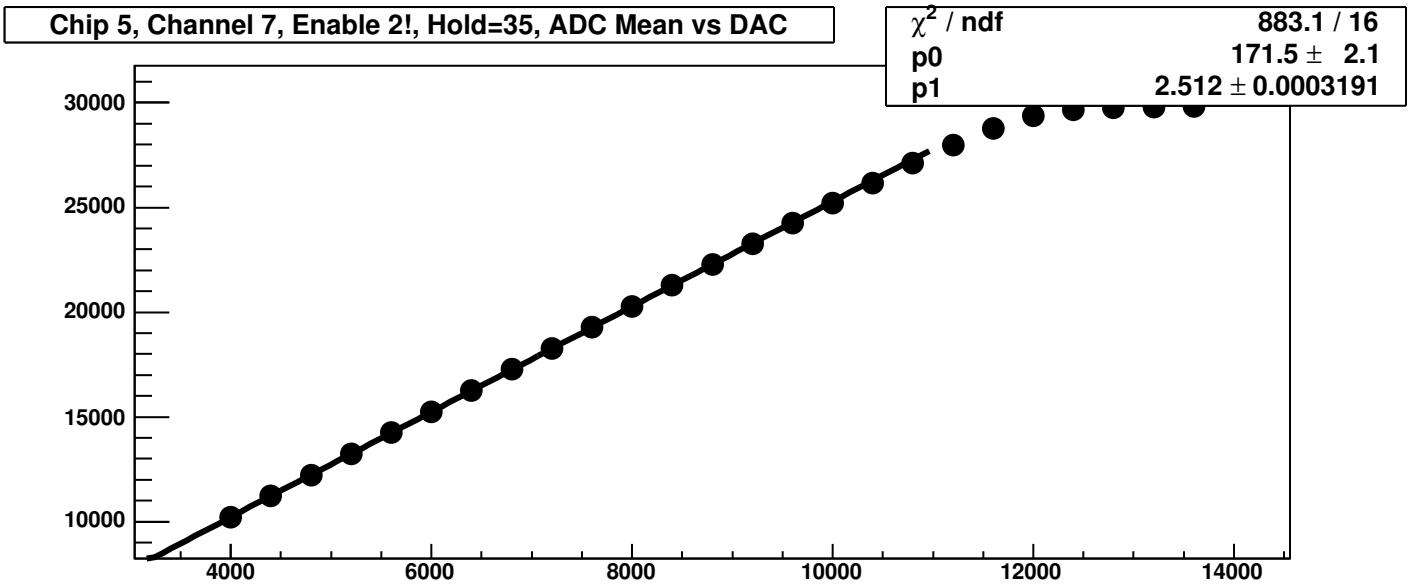


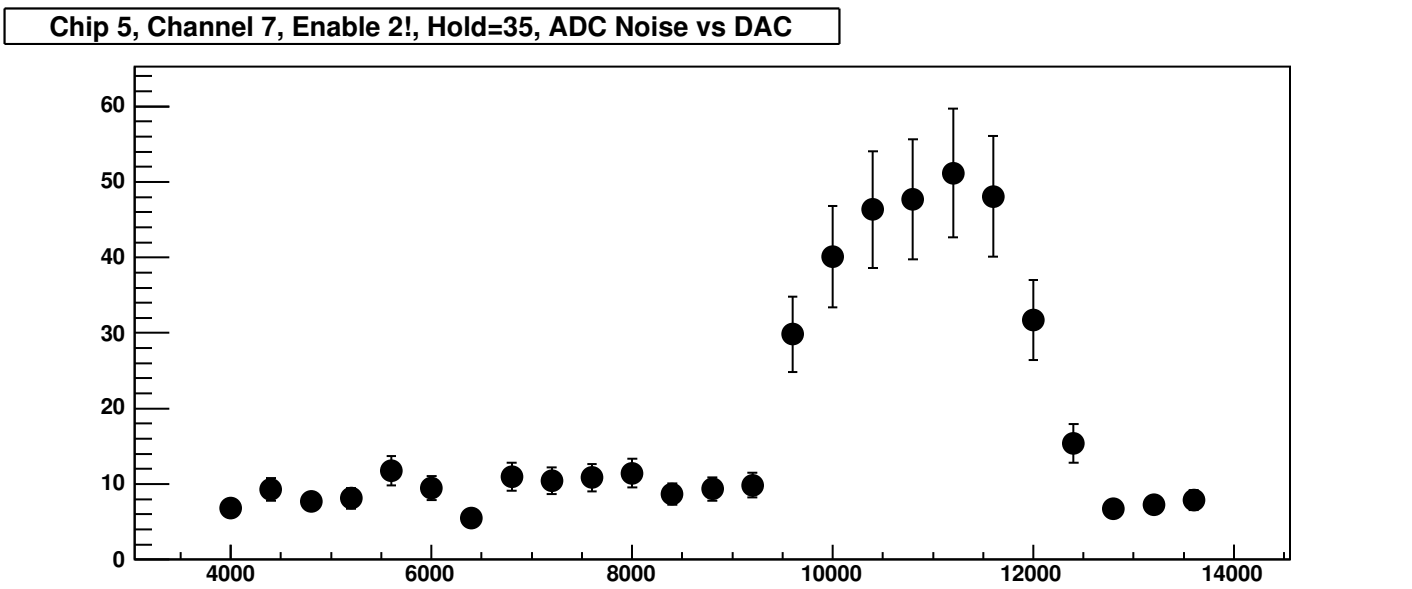
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

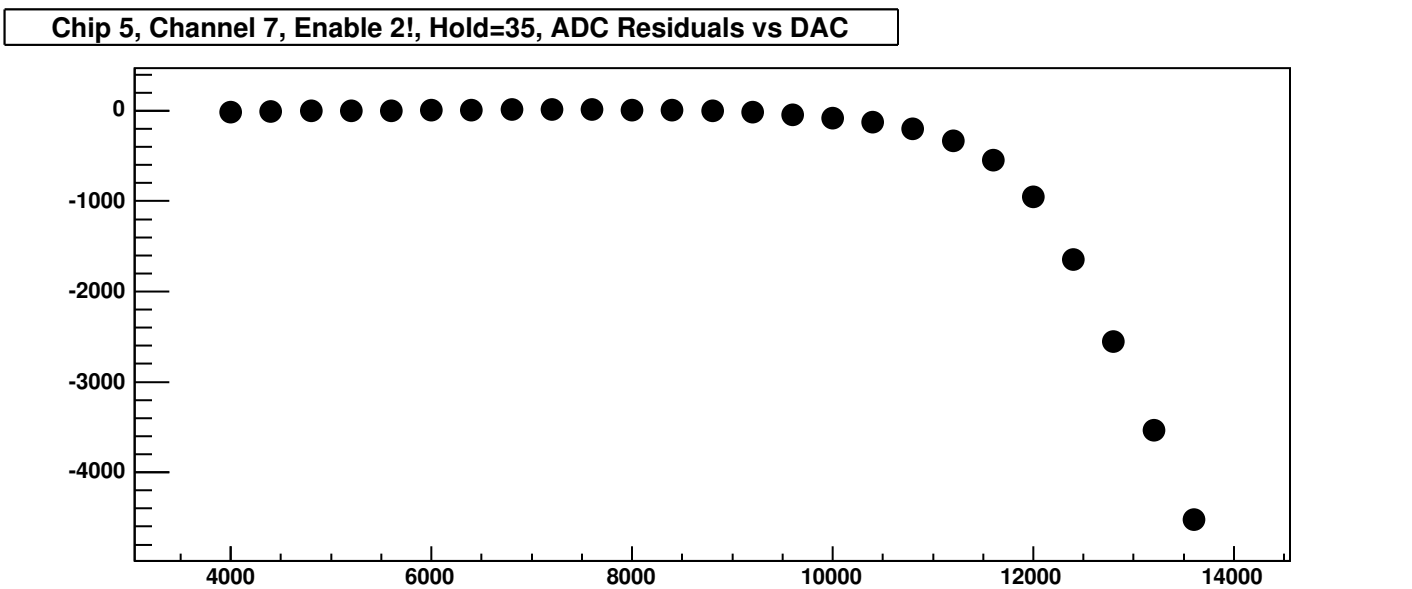


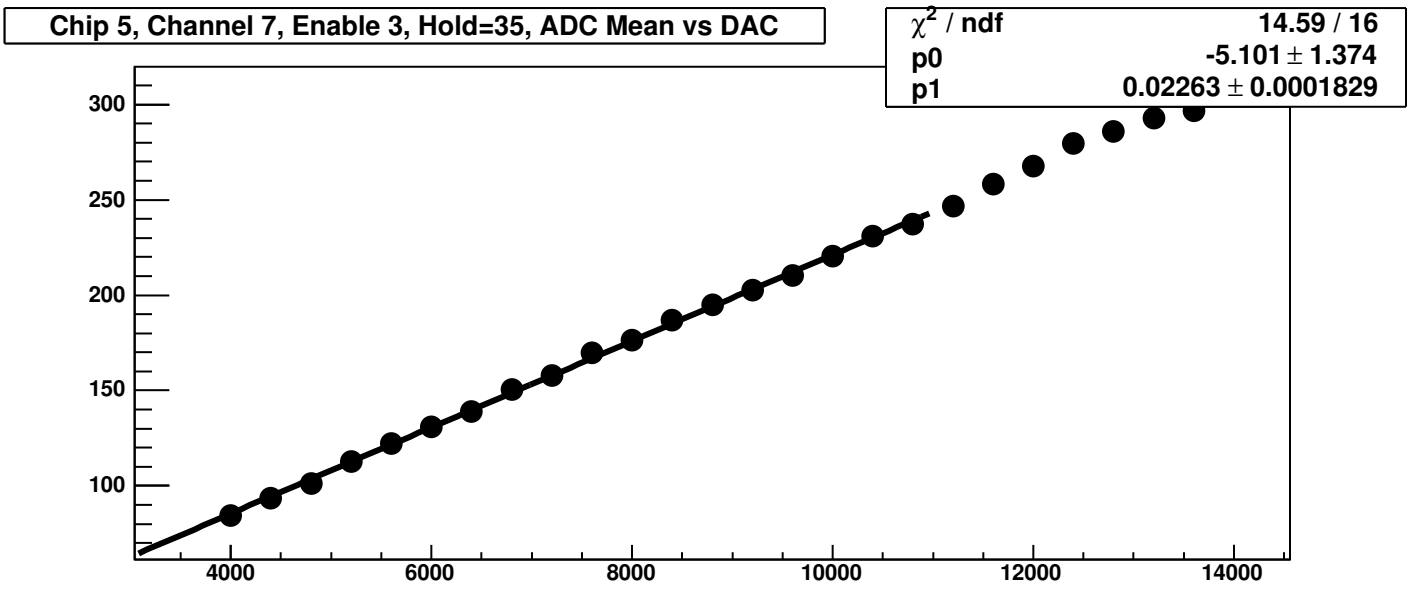
Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

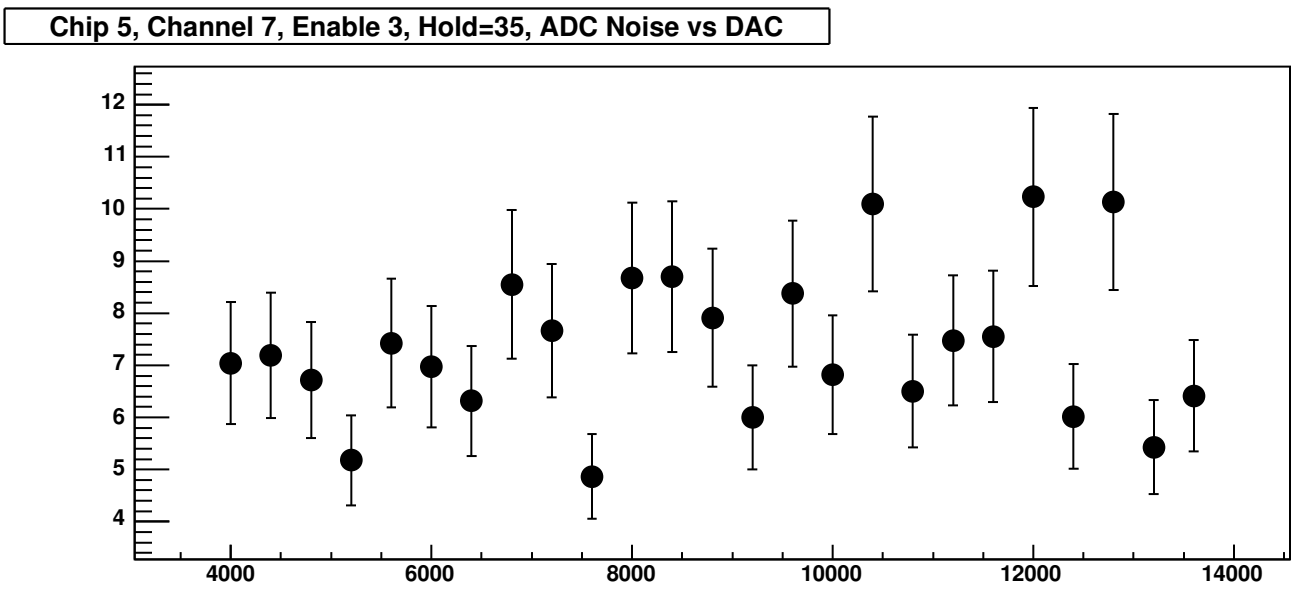


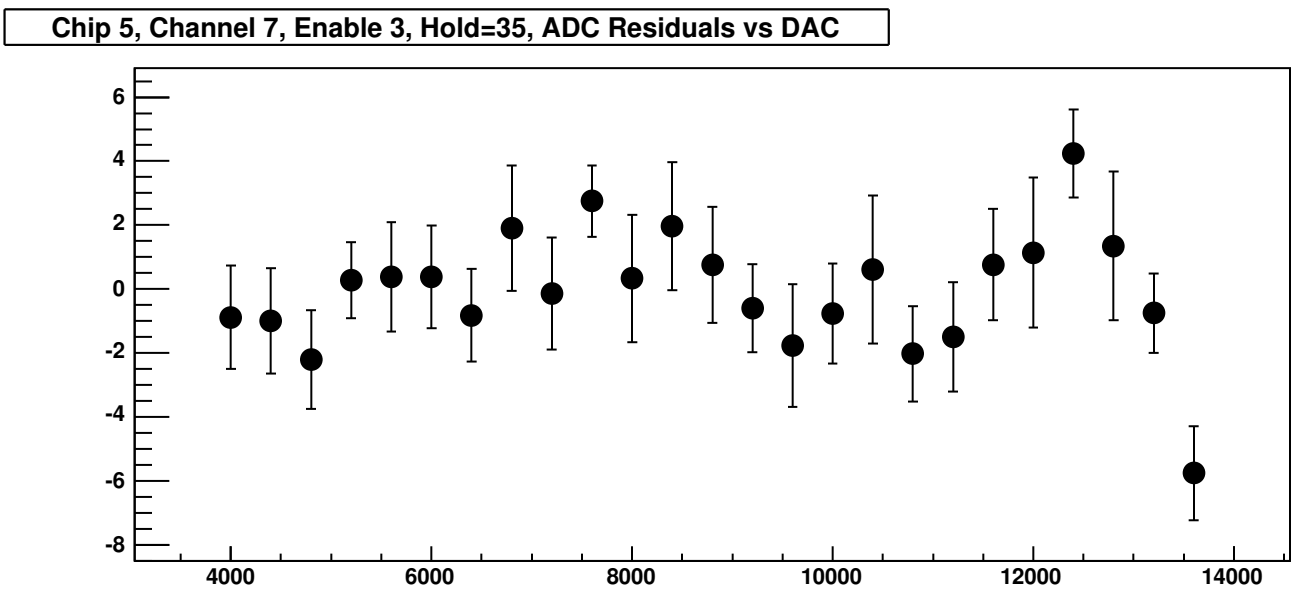

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 883.1 / 16
p0 171.5 ± 2.1
p1 2.512 ± 0.0003191
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

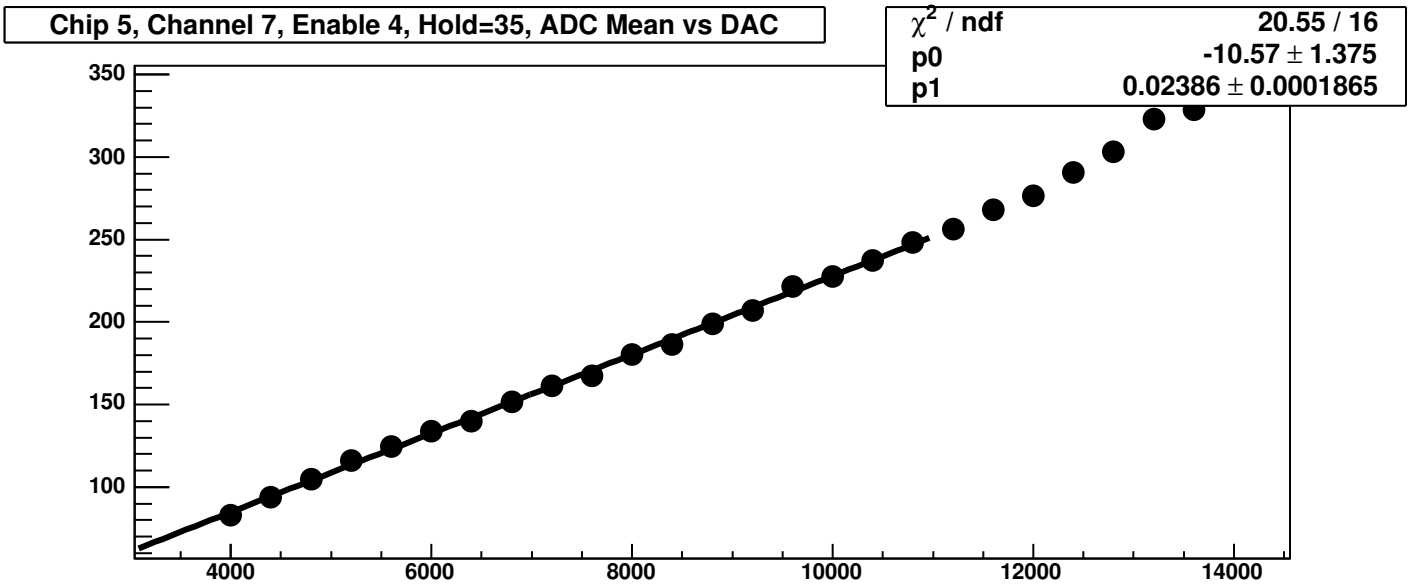

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

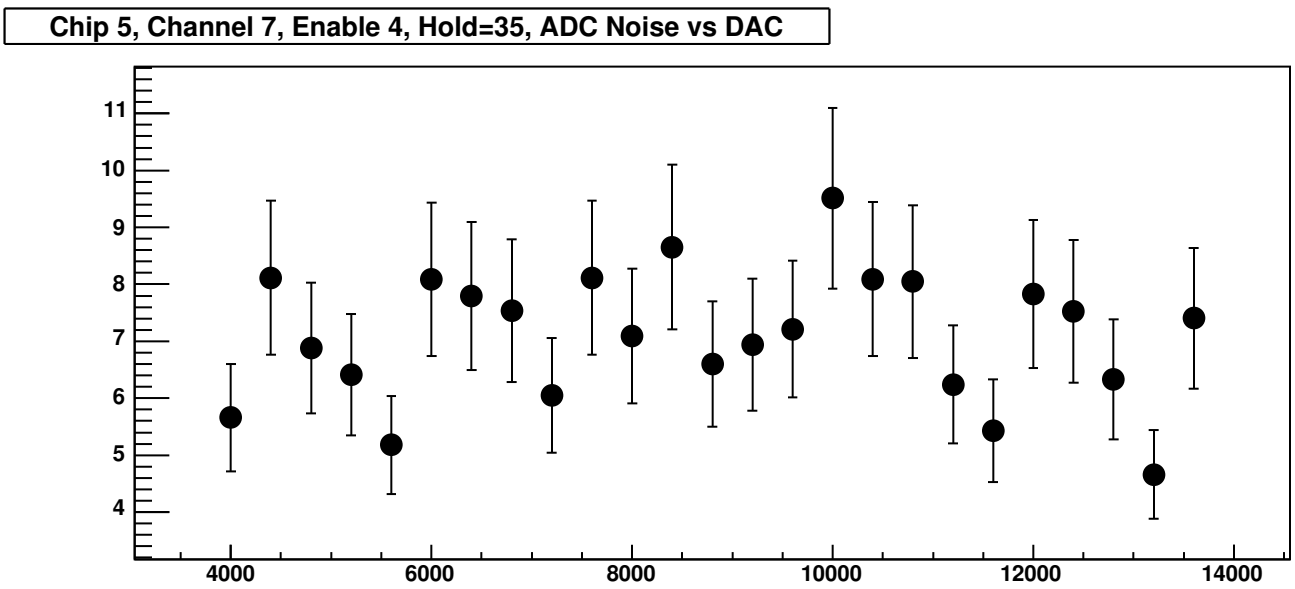

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

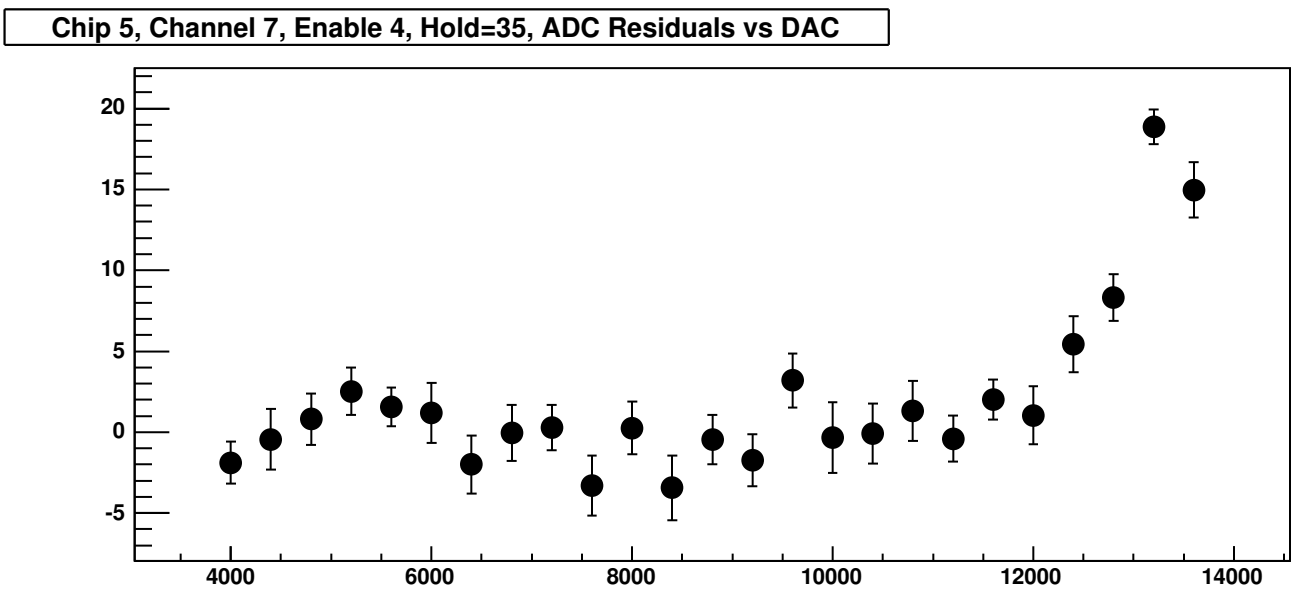

Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.59 / 16
p0 -5.101 ± 1.374
p1 0.02263 ± 0.0001829
300
250
200
150
100
4000
6000
8000
10000
12000
14000


Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

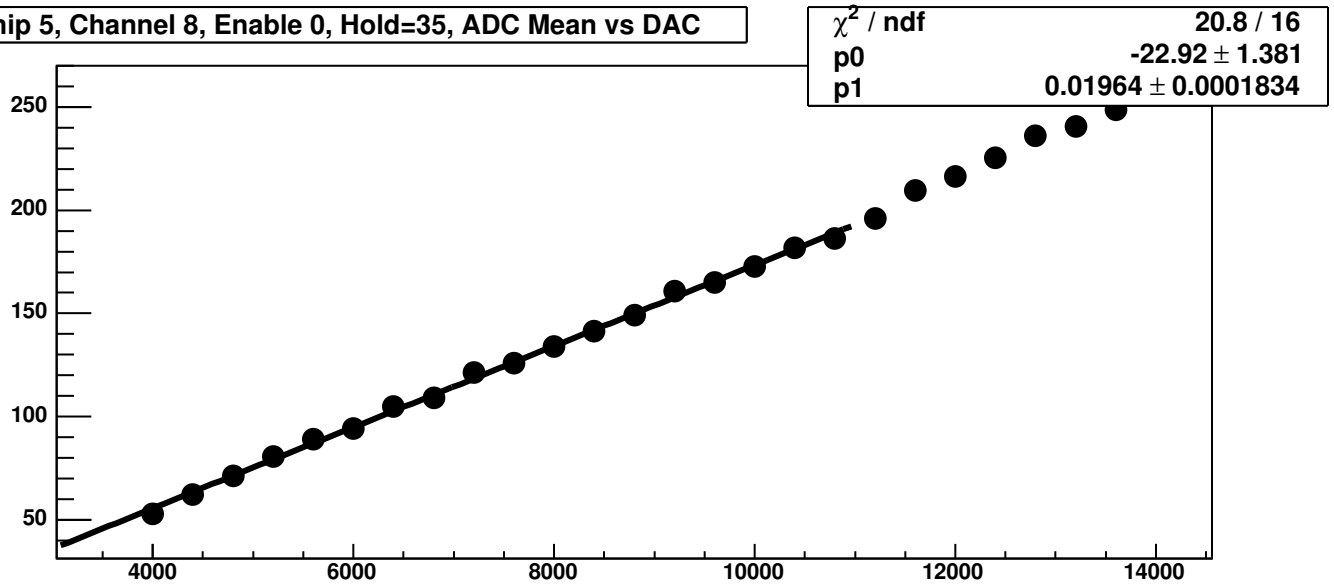

Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000


Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.55 / 16
p0 -10.57 ± 1.375
p1 0.02386 ± 0.0001865
350
300
250
200
150
100
4000
6000
8000
10000
12000
14000

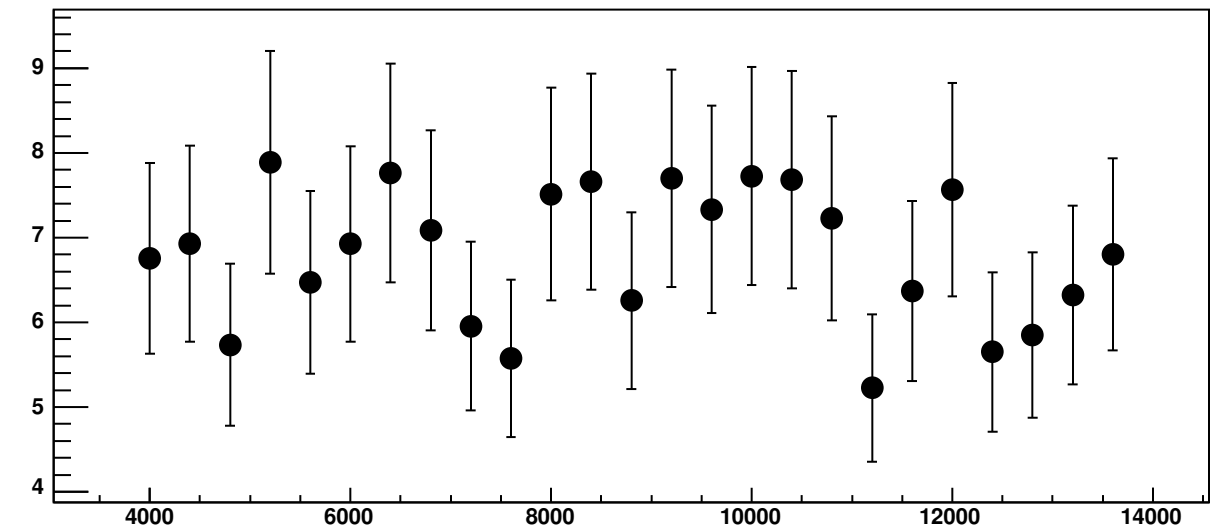

Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

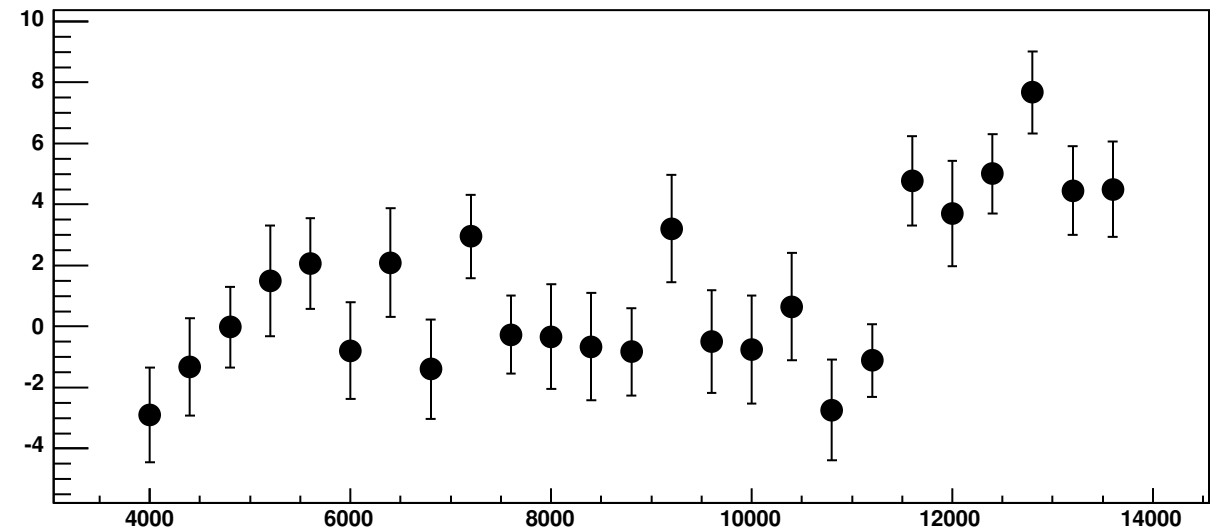
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

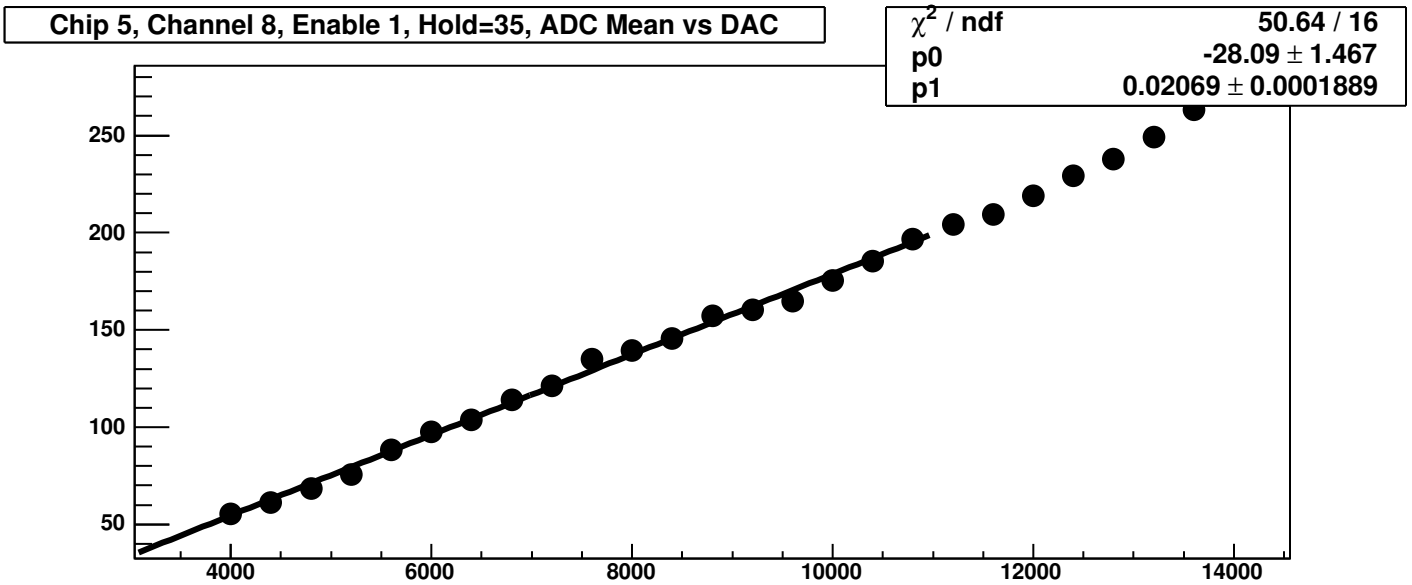


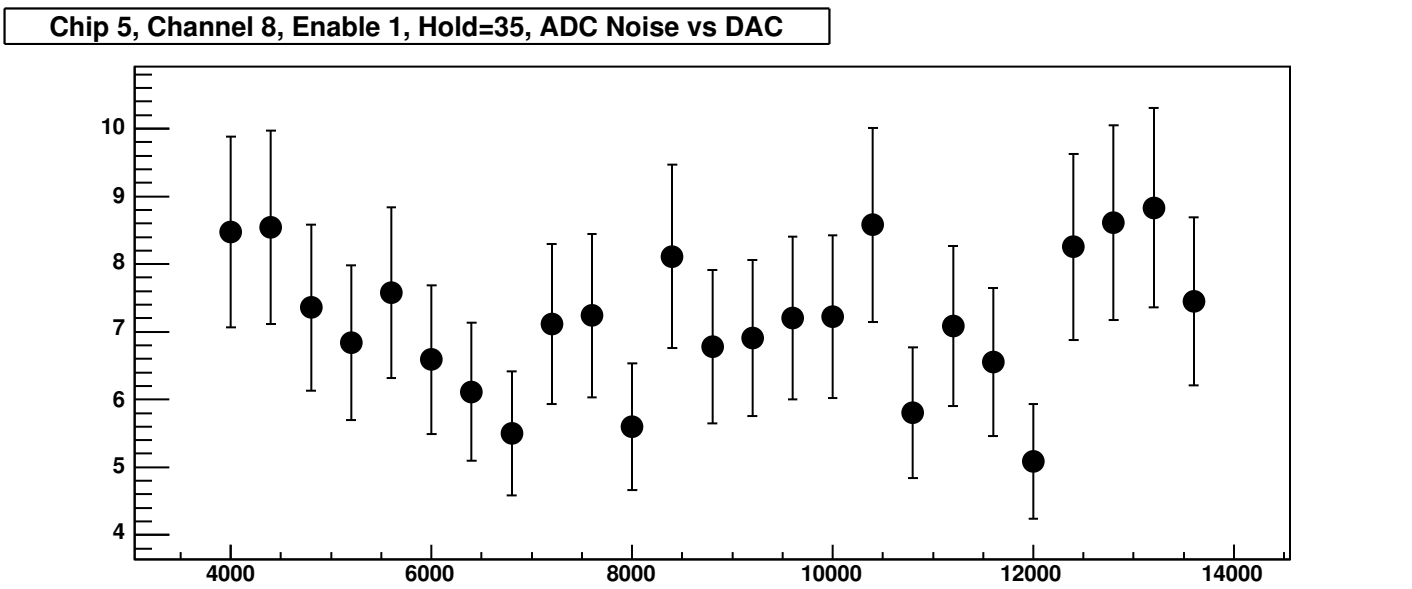
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC

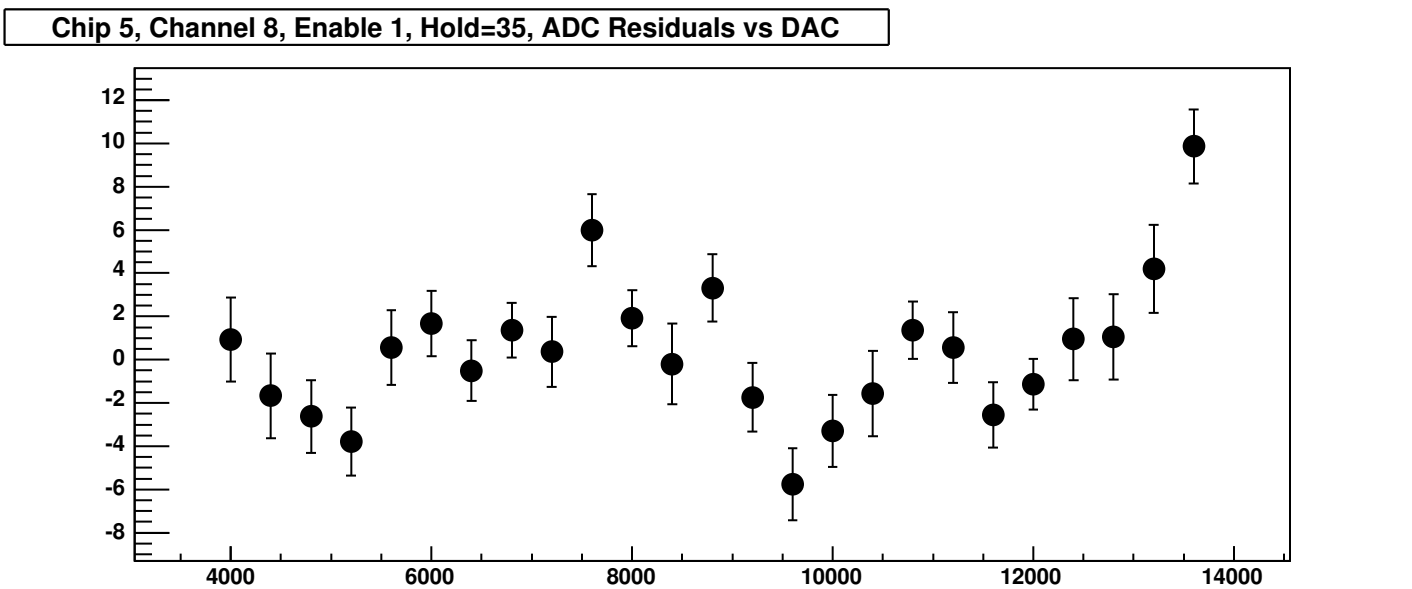


Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

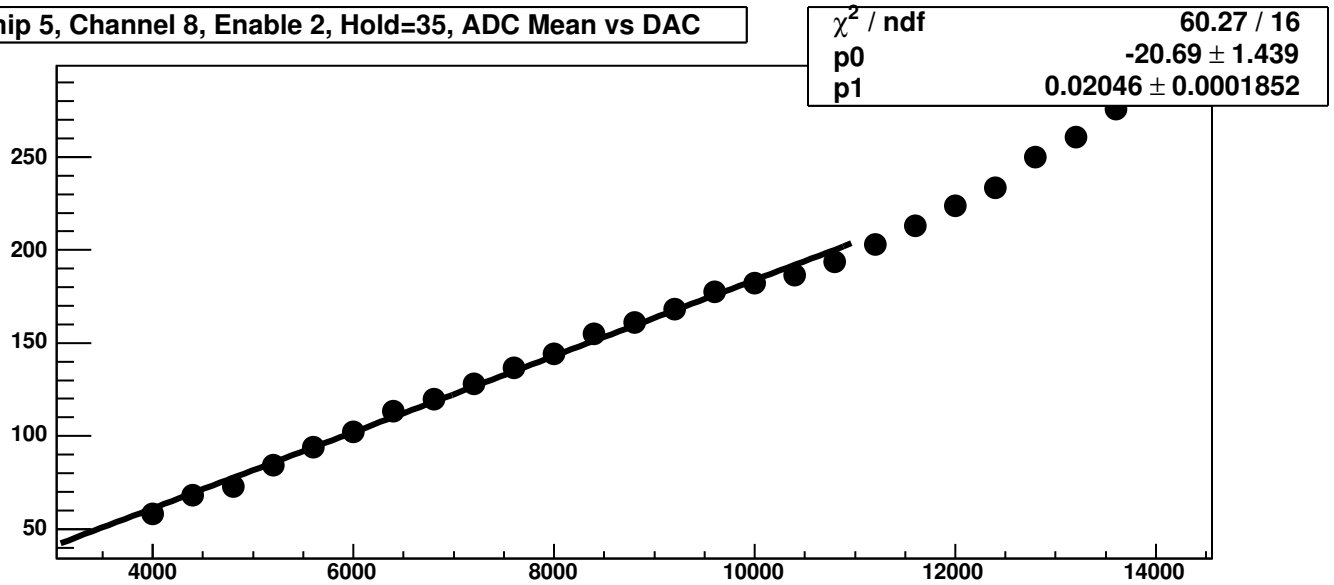



Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 50.64 / 16
p0 -28.09 ± 1.467
p1 0.02069 ± 0.0001889
250
200
150
100
50
4000
6000
8000
10000
12000
14000

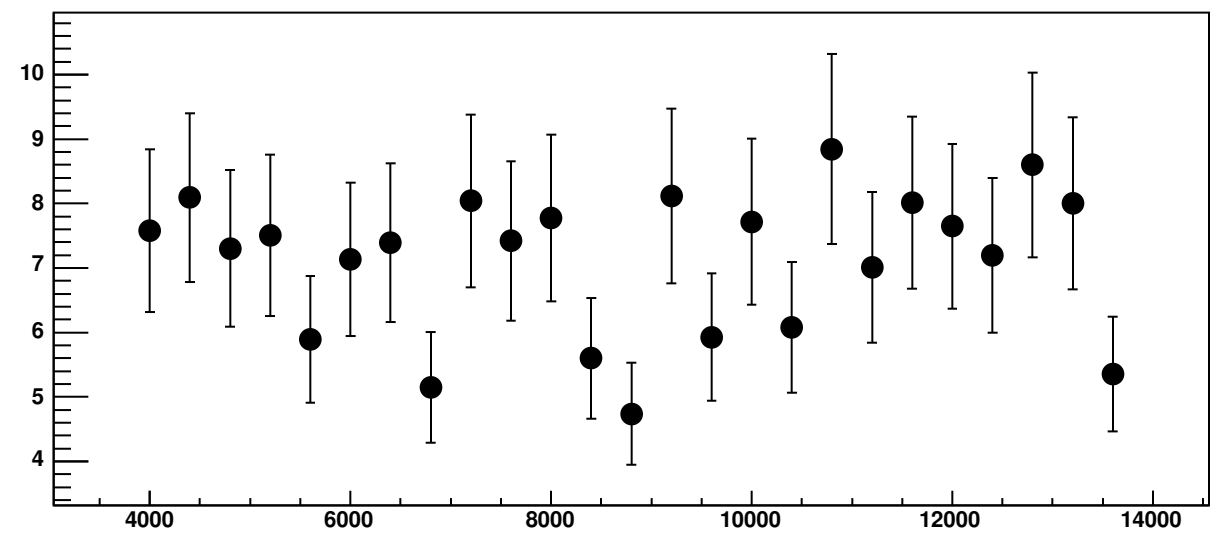

Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

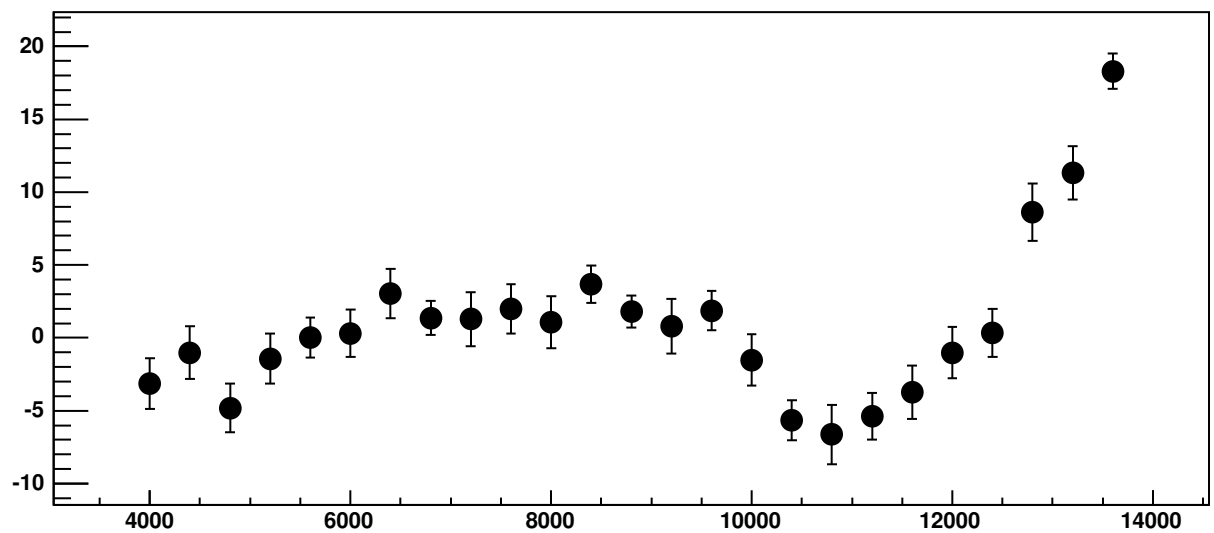
Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

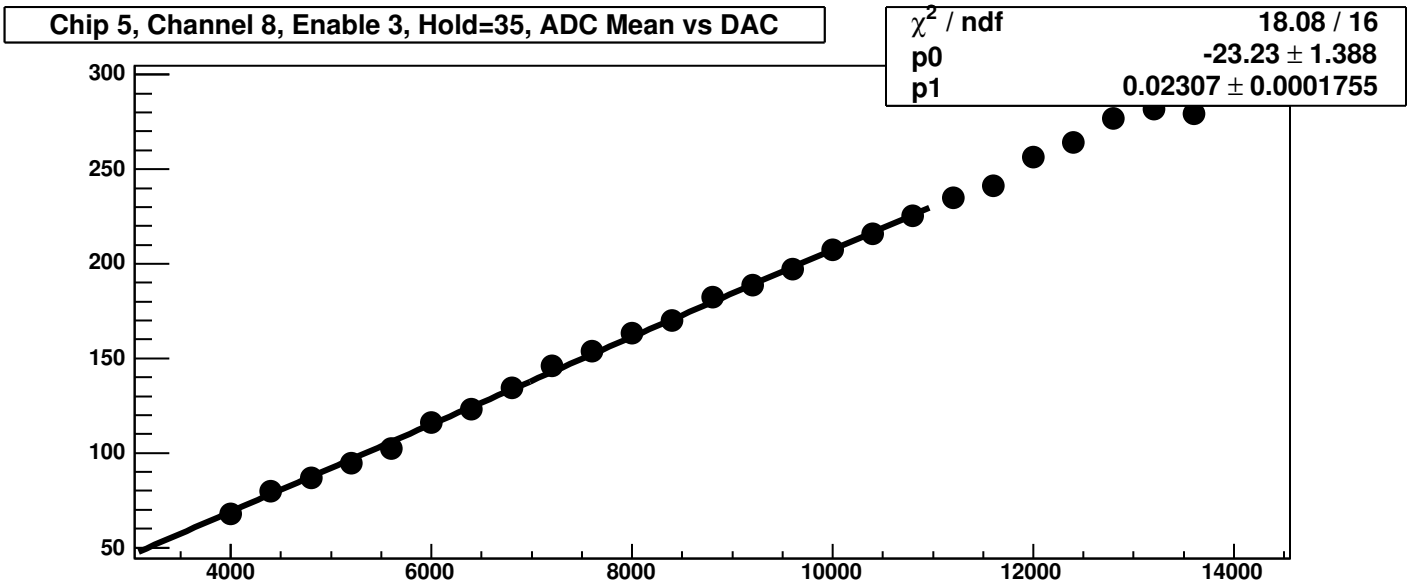


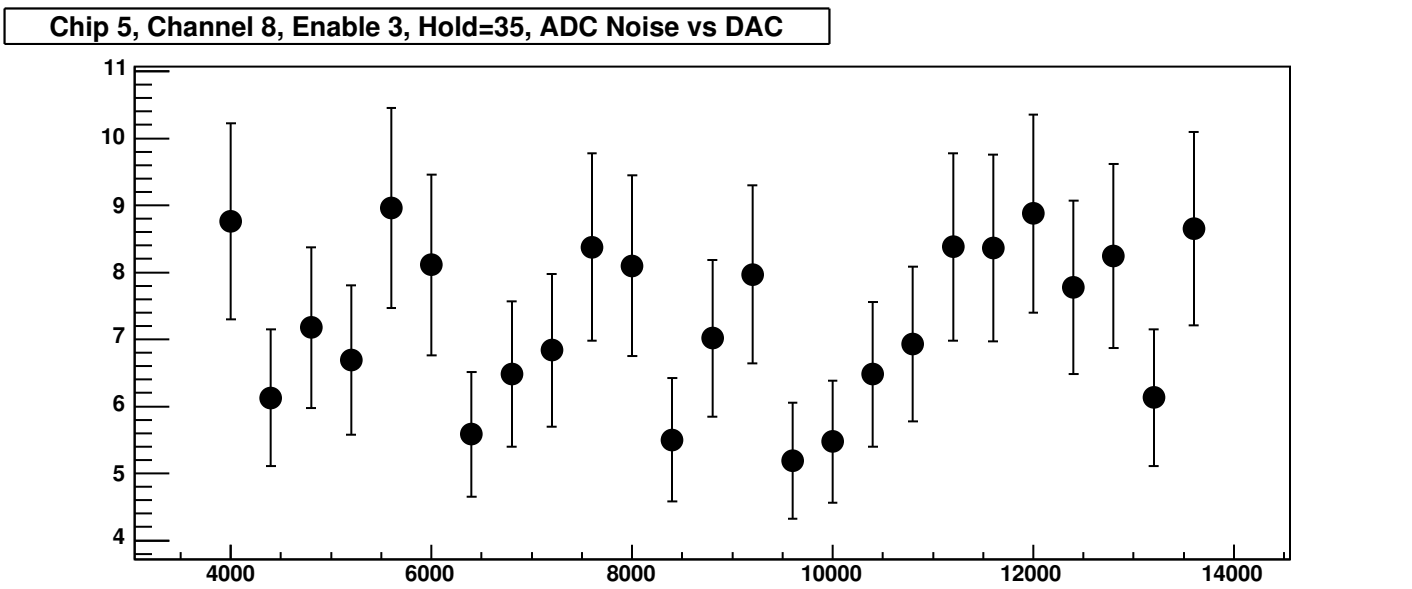
Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

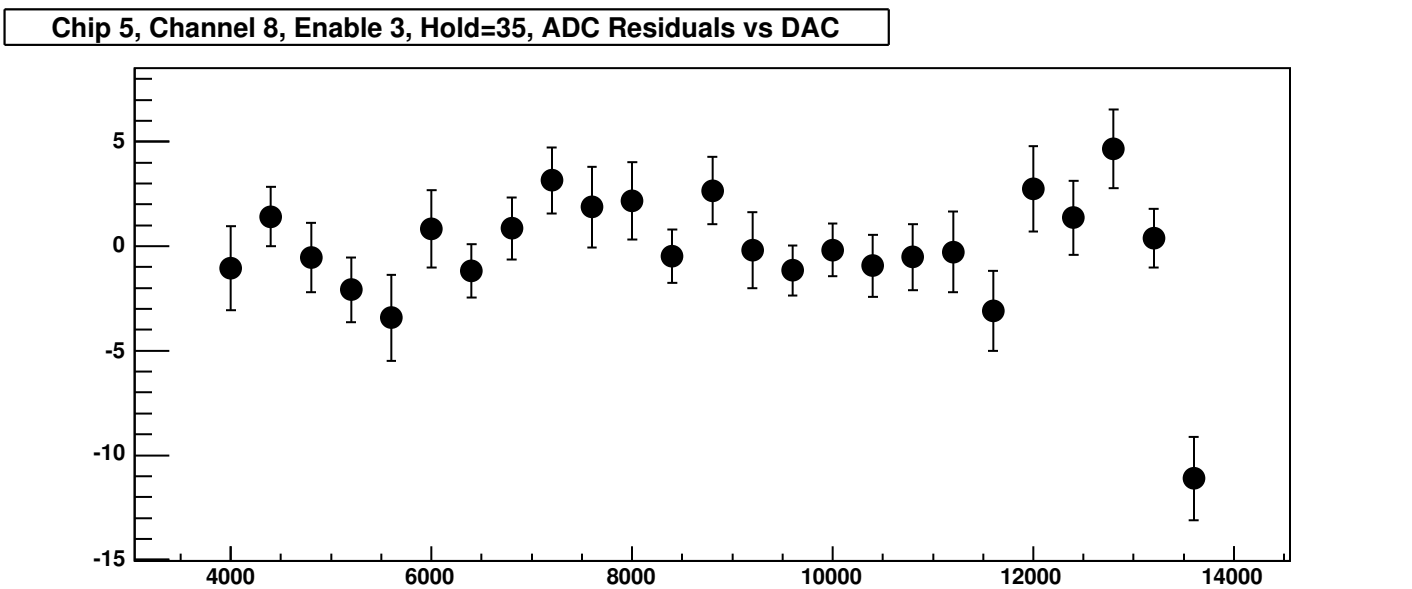


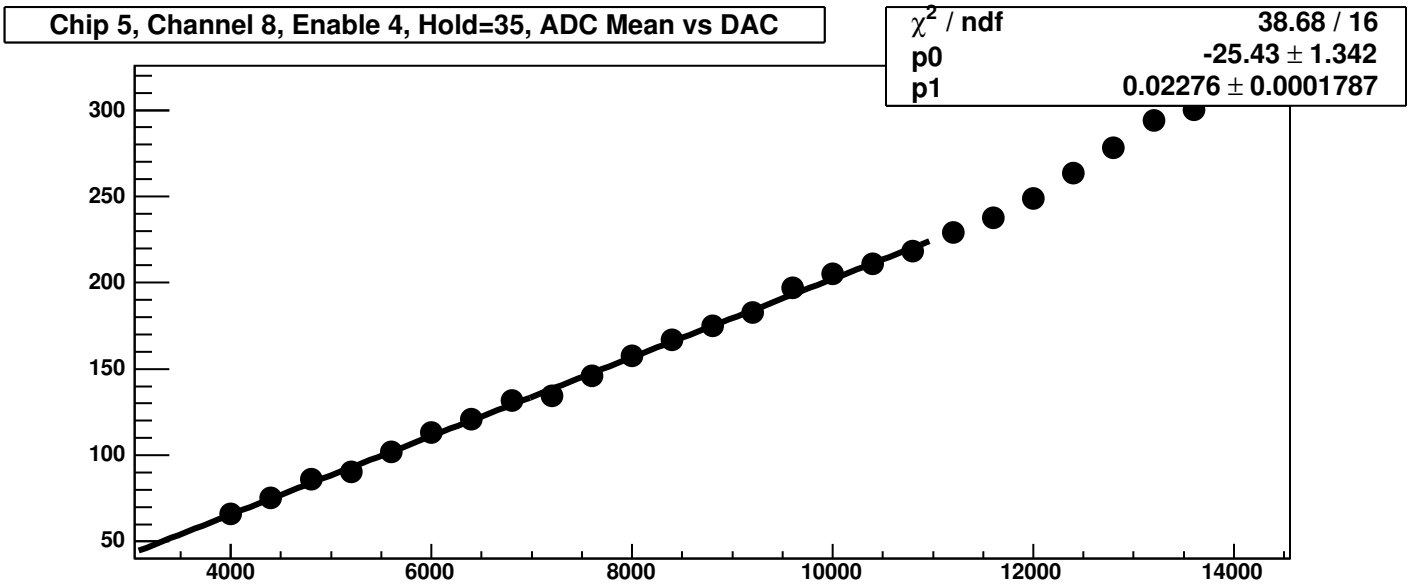
Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

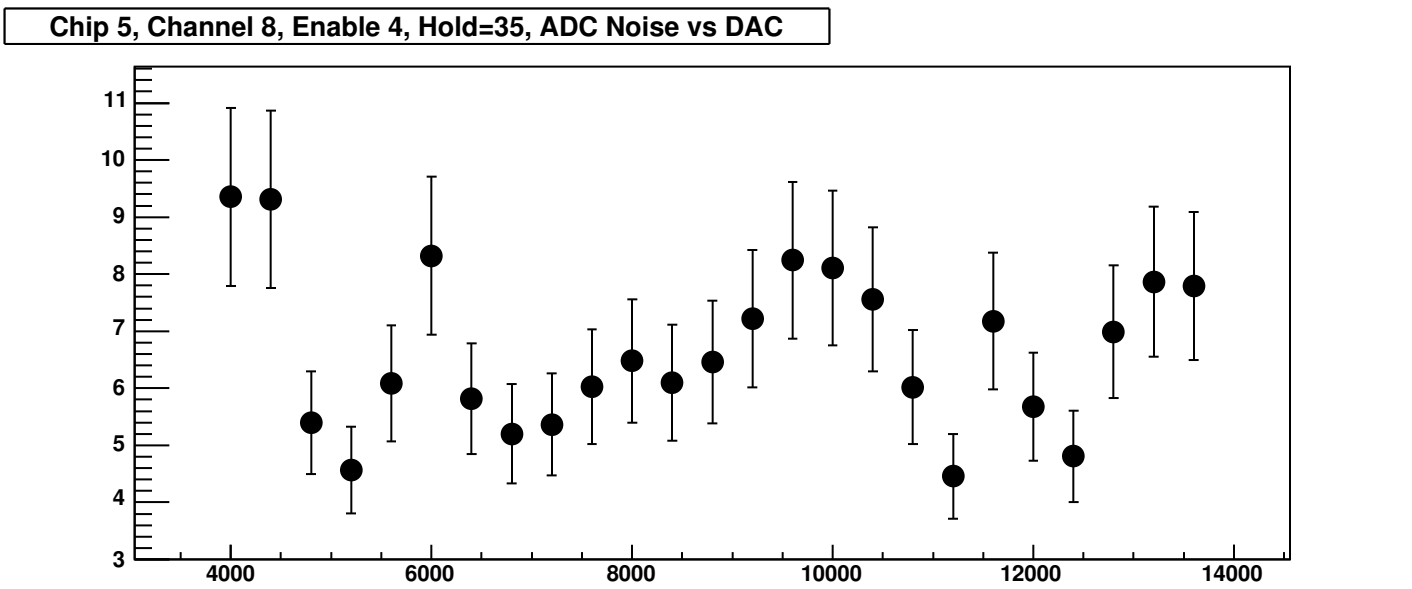


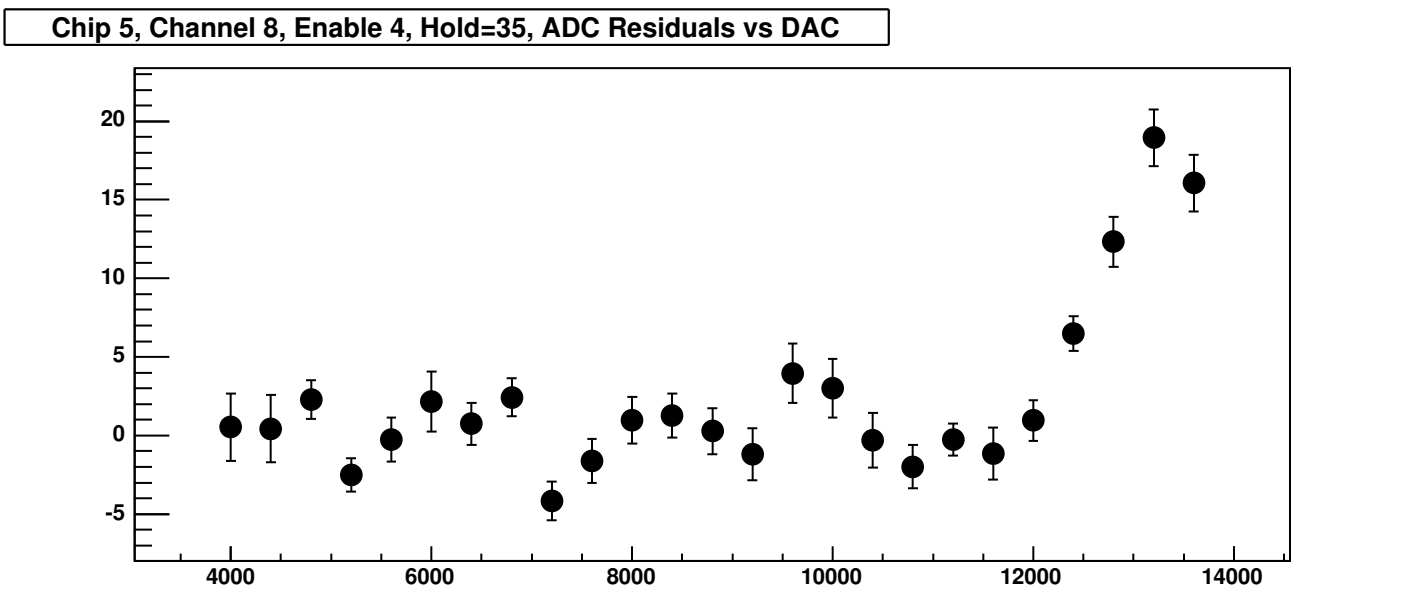

Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.08 / 16
p0 -23.23 ± 1.388
p1 0.02307 ± 0.0001755
300
250
200
150
100
50
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

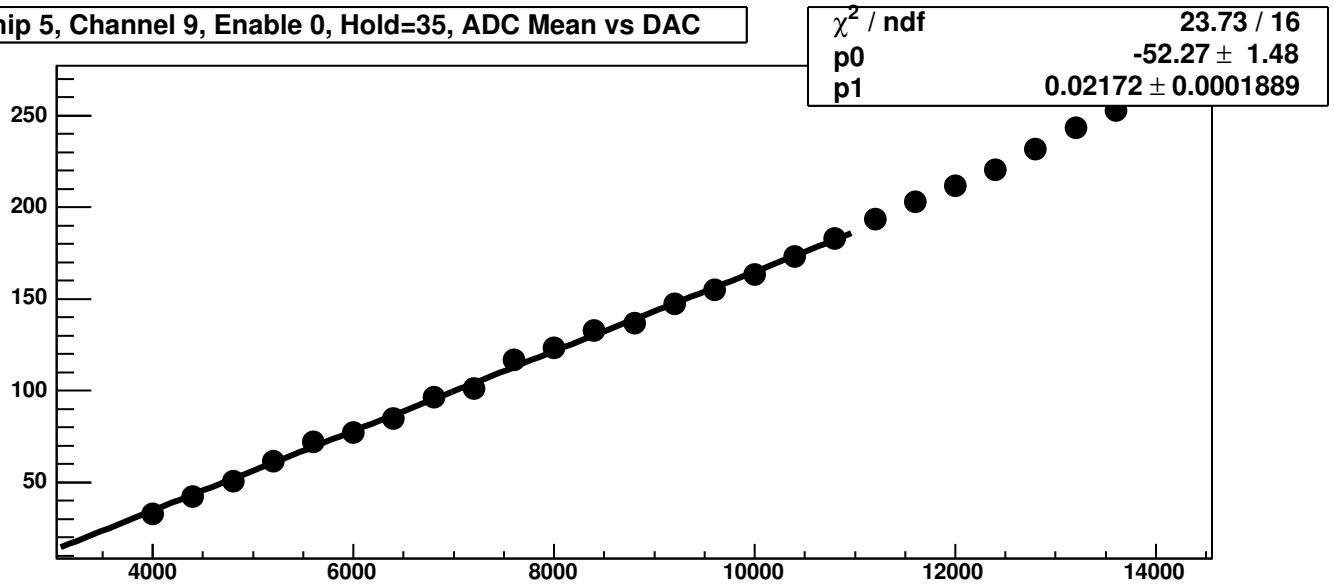

Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.68 / 16
p0 -25.43 ± 1.342
p1 0.02276 ± 0.0001787
300
250
200
150
100
50
4000
6000
8000
10000
12000
14000

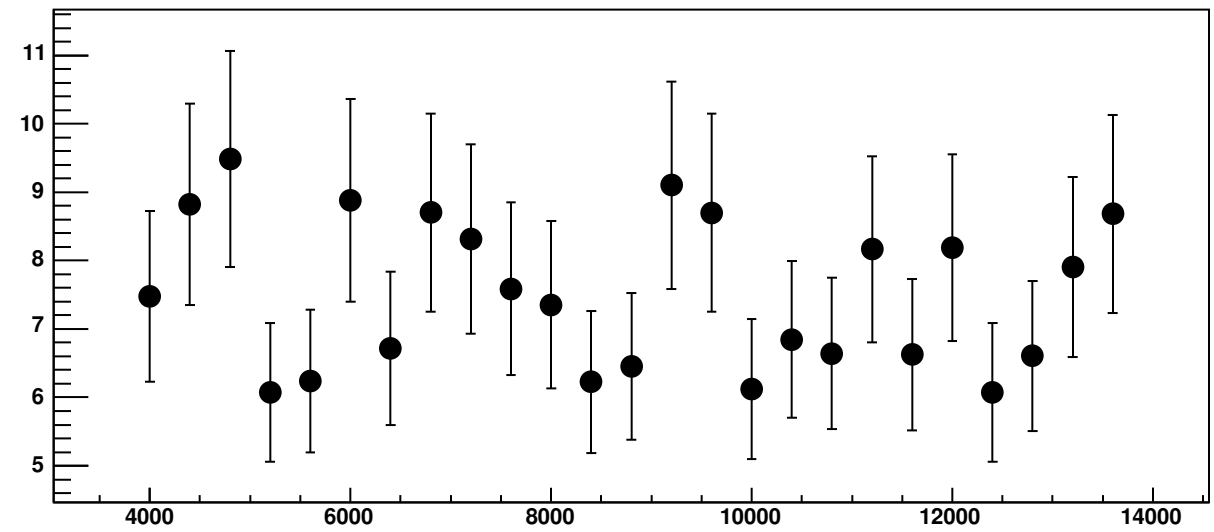

Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

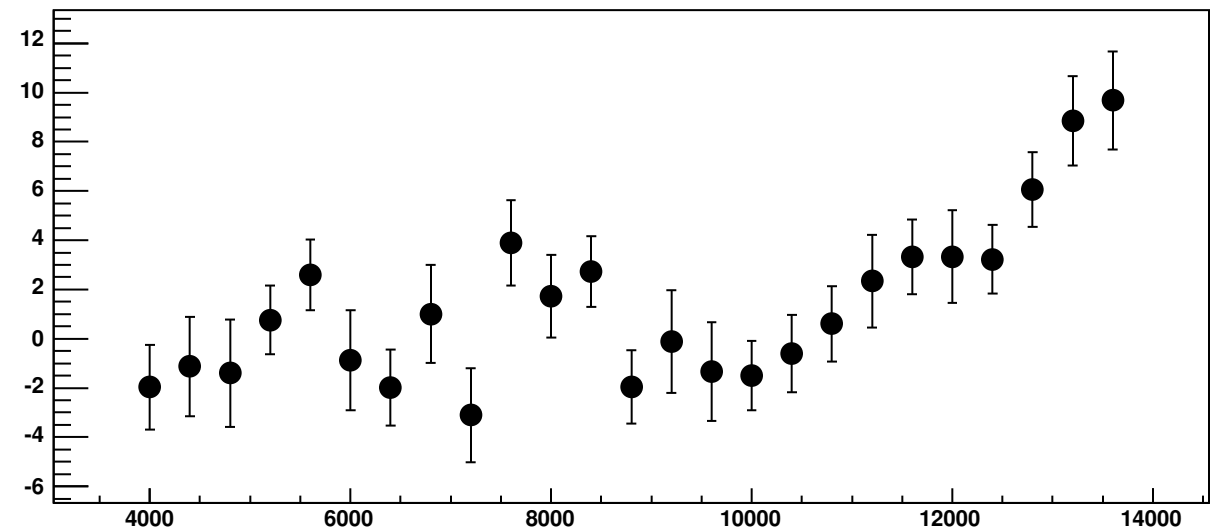
Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



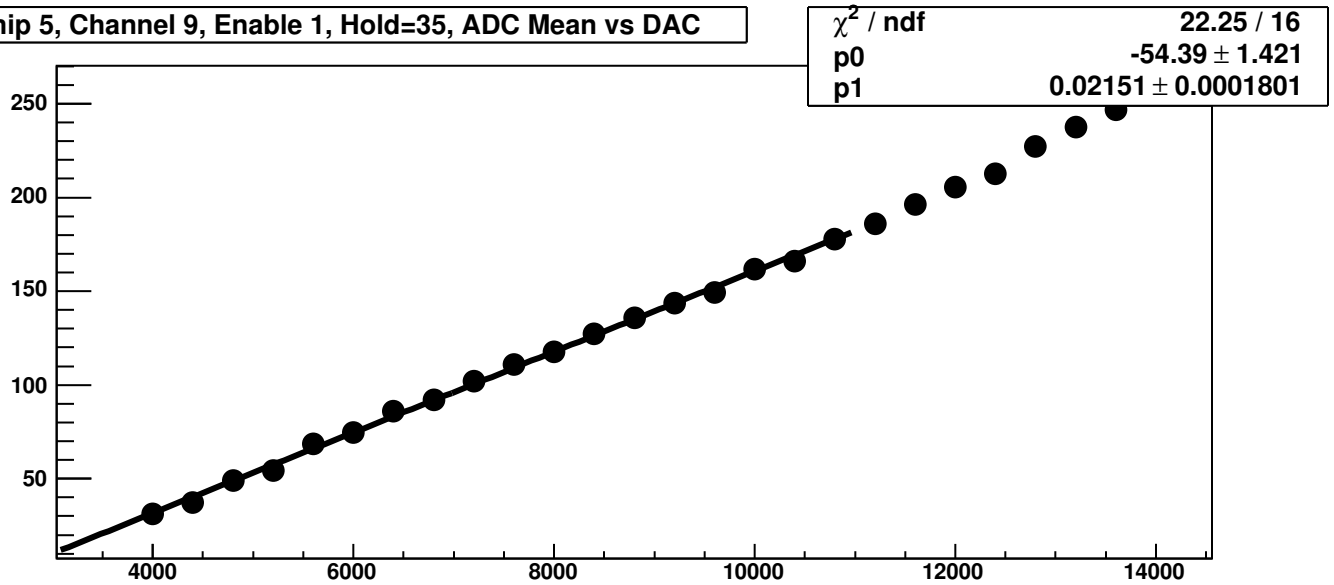
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



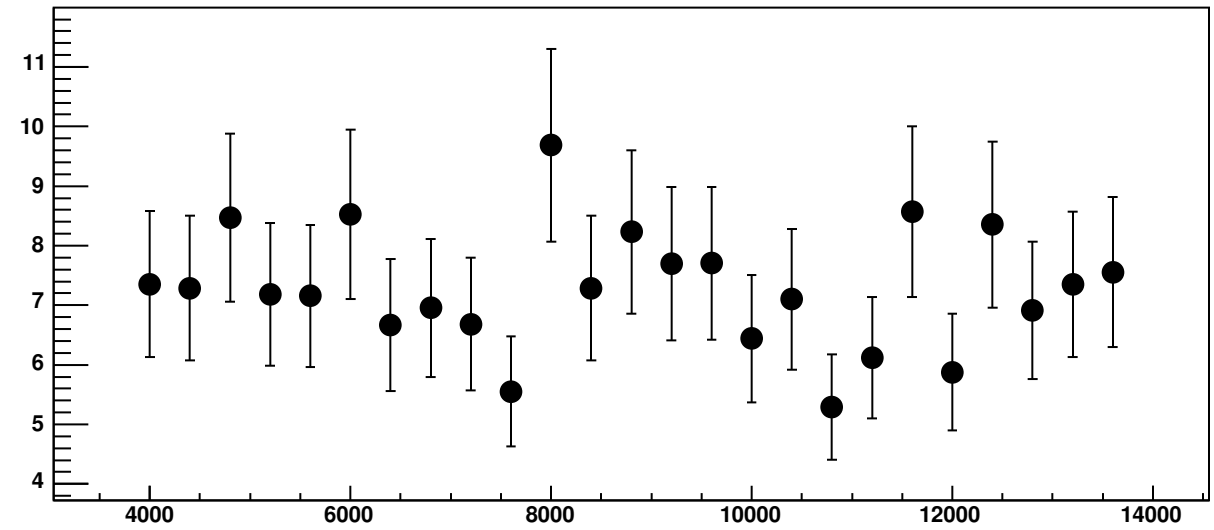
Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



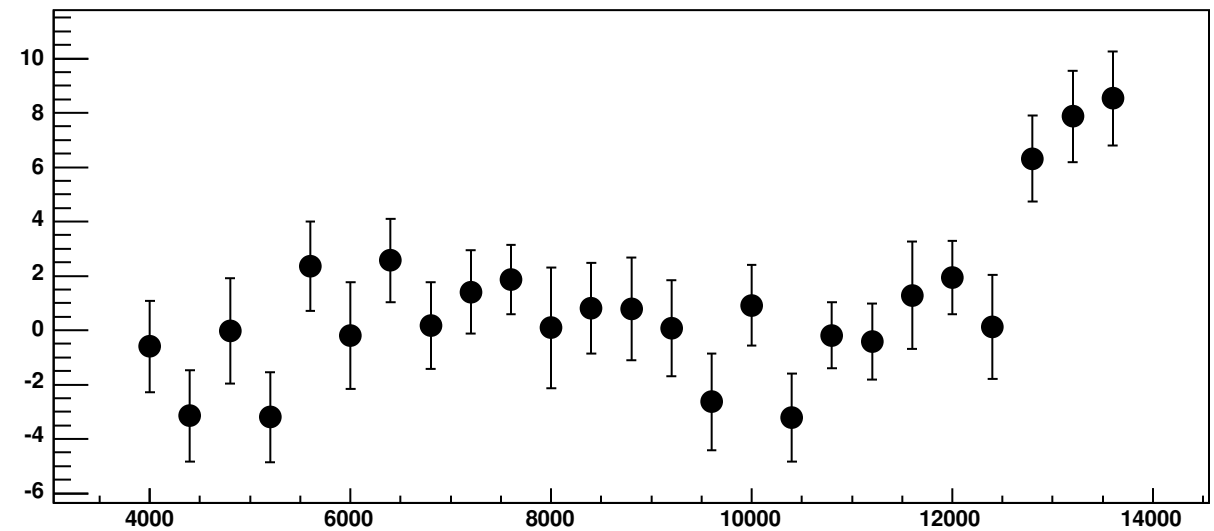
Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

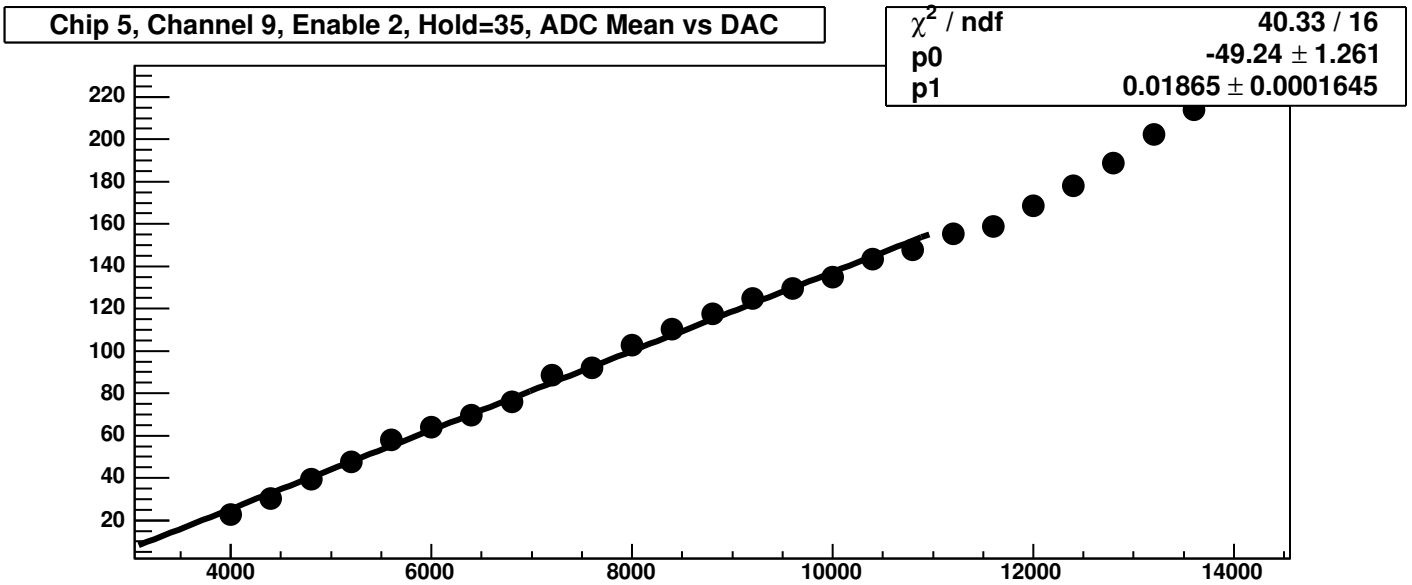


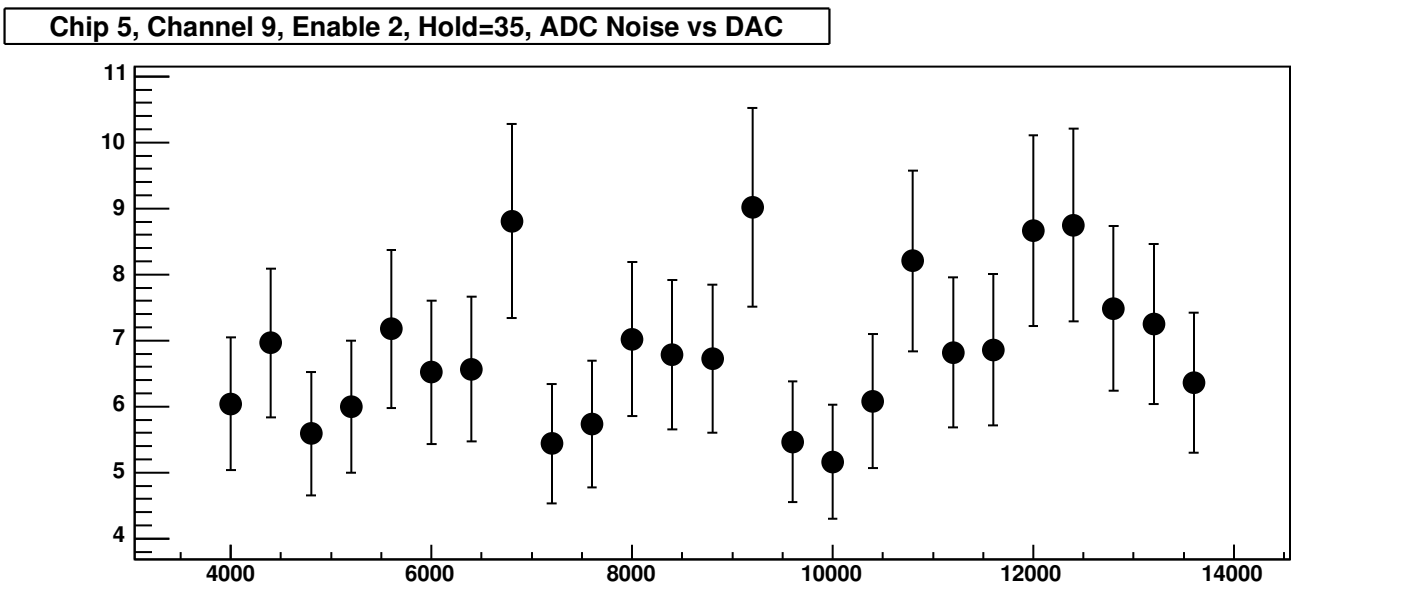
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

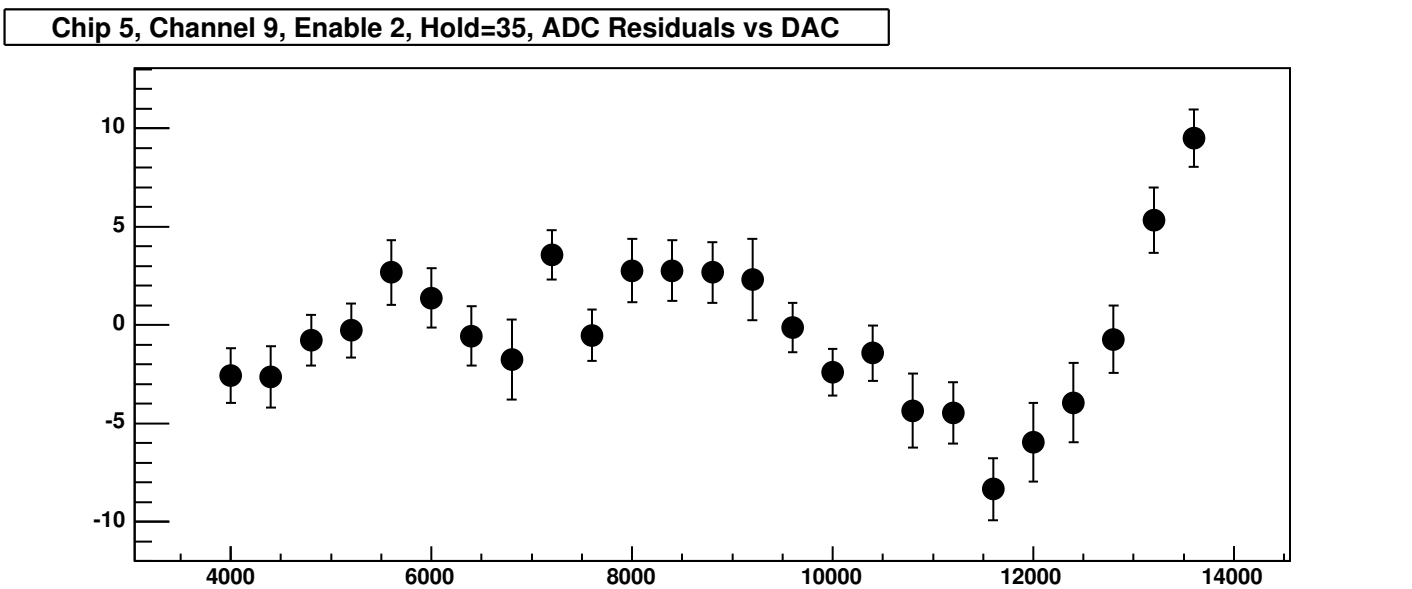


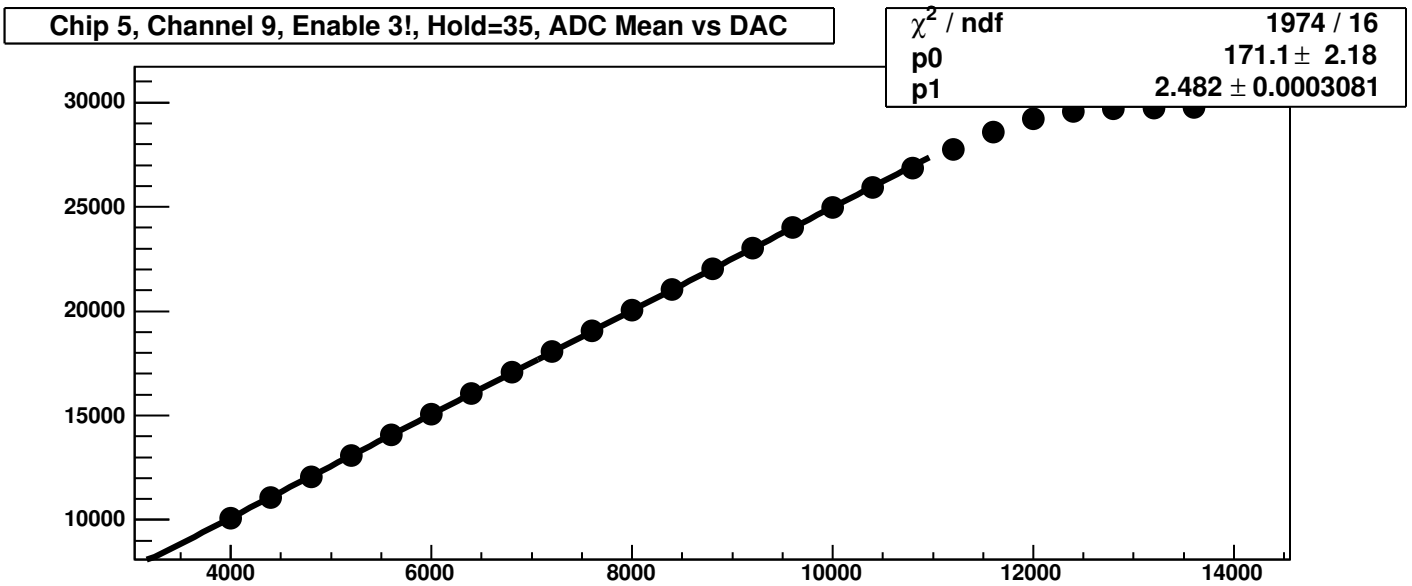
Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

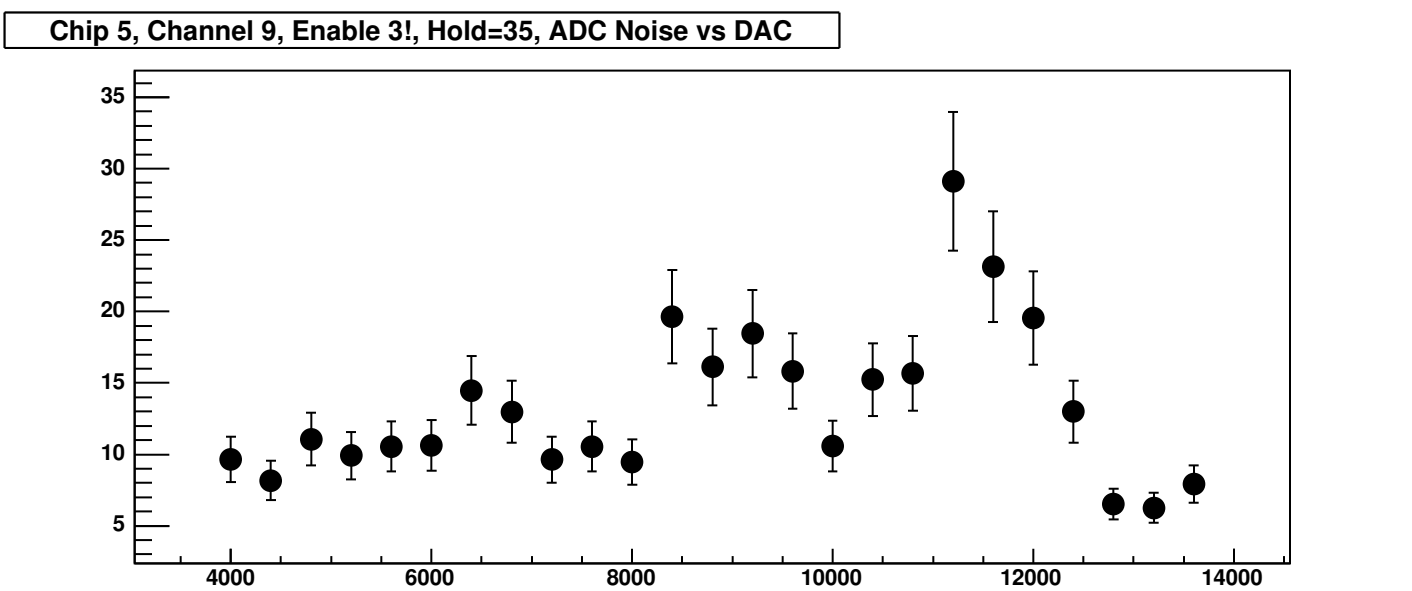


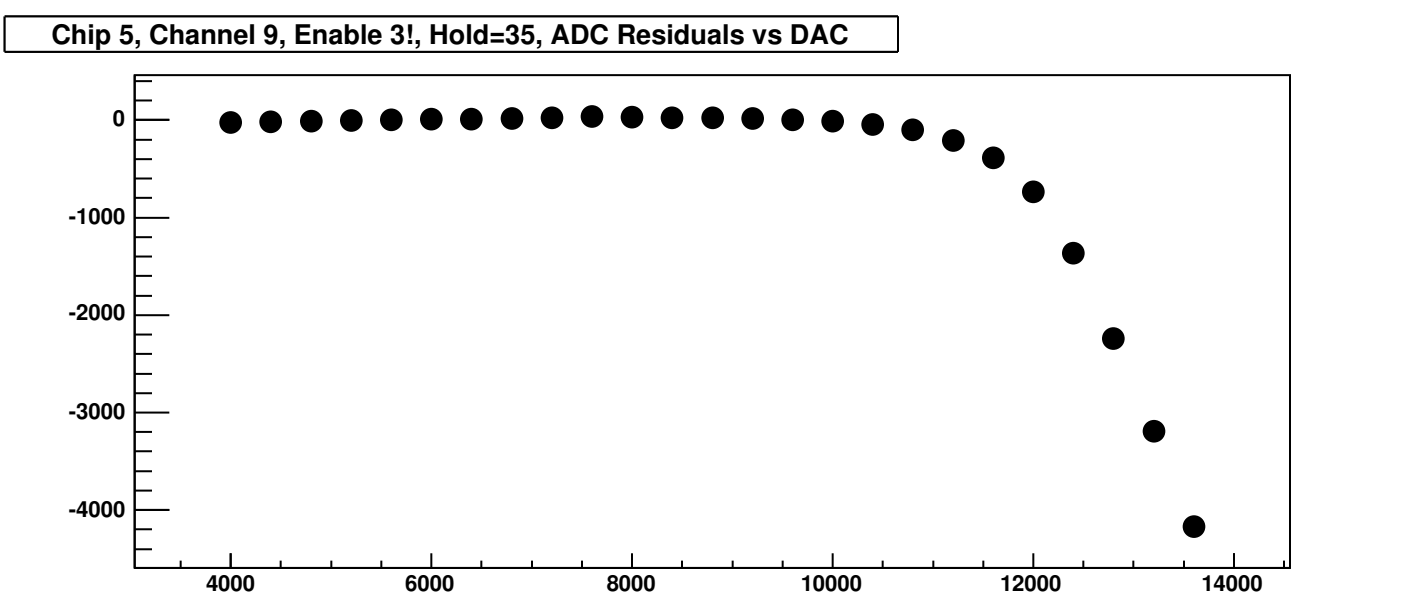

Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.33 / 16
p0 -49.24 ± 1.261
p1 0.01865 ± 0.0001645
220
200
180
160
140
120
100
80
60
40
20
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

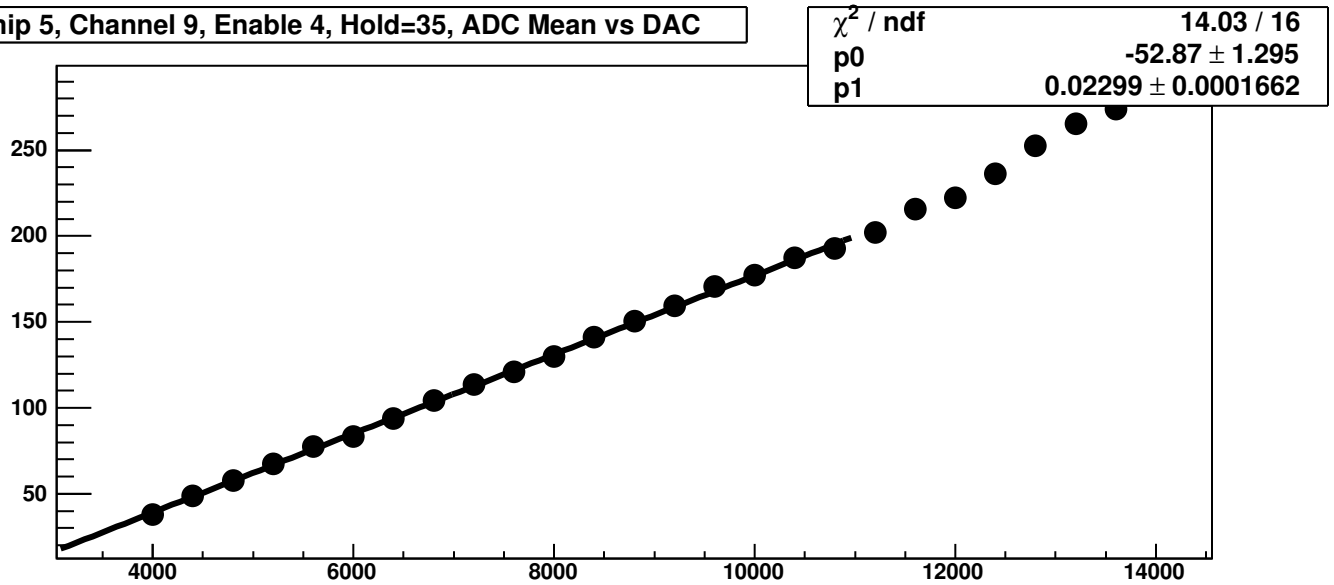

Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1974 / 16
p0 171.1 ± 2.18
p1 2.482 ± 0.0003081
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

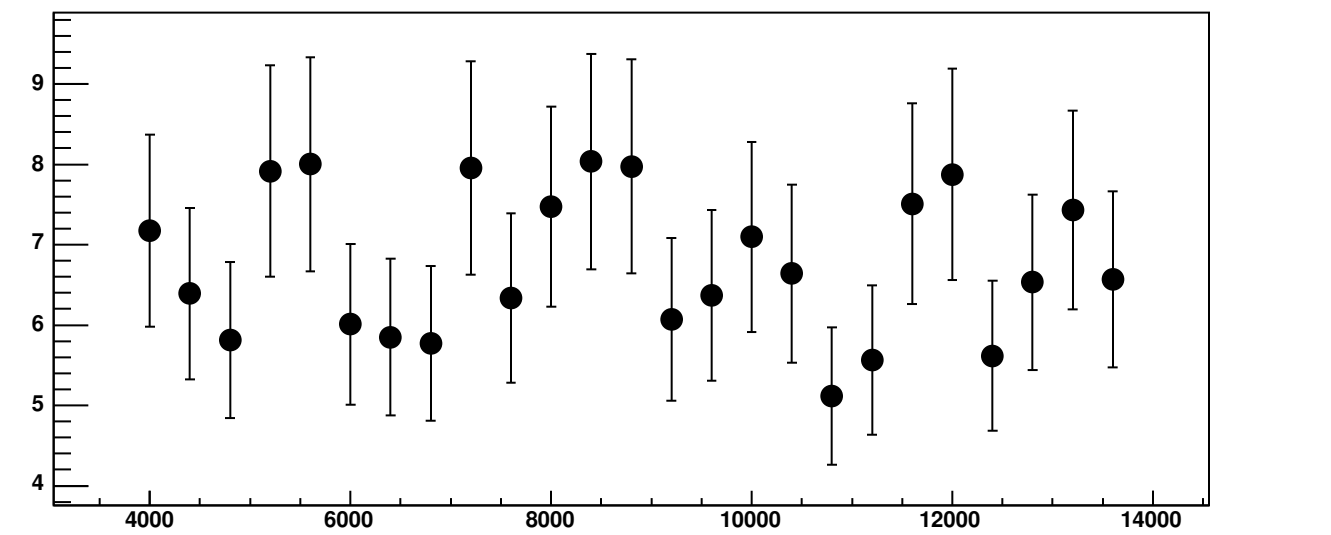

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

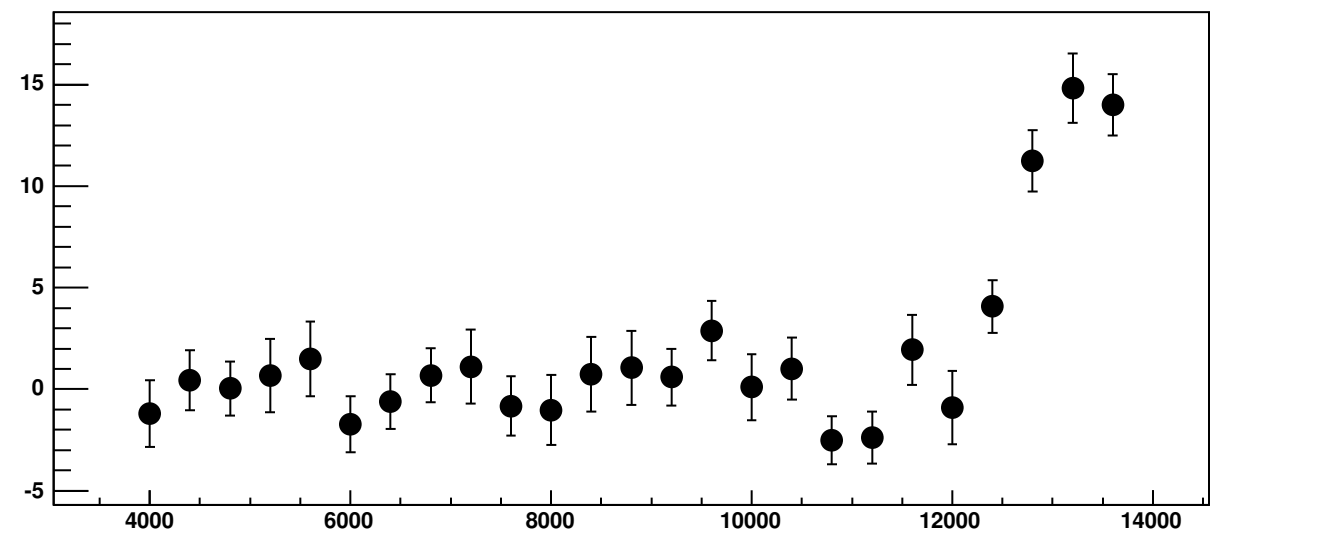
Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

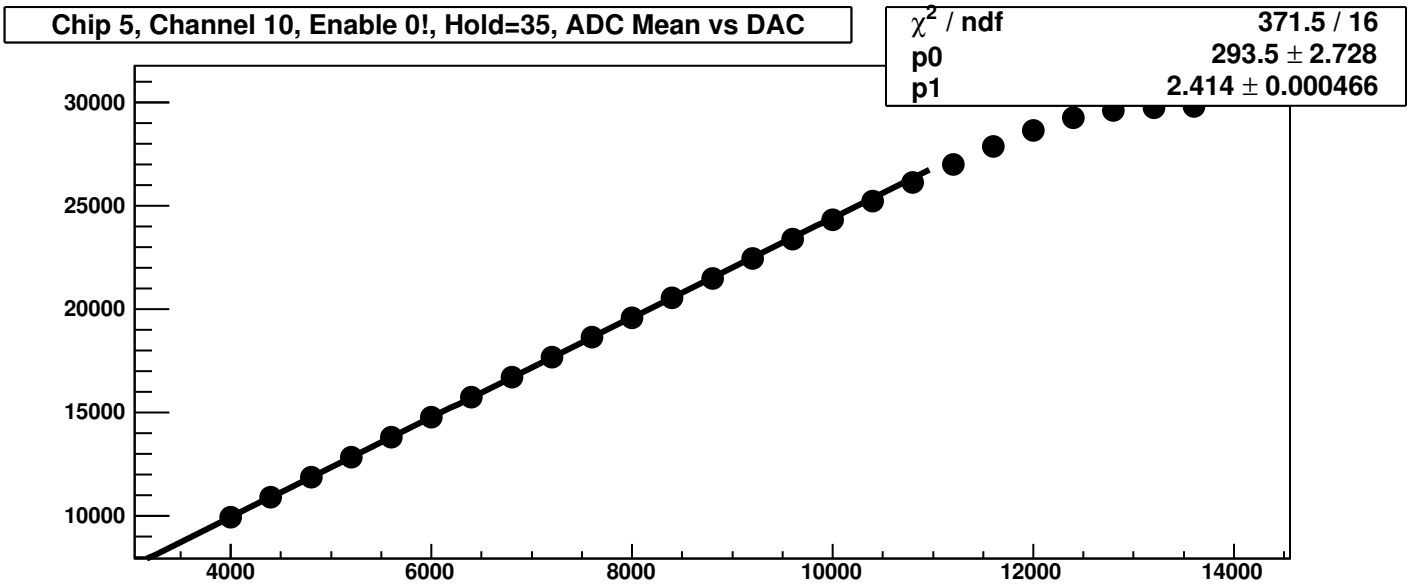


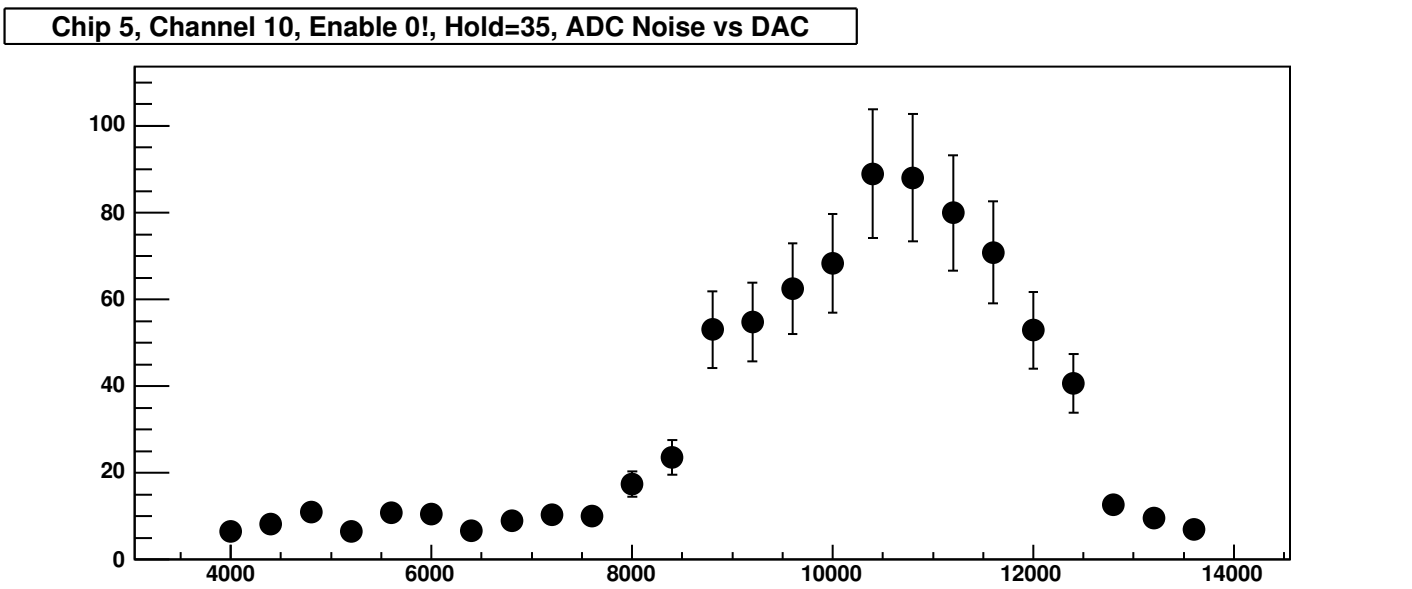
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

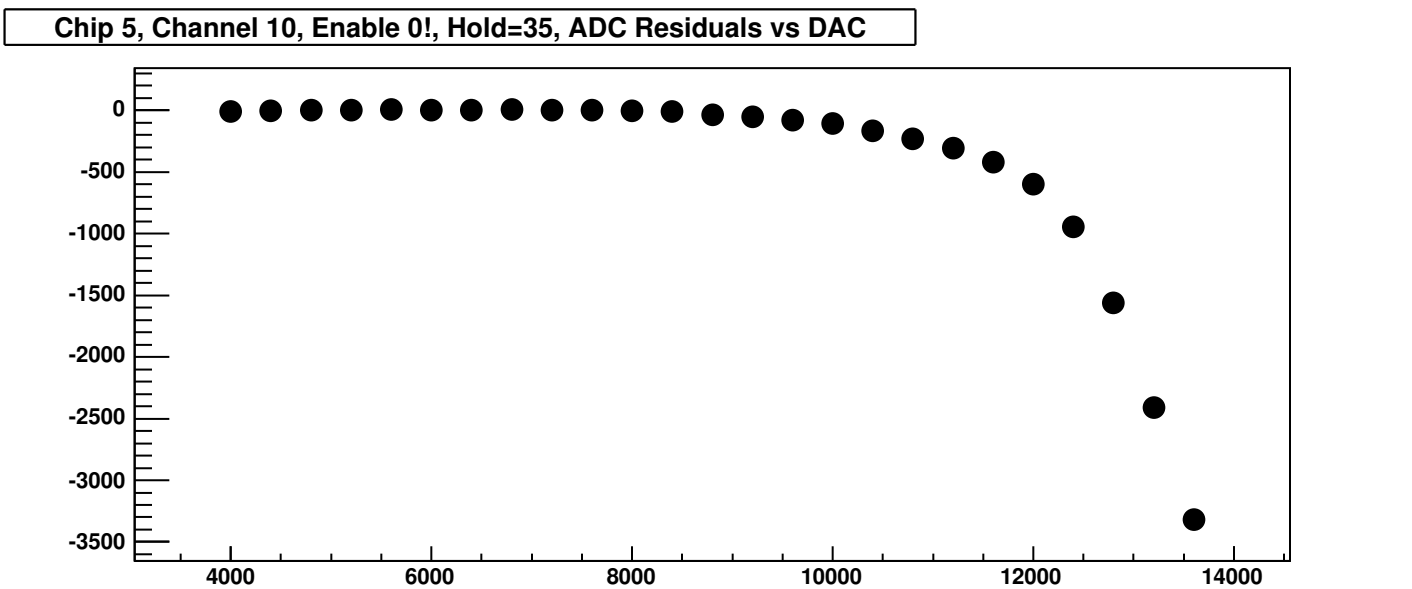


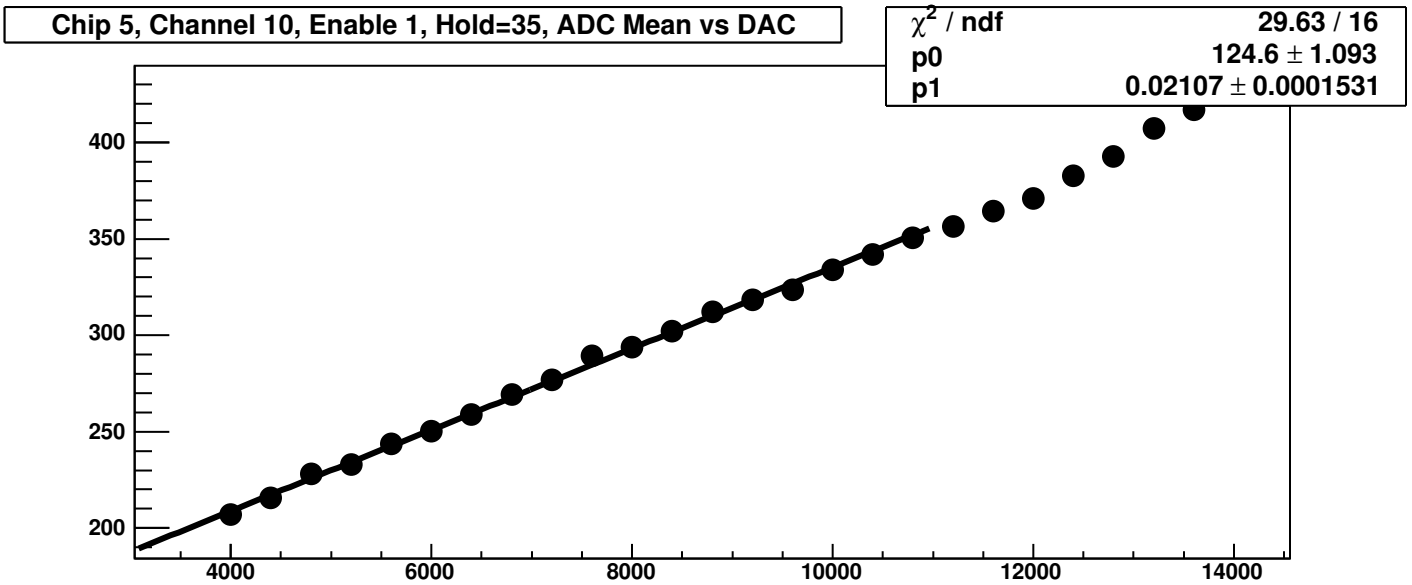
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

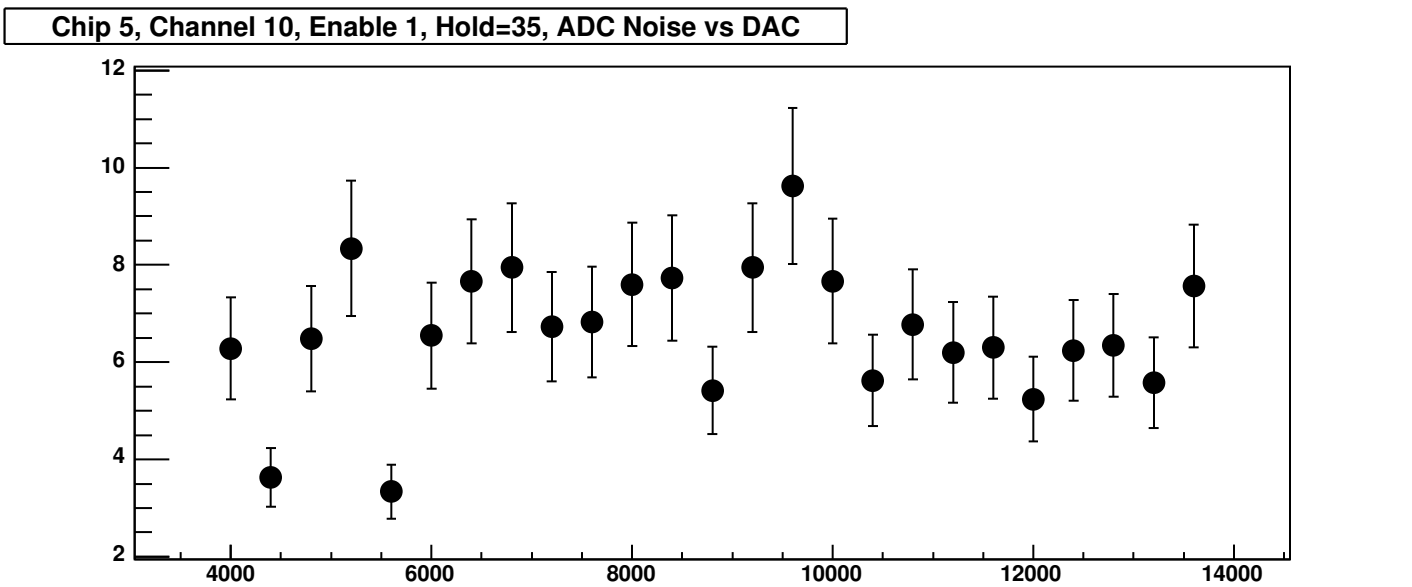


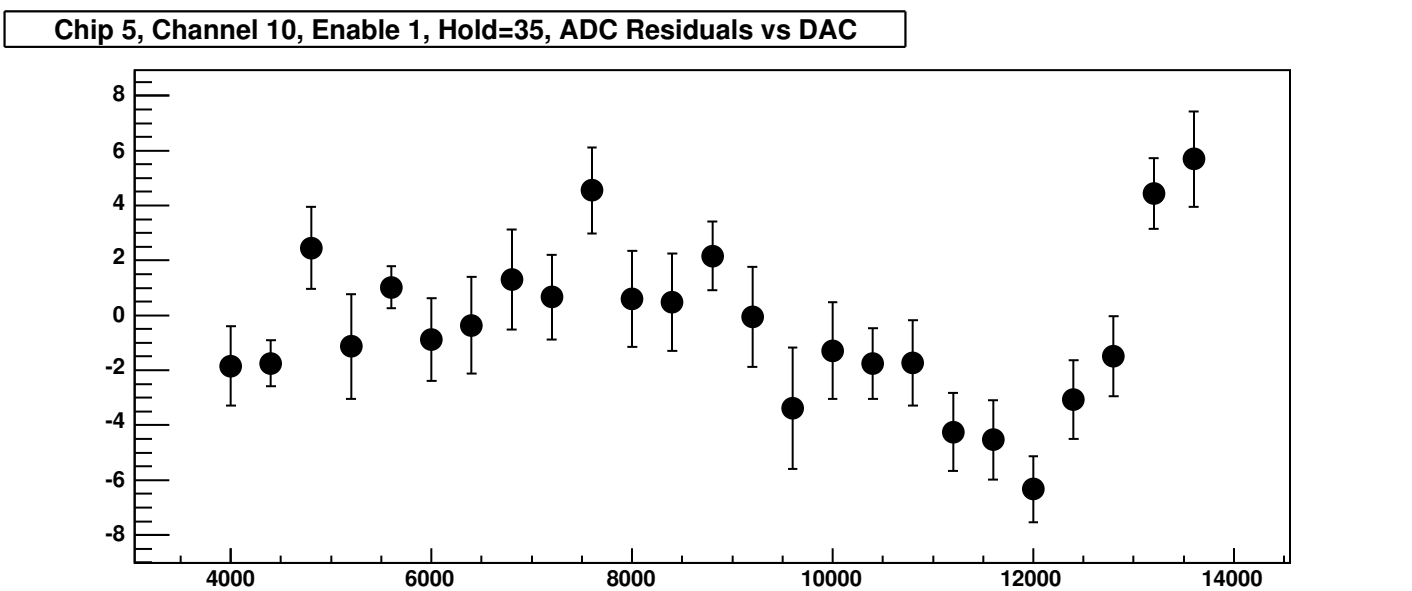

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 371.5 / 16
p0 293.5 ± 2.728
p1 2.414 ± 0.000466
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

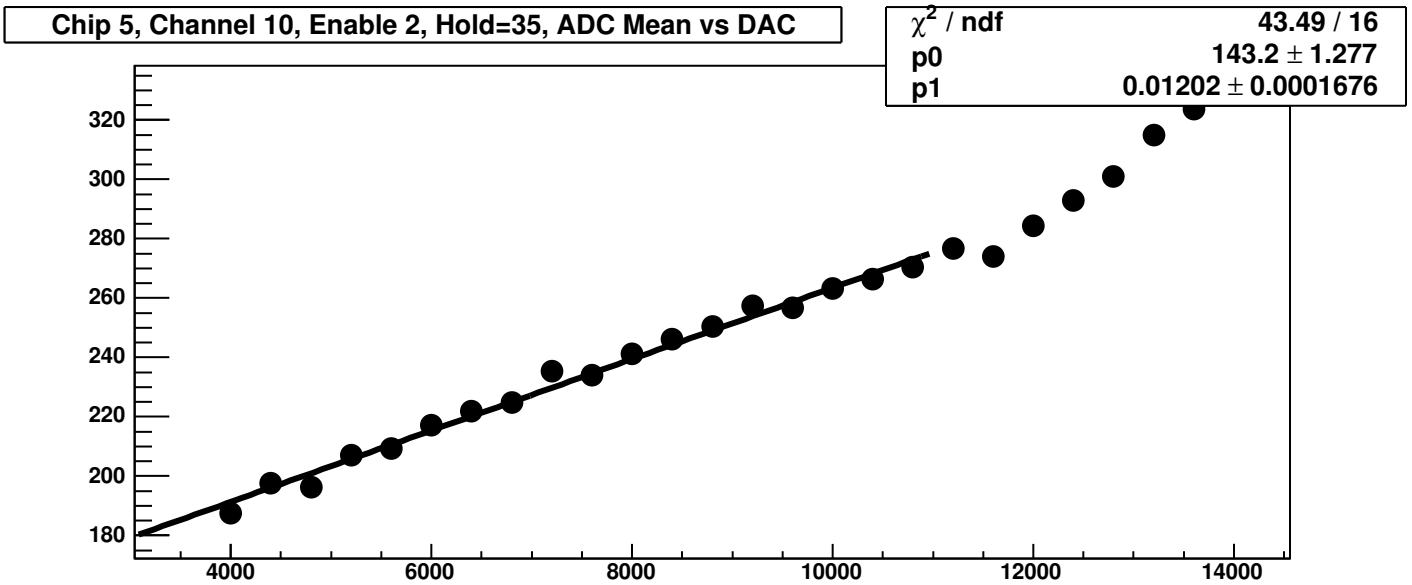

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

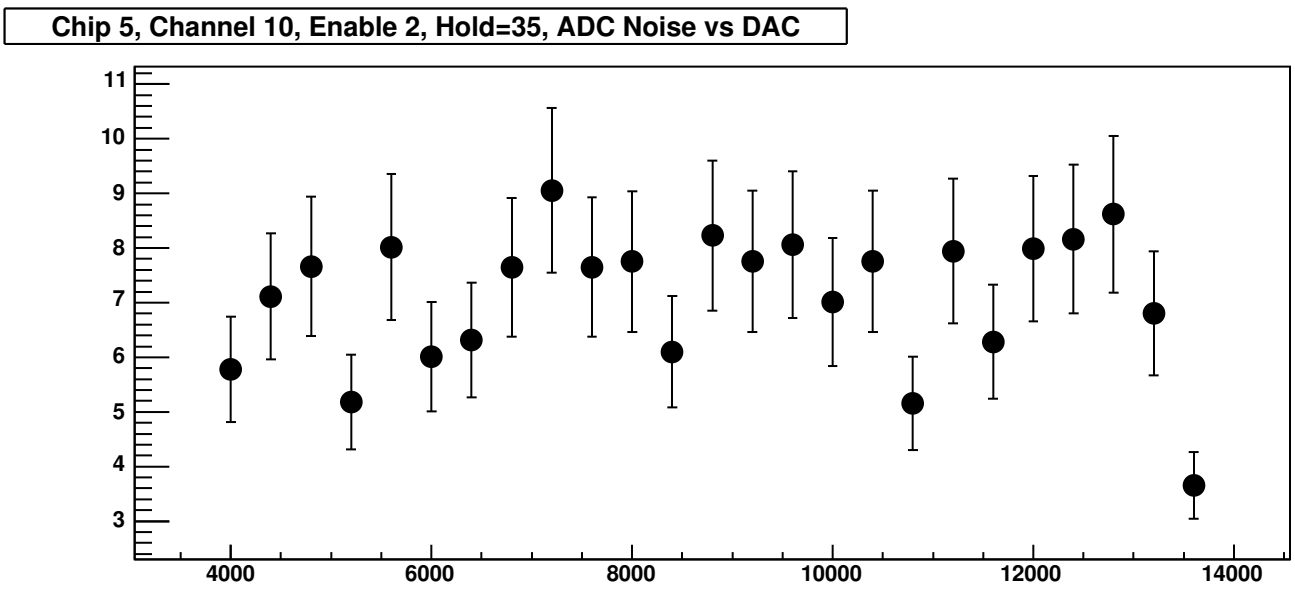

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

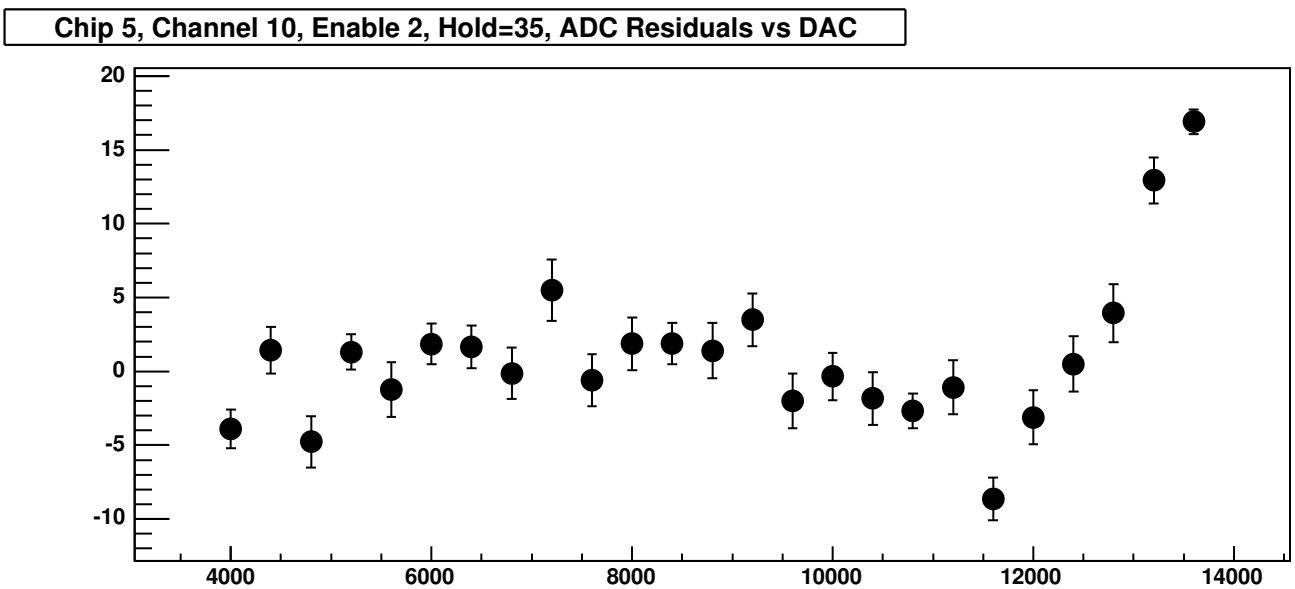

Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.63 / 16
p0 124.6 ± 1.093
p1 0.02107 ± 0.0001531
400
350
300
250
200
4000
6000
8000
10000
12000
14000

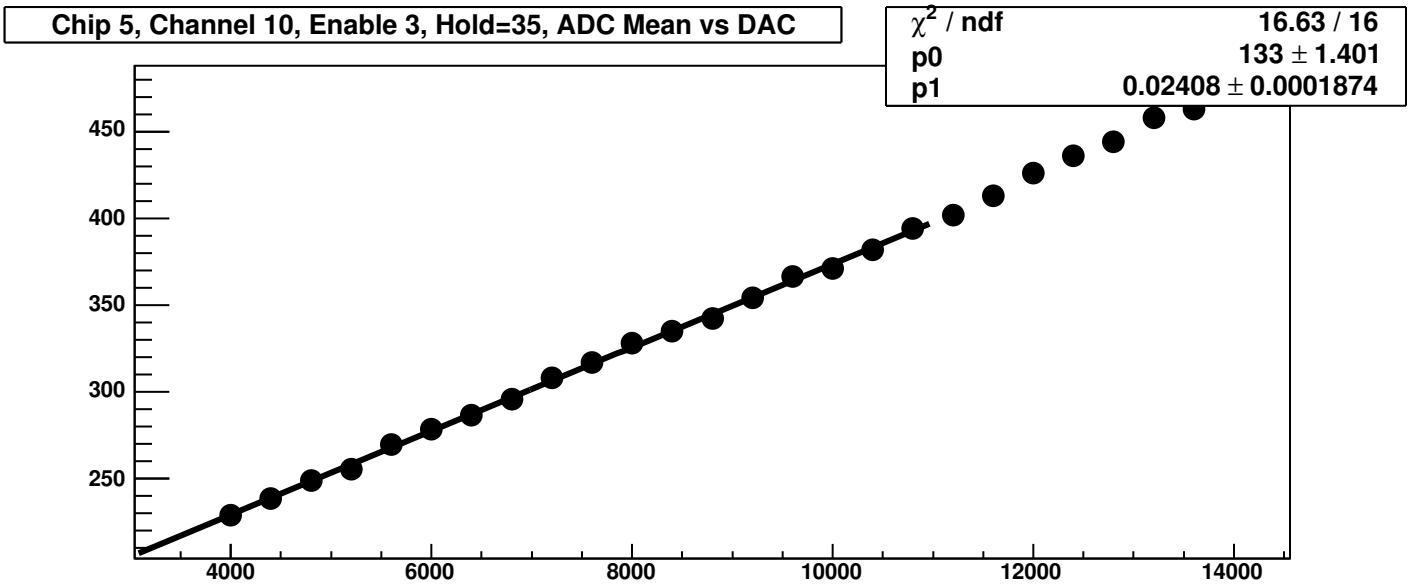

Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
4000
6000
8000
10000
12000
14000

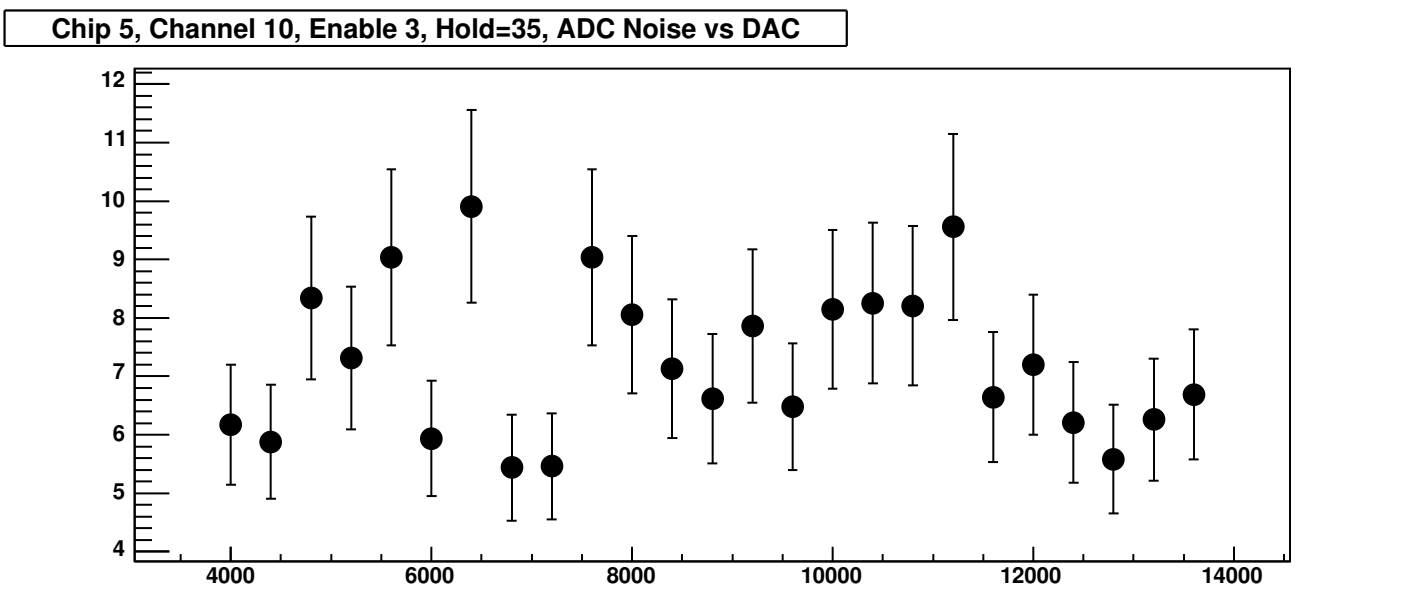

Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

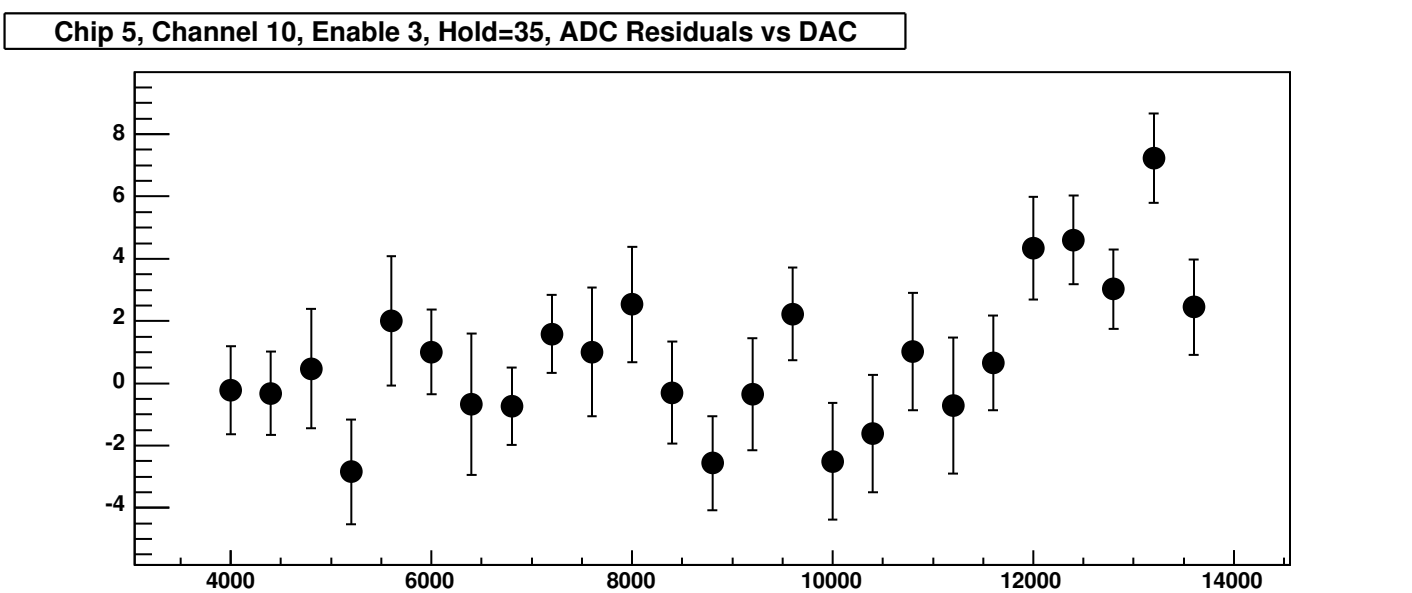

Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.49 / 16
p0 143.2 ± 1.277
p1 0.01202 ± 0.0001676
320
300
280
260
240
220
200
180
4000
6000
8000
10000
12000
14000

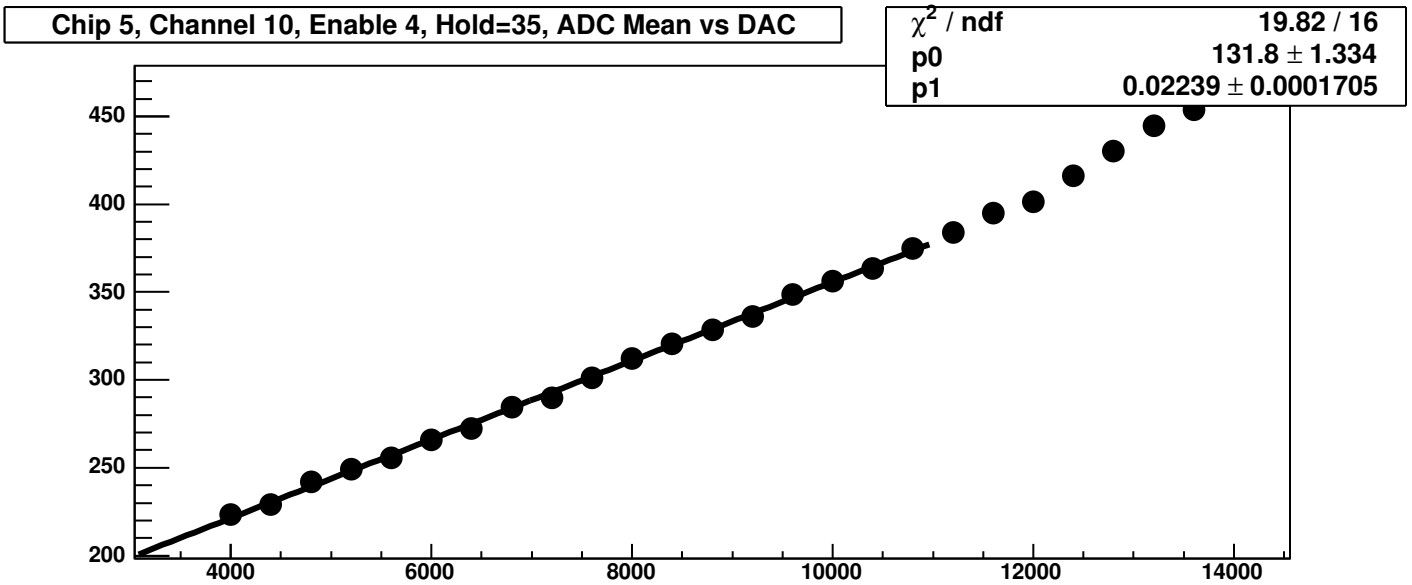

Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

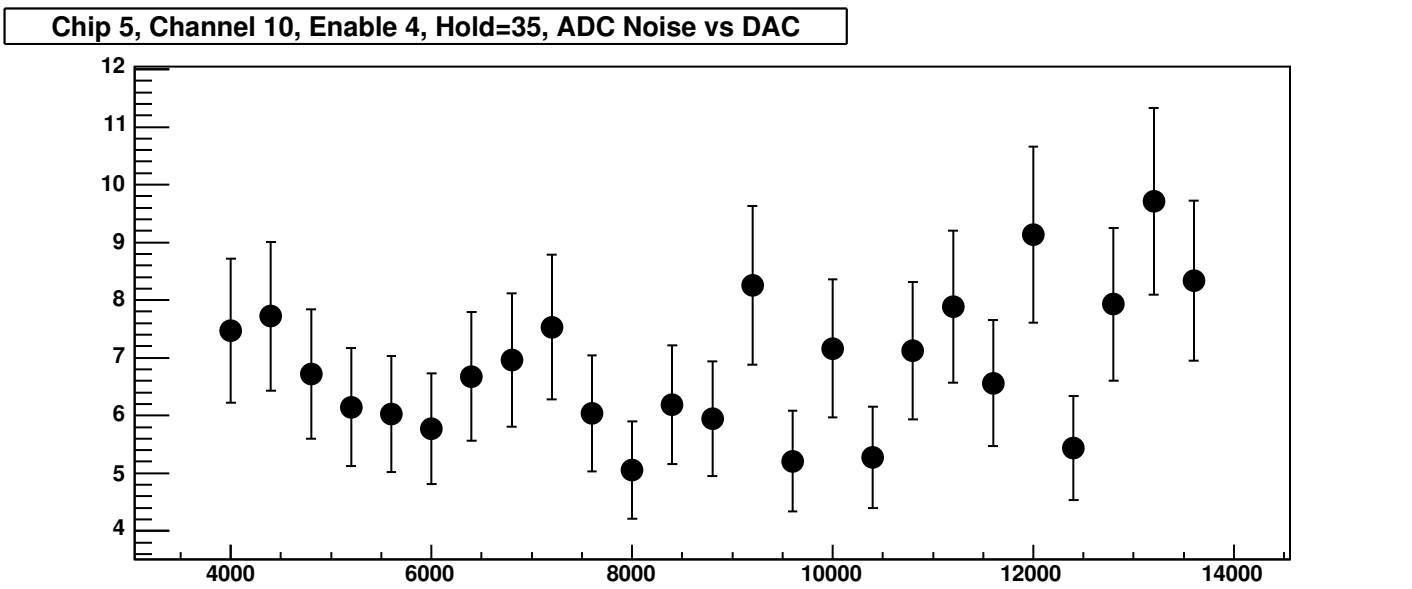

Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
4000
6000
8000
10000
12000
14000

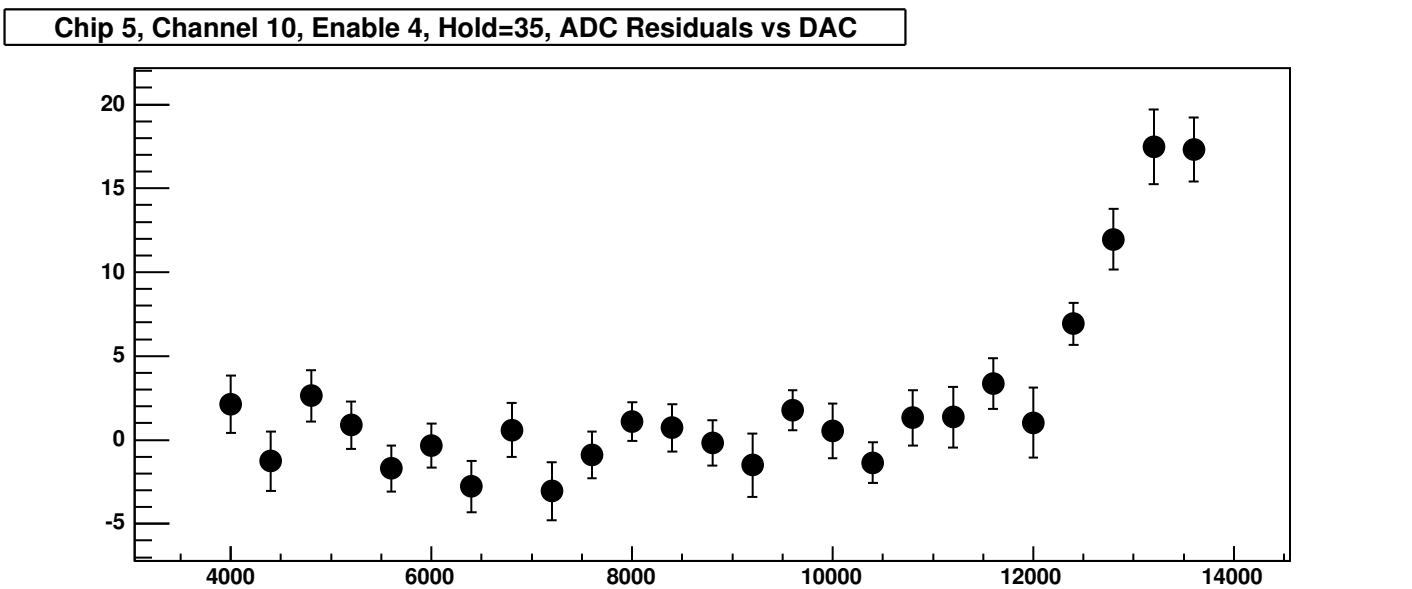

Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.63 / 16
p0 133 ± 1.401
p1 0.02408 ± 0.0001874
450
400
350
300
250
4000
6000
8000
10000
12000
14000

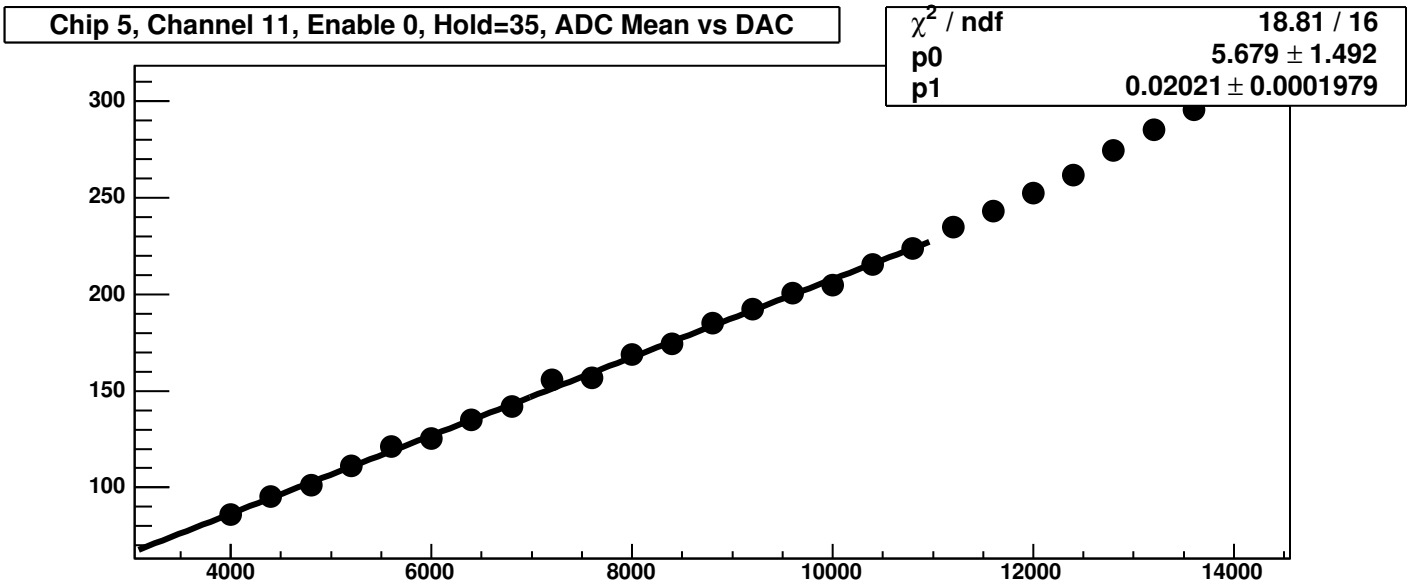

Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

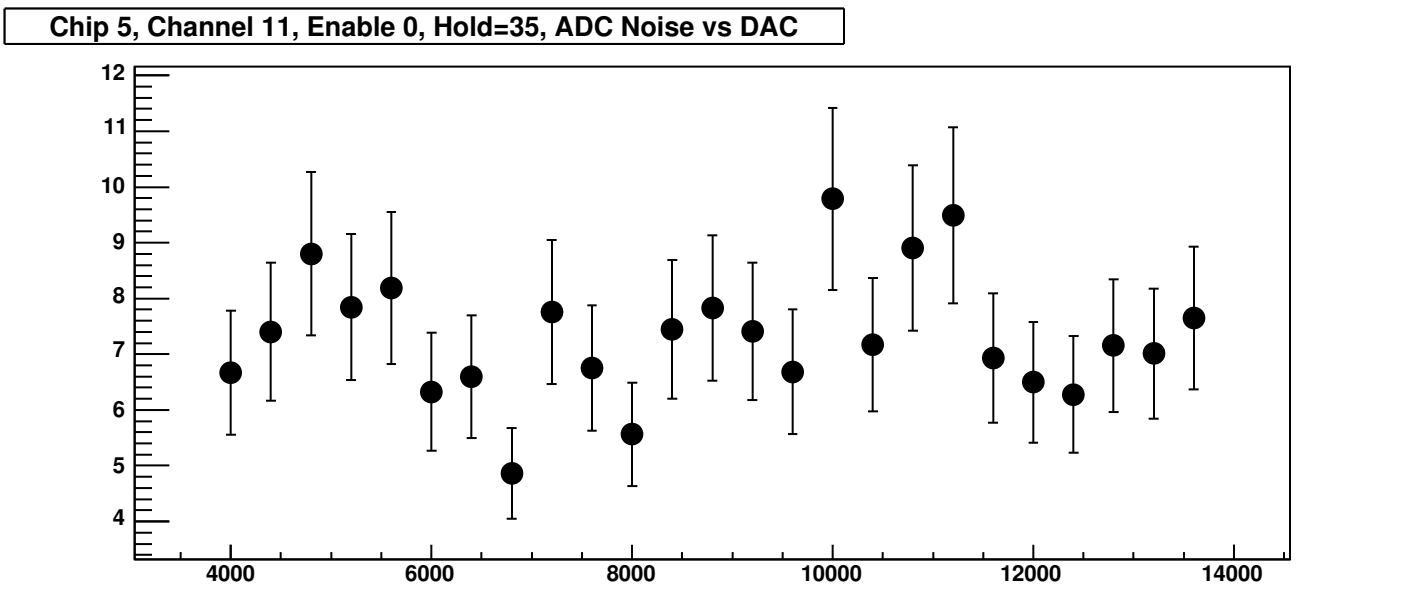

Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
4000
6000
8000
10000
12000
14000

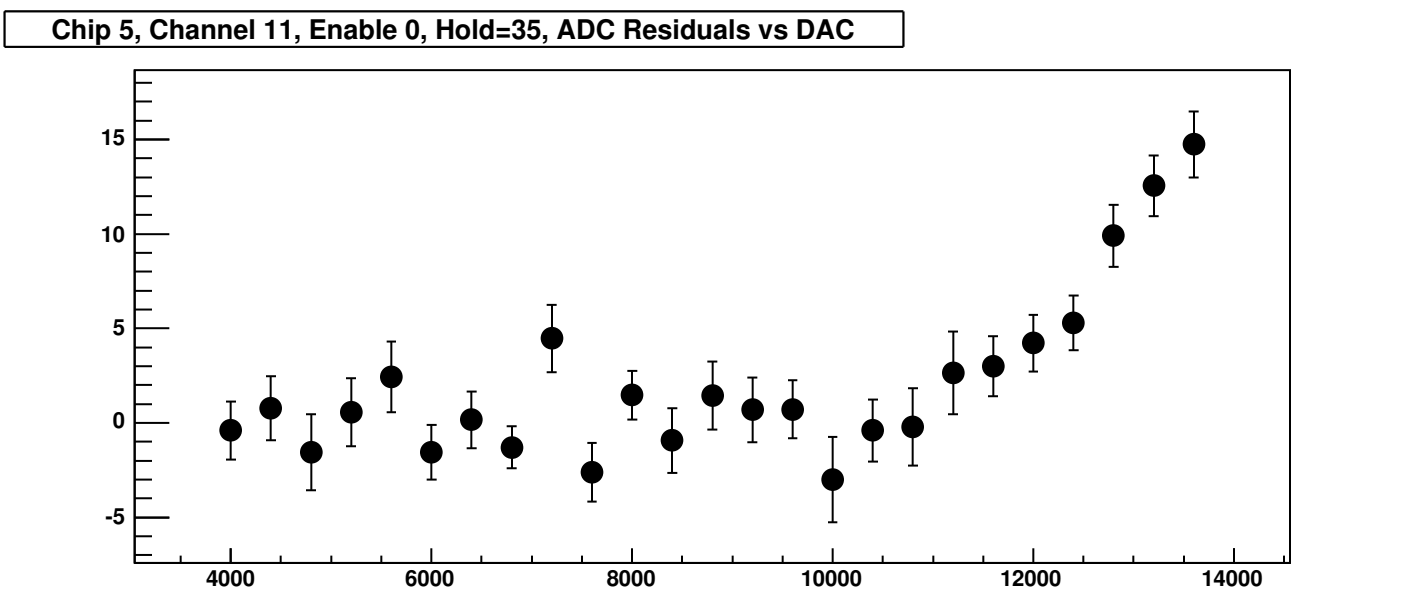

Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.82 / 16
p0 131.8 ± 1.334
p1 0.02239 ± 0.0001705
450
400
350
300
250
200
4000
6000
8000
10000
12000
14000

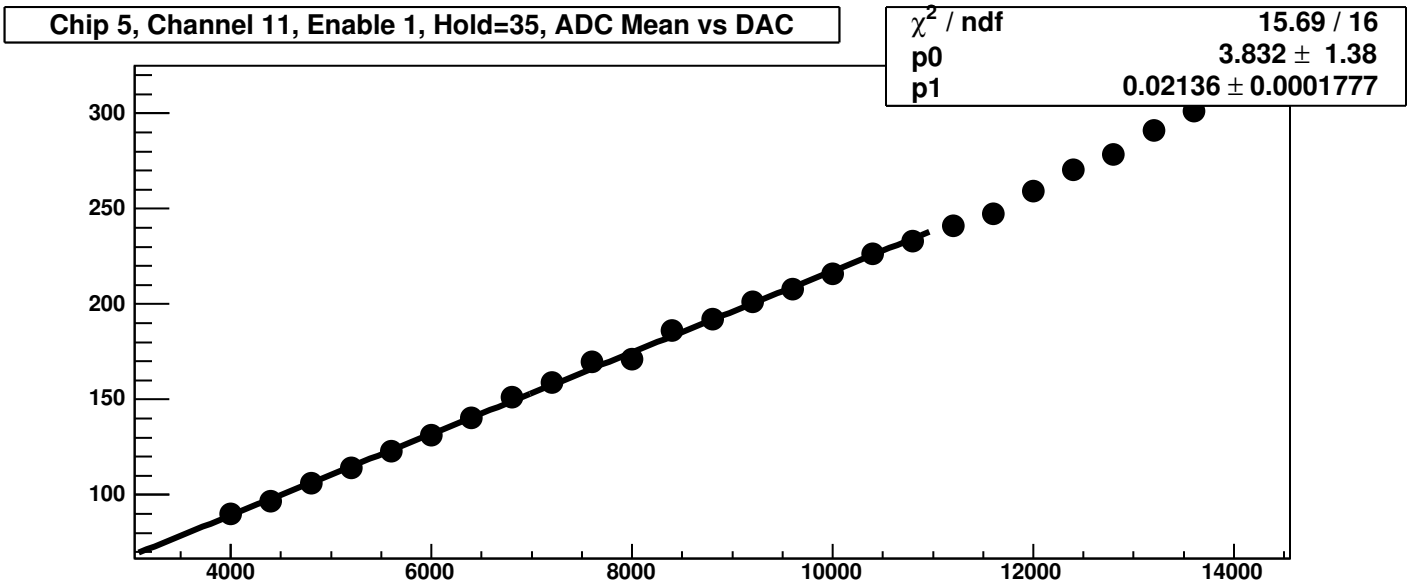

Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

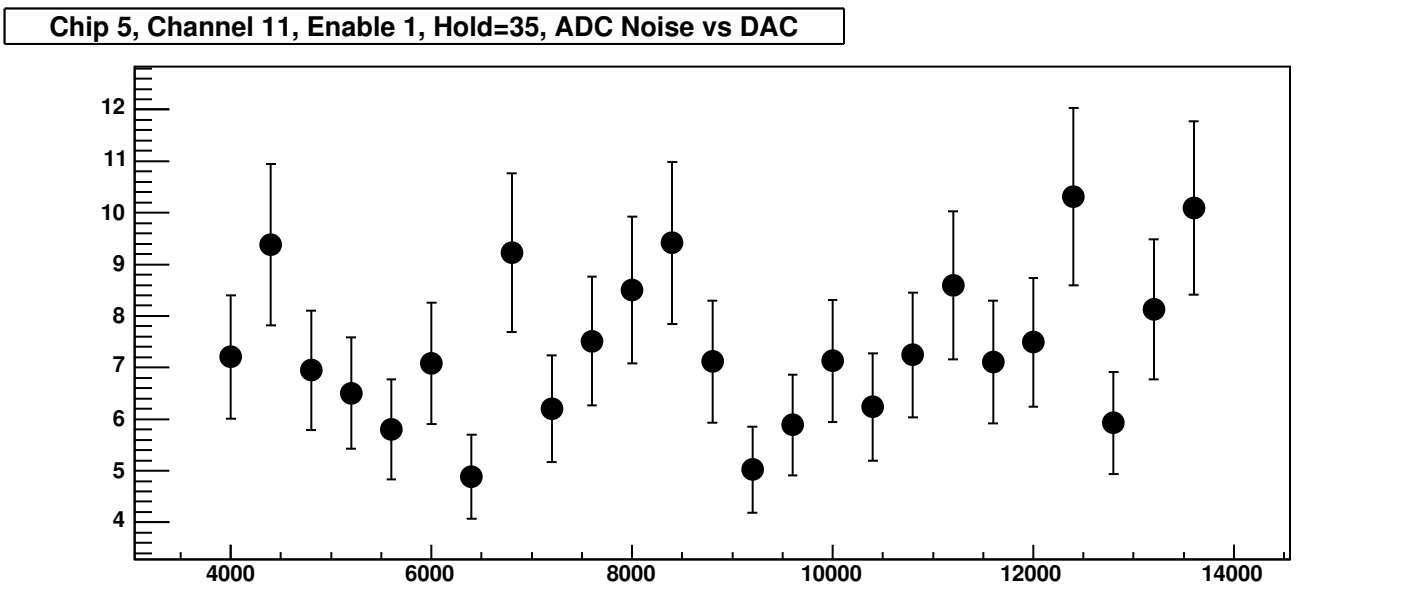

Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

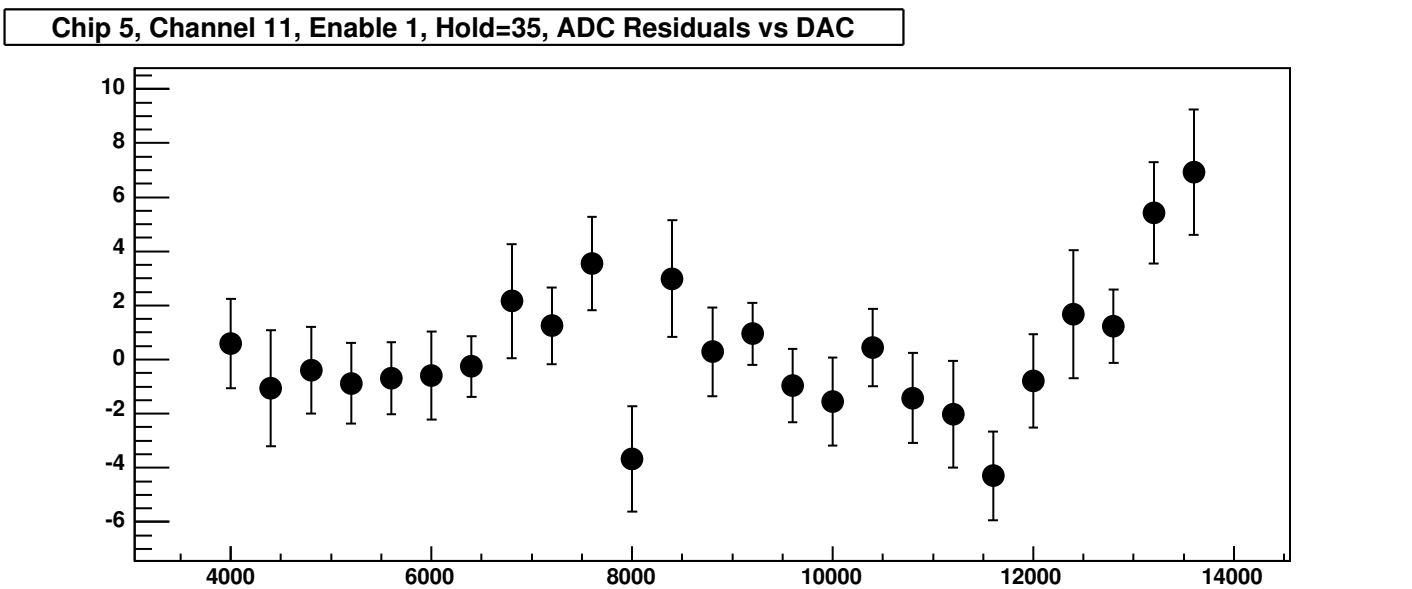

Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18.81 / 16
p0 5.679 ± 1.492
p1 0.02021 ± 0.0001979
300
250
200
150
100
4000
6000
8000
10000
12000
14000

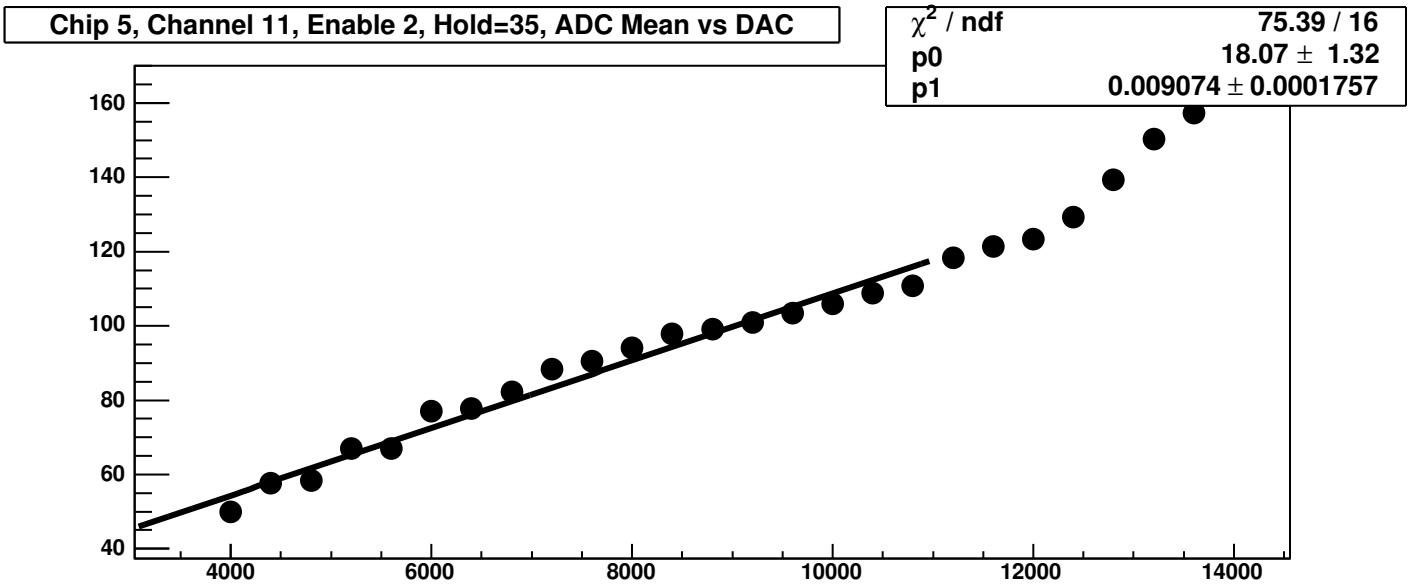

Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

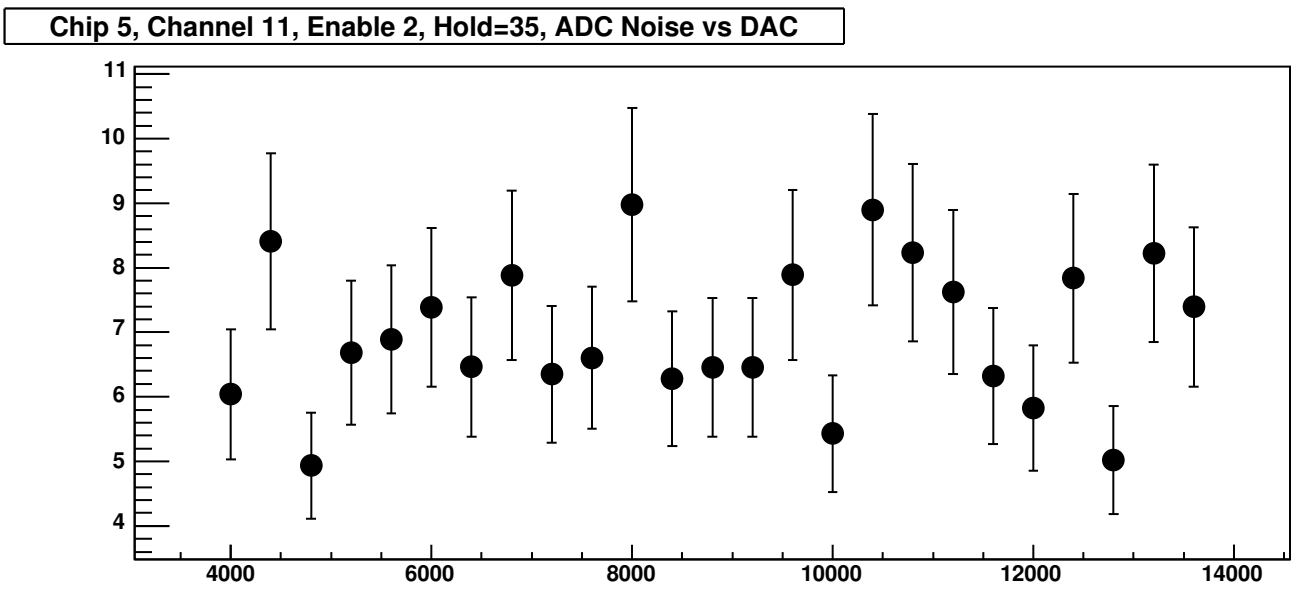

Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

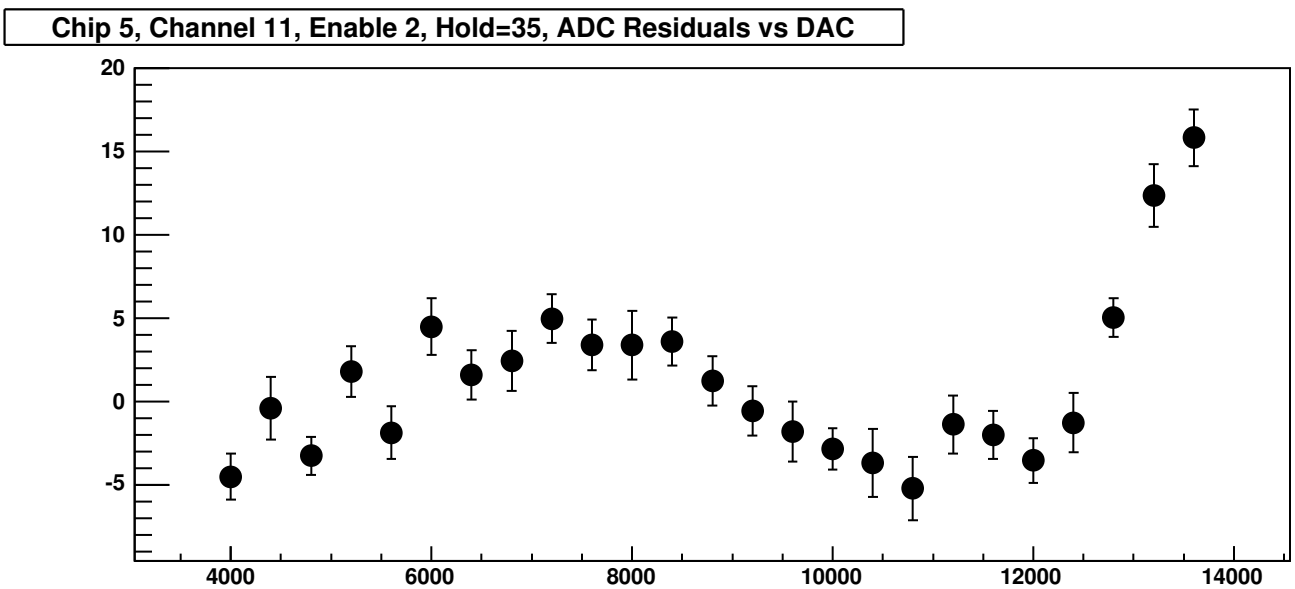

Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 15.69 / 16
p0 3.832 ± 1.38
p1 0.02136 ± 0.0001777
300
250
200
150
100
4000
6000
8000
10000
12000
14000

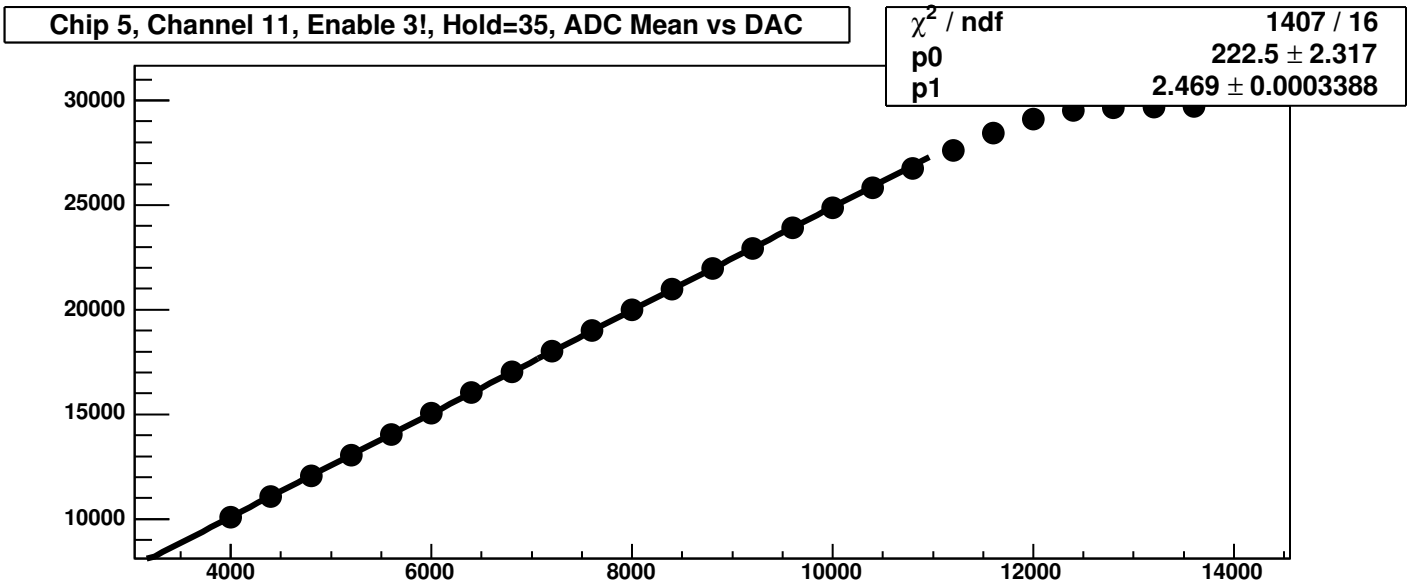

Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

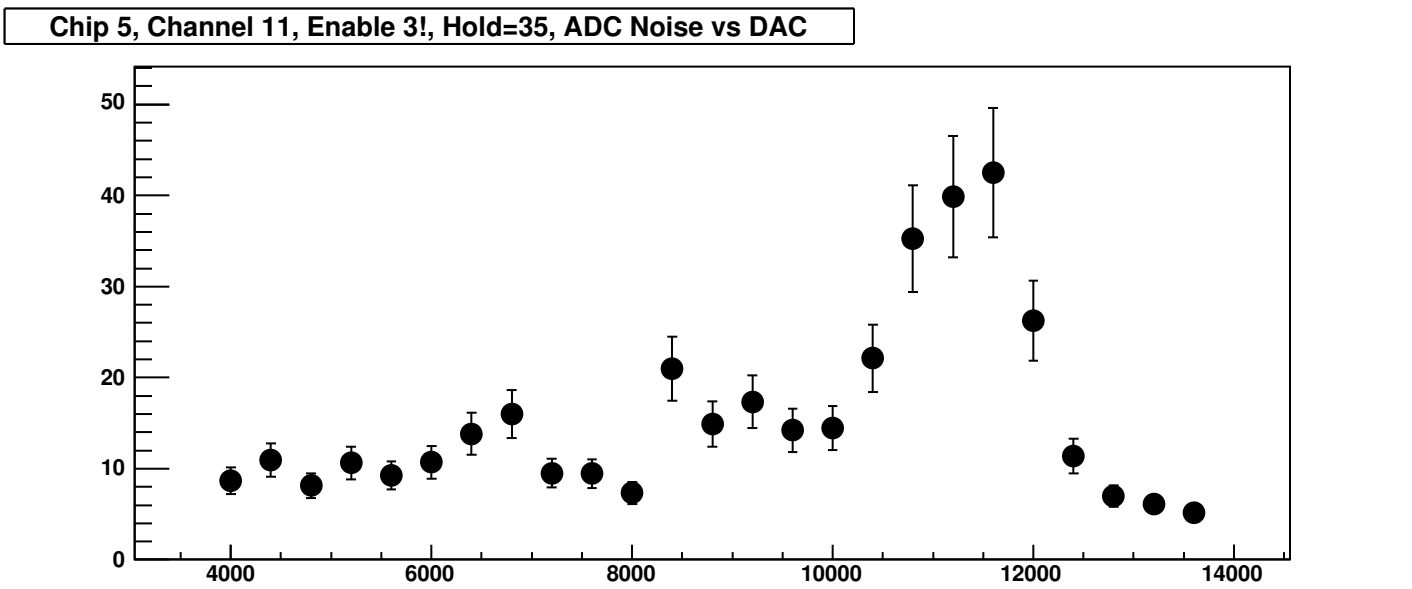

Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

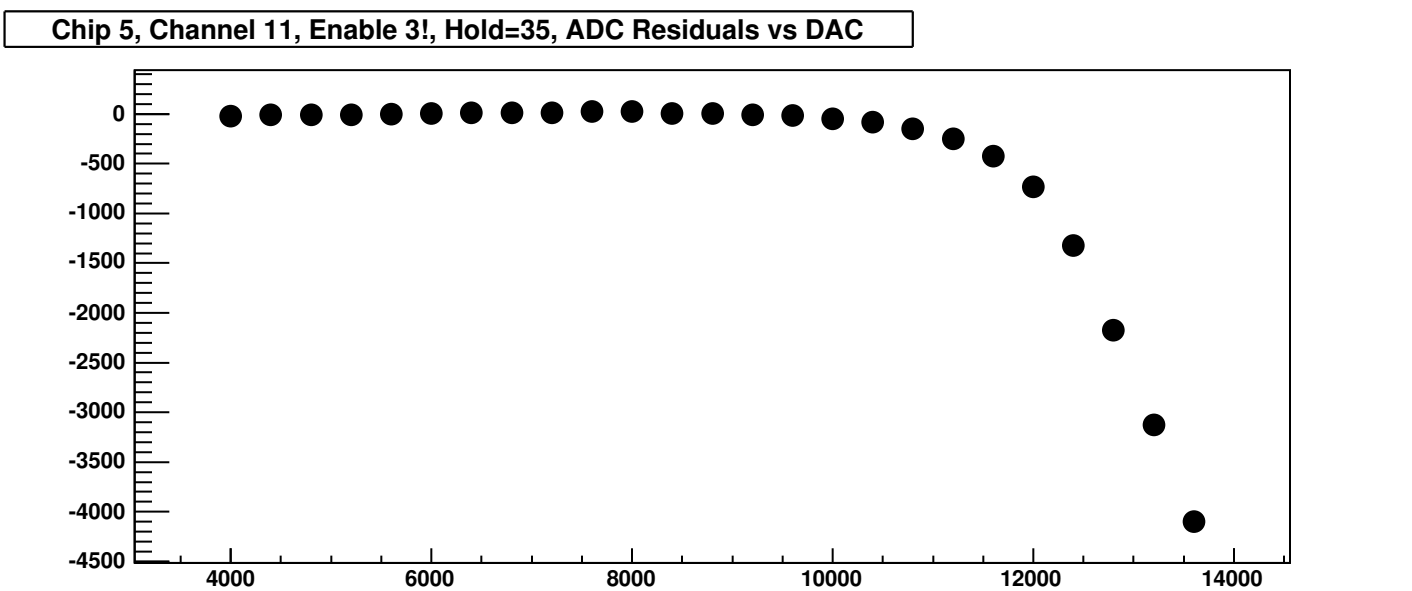

Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 75.39 / 16
p0 18.07 ± 1.32
p1 0.009074 ± 0.0001757
160
140
120
100
80
60
40
4000
6000
8000
10000
12000
14000

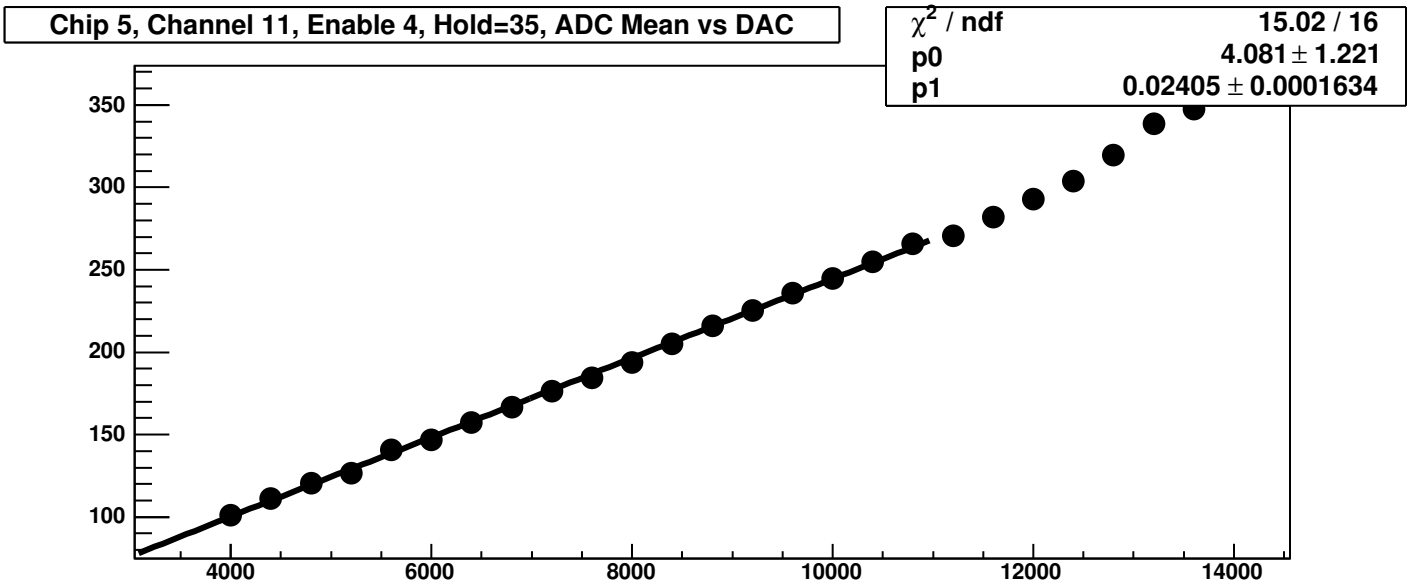

Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

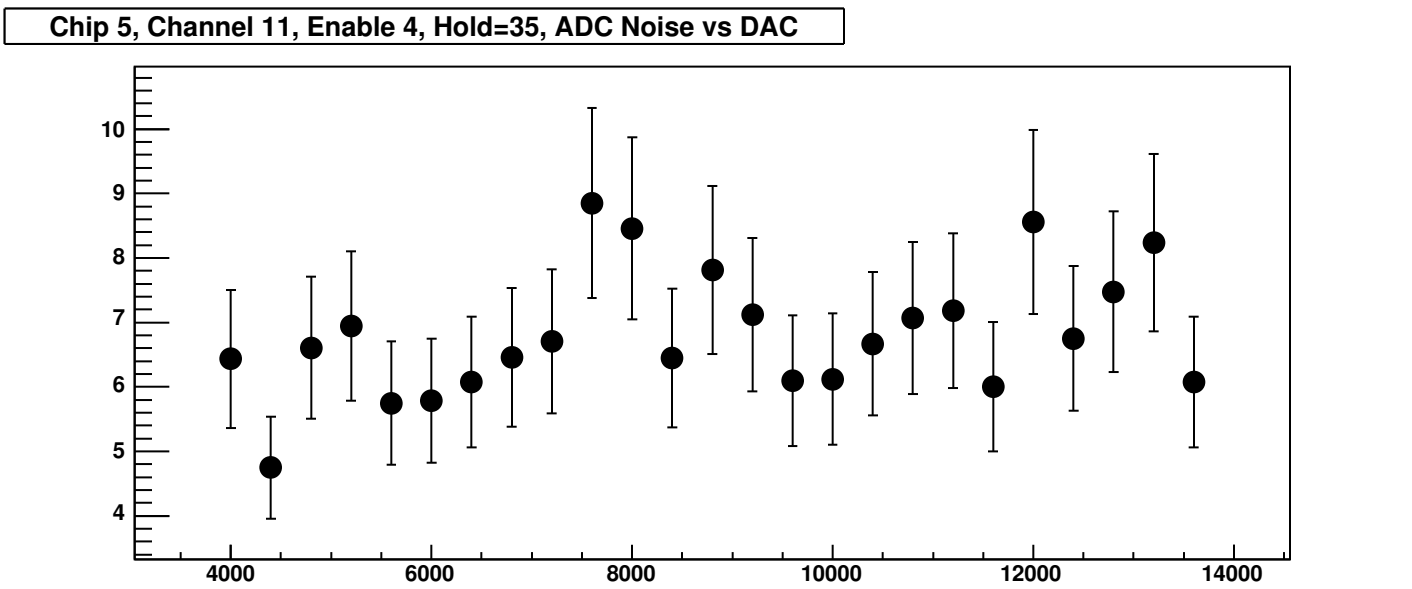

Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

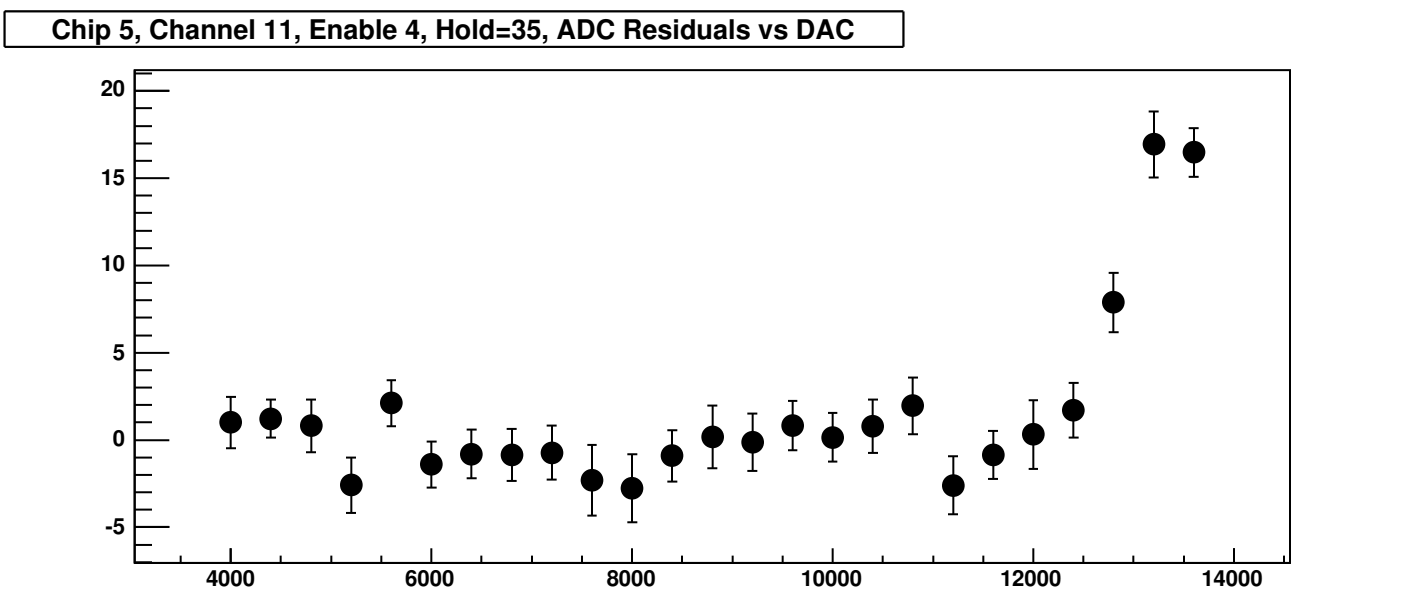

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 1407 / 16
p0 222.5 ± 2.317
p1 2.469 ± 0.0003388
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

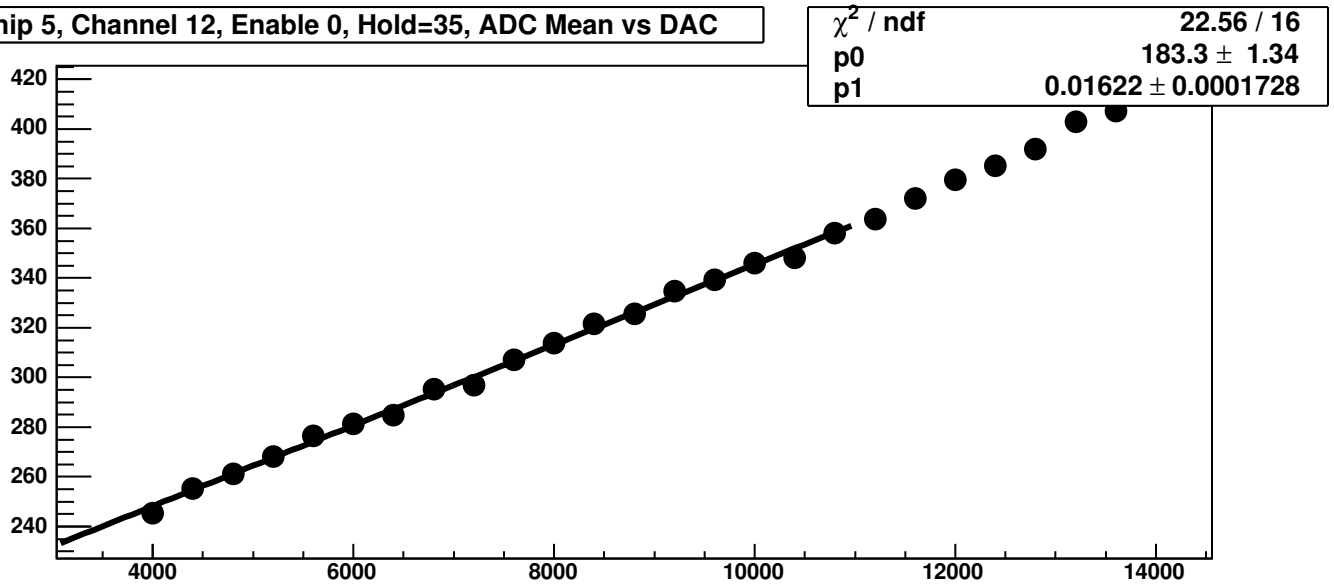

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.02 / 16
p0 4.081 ± 1.221
p1 0.02405 ± 0.0001634
350
300
250
200
150
100
4000
6000
8000
10000
12000
14000

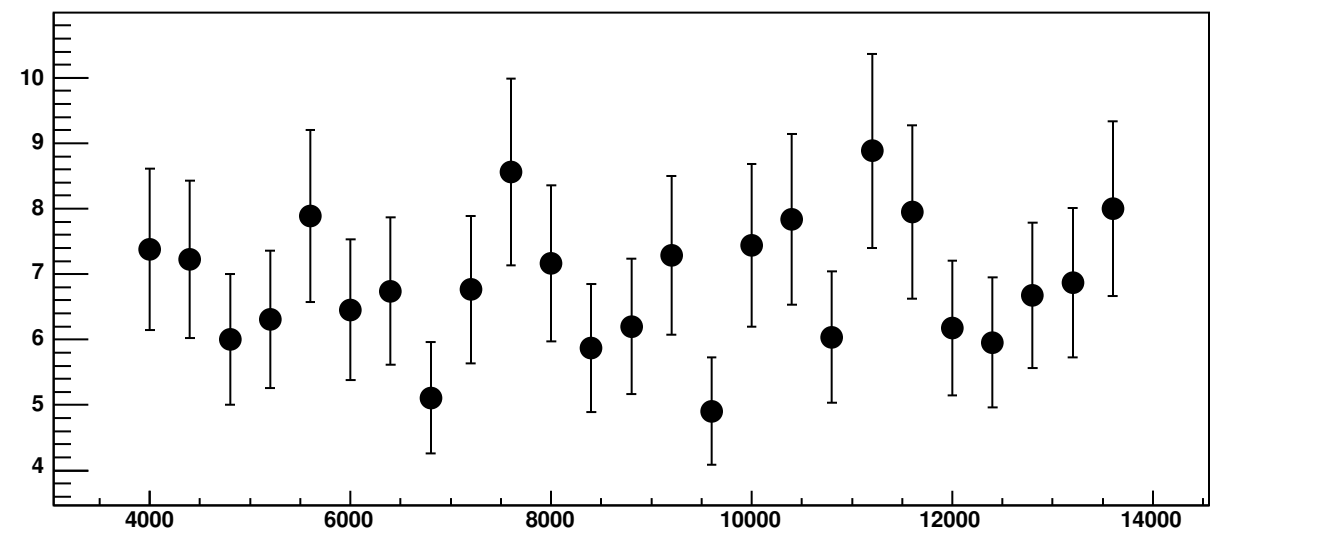

Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

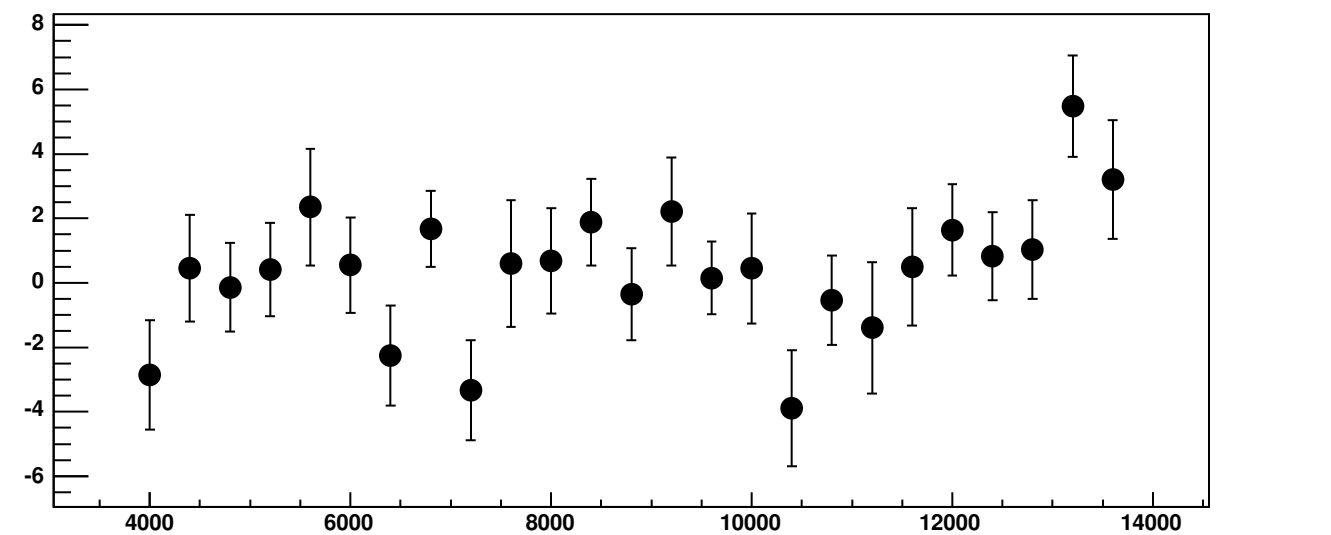
Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

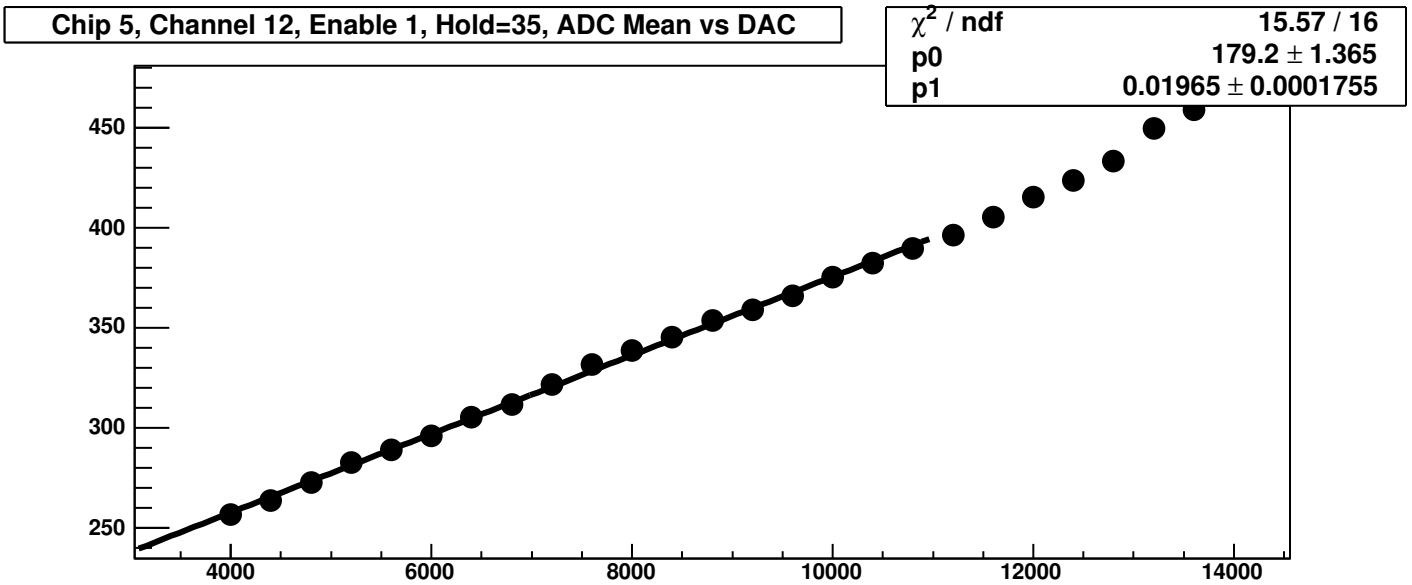


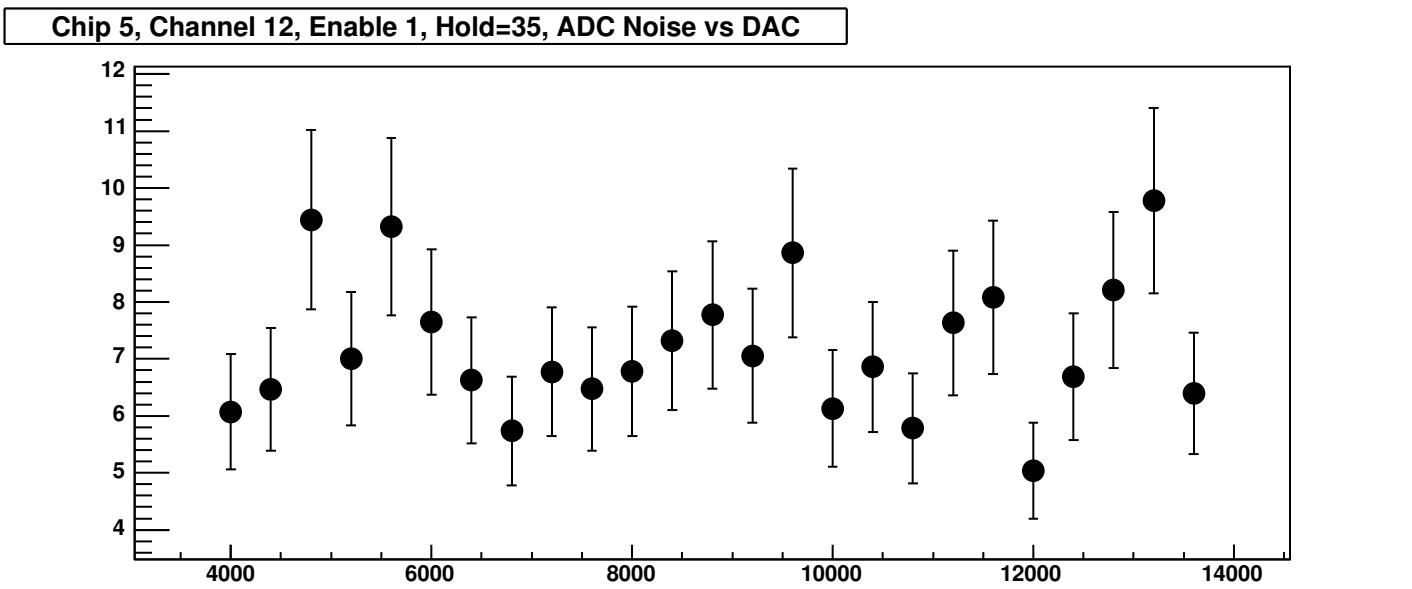
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

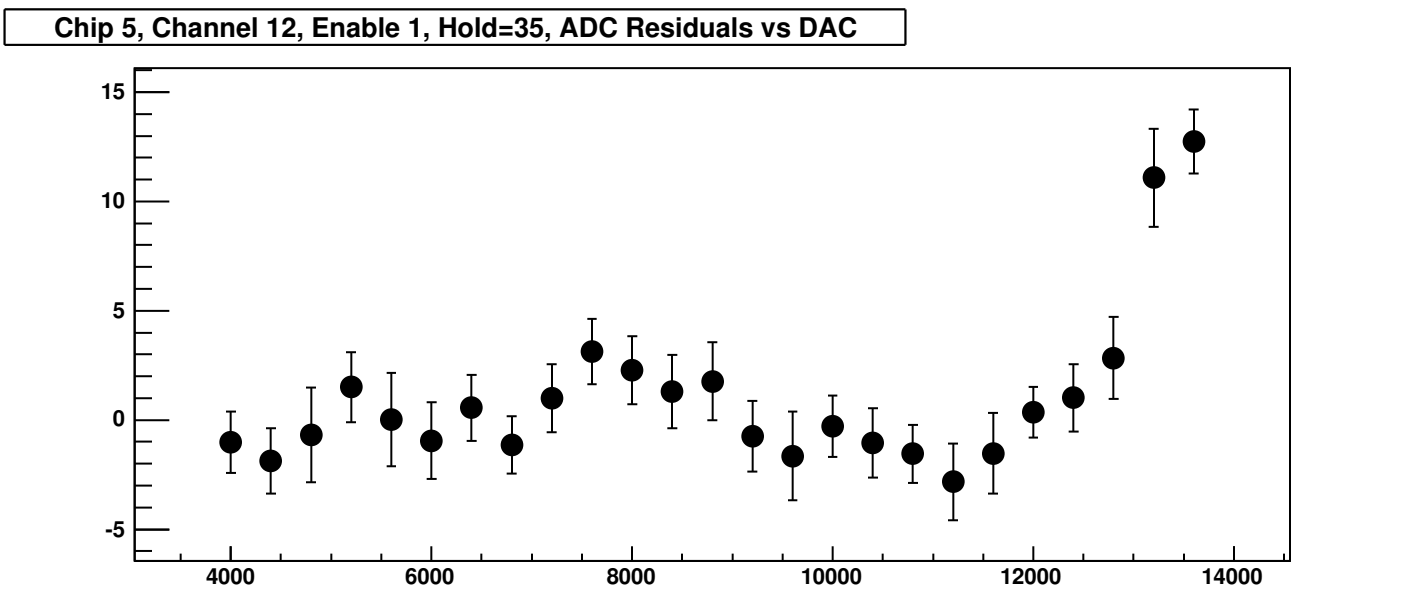


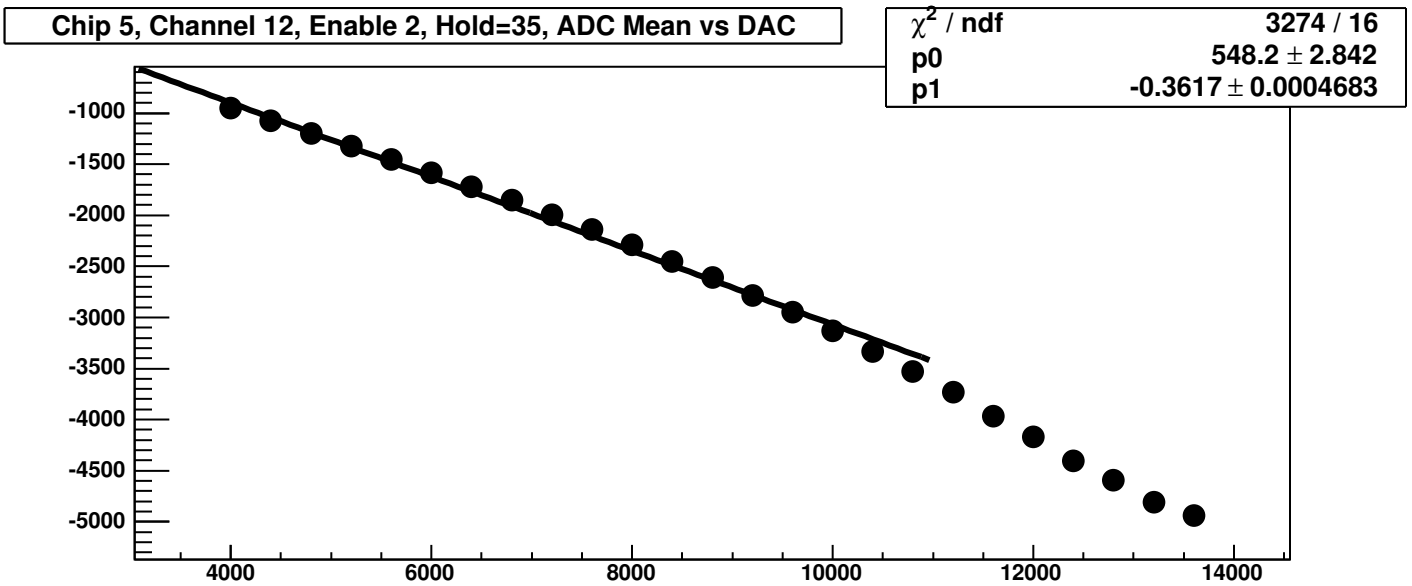
Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

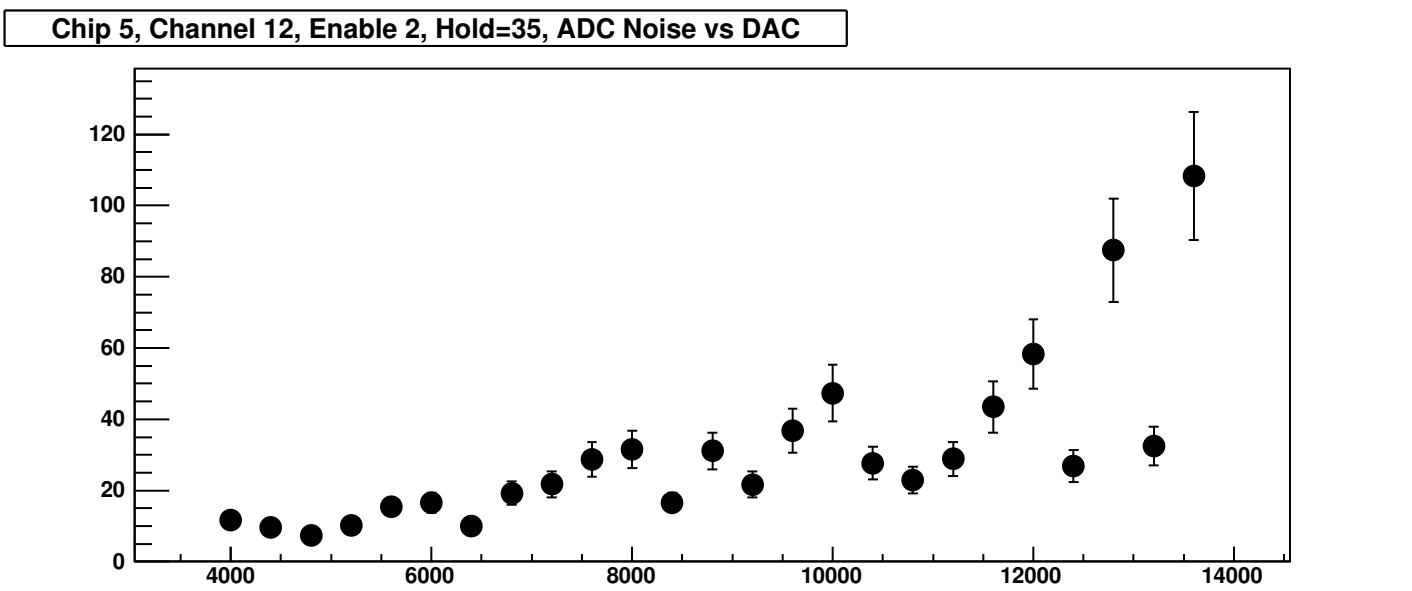


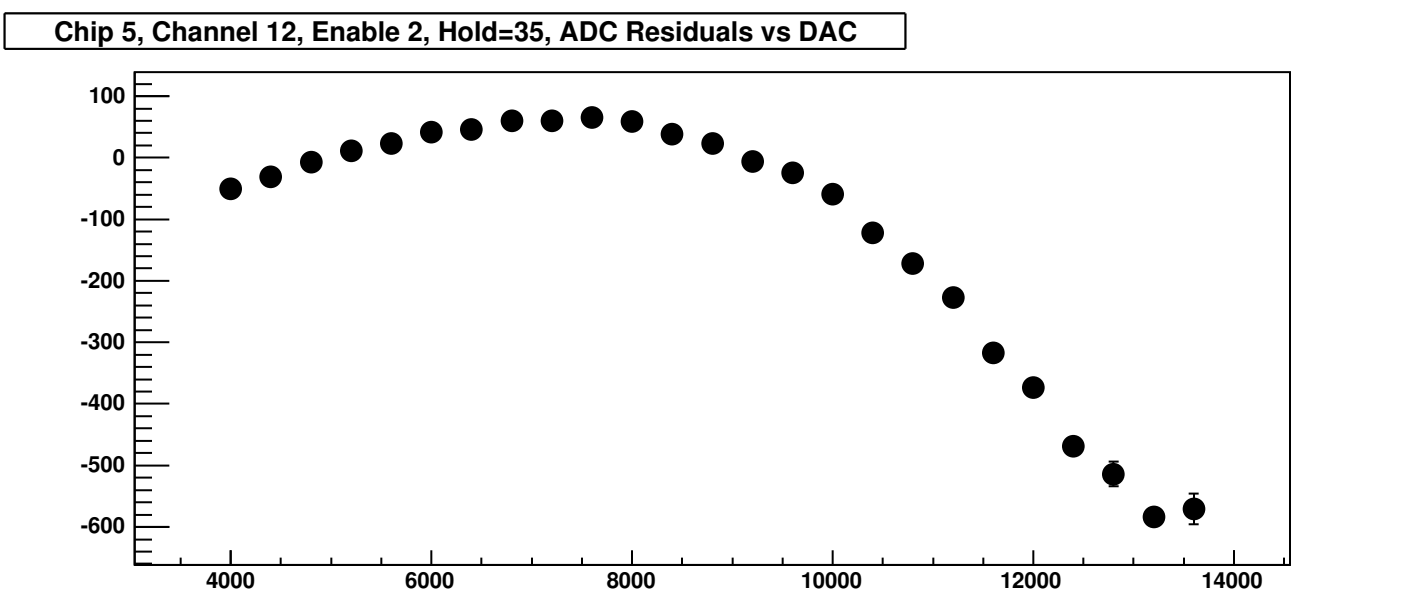

Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 15.57 / 16
p0 179.2 ± 1.365
p1 0.01965 ± 0.0001755
450
400
350
300
250
4000
6000
8000
10000
12000
14000

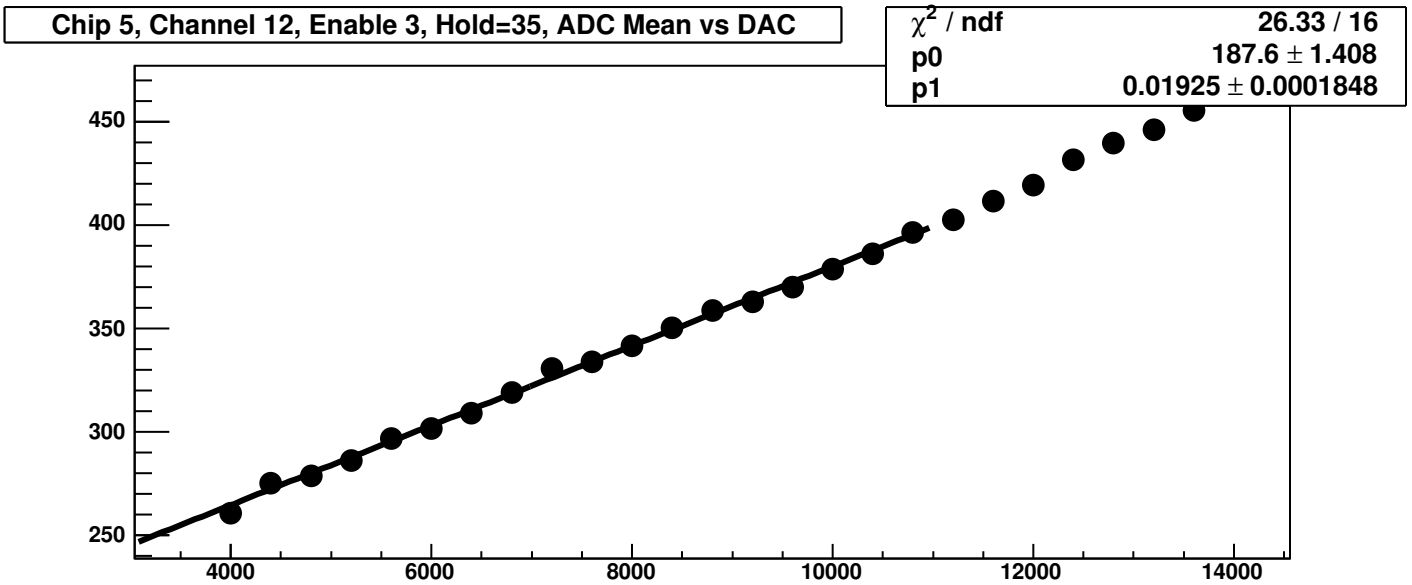

Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

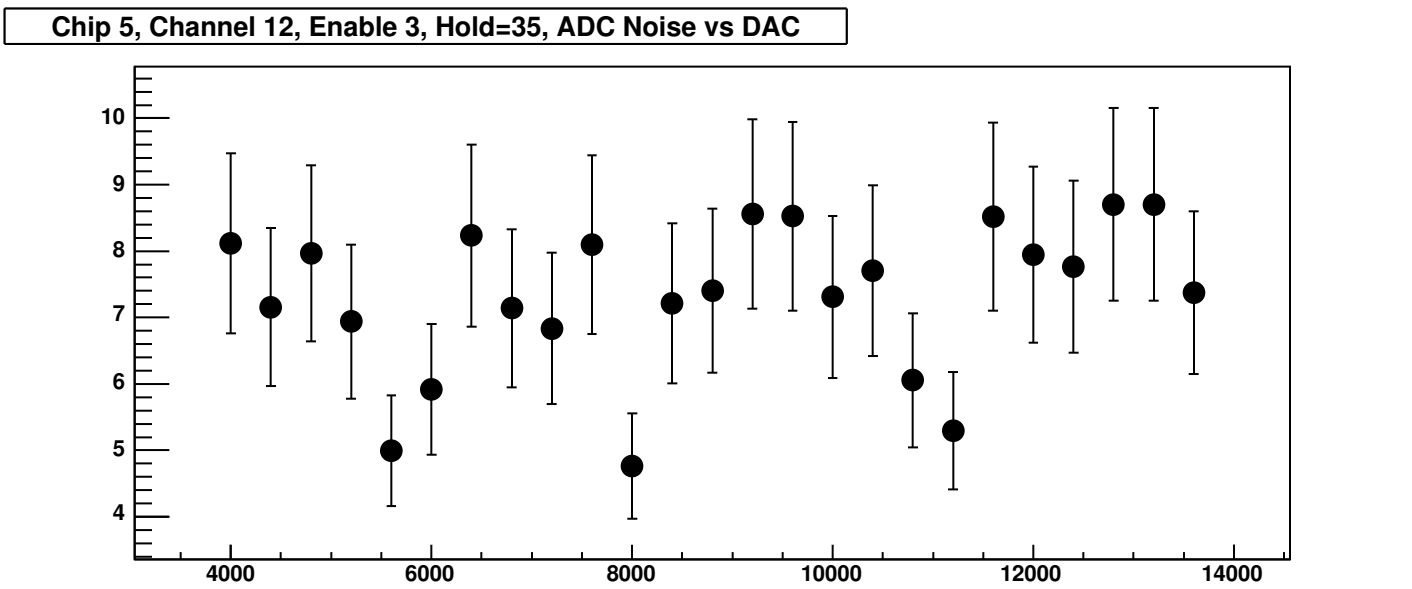

Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

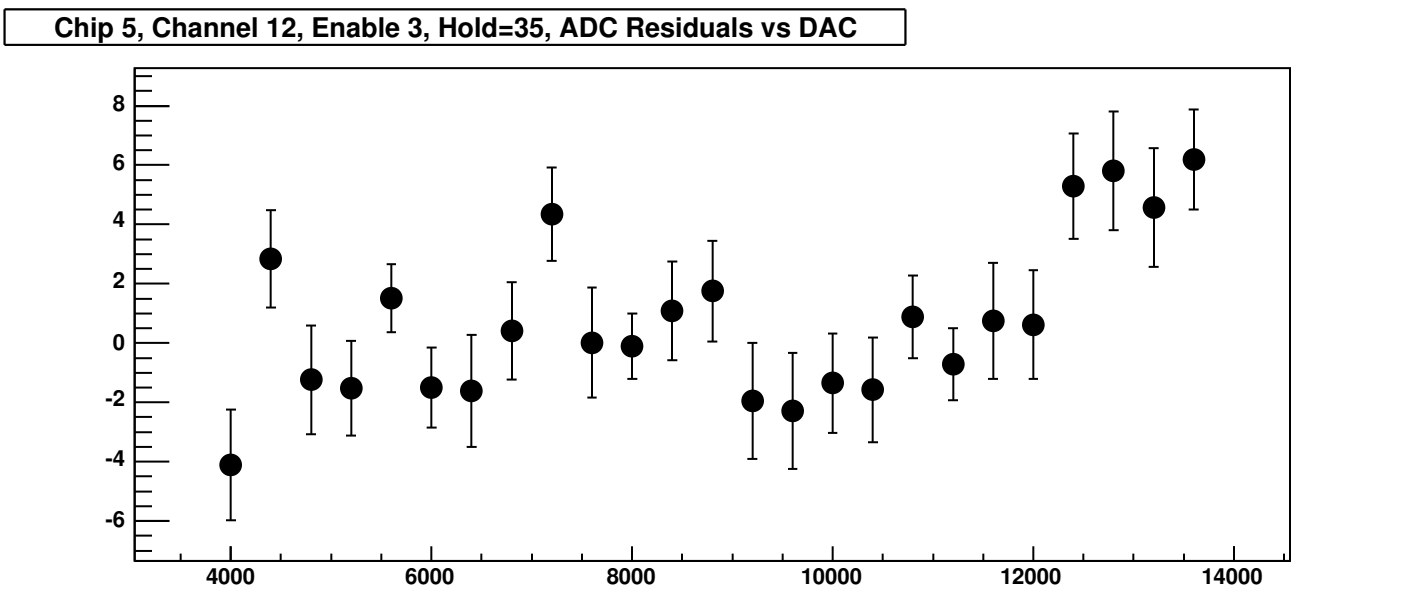

Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3274 / 16
p0 548.2 ± 2.842
p1 -0.3617 ± 0.0004683
-1000
-1500
-2000
-2500
-3000
-3500
-4000
-4500
-5000
4000
6000
8000
10000
12000
14000

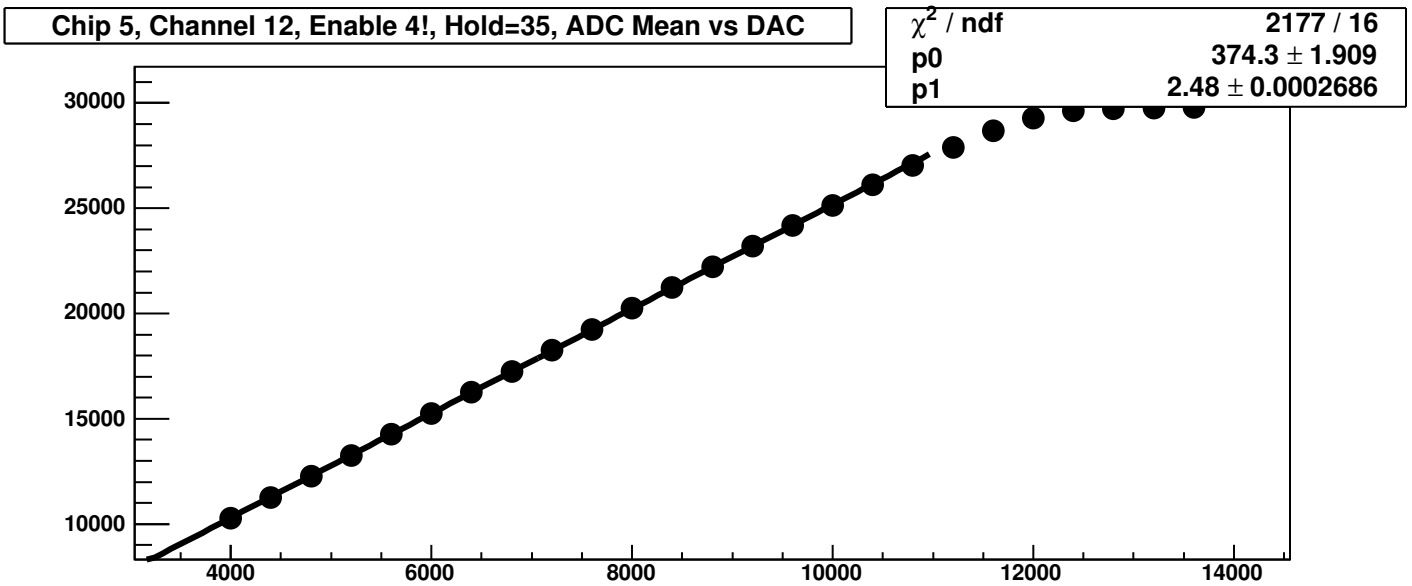

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000

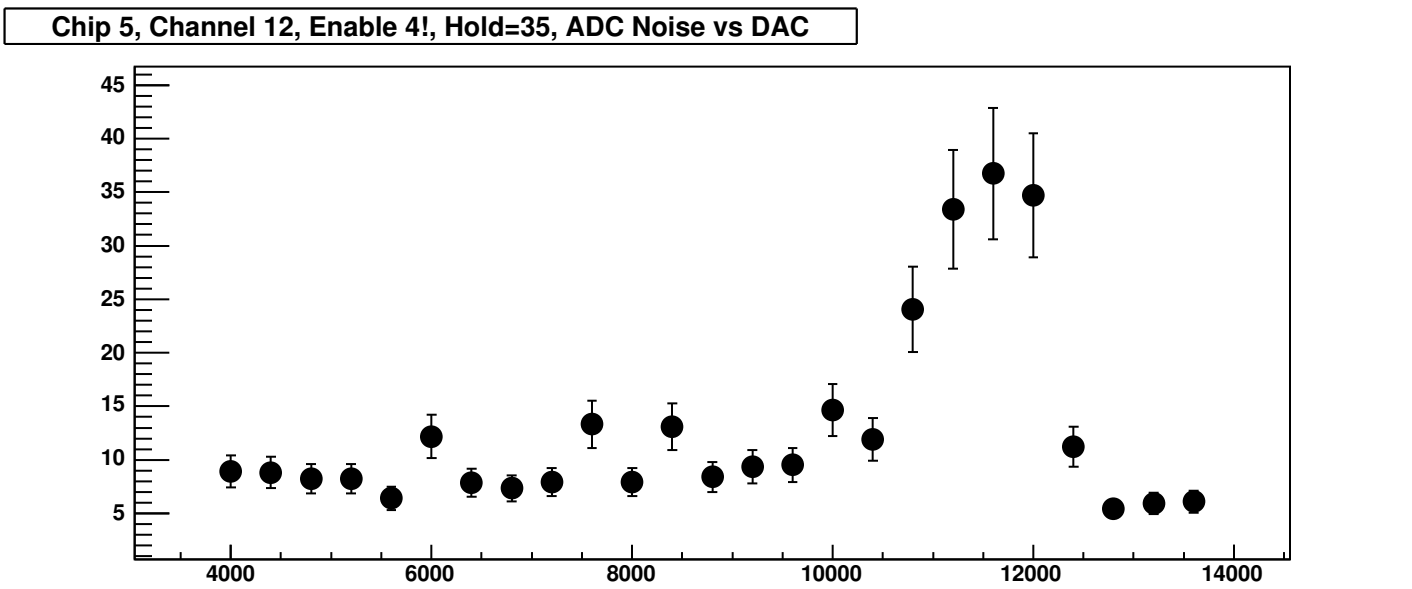

Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

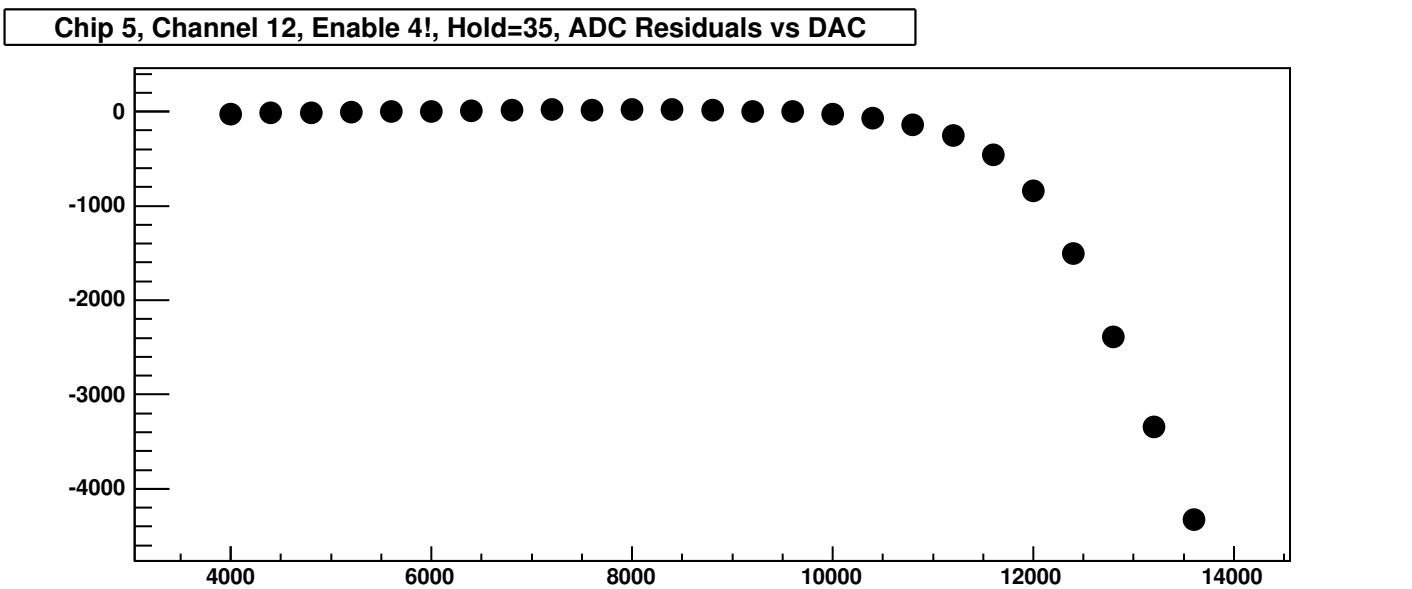

Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.33 / 16
p0 187.6 ± 1.408
p1 0.01925 ± 0.0001848
450
400
350
300
250
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

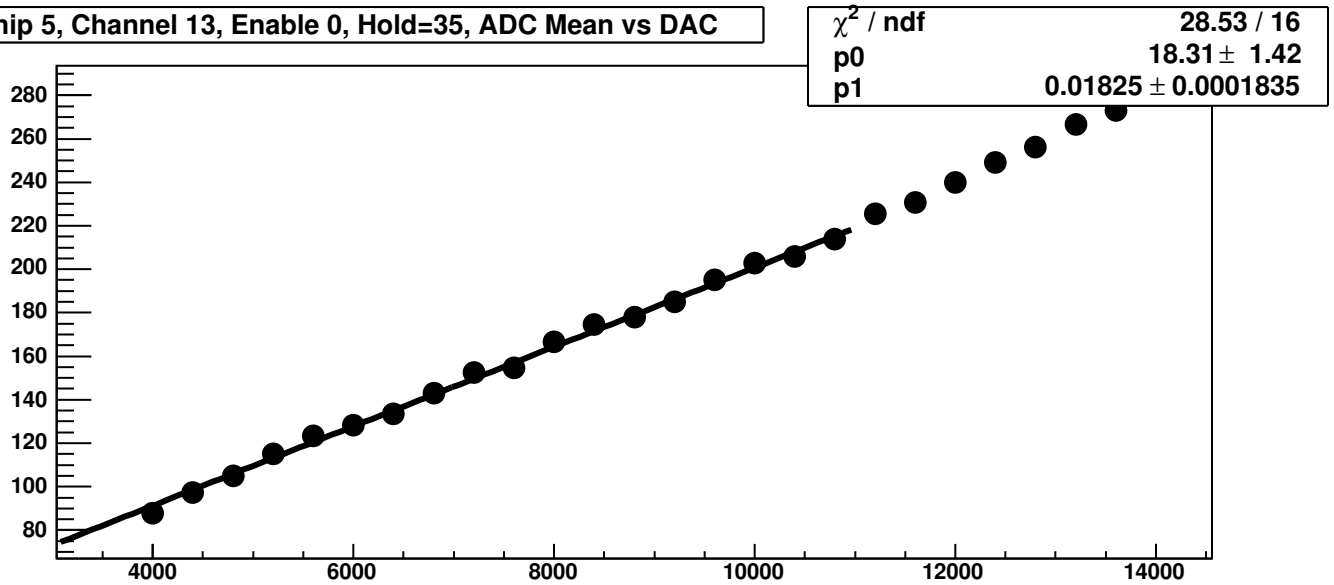

Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 2177 / 16
p0 374.3 ± 1.909
p1 2.48 ± 0.0002686
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

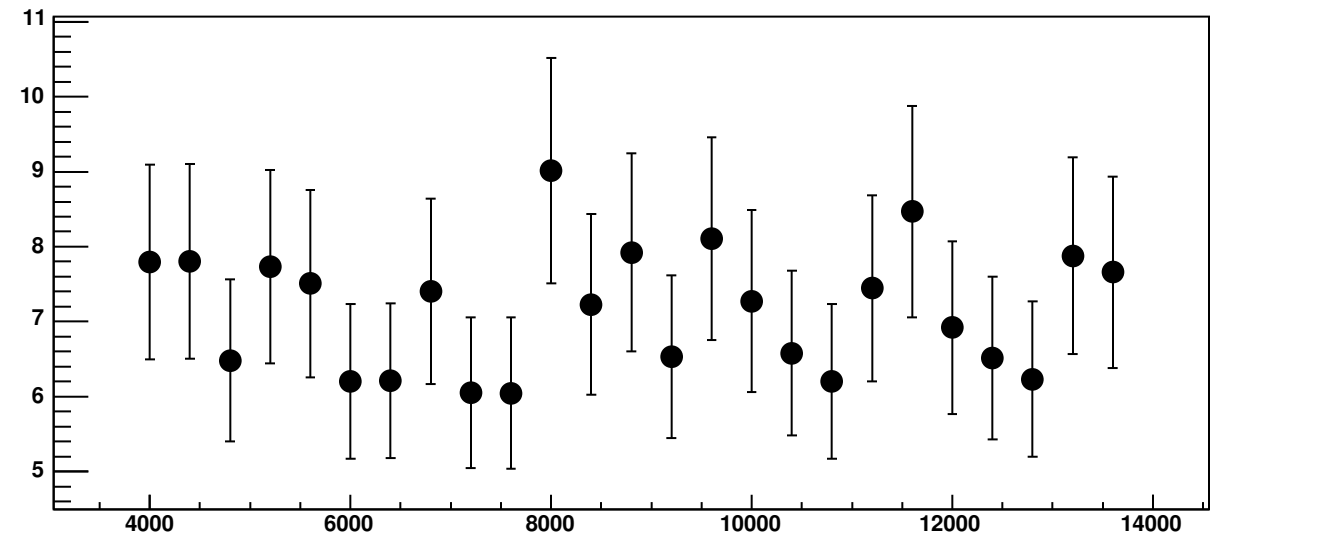

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
4000
6000
8000
10000
12000
14000

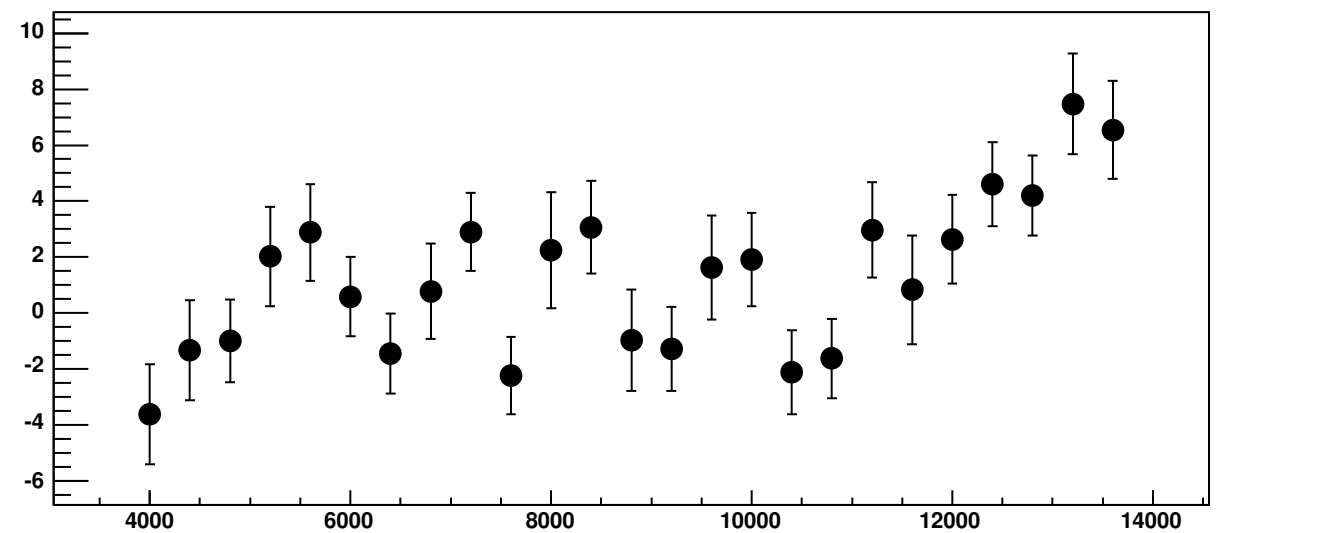
Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

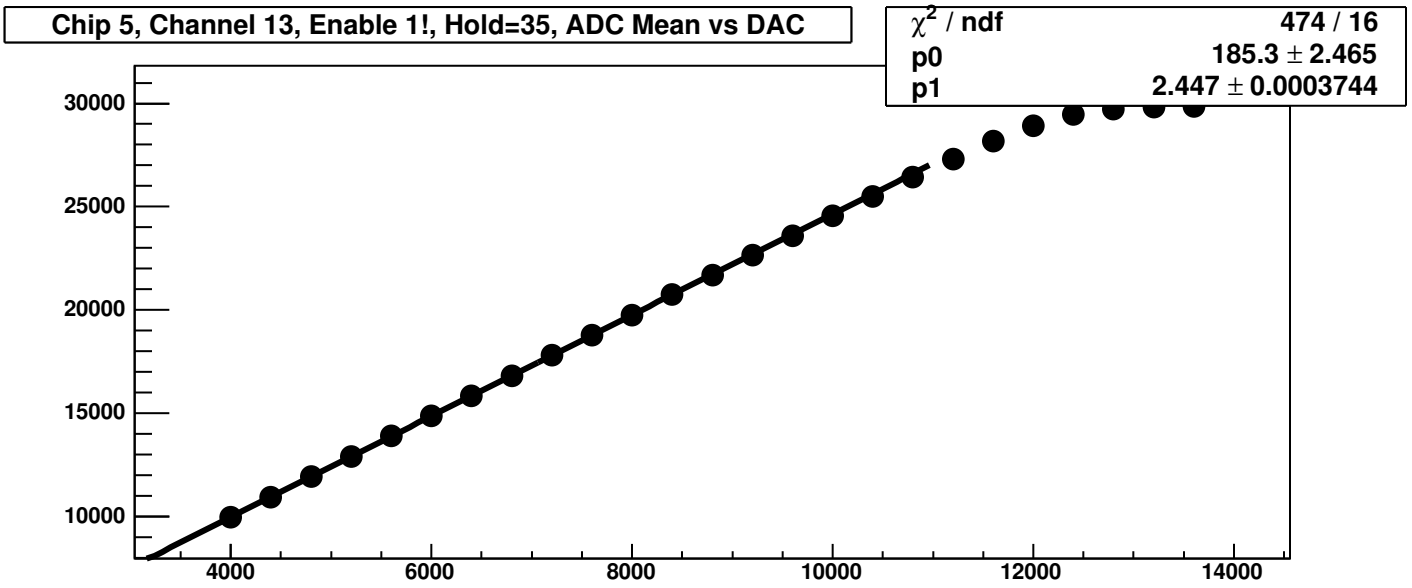


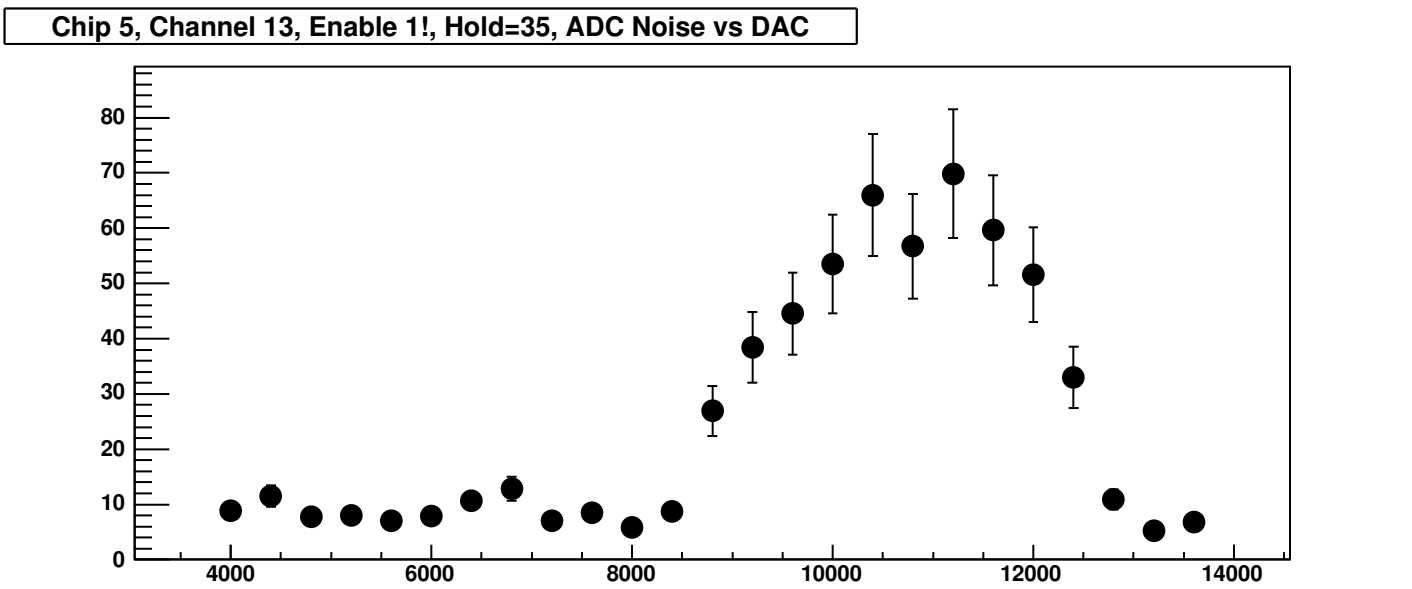
Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

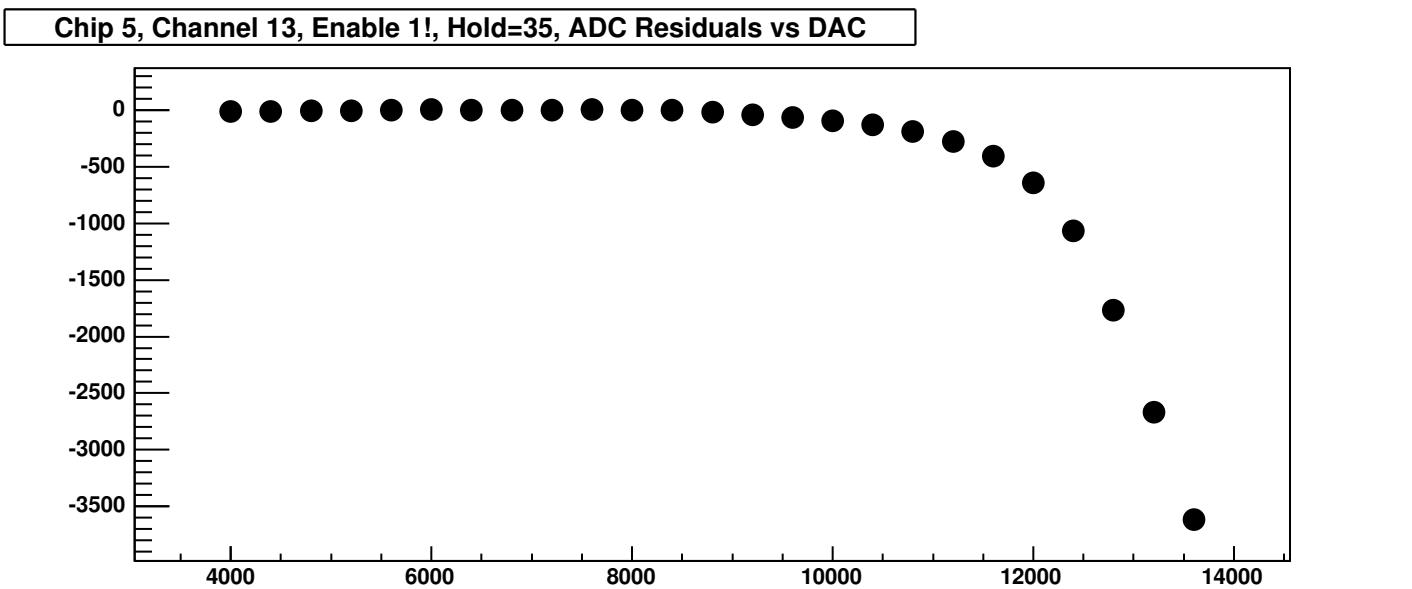


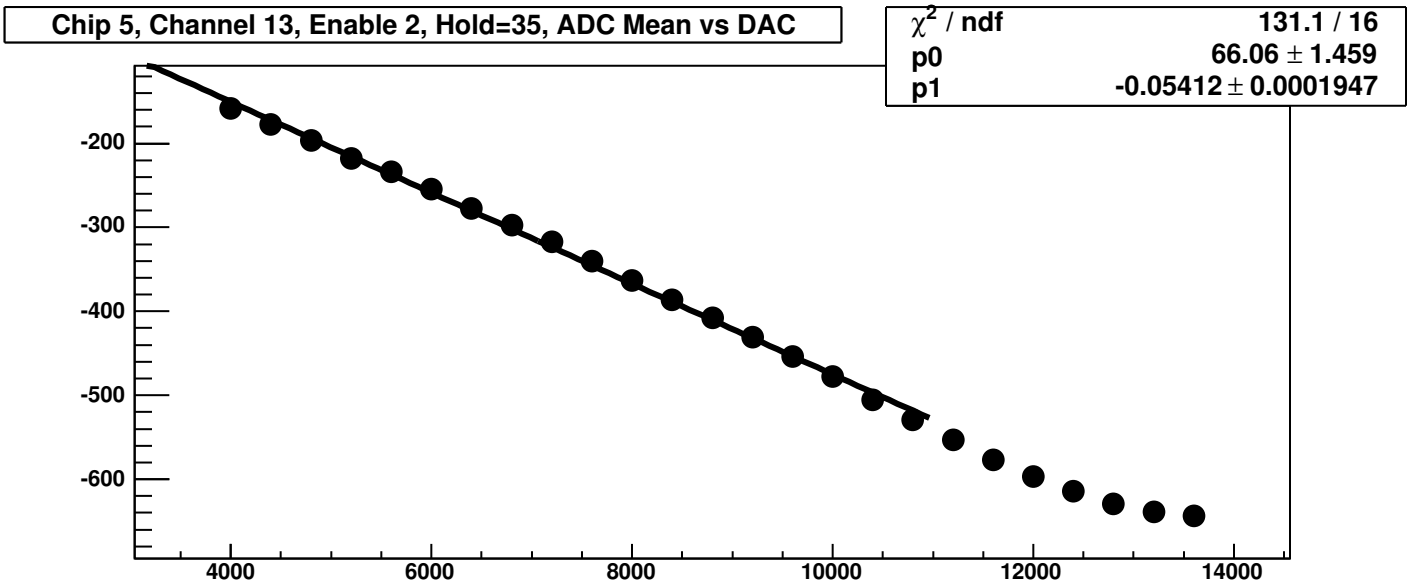
Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

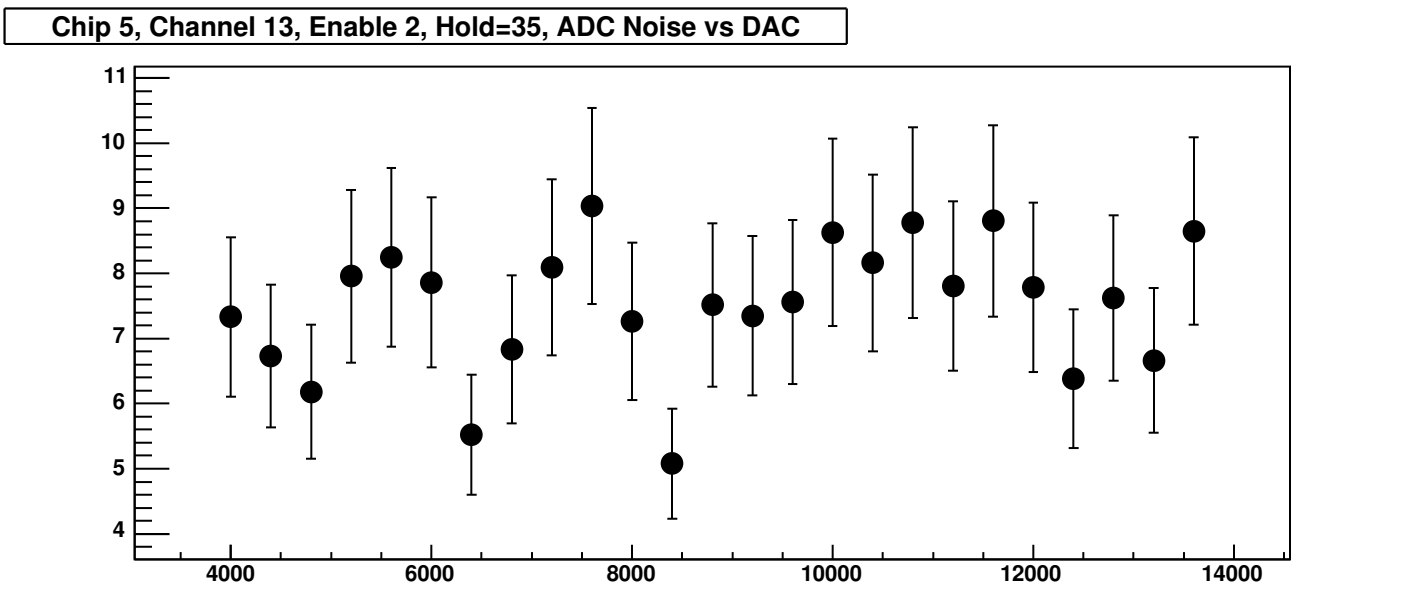


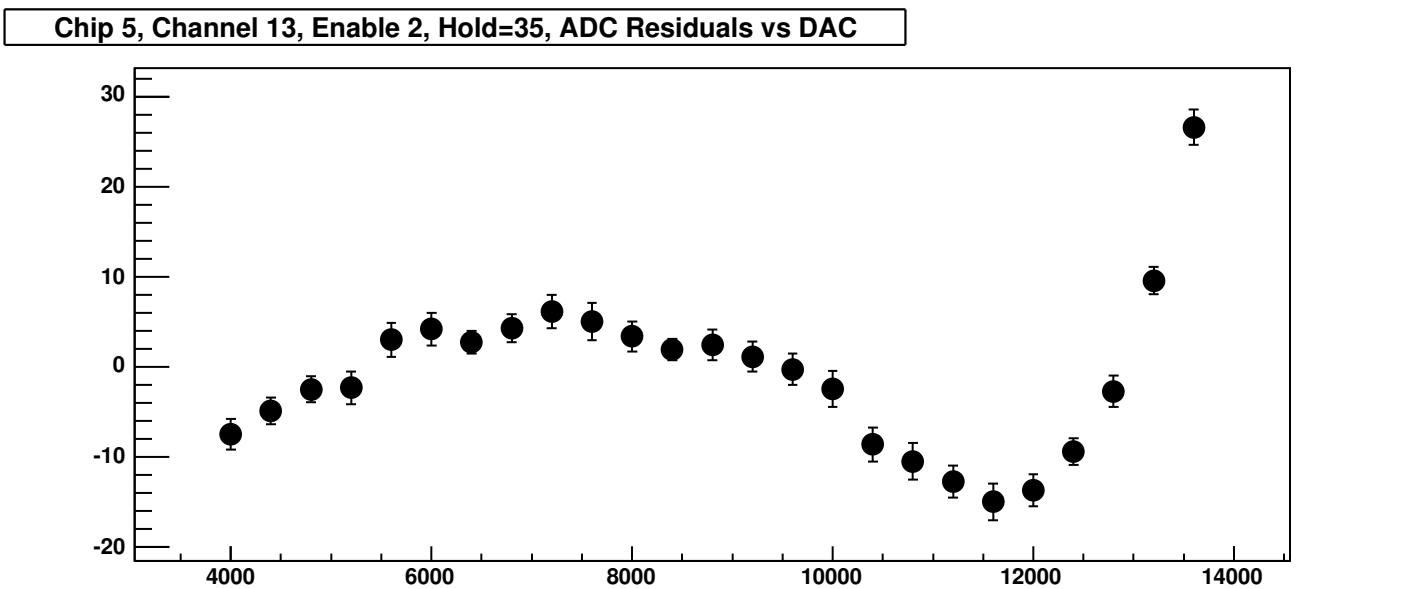

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 474 / 16
p0 185.3 ± 2.465
p1 2.447 ± 0.0003744
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
4000
6000
8000
10000
12000
14000

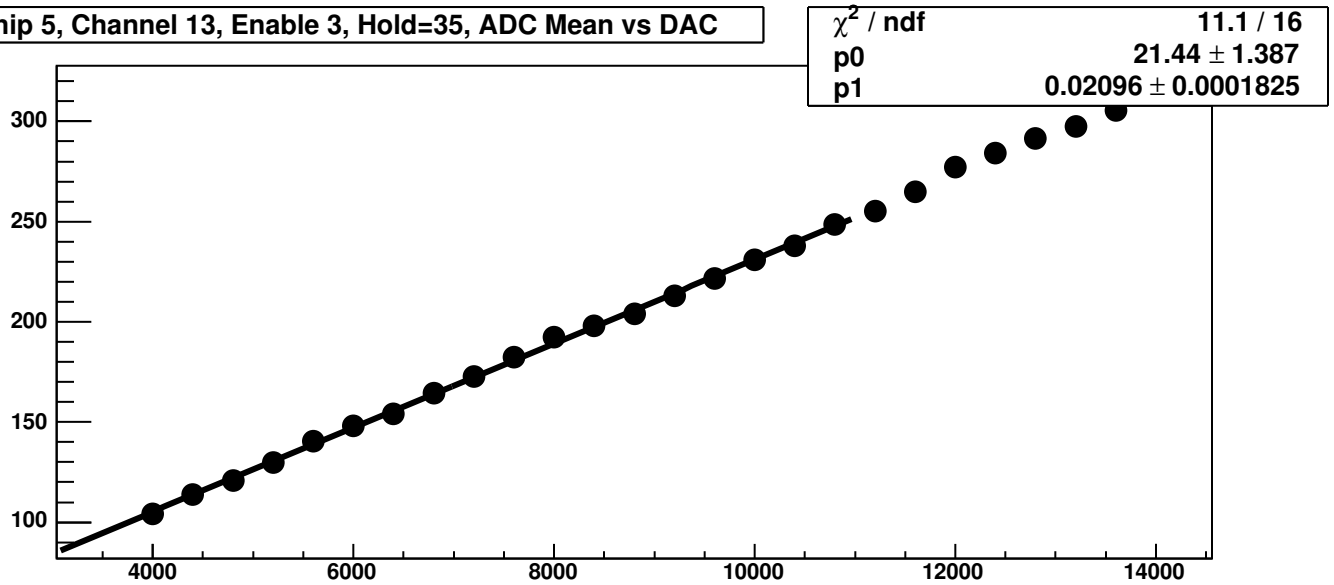

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000


Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 131.1 / 16
p0 66.06 ± 1.459
p1 -0.05412 ± 0.0001947
-200
-300
-400
-500
-600
4000
6000
8000
10000
12000
14000

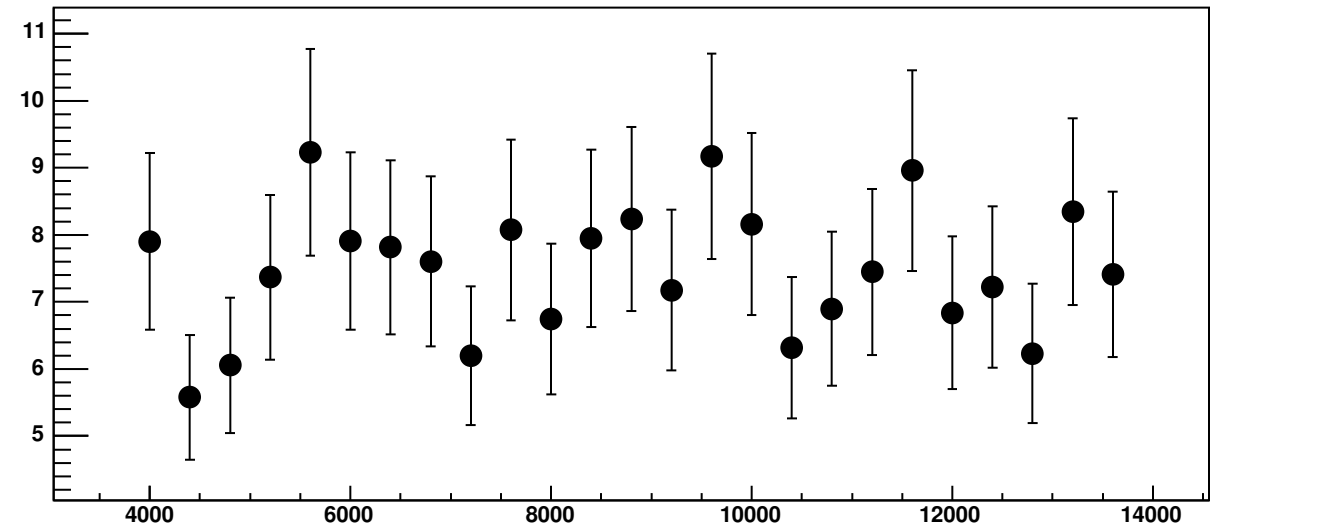

Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
4000
6000
8000
10000
12000
14000

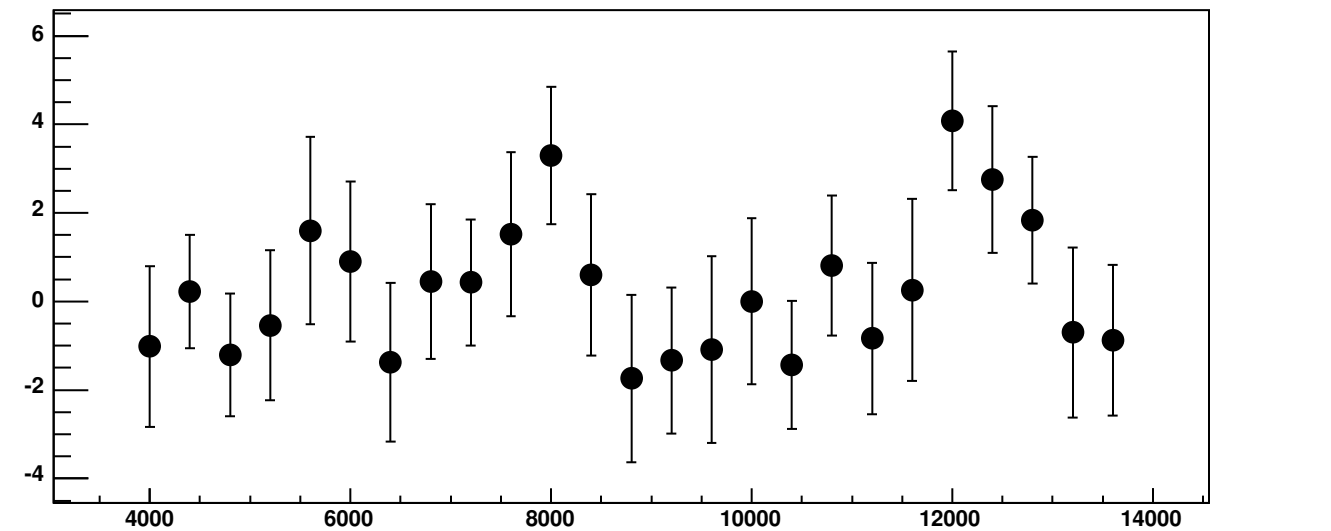
Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

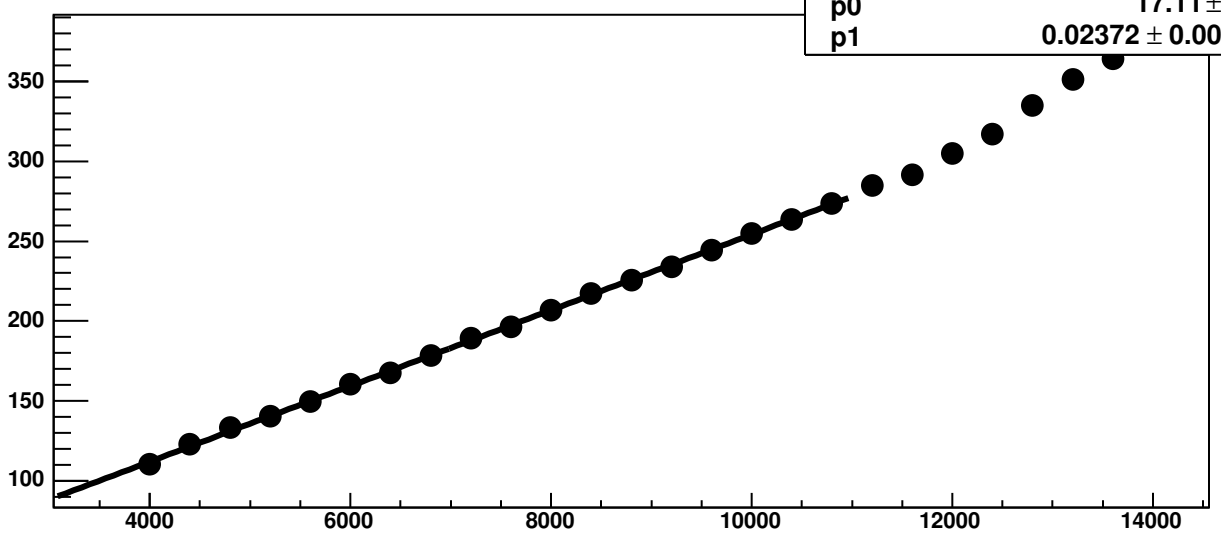


Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

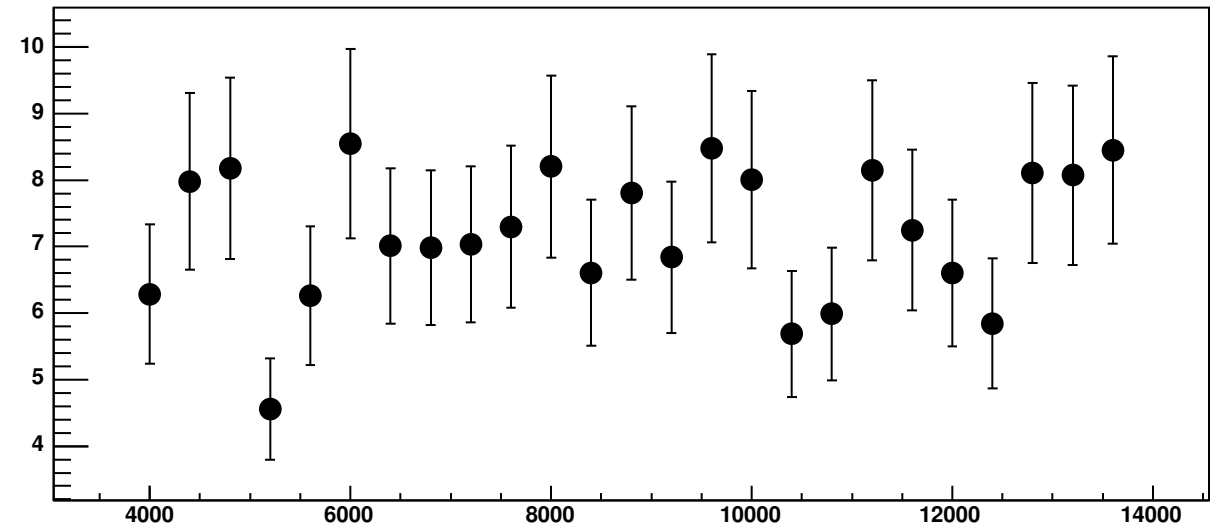


Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

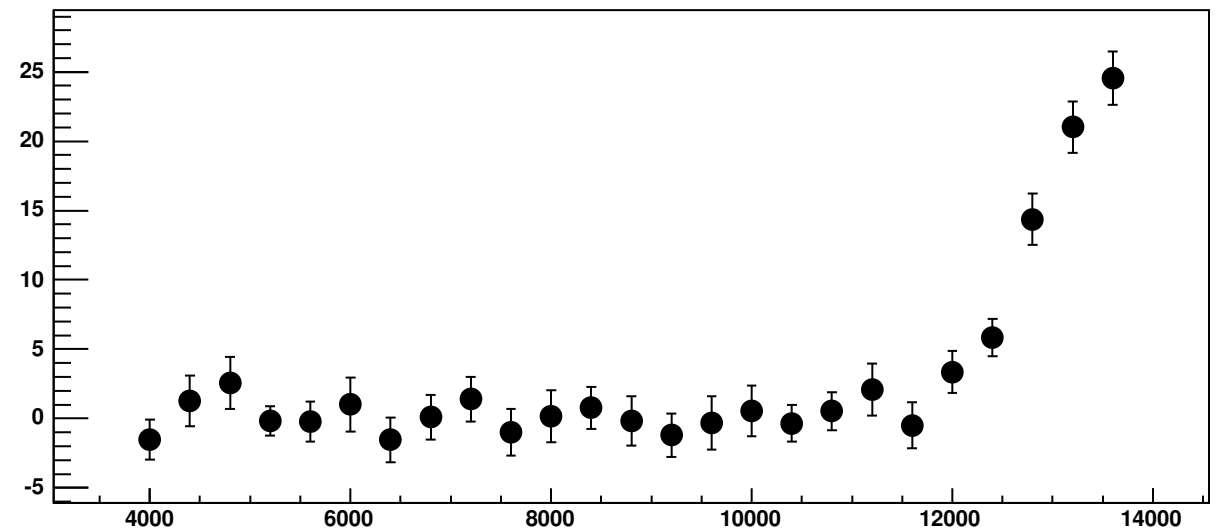
χ^2 / ndf	6.986 / 16
p0	17.11 ± 1.305
p1	0.02372 ± 0.0001706

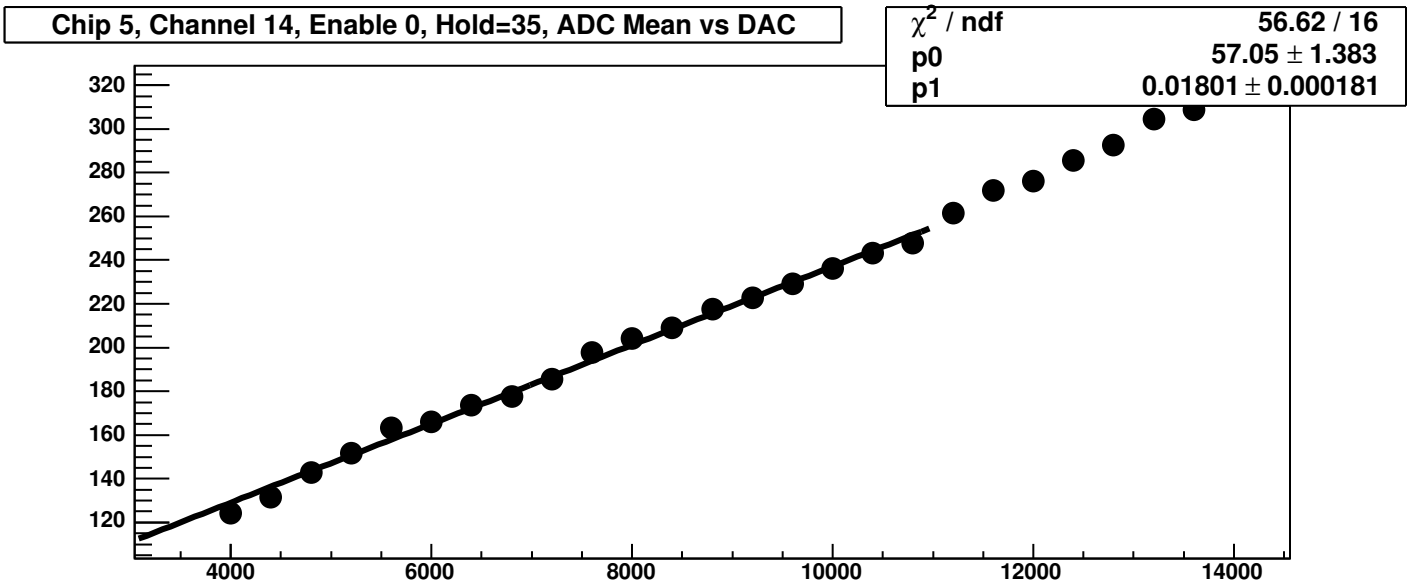


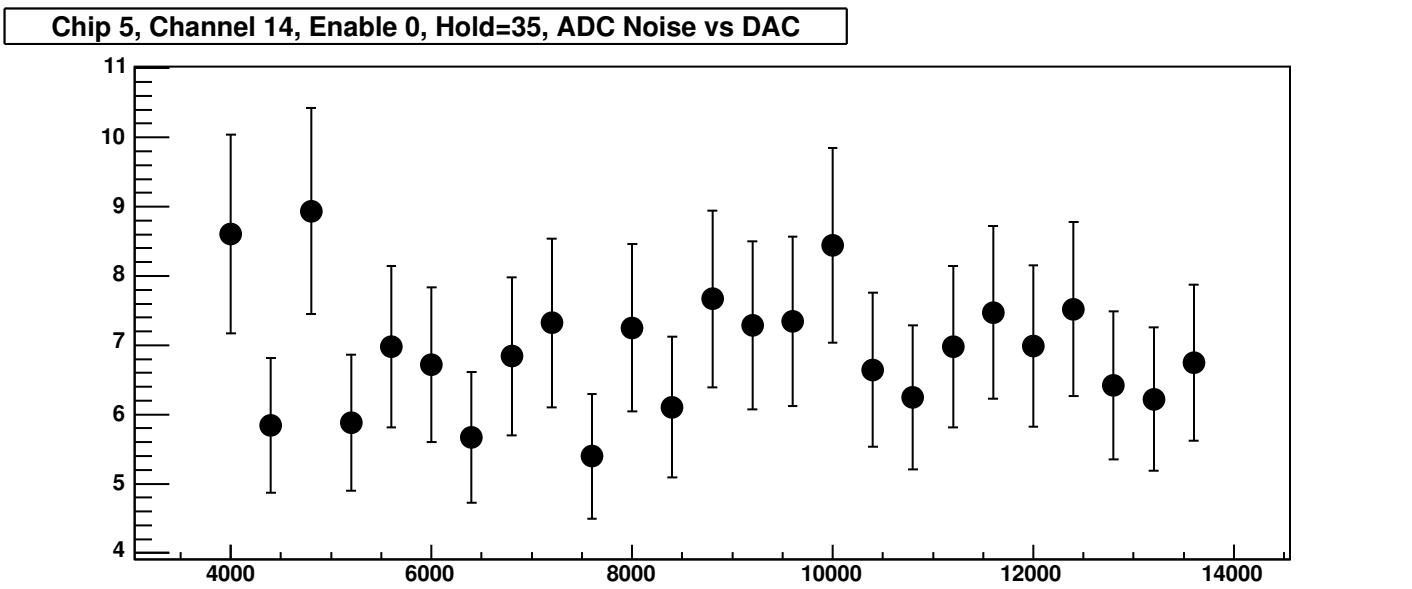
Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

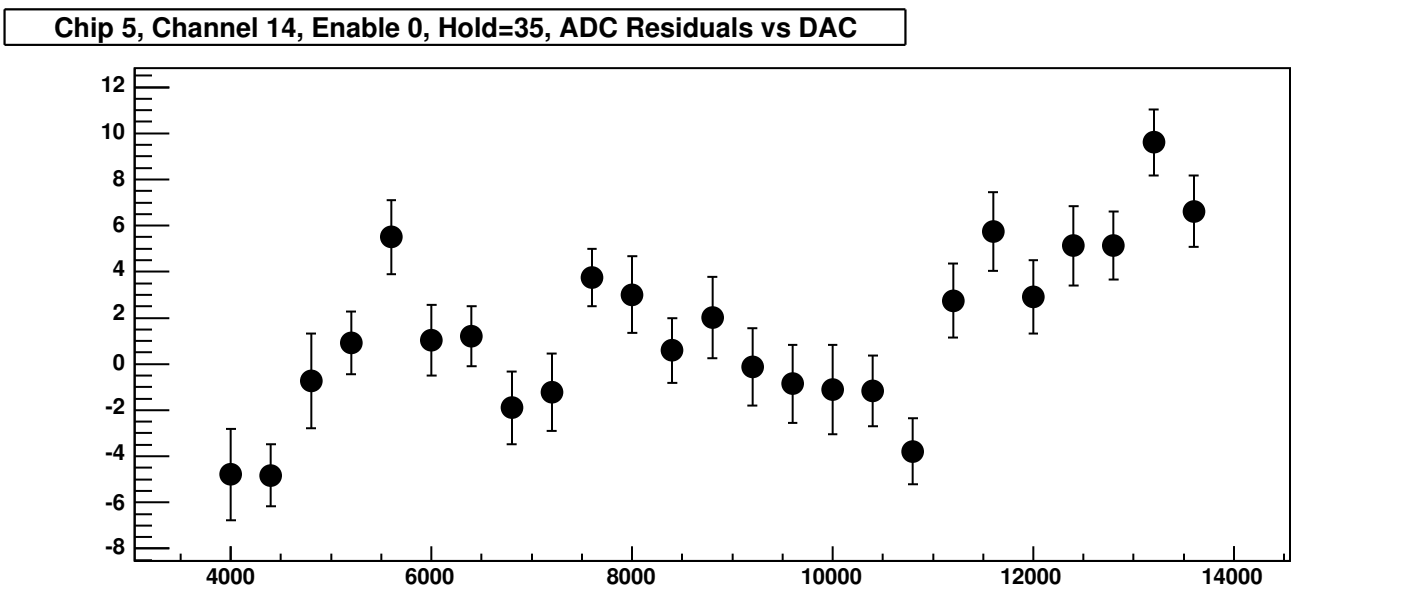


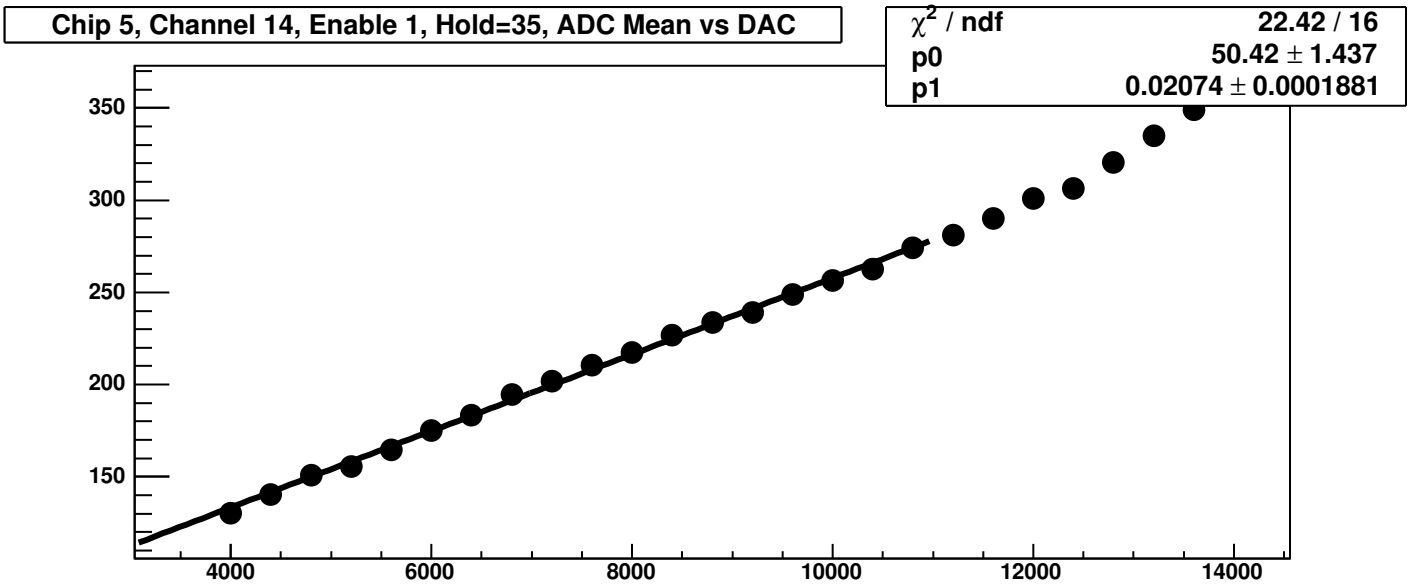
Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

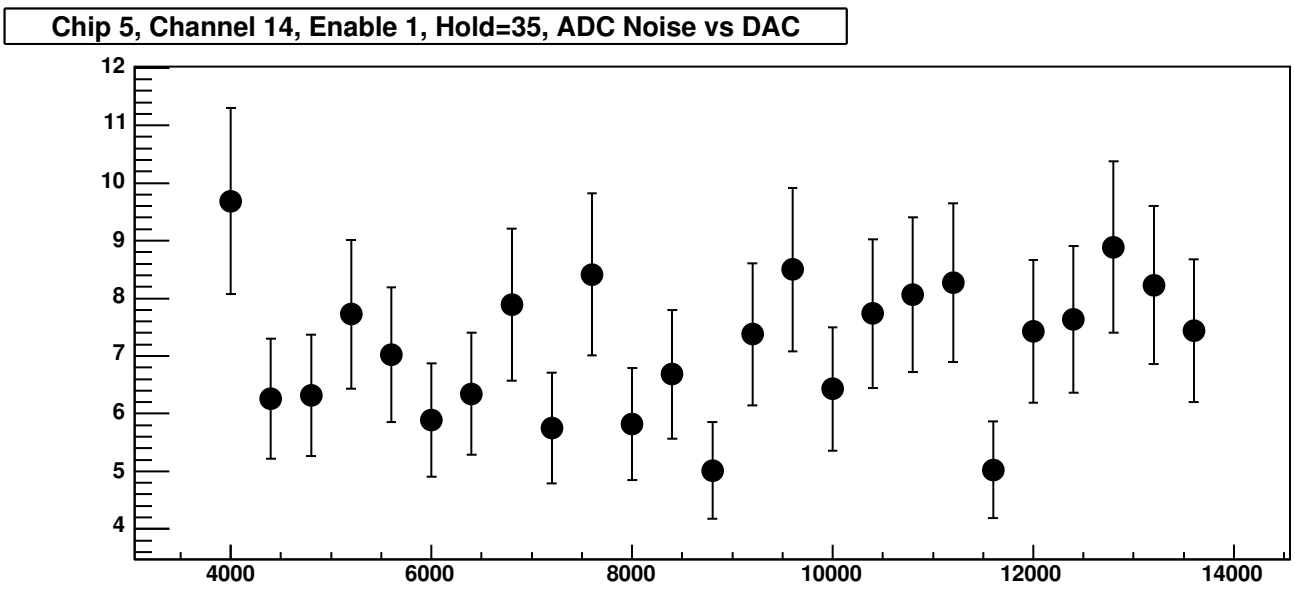


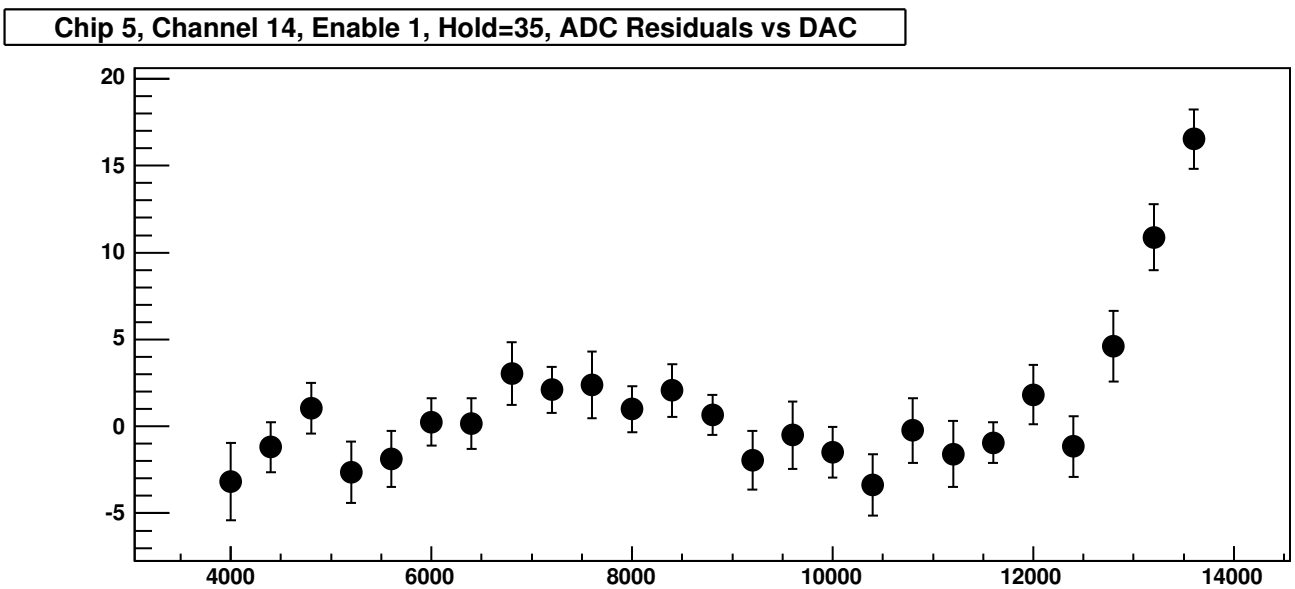

Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 56.62 / 16
p0 57.05 ± 1.383
p1 0.01801 ± 0.000181
320
300
280
260
240
220
200
180
160
140
120
4000
6000
8000
10000
12000
14000

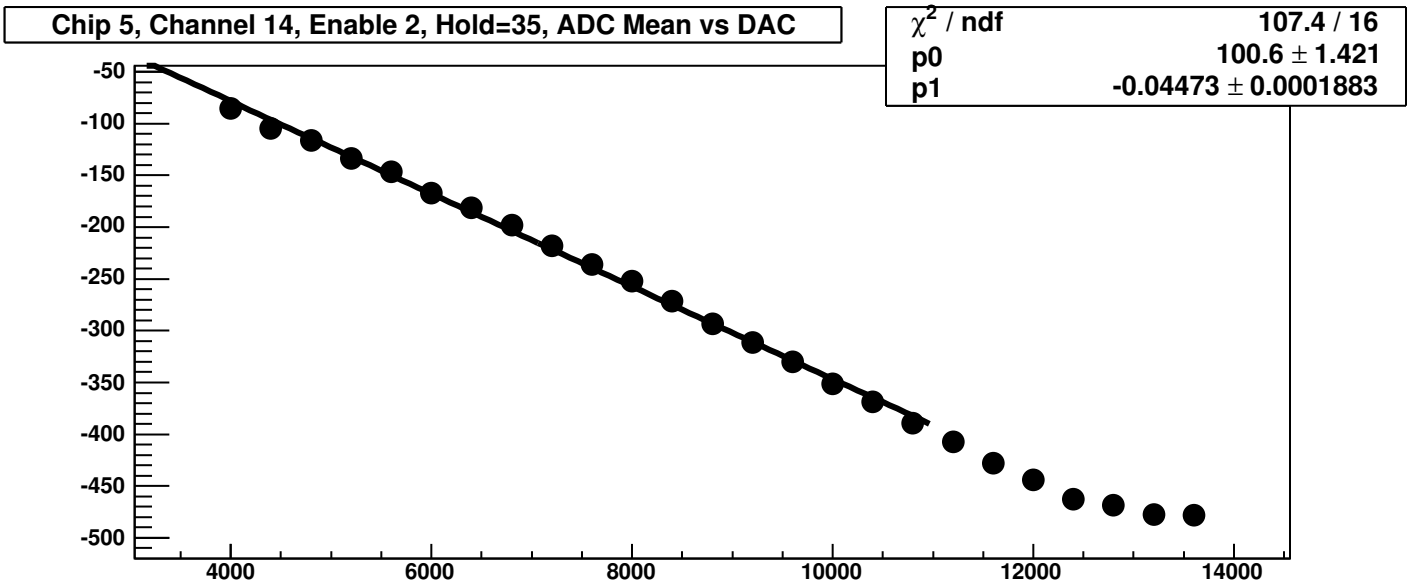

Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

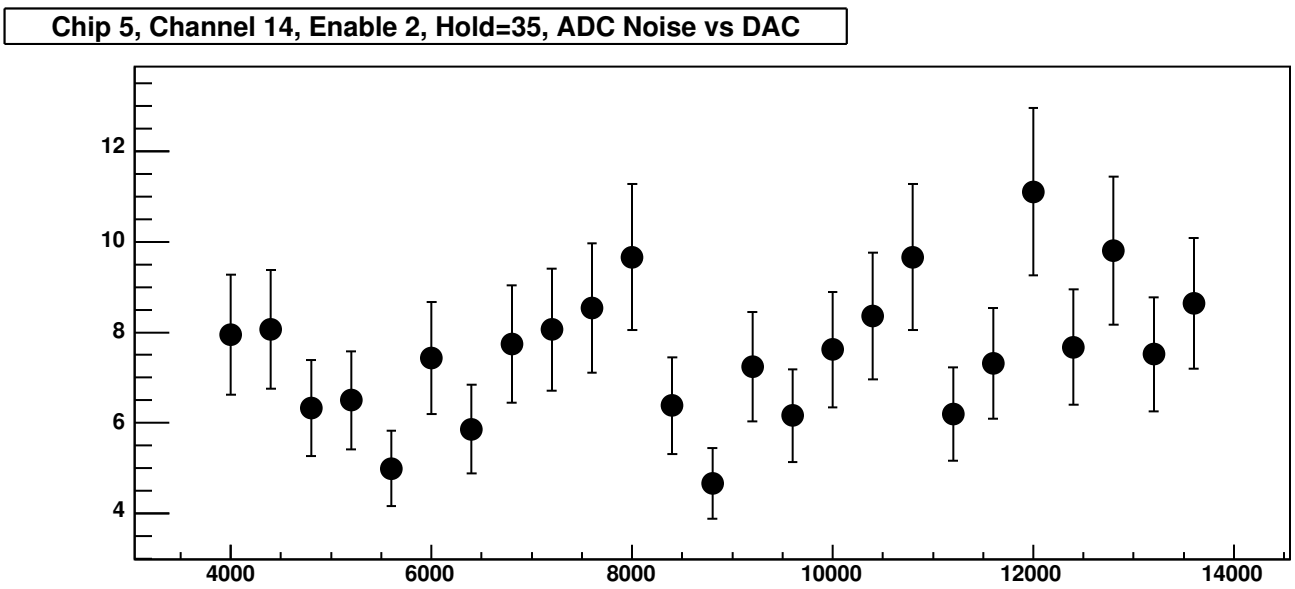

Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
4000
6000
8000
10000
12000
14000

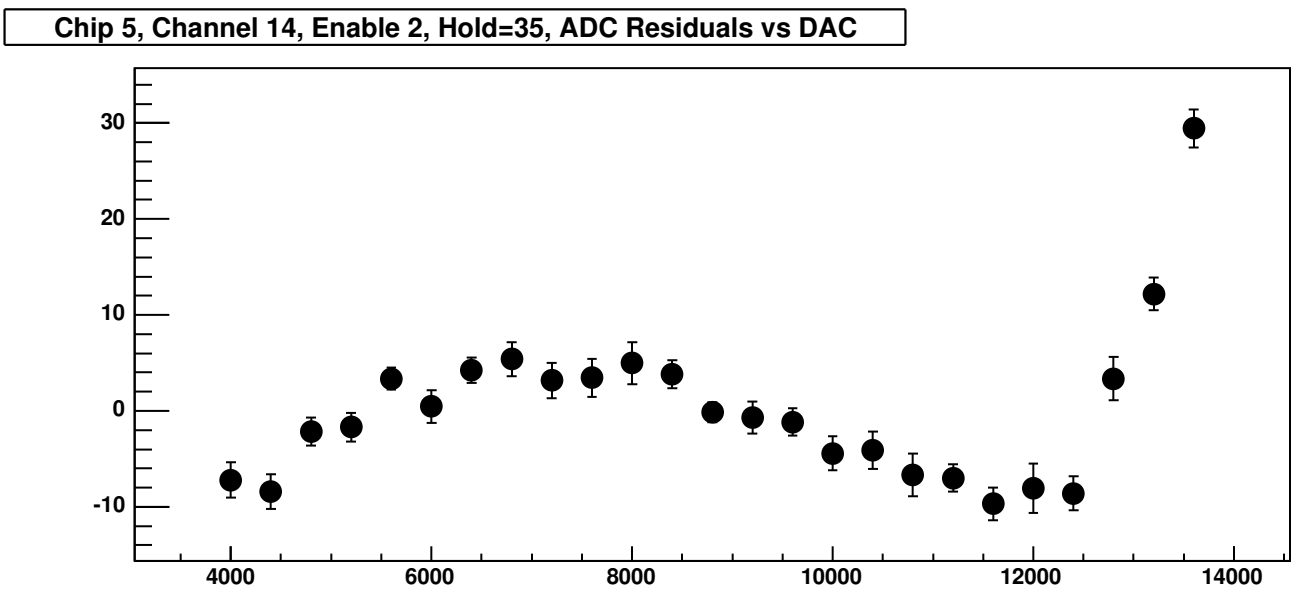

Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.42 / 16
p0 50.42 ± 1.437
p1 0.02074 ± 0.0001881
350
300
250
200
150
4000
6000
8000
10000
12000
14000

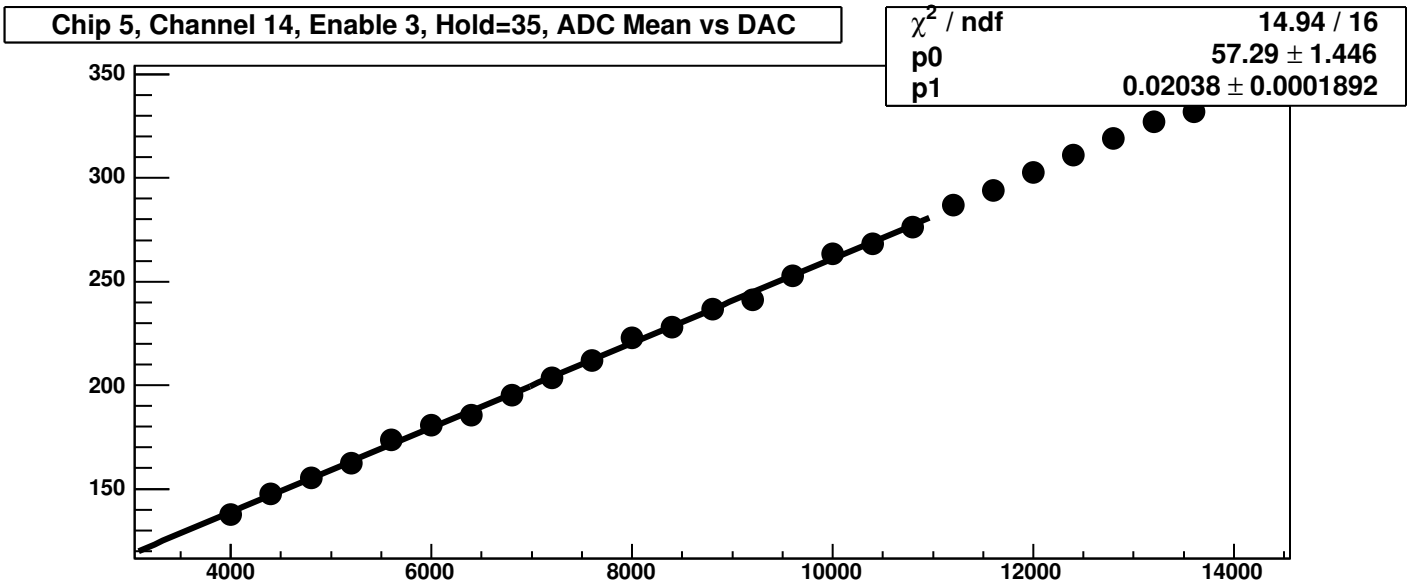

Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

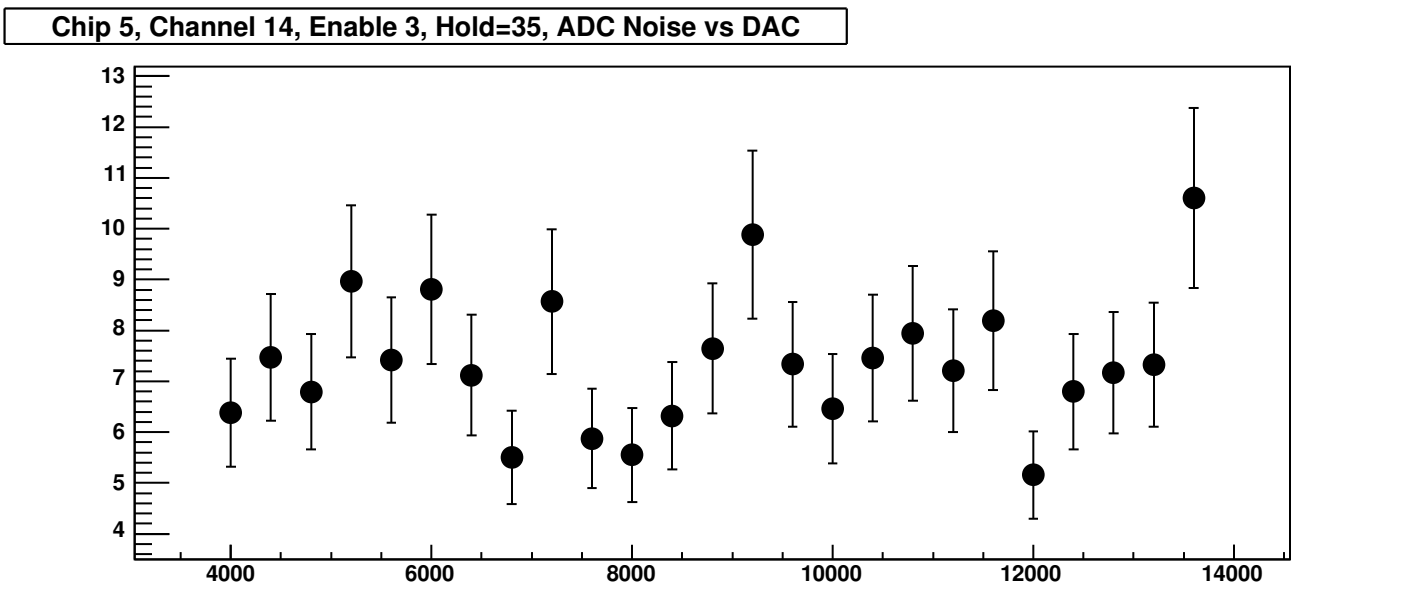

Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

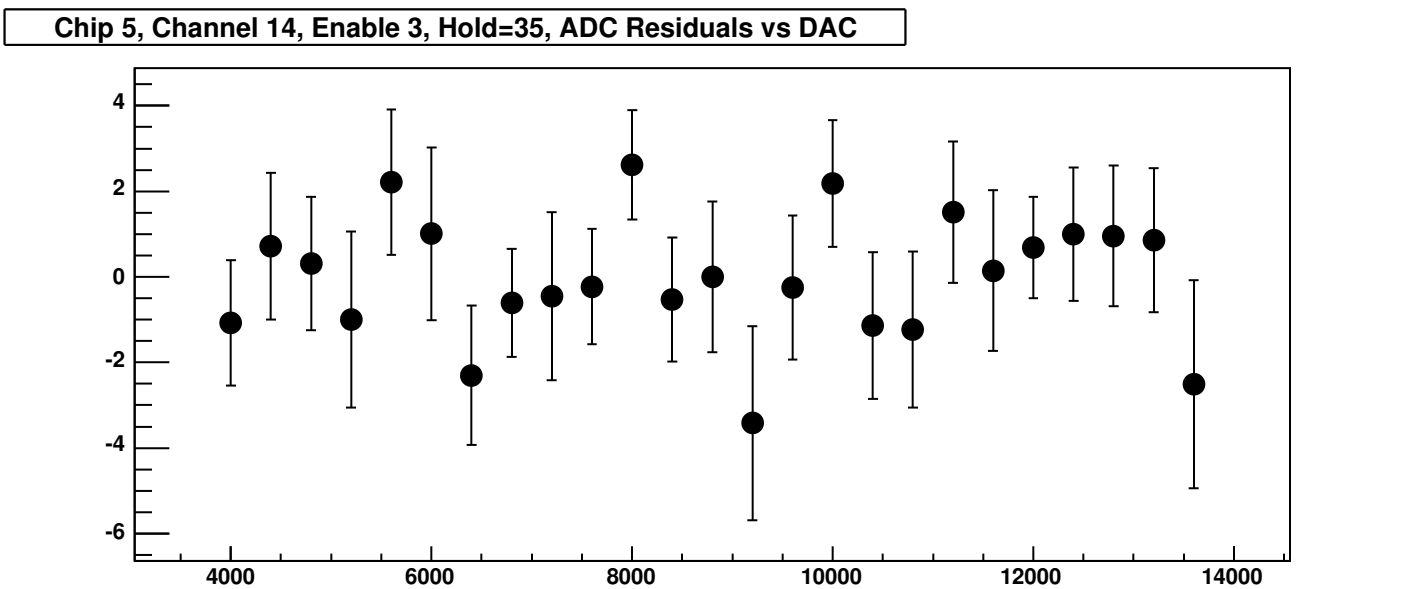

Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 107.4 / 16
p0 100.6 ± 1.421
p1 -0.04473 ± 0.0001883
-50
-100
-150
-200
-250
-300
-350
-400
-450
-500
4000
6000
8000
10000
12000
14000

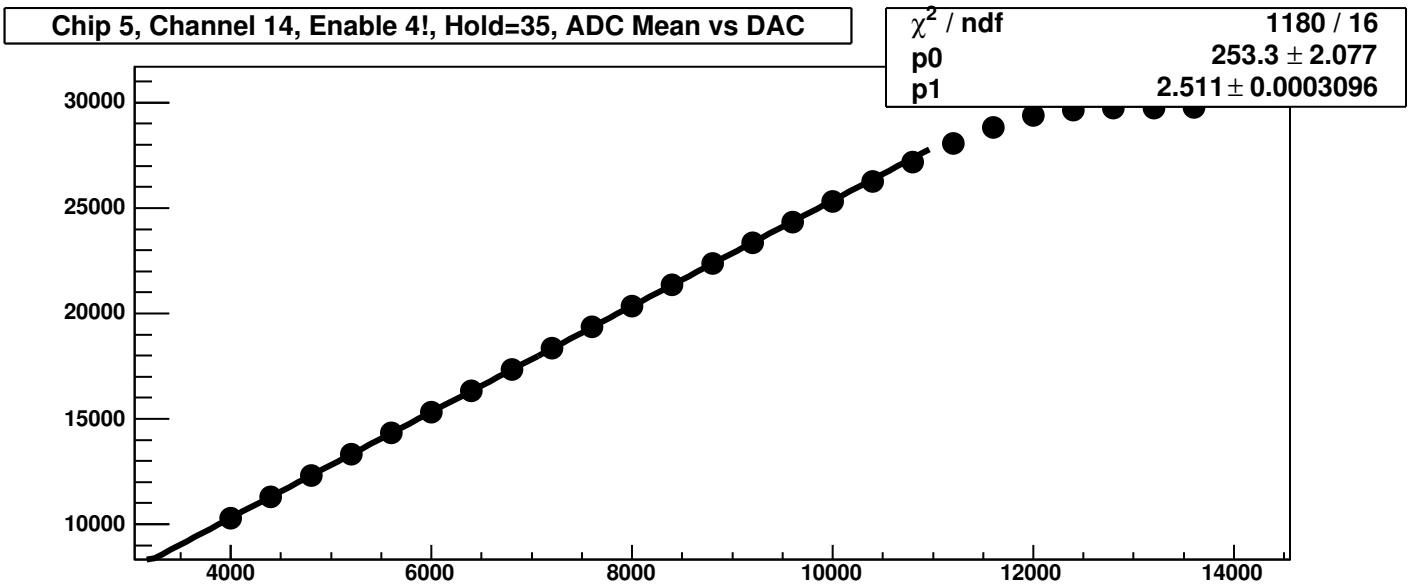

Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
4000
6000
8000
10000
12000
14000

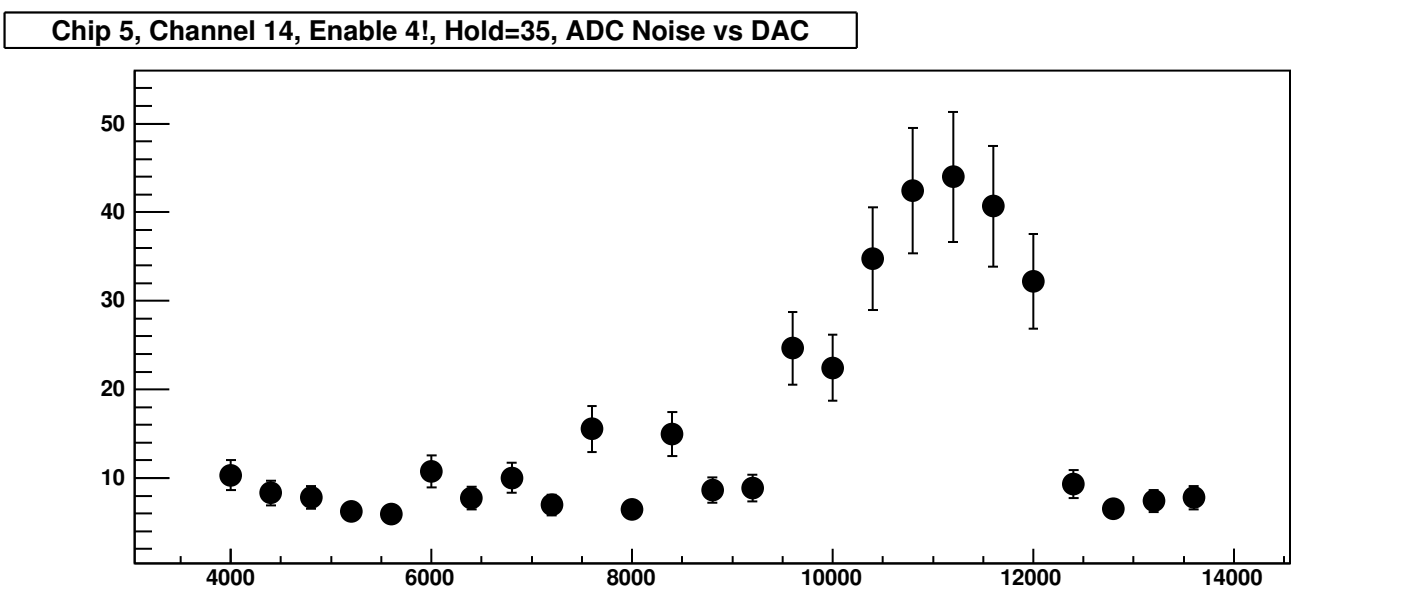

Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
4000
6000
8000
10000
12000
14000

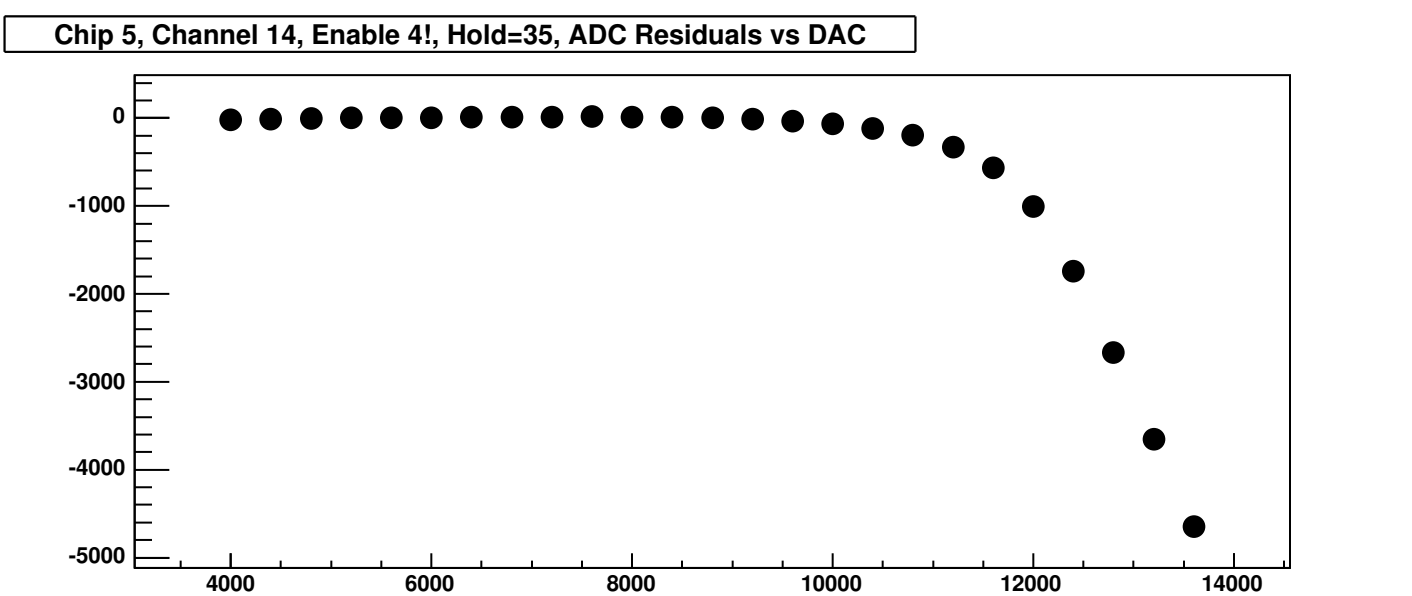

Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.94 / 16
p0 57.29 ± 1.446
p1 0.02038 ± 0.0001892
350
300
250
200
150
4000
6000
8000
10000
12000
14000

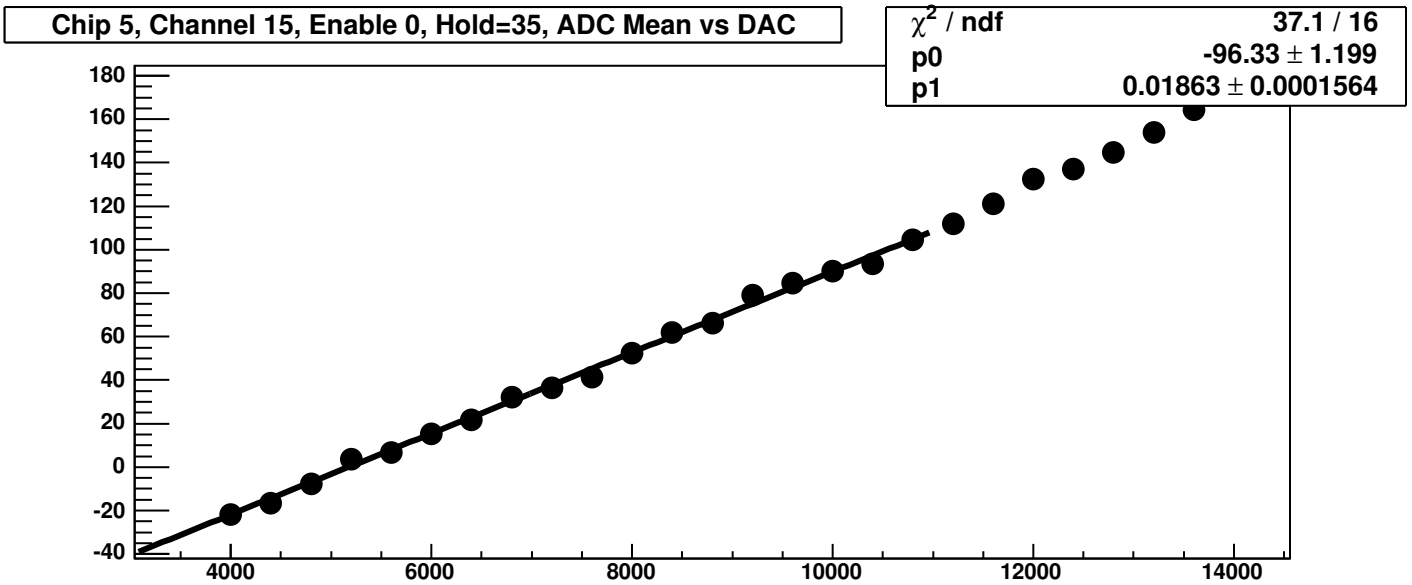

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

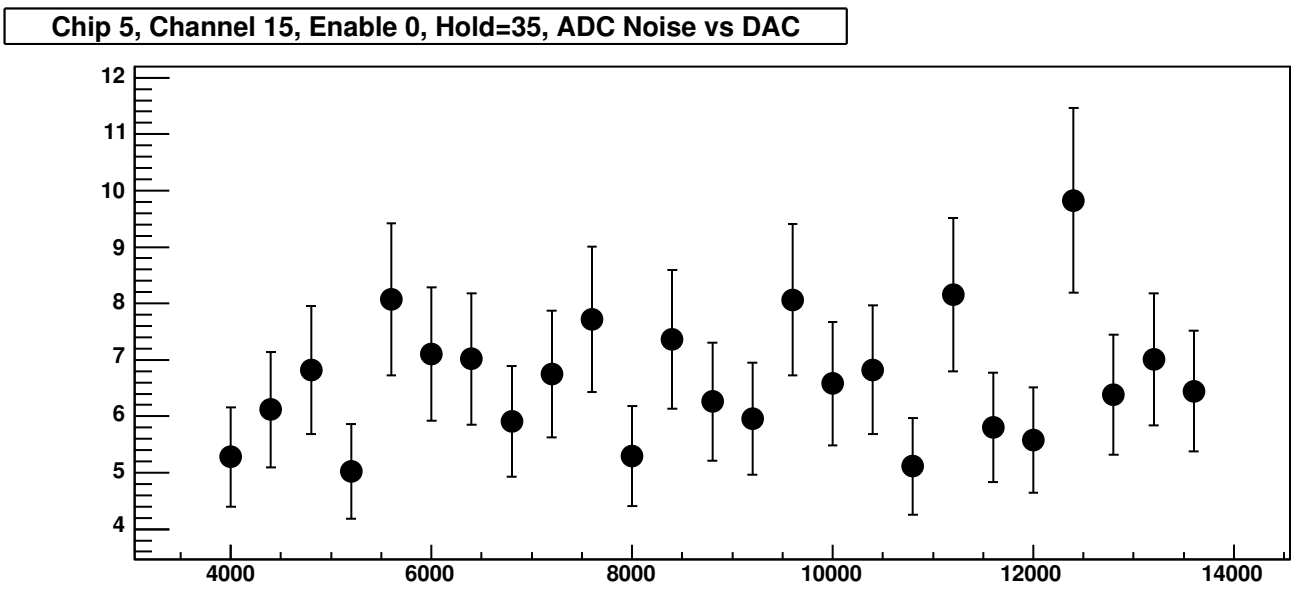

Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

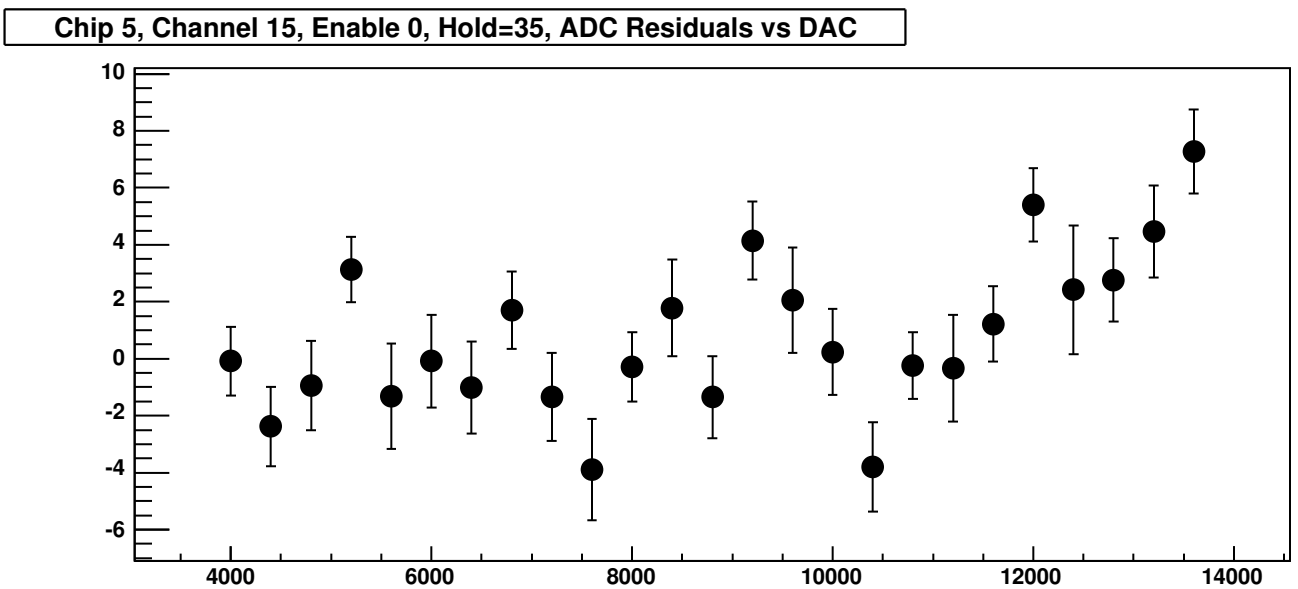

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1180 / 16
p0 253.3 ± 2.077
p1 2.511 ± 0.0003096
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

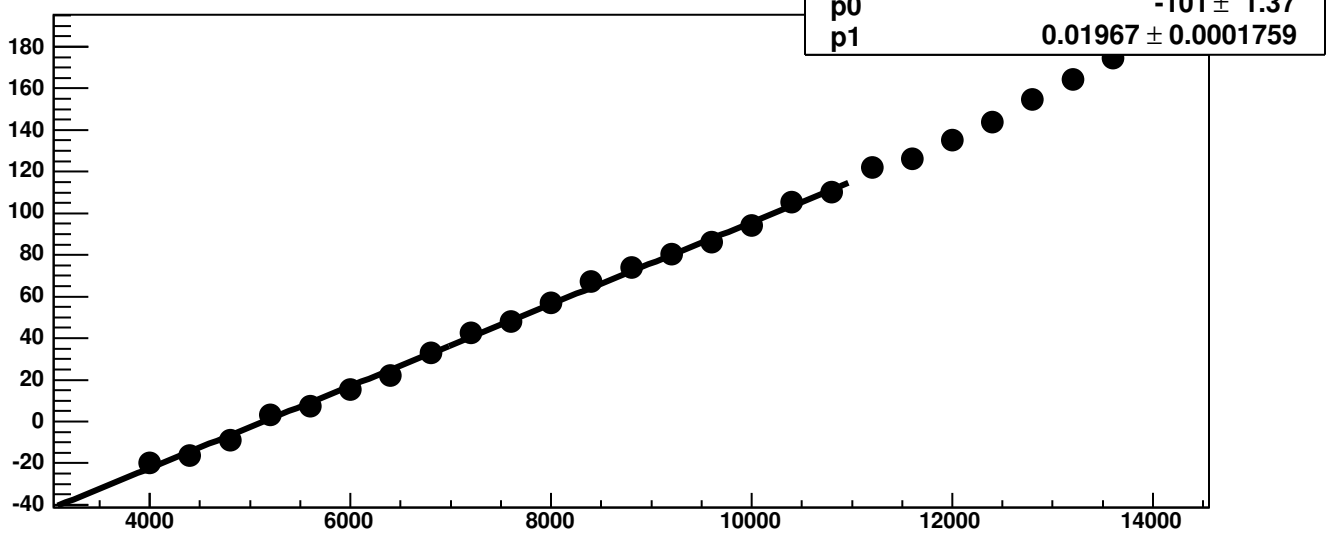

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-1000
-2000
-3000
-4000
-5000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.1 / 16
p0 -96.33 ± 1.199
p1 0.01863 ± 0.0001564
180
160
140
120
100
80
60
40
20
0
-20
-40
4000
6000
8000
10000
12000
14000

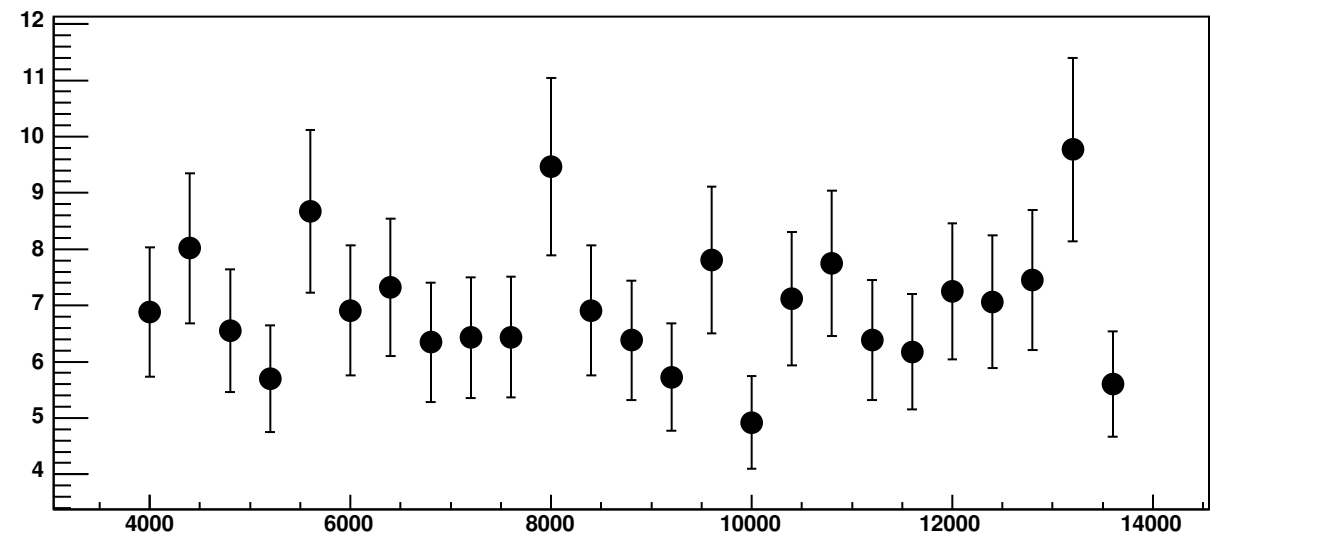

Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

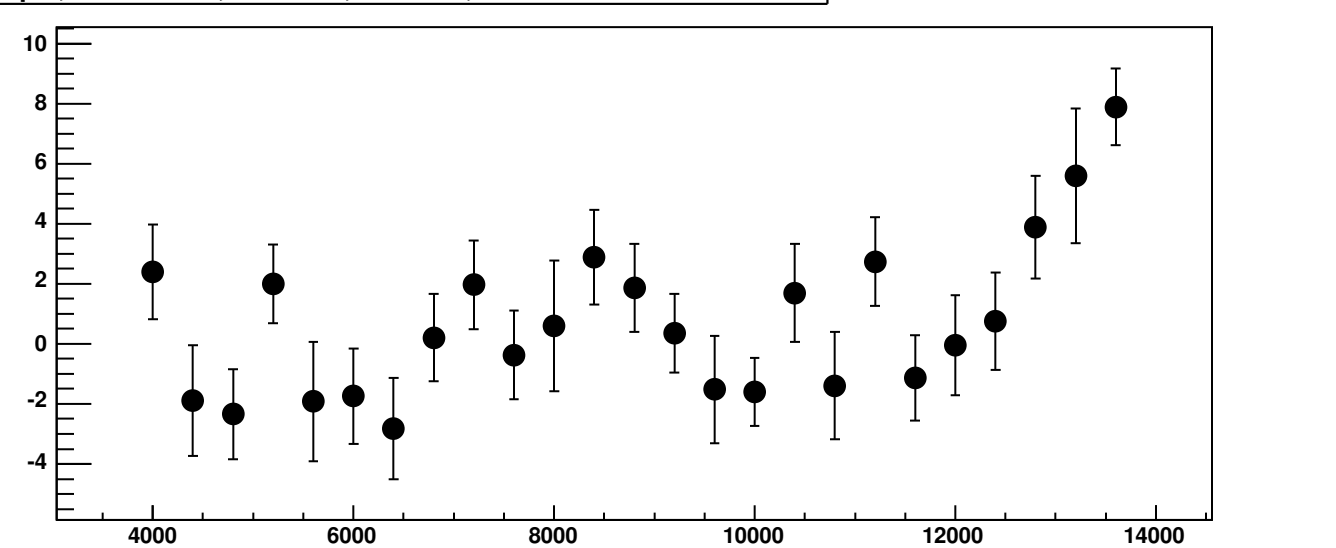
Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

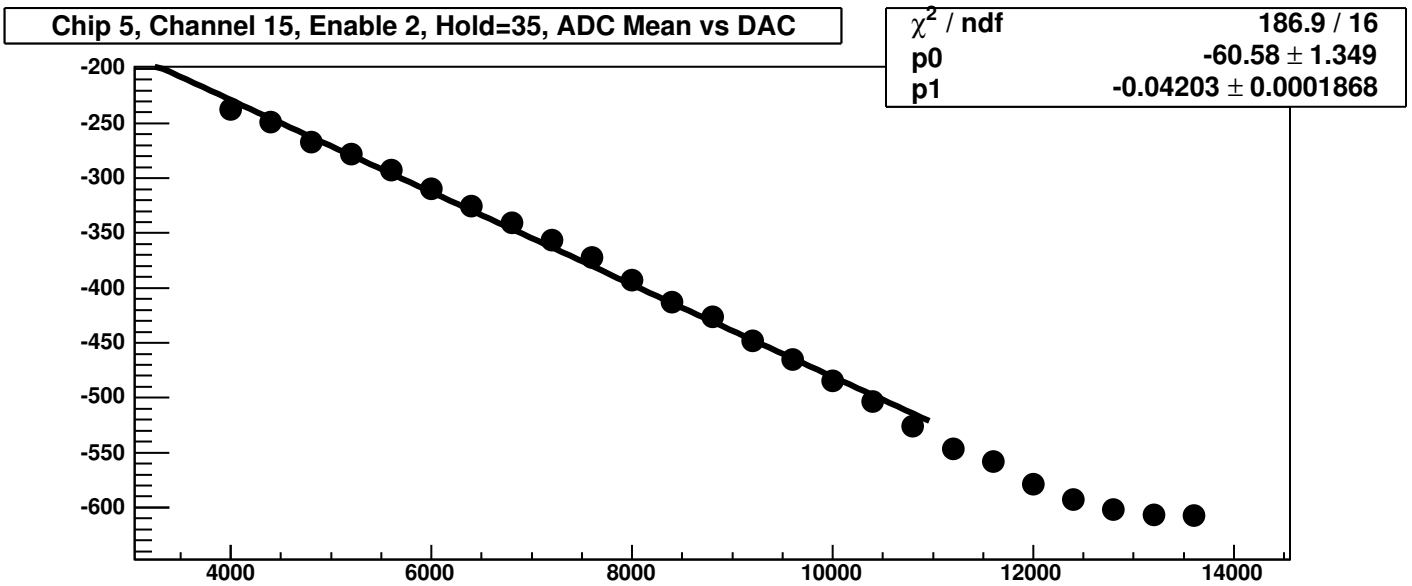


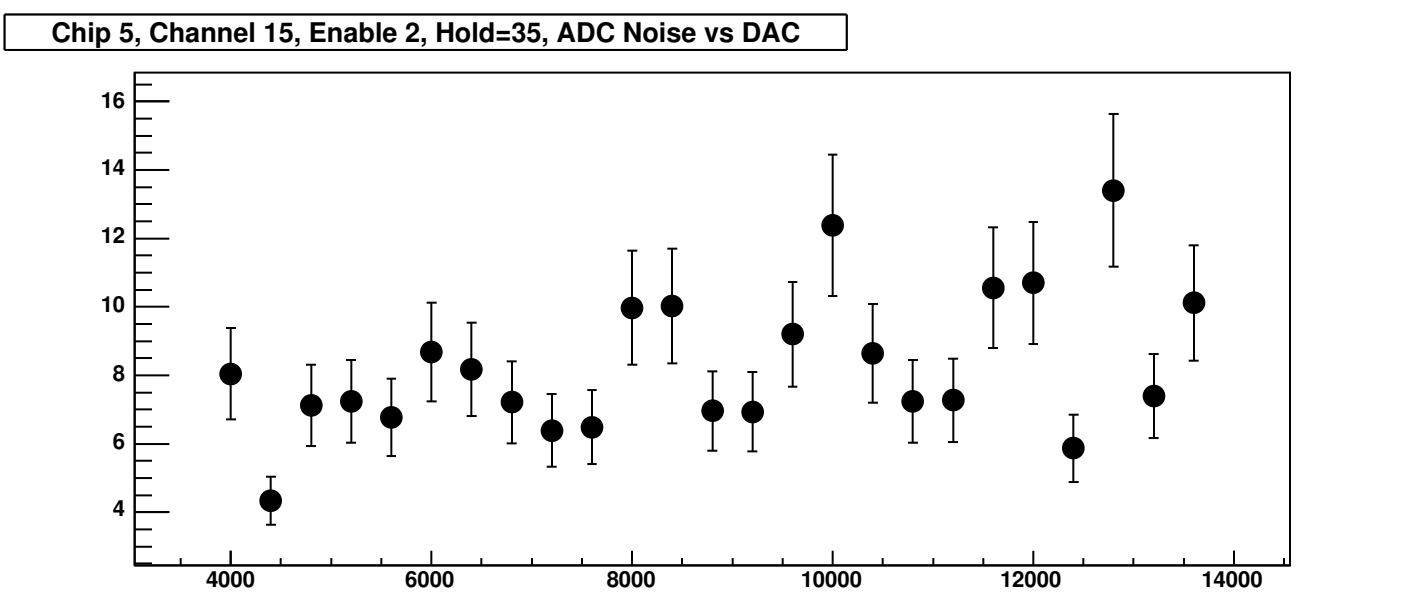
Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

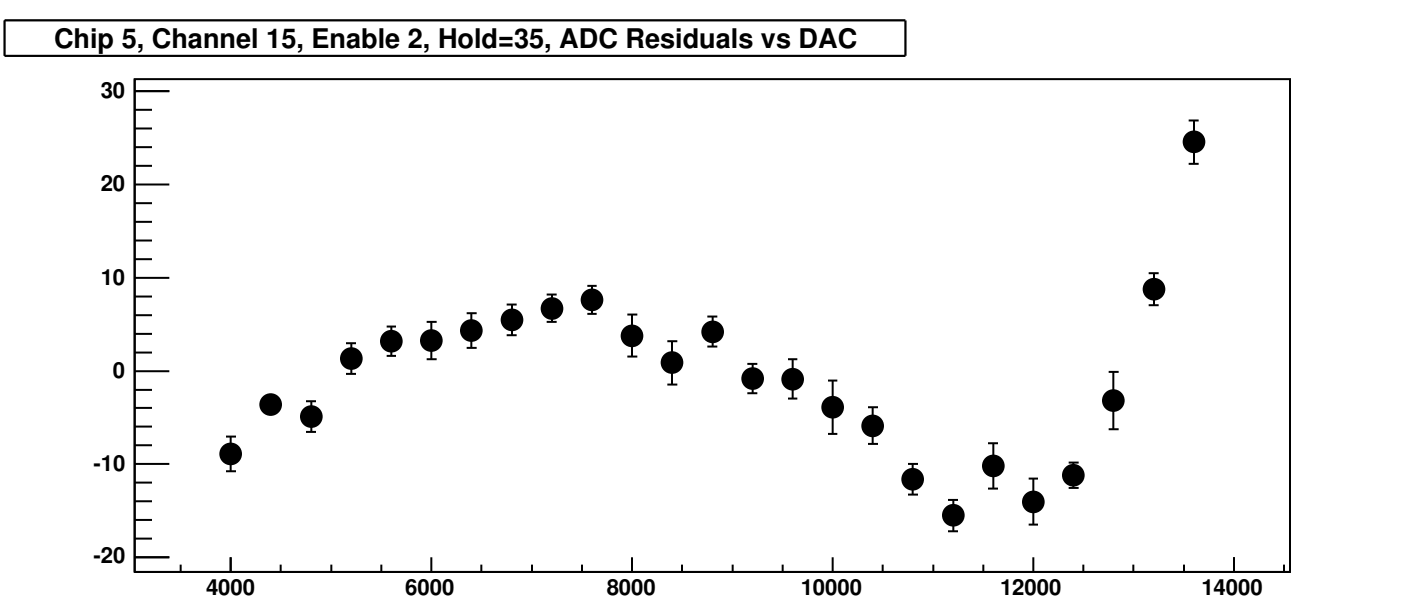


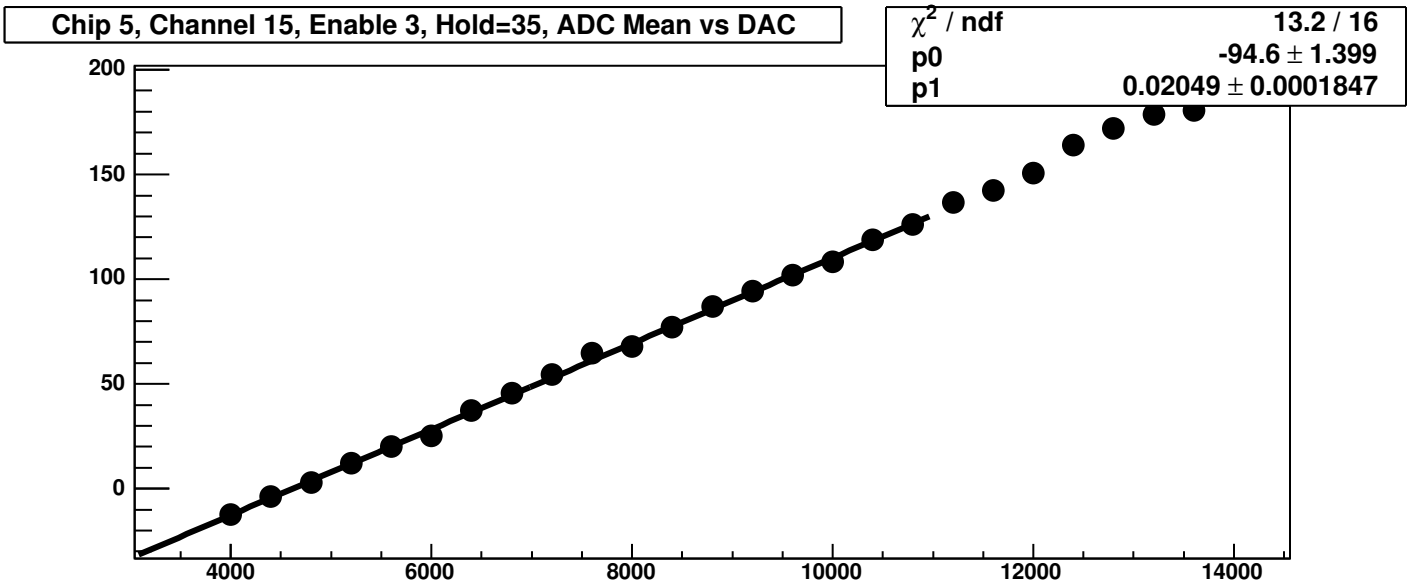
Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

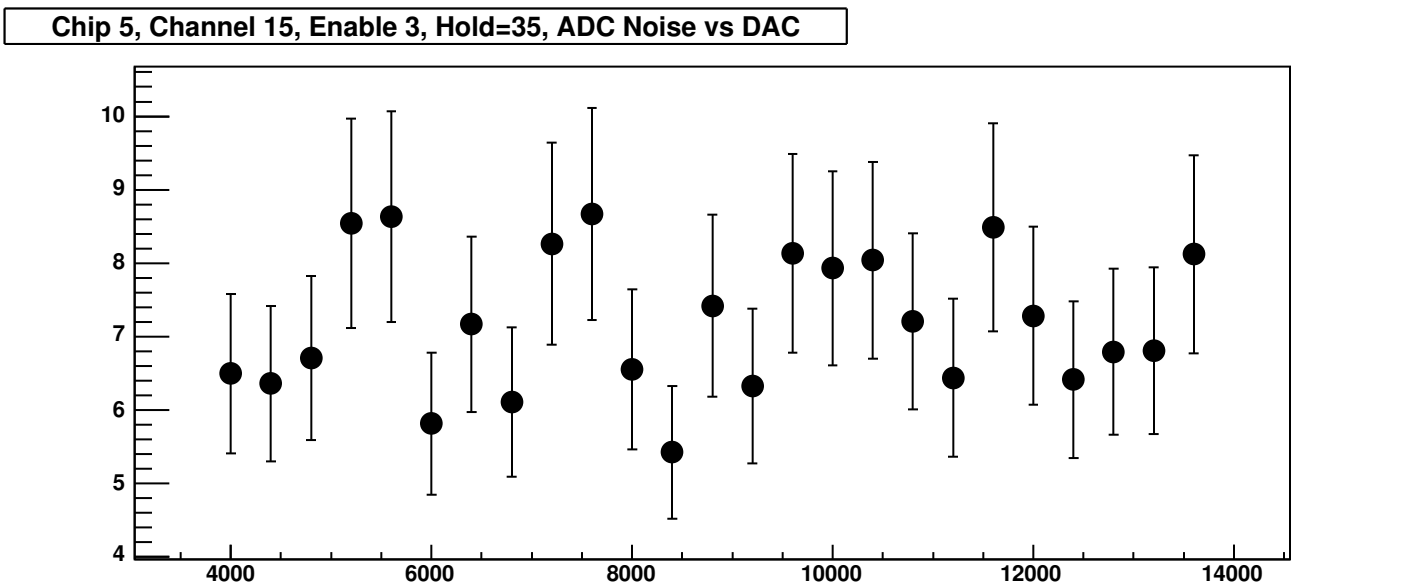


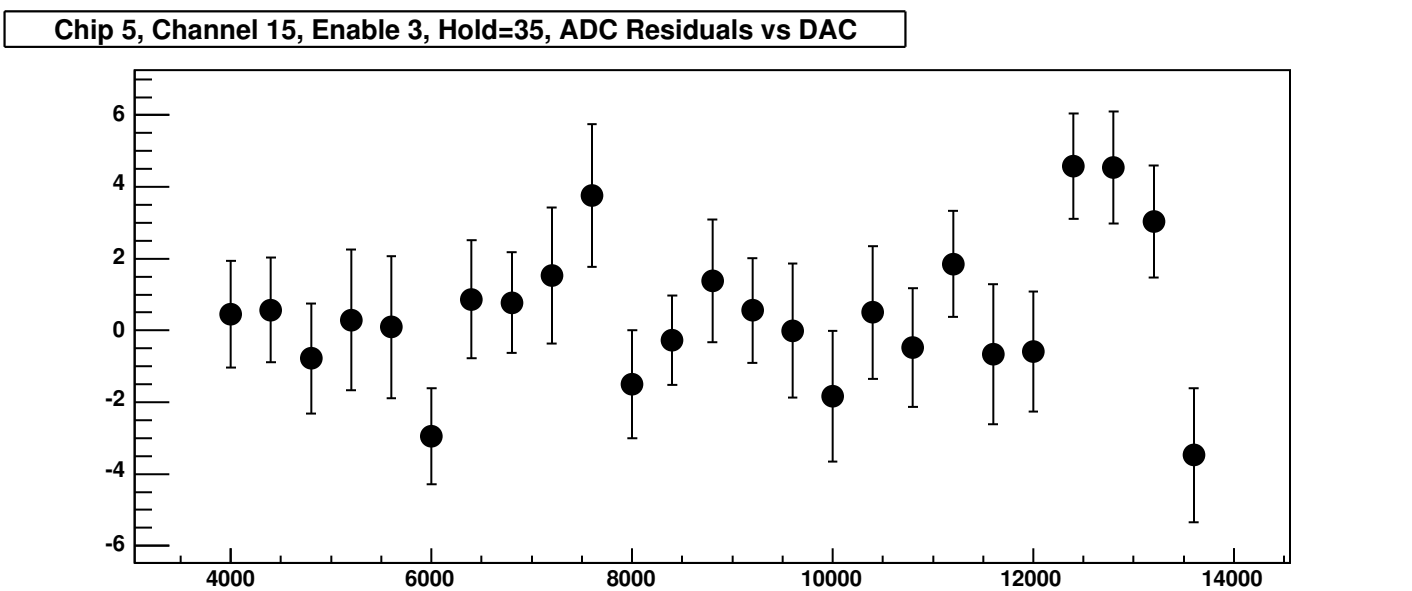

Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 186.9 / 16
p0 -60.58 ± 1.349
p1 -0.04203 ± 0.0001868
-200
-250
-300
-350
-400
-450
-500
-550
-600
4000
6000
8000
10000
12000
14000

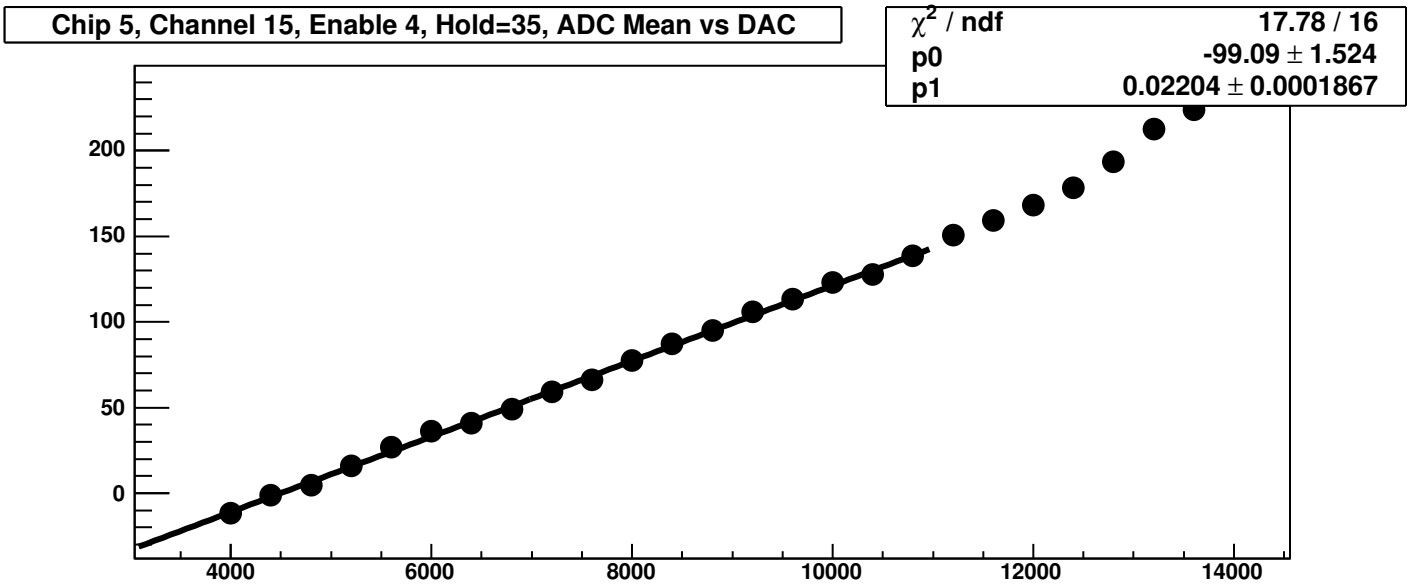

Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000

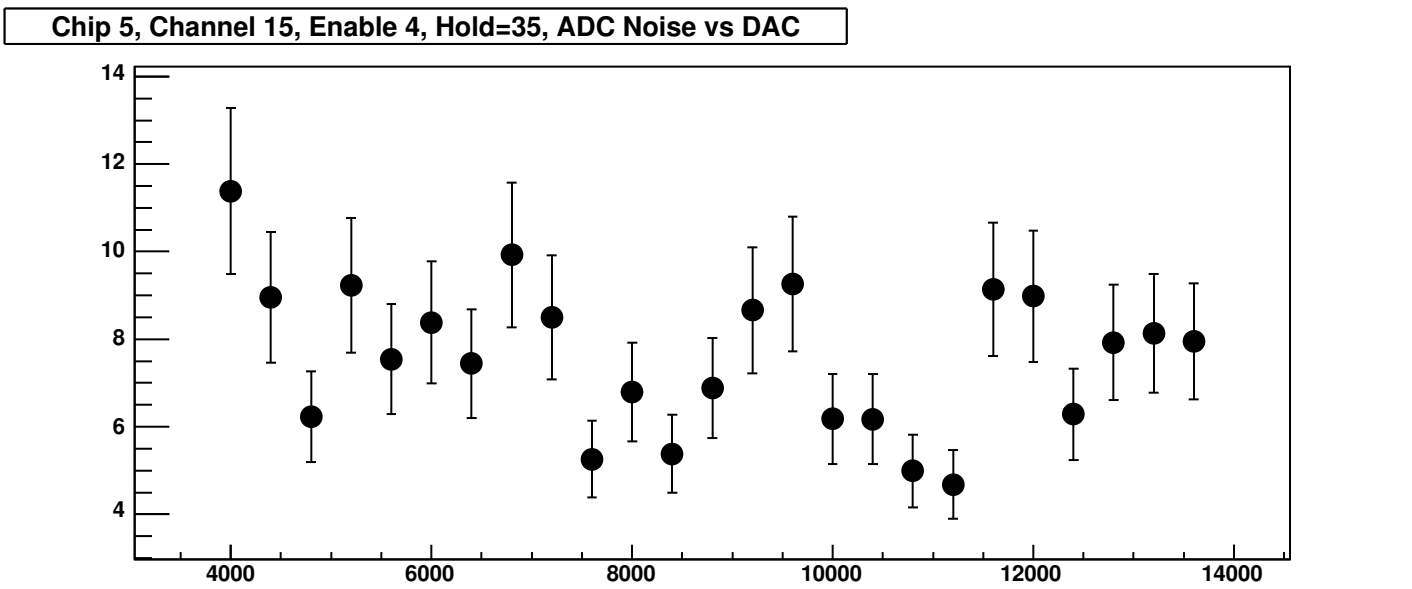

Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
4000
6000
8000
10000
12000
14000

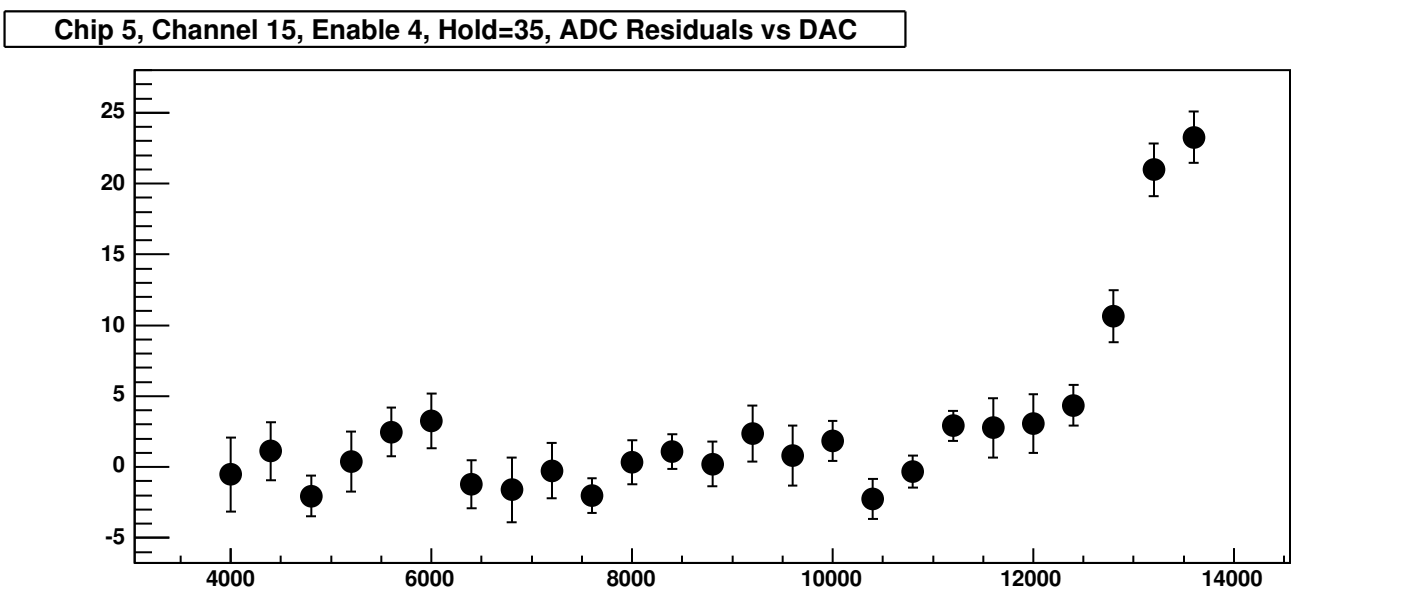

Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 13.2 / 16
p0 -94.6 ± 1.399
p1 0.02049 ± 0.0001847
200
150
100
50
0
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

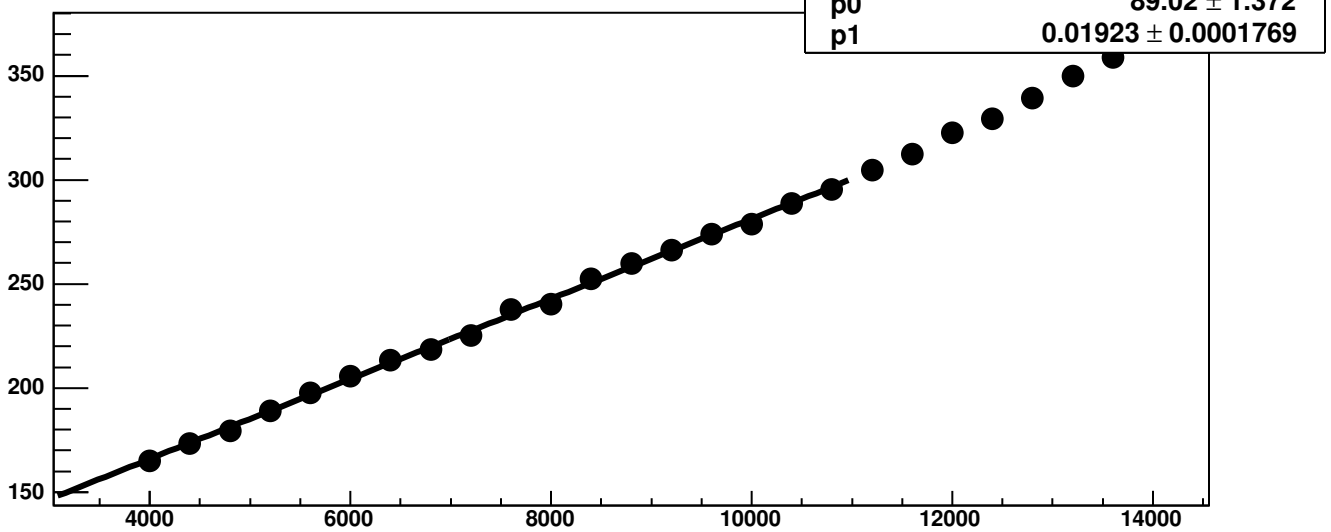

Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.78 / 16
p0 -99.09 ± 1.524
p1 0.02204 ± 0.0001867
200
150
100
50
0
4000
6000
8000
10000
12000
14000

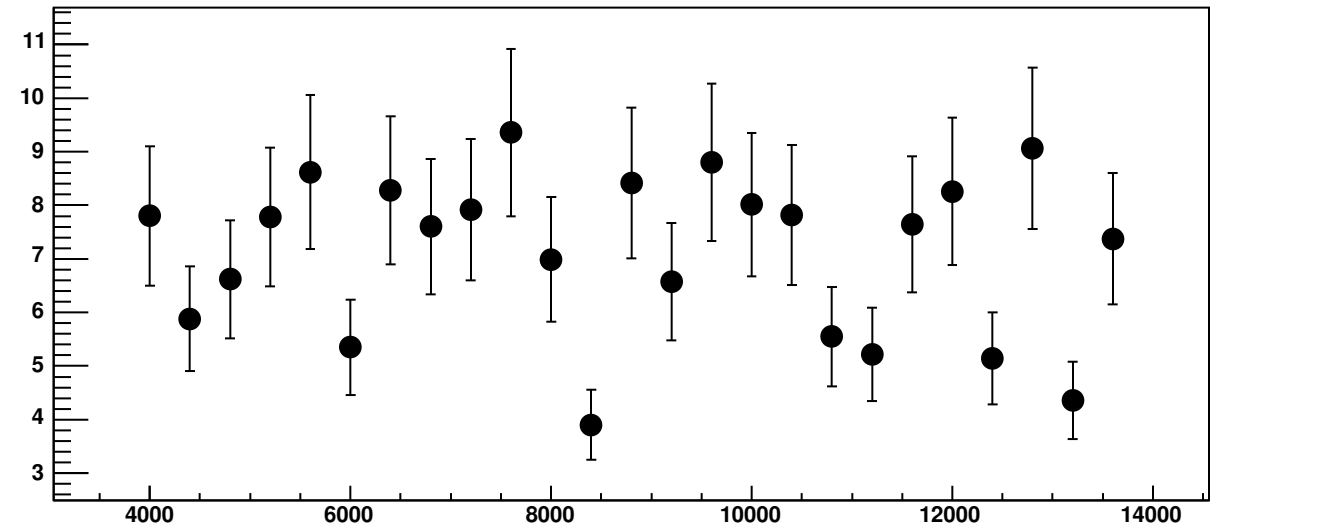

Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

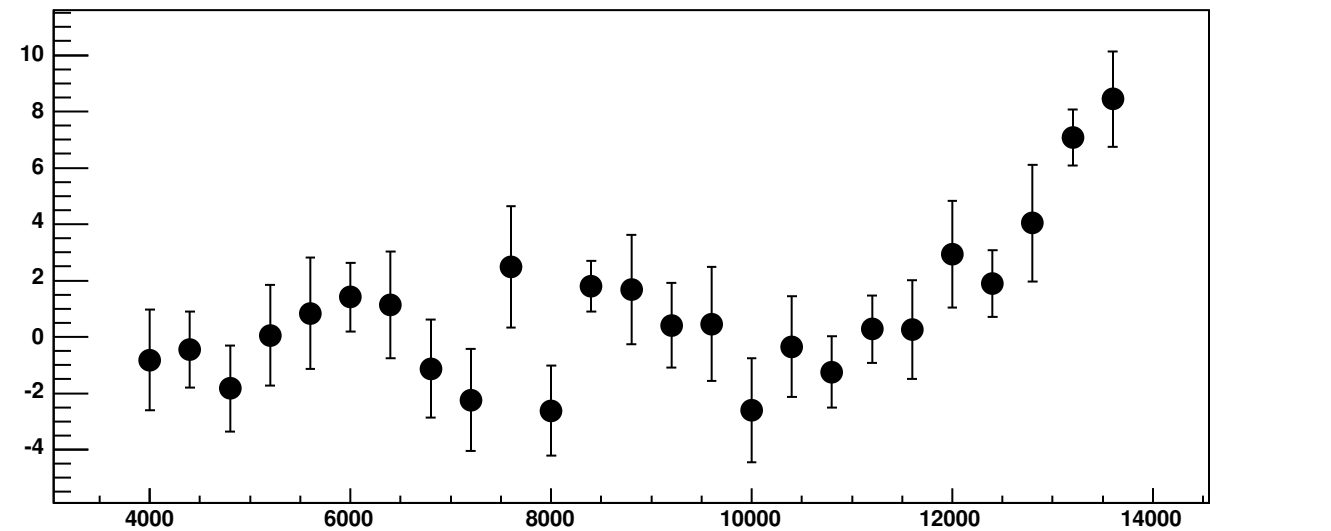
Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

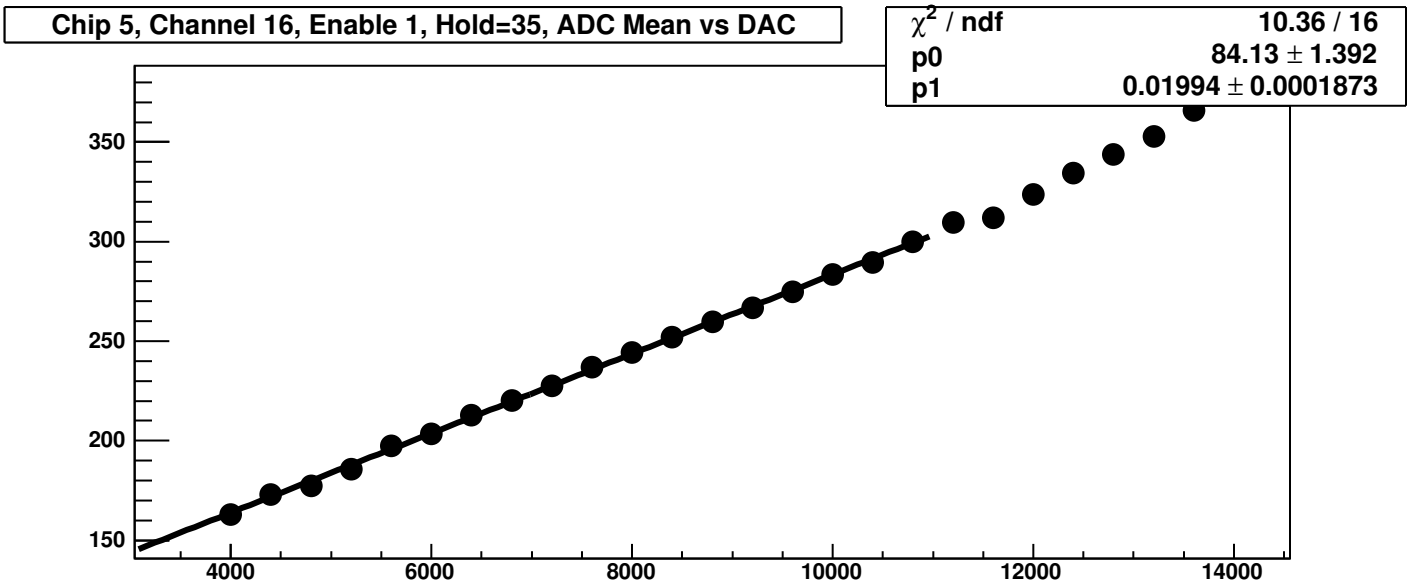


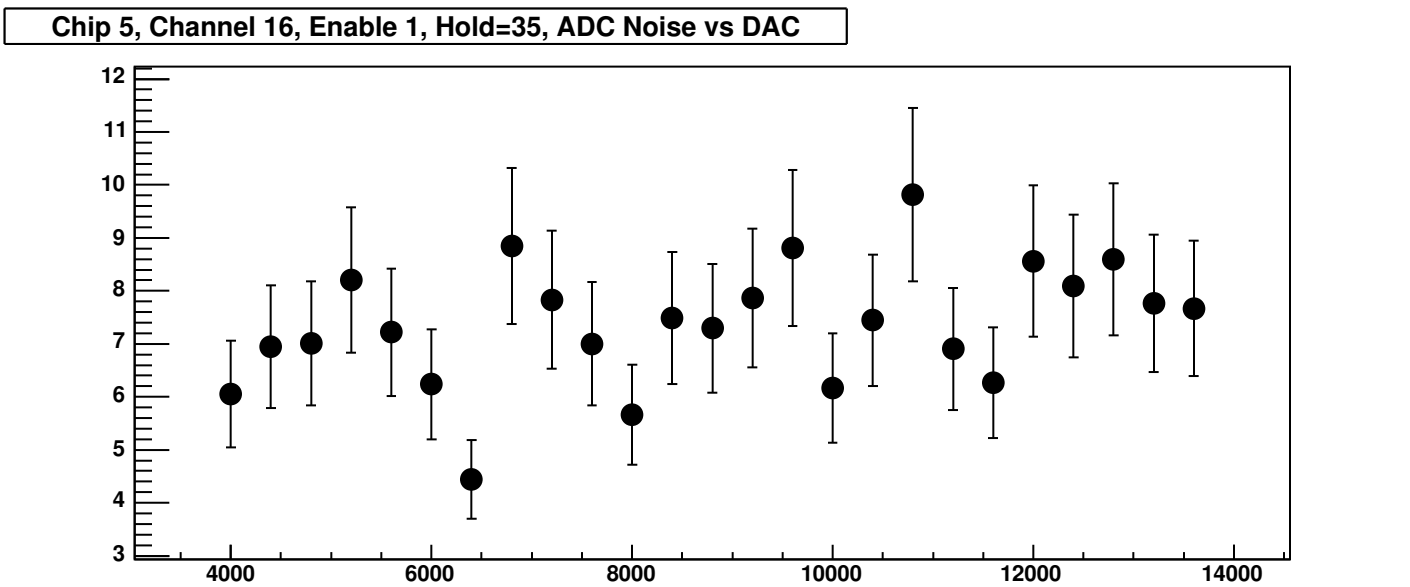
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

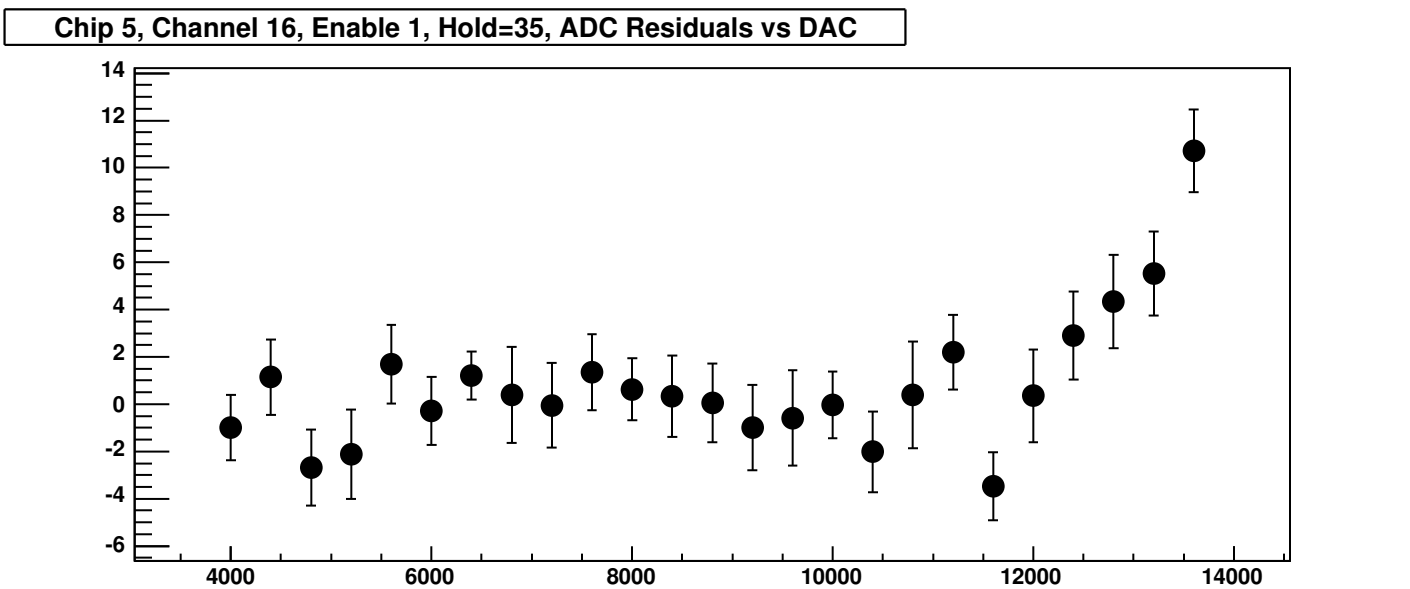


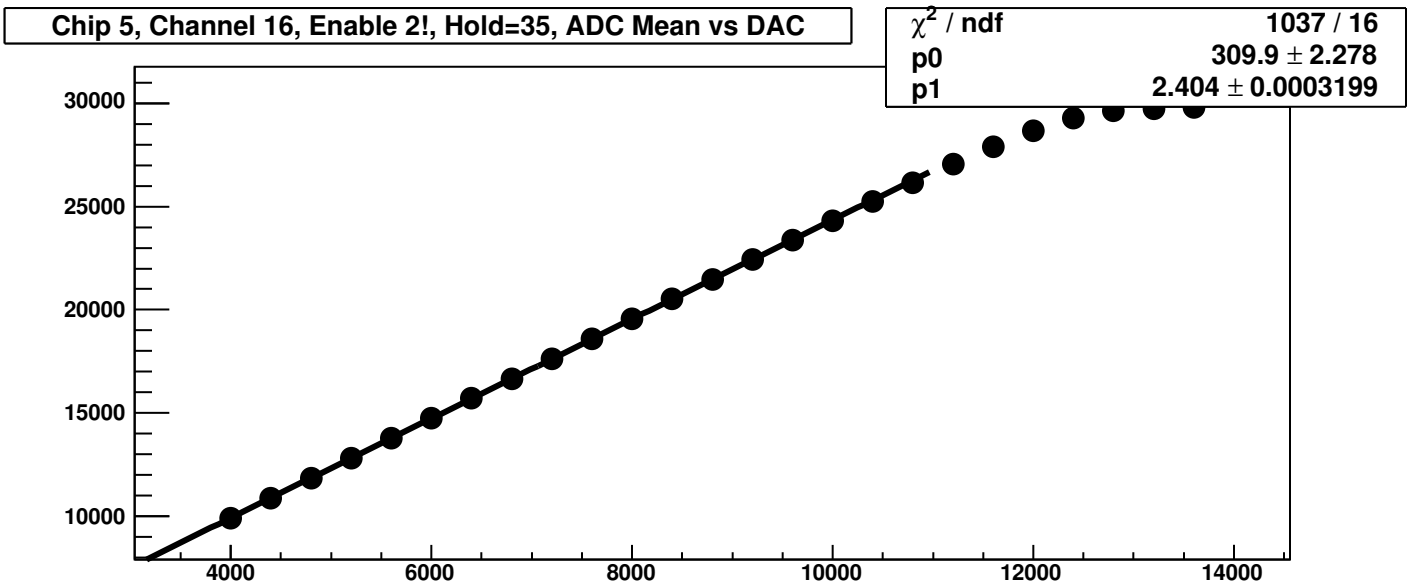
Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

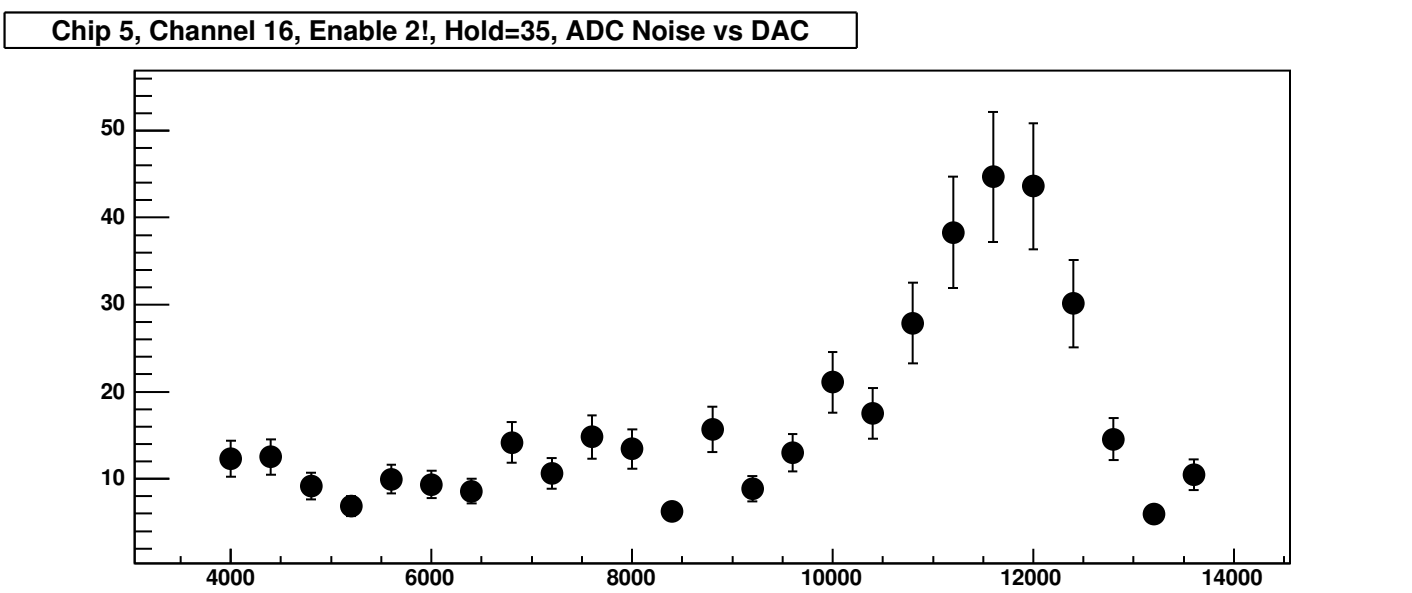


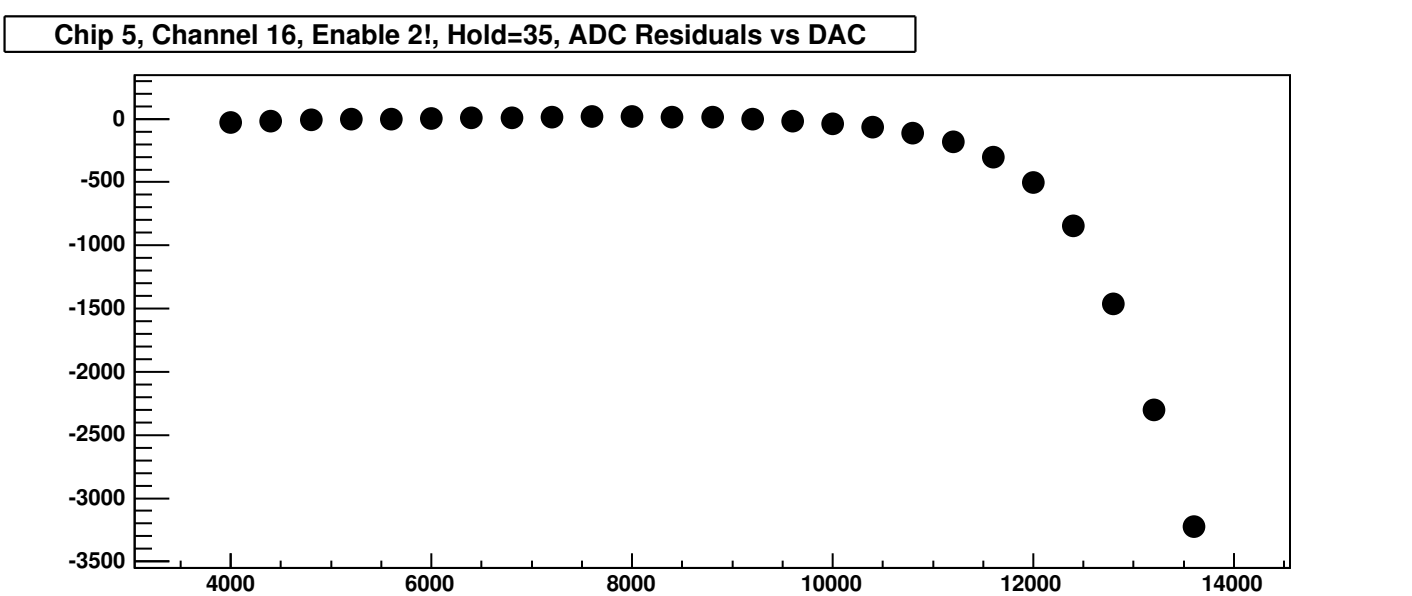

Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 10.36 / 16
p0 84.13 ± 1.392
p1 0.01994 ± 0.0001873
350
300
250
200
150
4000
6000
8000
10000
12000
14000

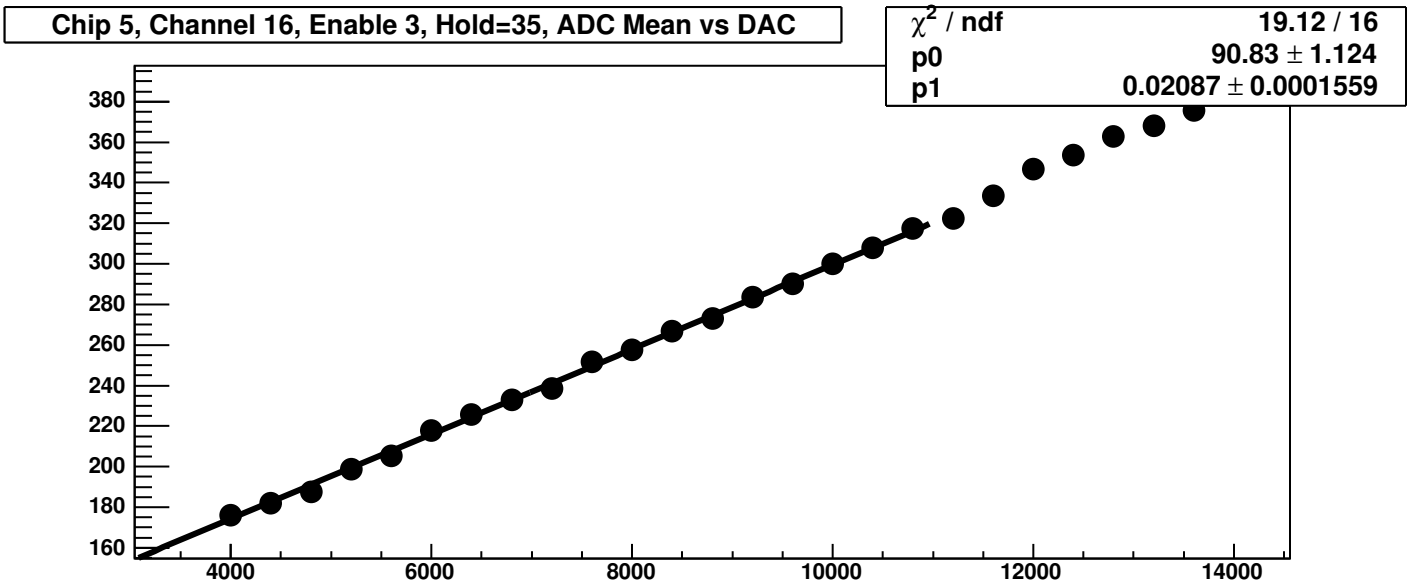

Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

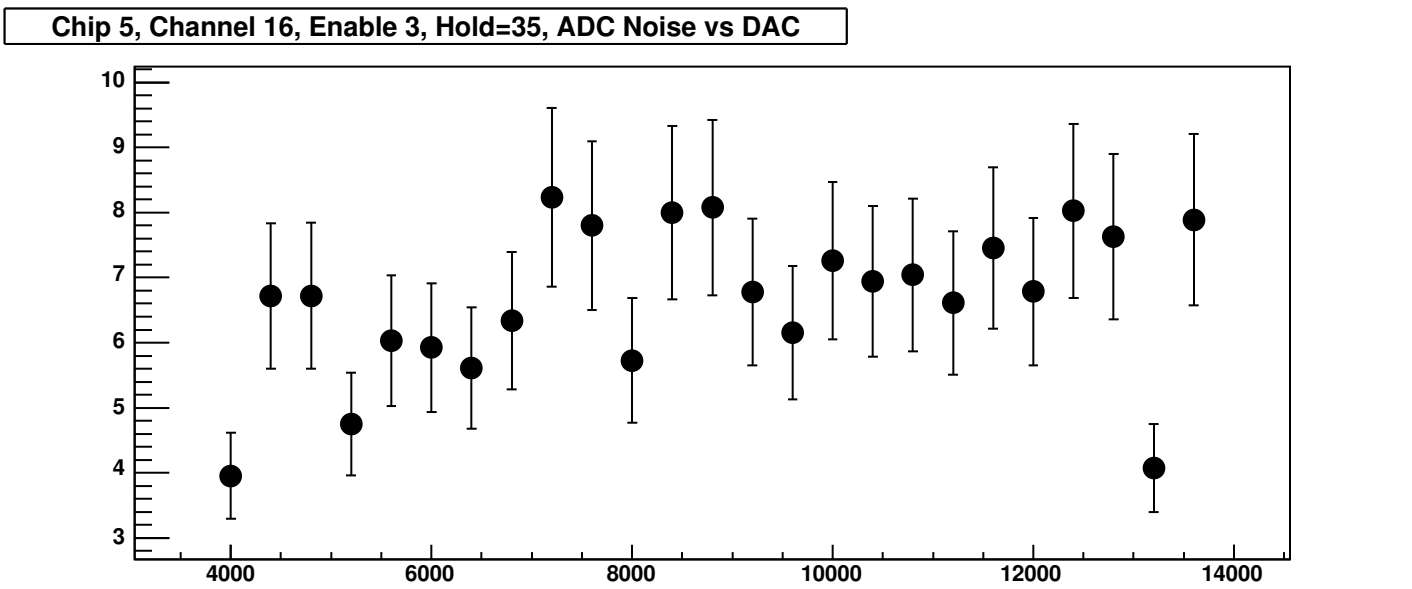

Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

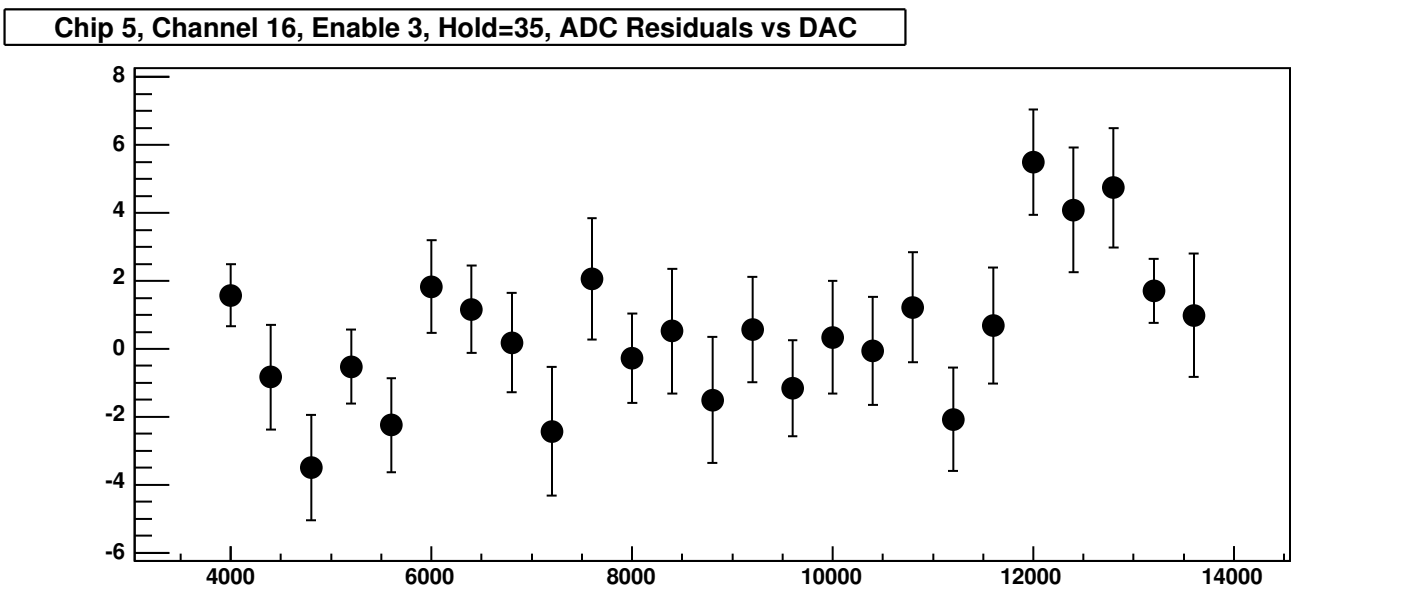

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1037 / 16
p0 309.9 ± 2.278
p1 2.404 ± 0.0003199
30000
25000
20000
15000
10000
4000
6000
8000
10000
12000
14000

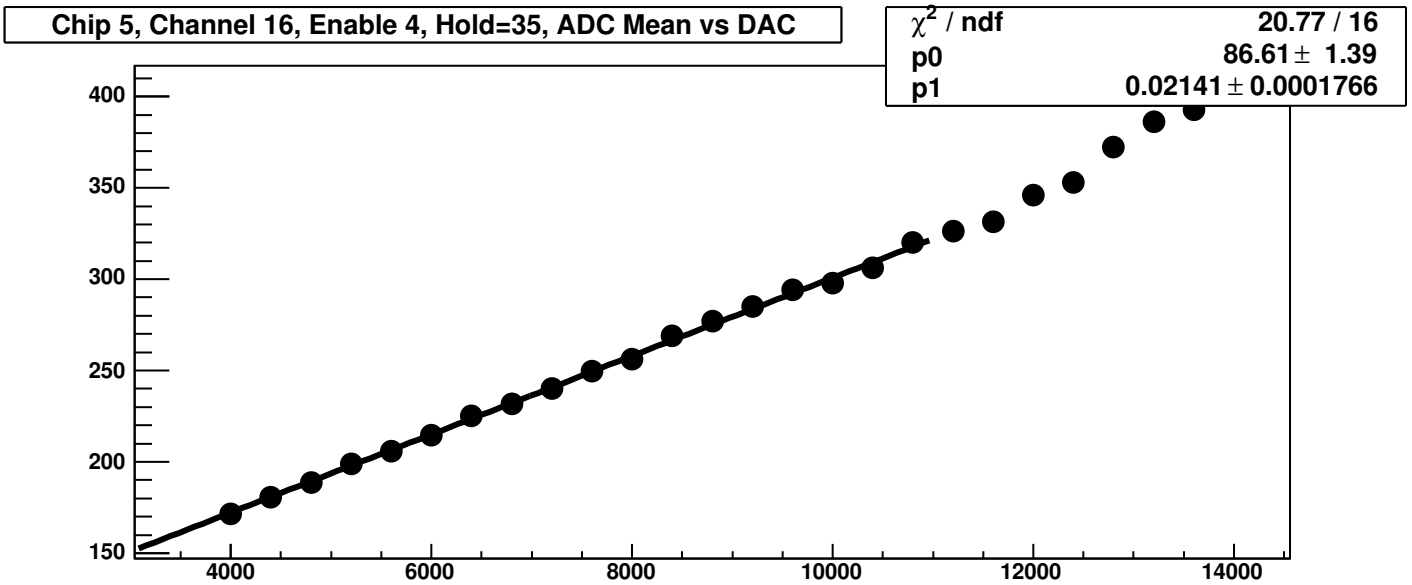

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
4000
6000
8000
10000
12000
14000

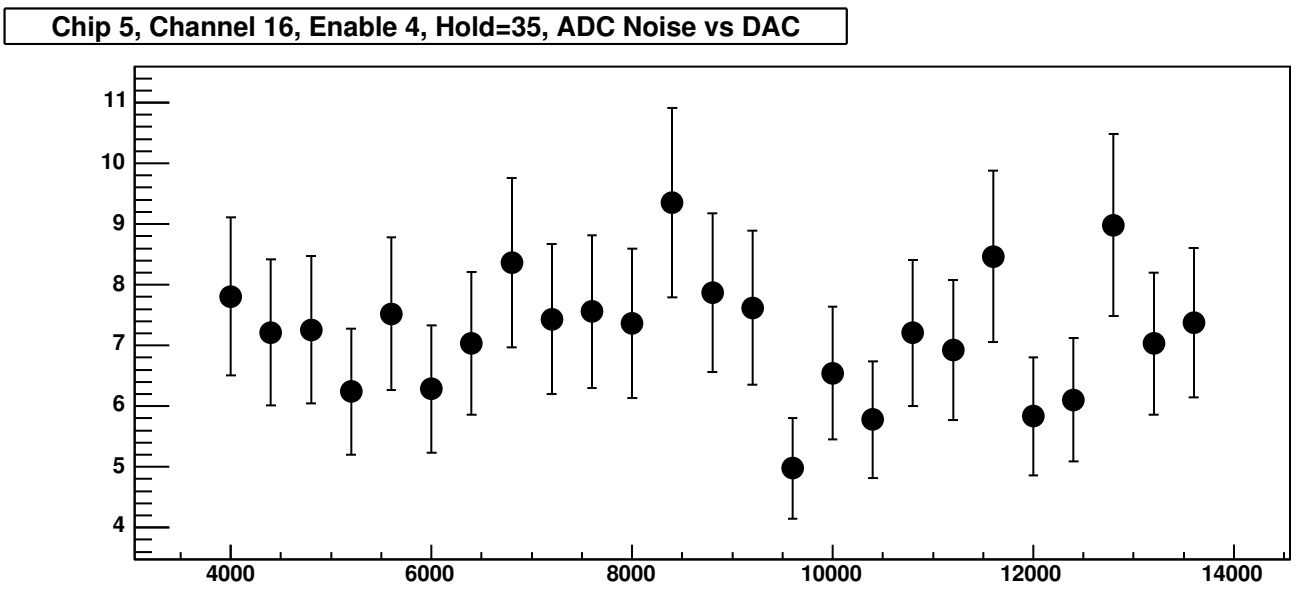

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
4000
6000
8000
10000
12000
14000

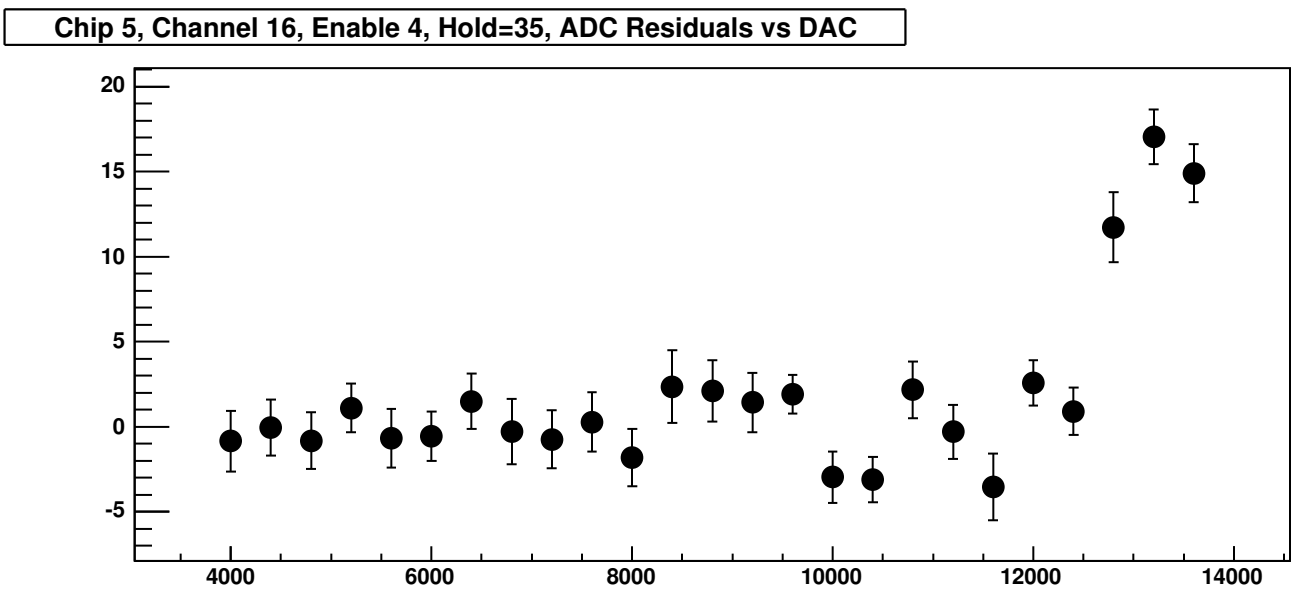

Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.12 / 16
p0 90.83 ± 1.124
p1 0.02087 ± 0.0001559
380
360
340
320
300
280
260
240
220
200
180
160
4000
6000
8000
10000
12000
14000

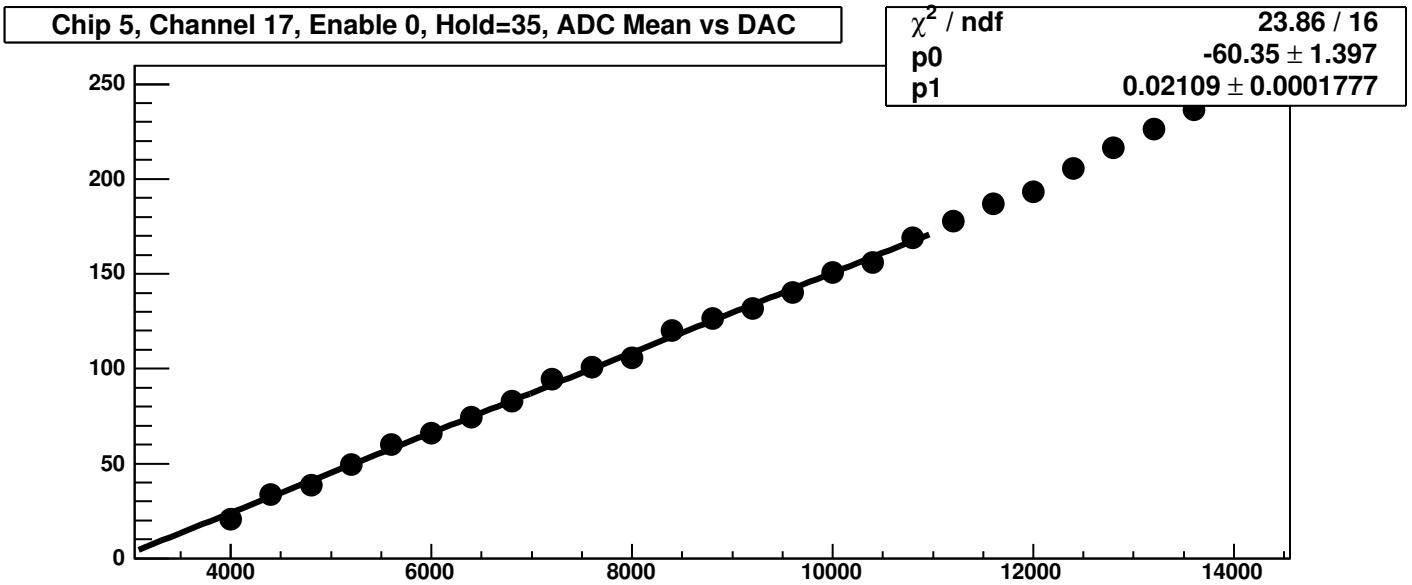

Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
4000
6000
8000
10000
12000
14000

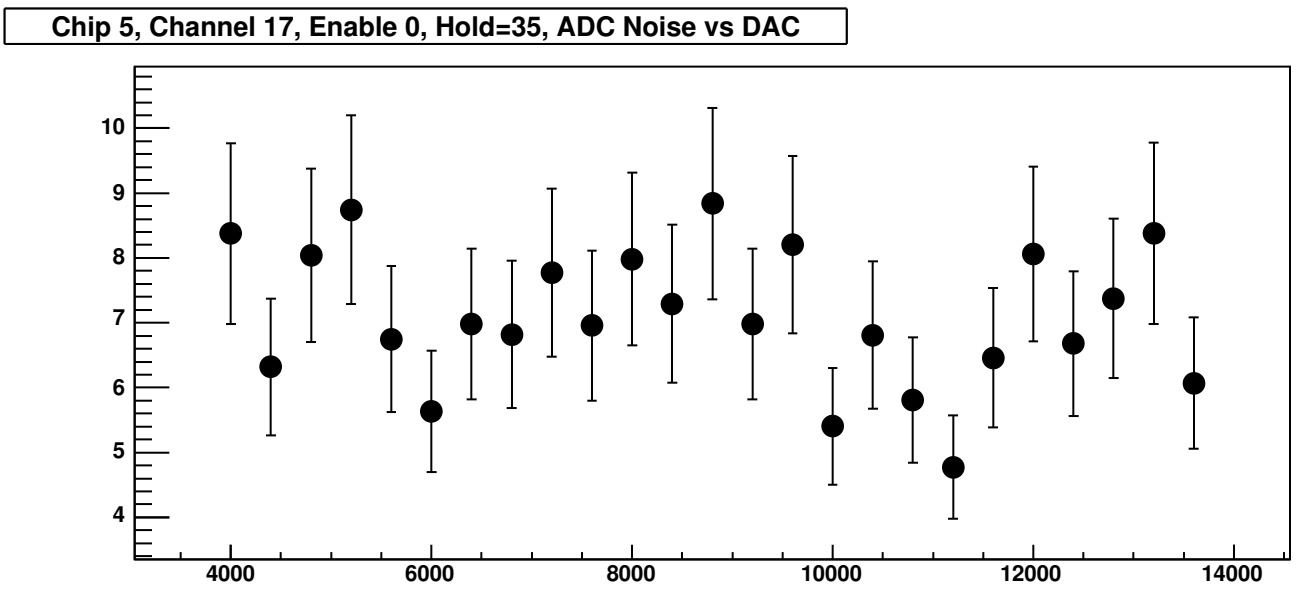

Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

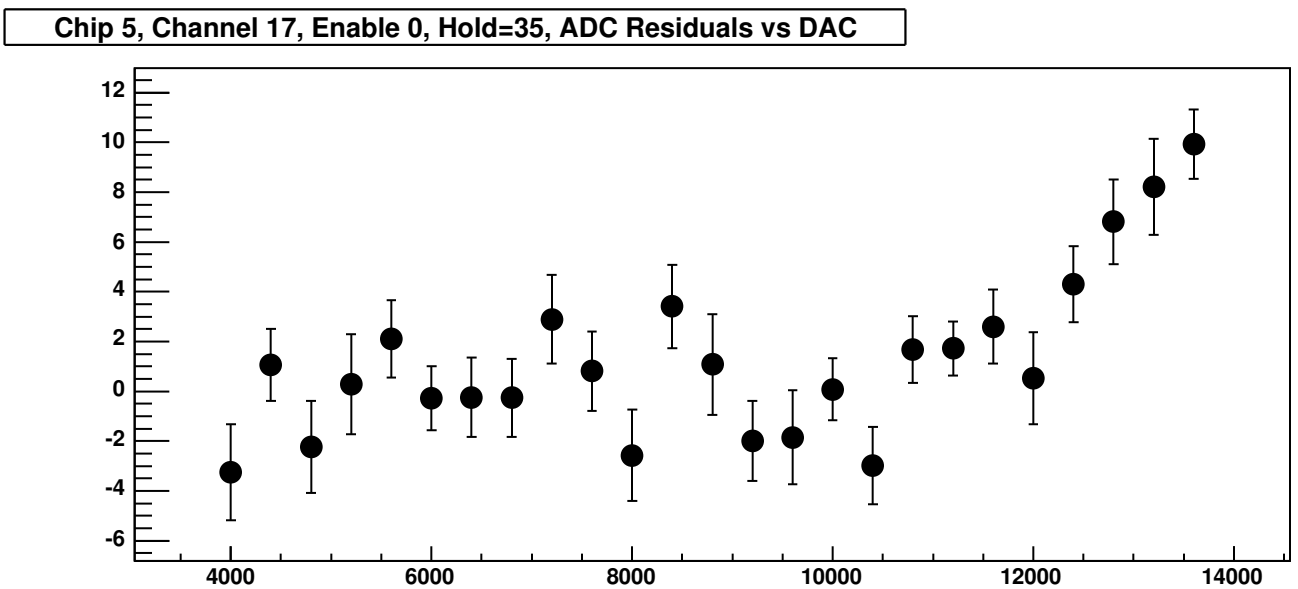

Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.77 / 16
p0 86.61 ± 1.39
p1 0.02141 ± 0.0001766
400
350
300
250
200
150
4000
6000
8000
10000
12000
14000

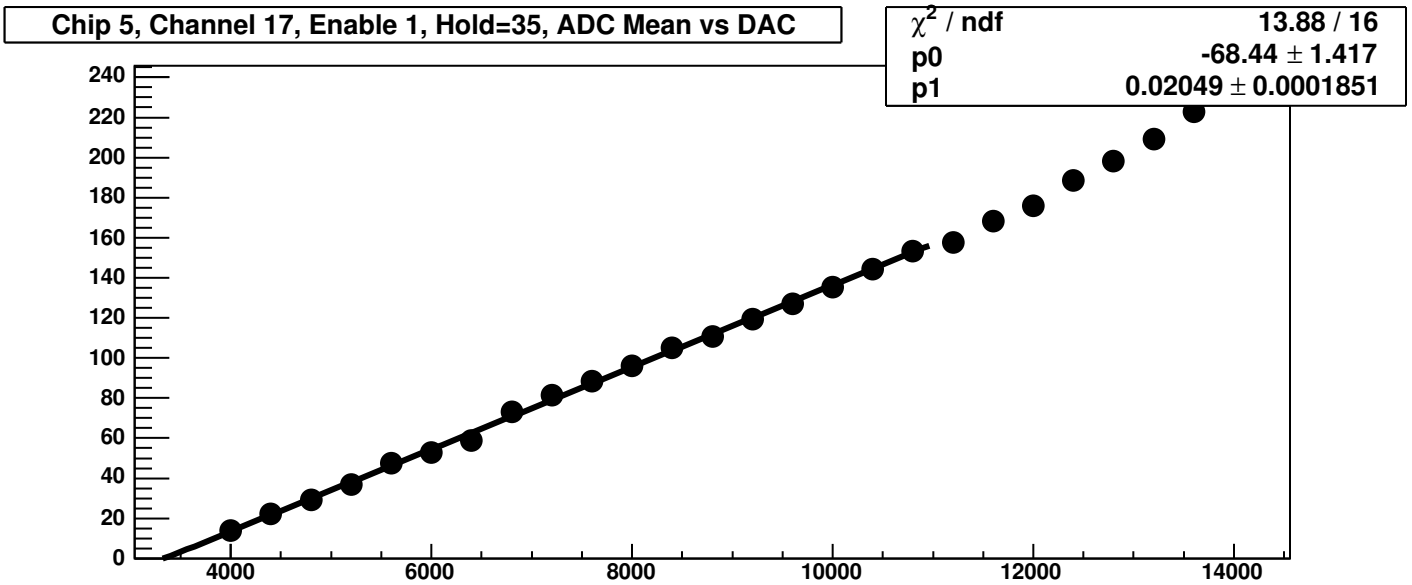

Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

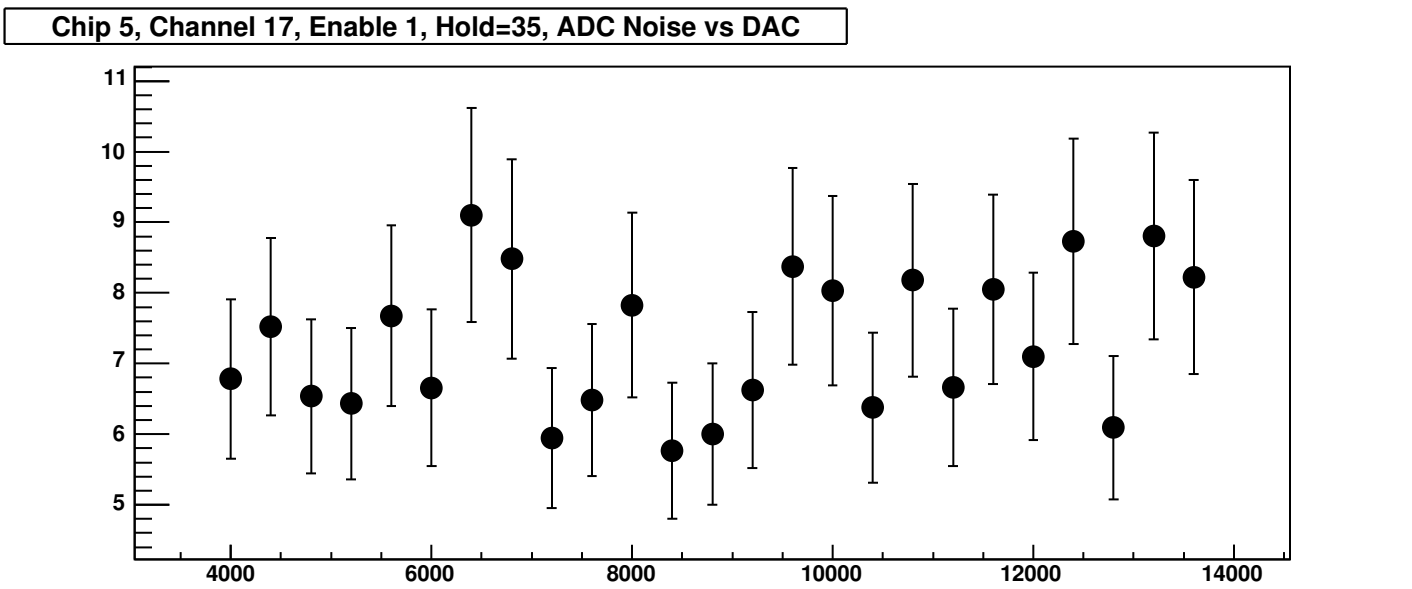

Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
4000
6000
8000
10000
12000
14000

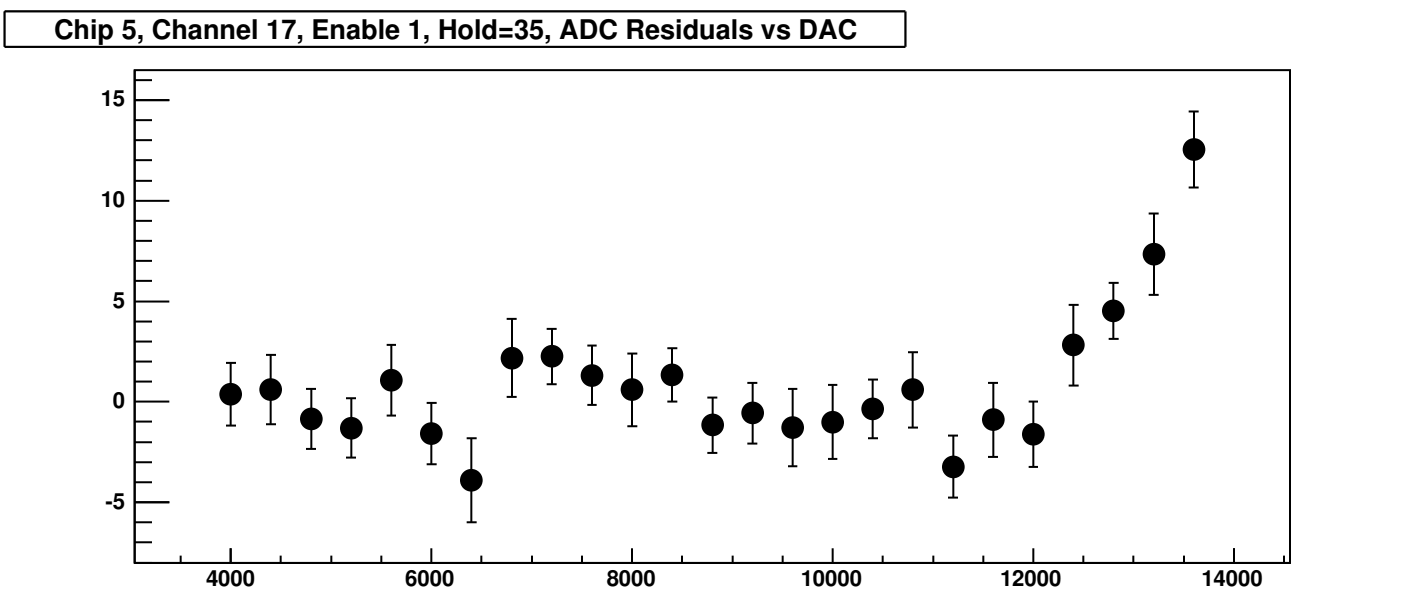

Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 23.86 / 16
p0 -60.35 ± 1.397
p1 0.02109 ± 0.0001777
250
200
150
100
50
0
4000
6000
8000
10000
12000
14000

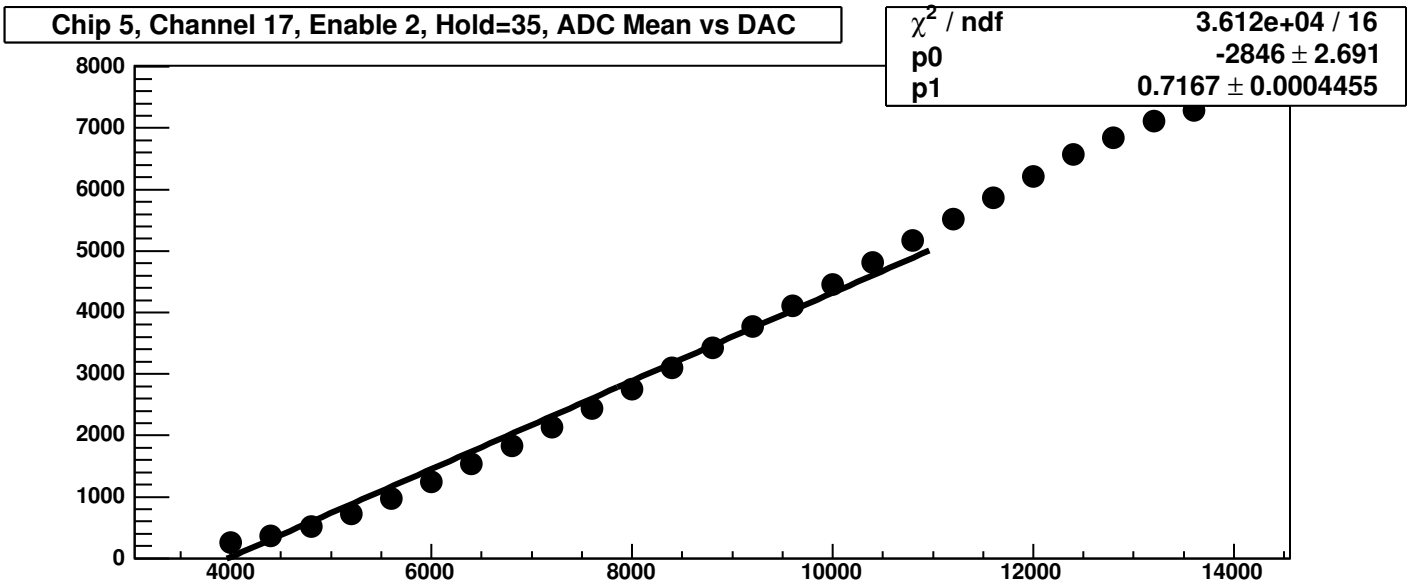

Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
4000
6000
8000
10000
12000
14000

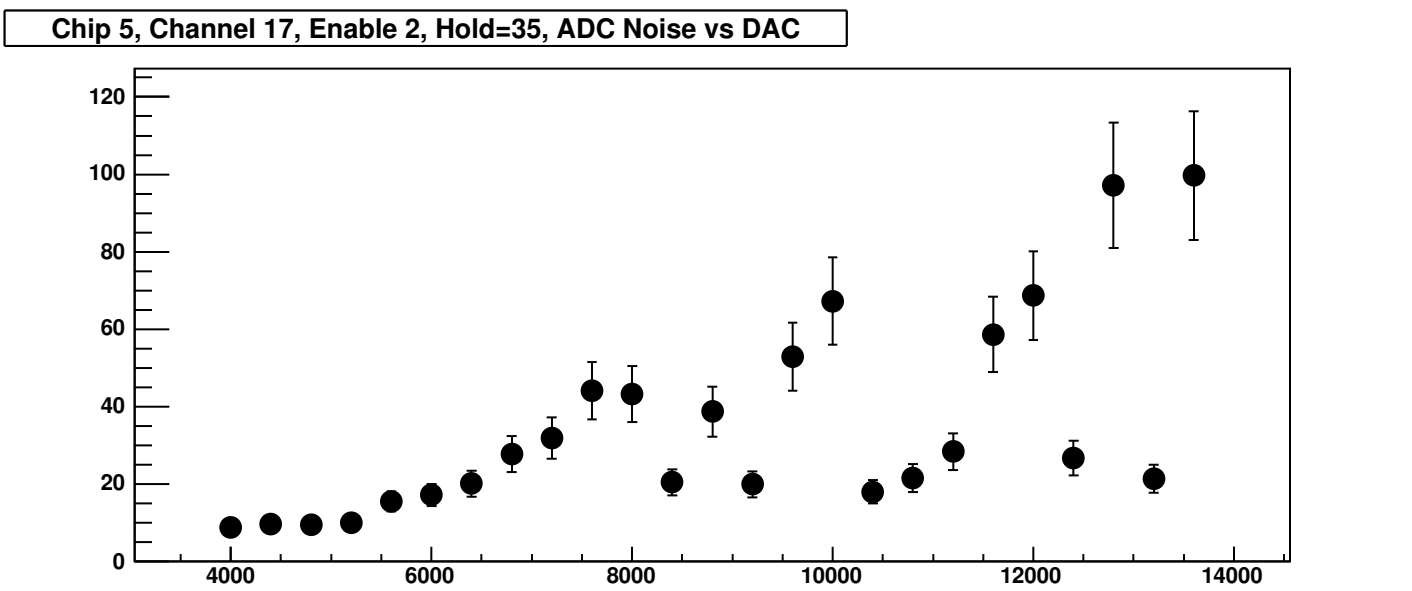

Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
4000
6000
8000
10000
12000
14000

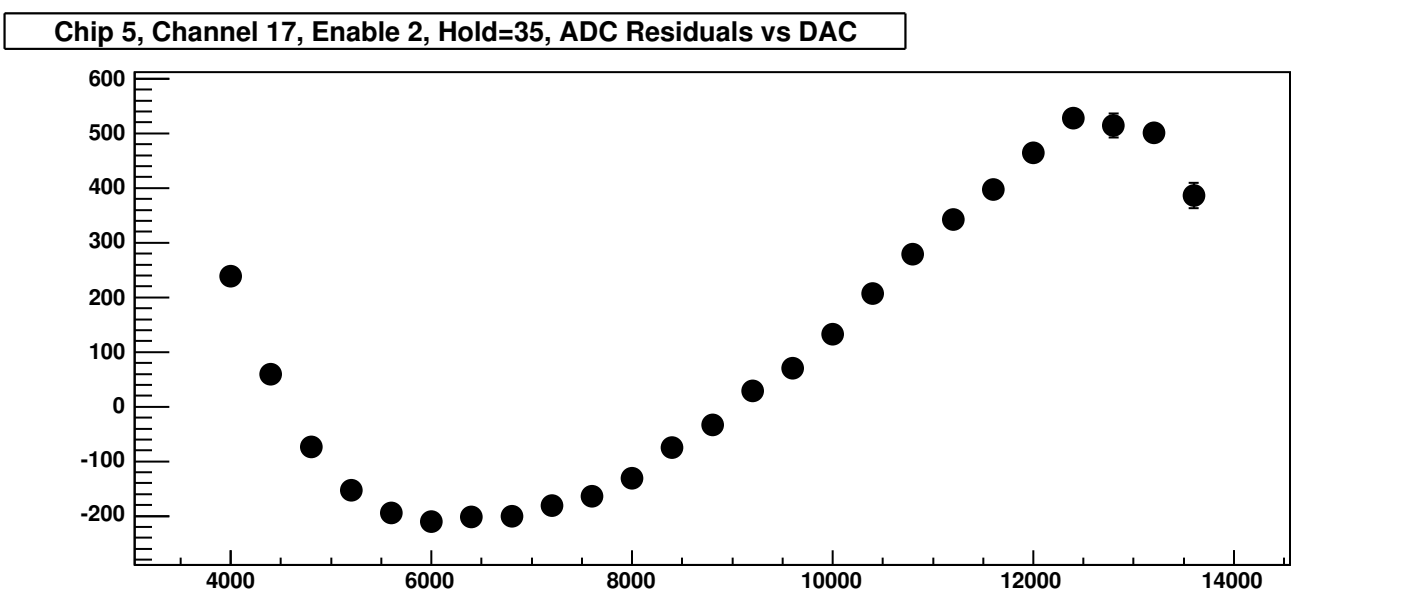

Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 13.88 / 16
p0 -68.44 ± 1.417
p1 0.02049 ± 0.0001851
240
220
200
180
160
140
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000


Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4000
6000
8000
10000
12000
14000

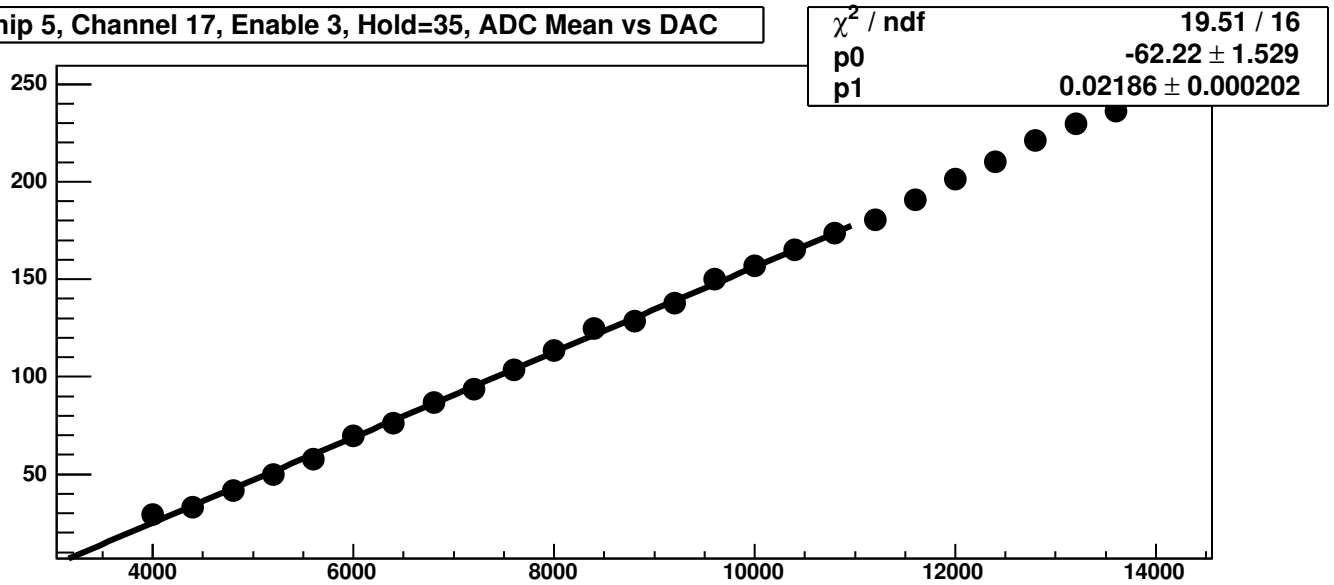

Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
4000
6000
8000
10000
12000
14000


Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3.612e+04 / 16
p0 -2846 ± 2.691
p1 0.7167 ± 0.0004455
8000
7000
6000
5000
4000
3000
2000
1000
0
4000
6000
8000
10000
12000
14000

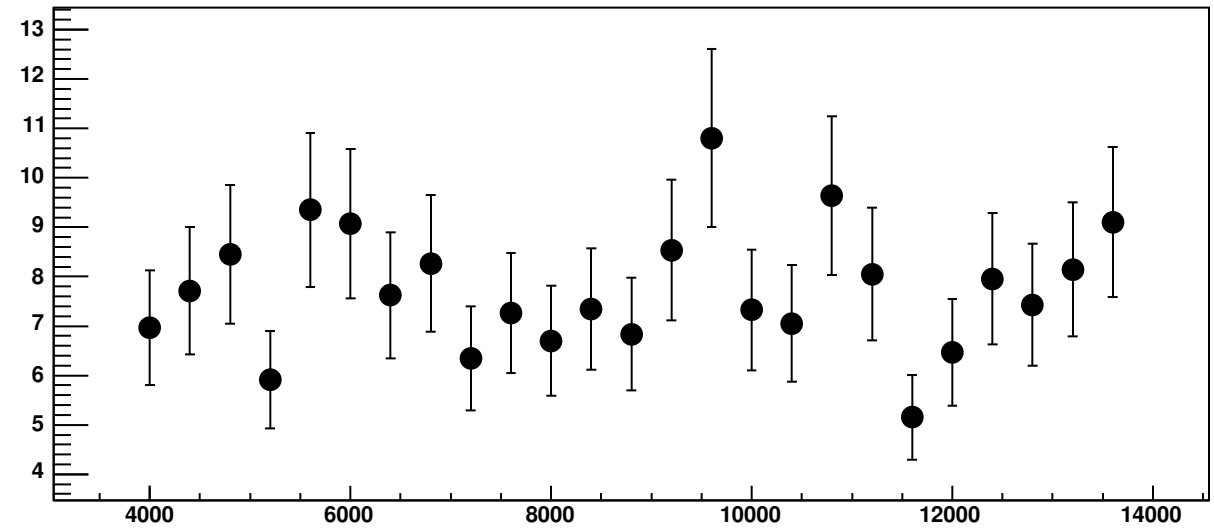
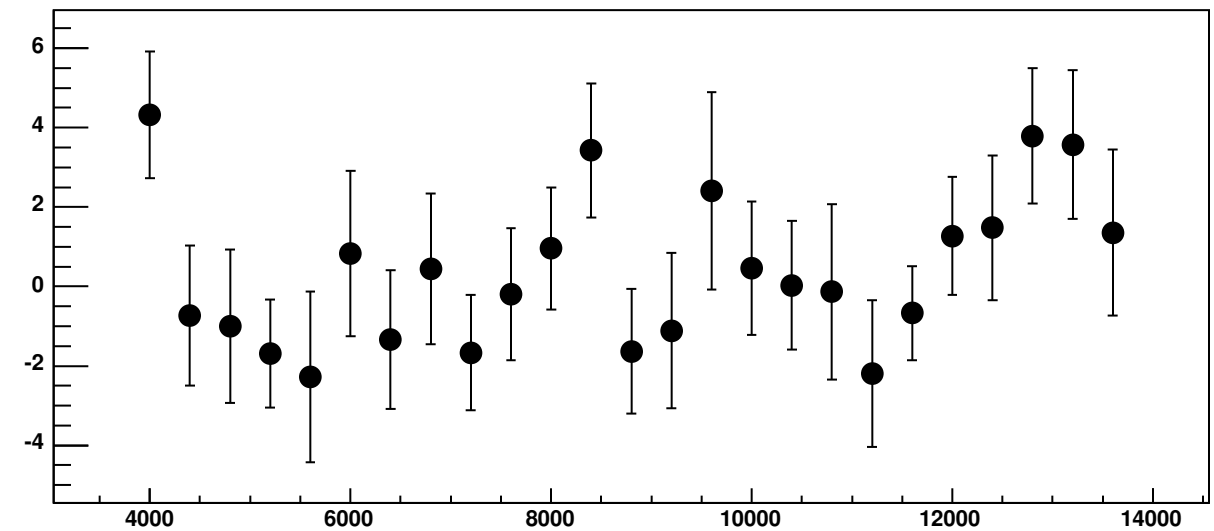

Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
4000
6000
8000
10000
12000
14000


Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
4000
6000
8000
10000
12000
14000

Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC