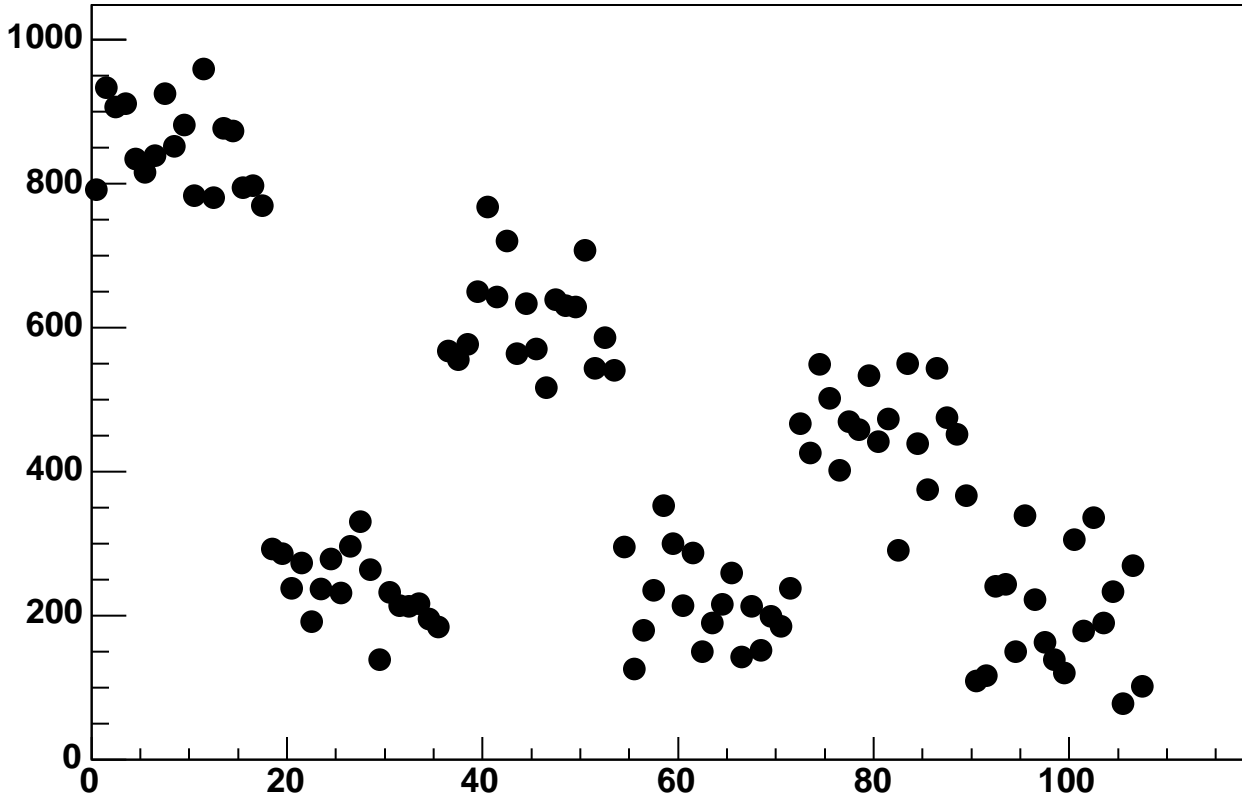
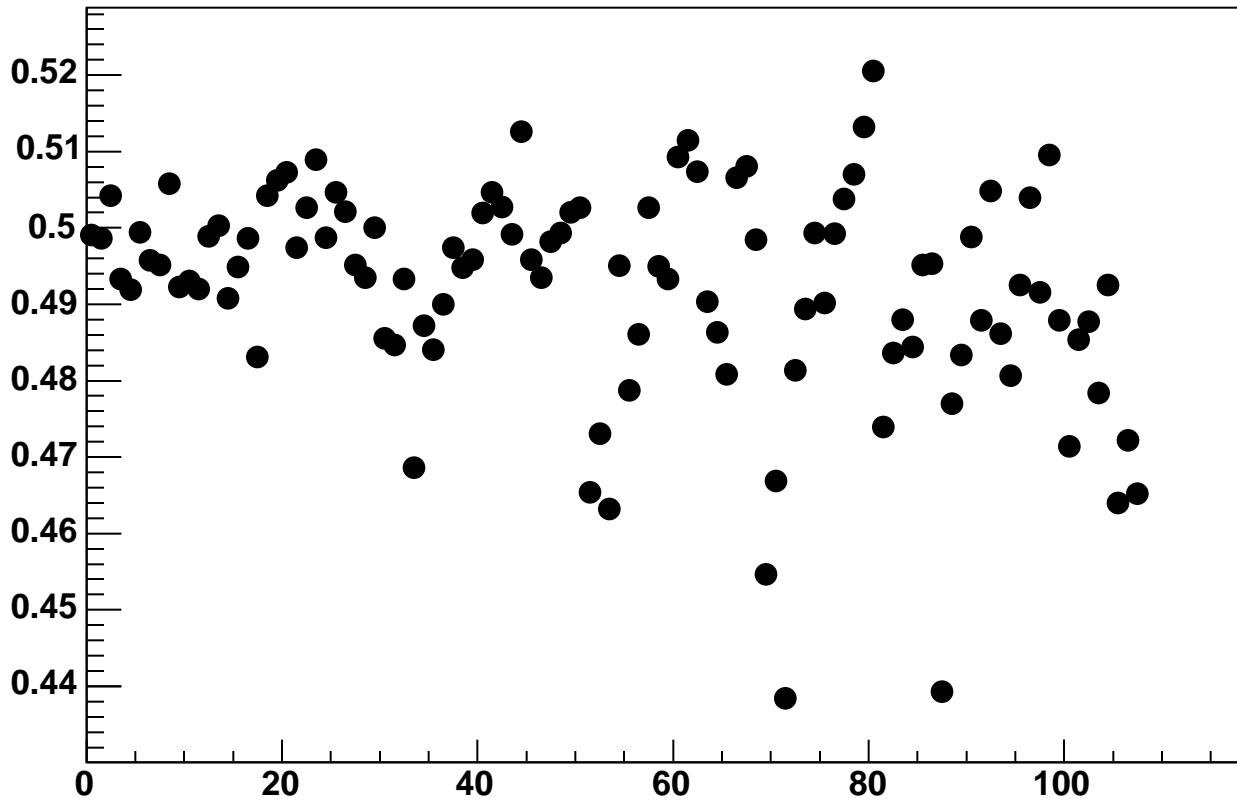


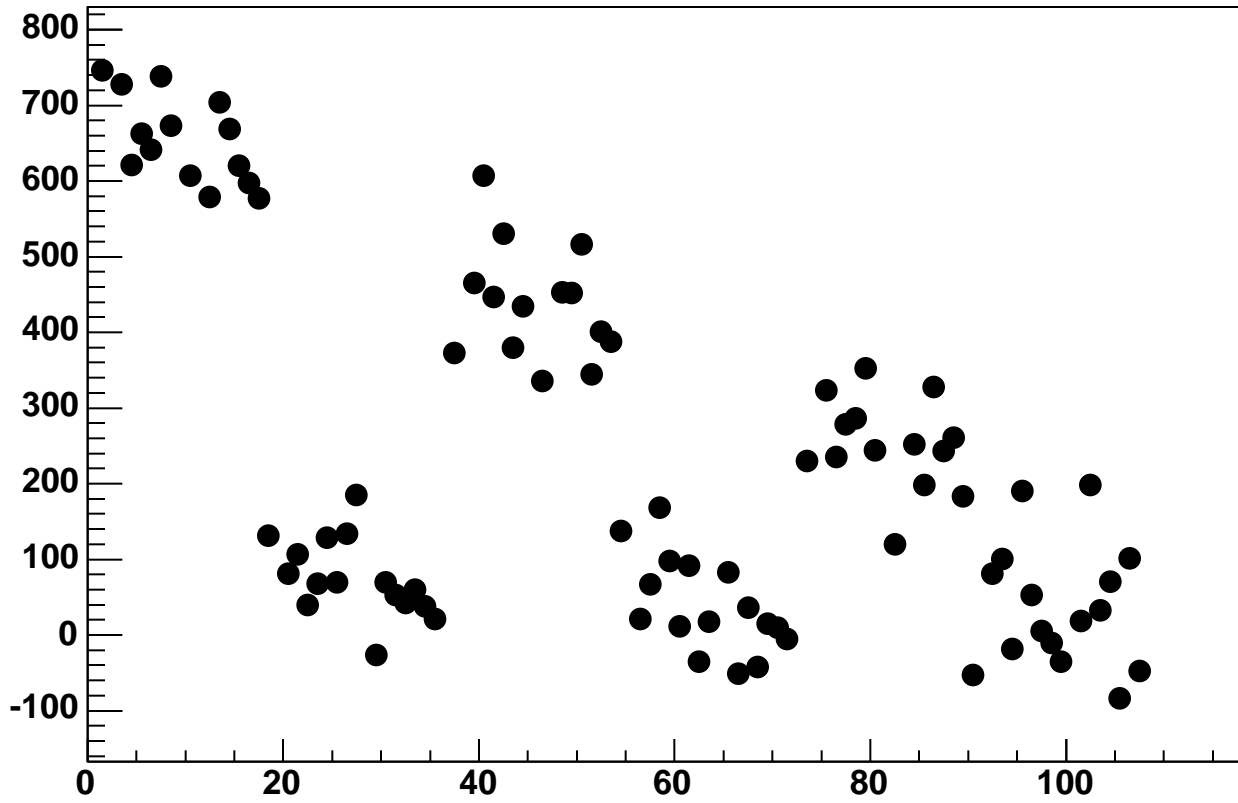
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



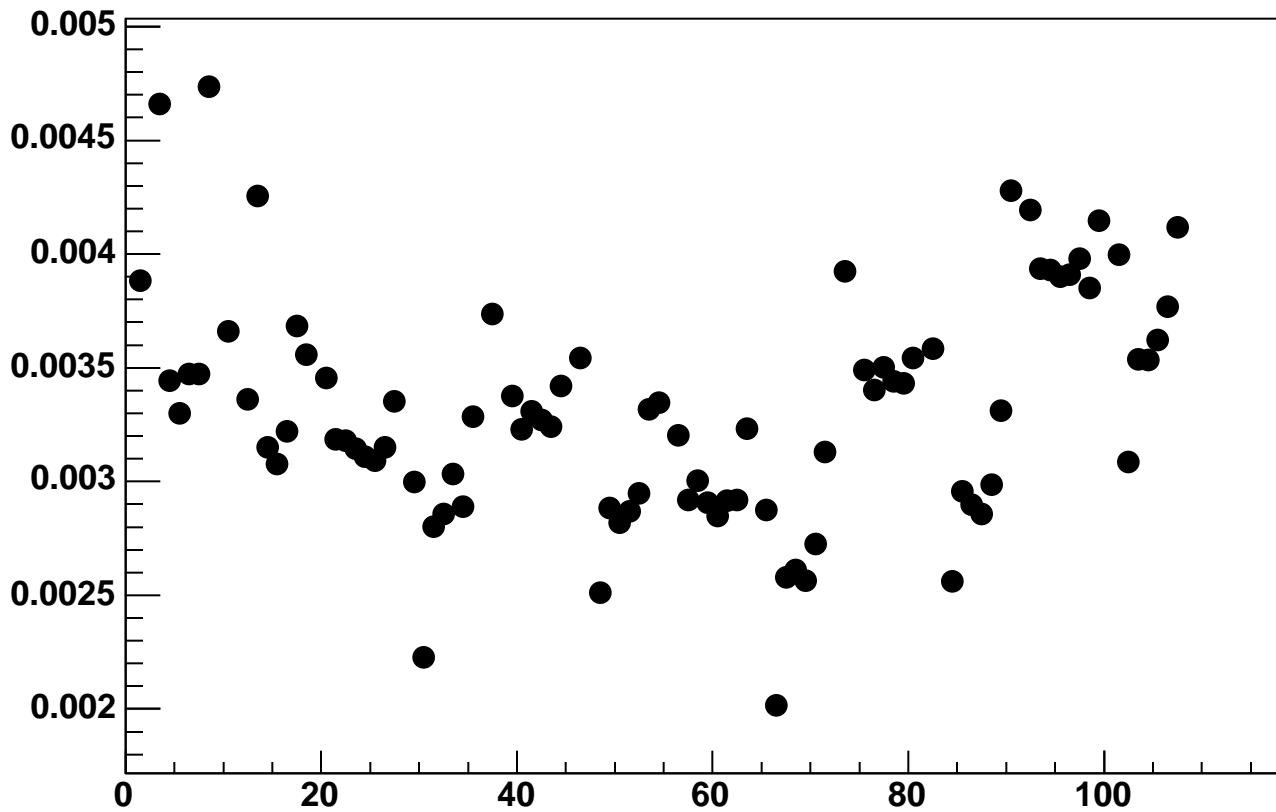
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



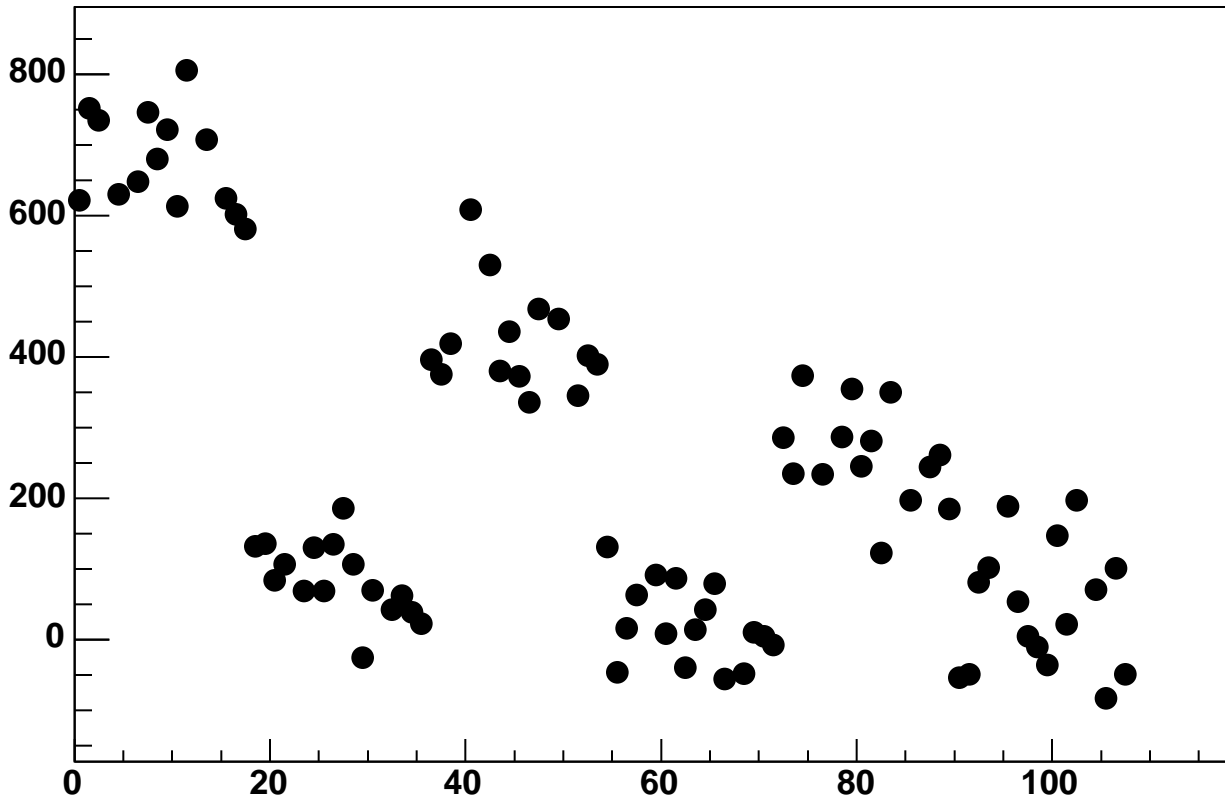
Disabled 0, Hold=35, Fit Intercept vs 18*Chip+Chan



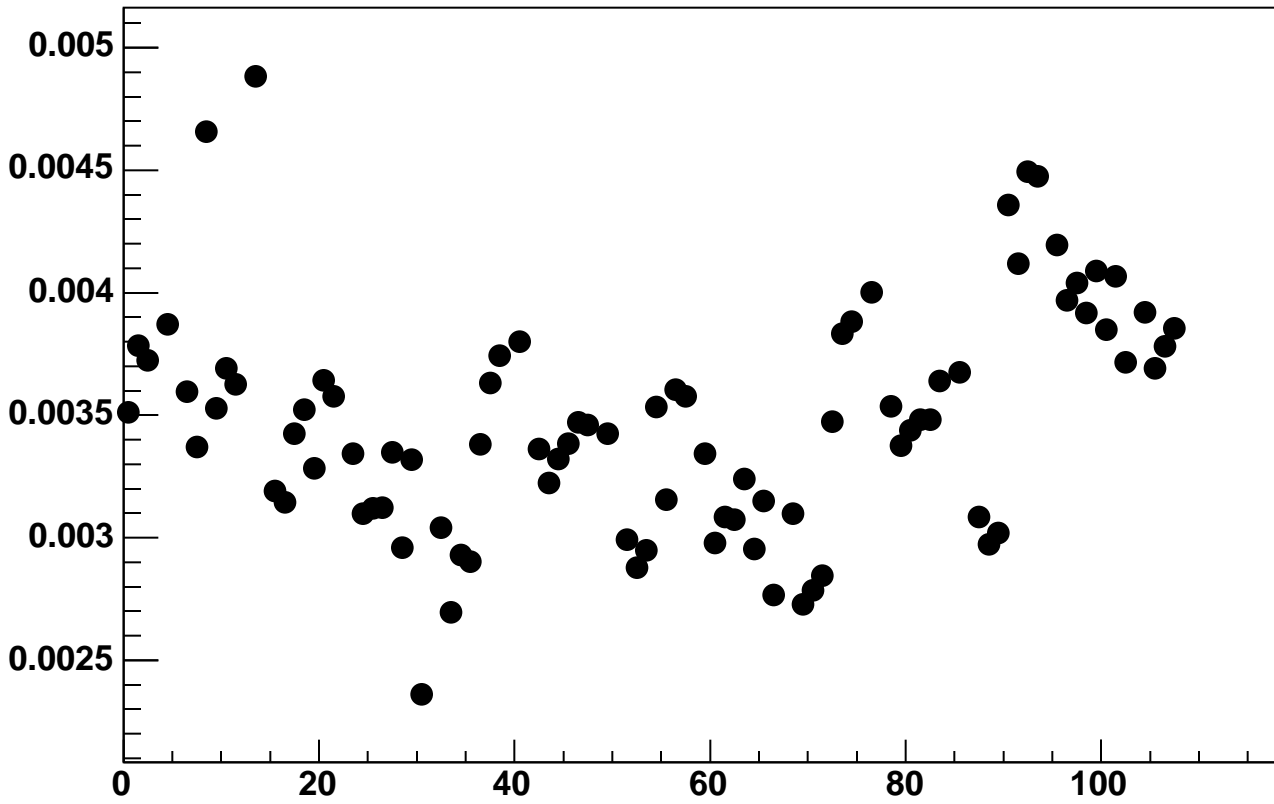
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



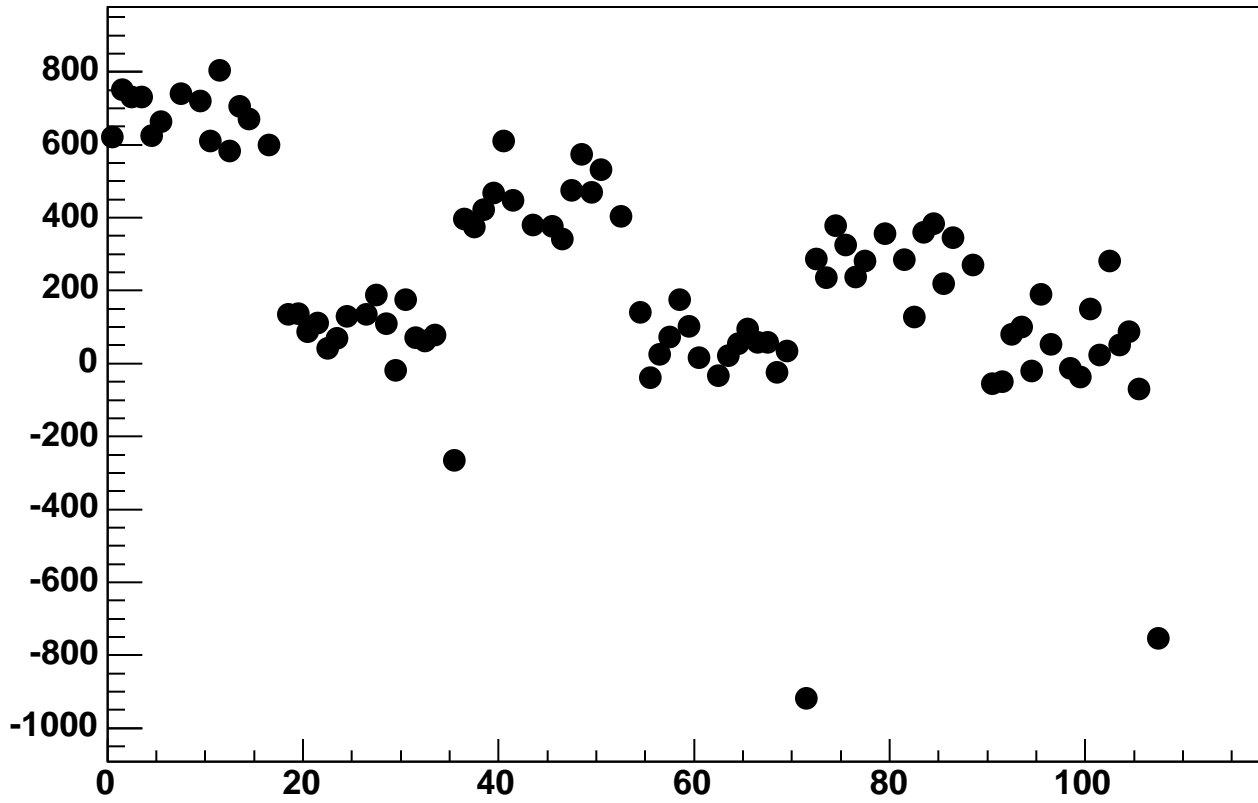
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



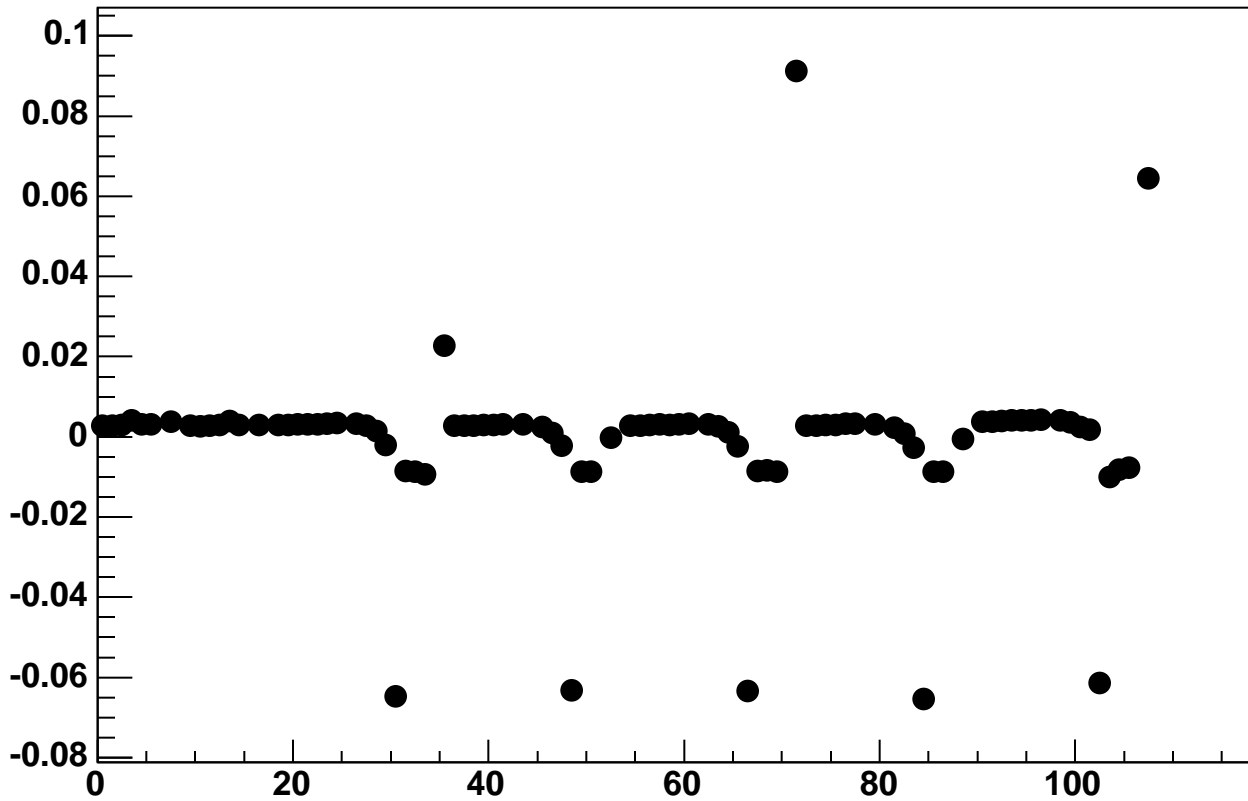
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



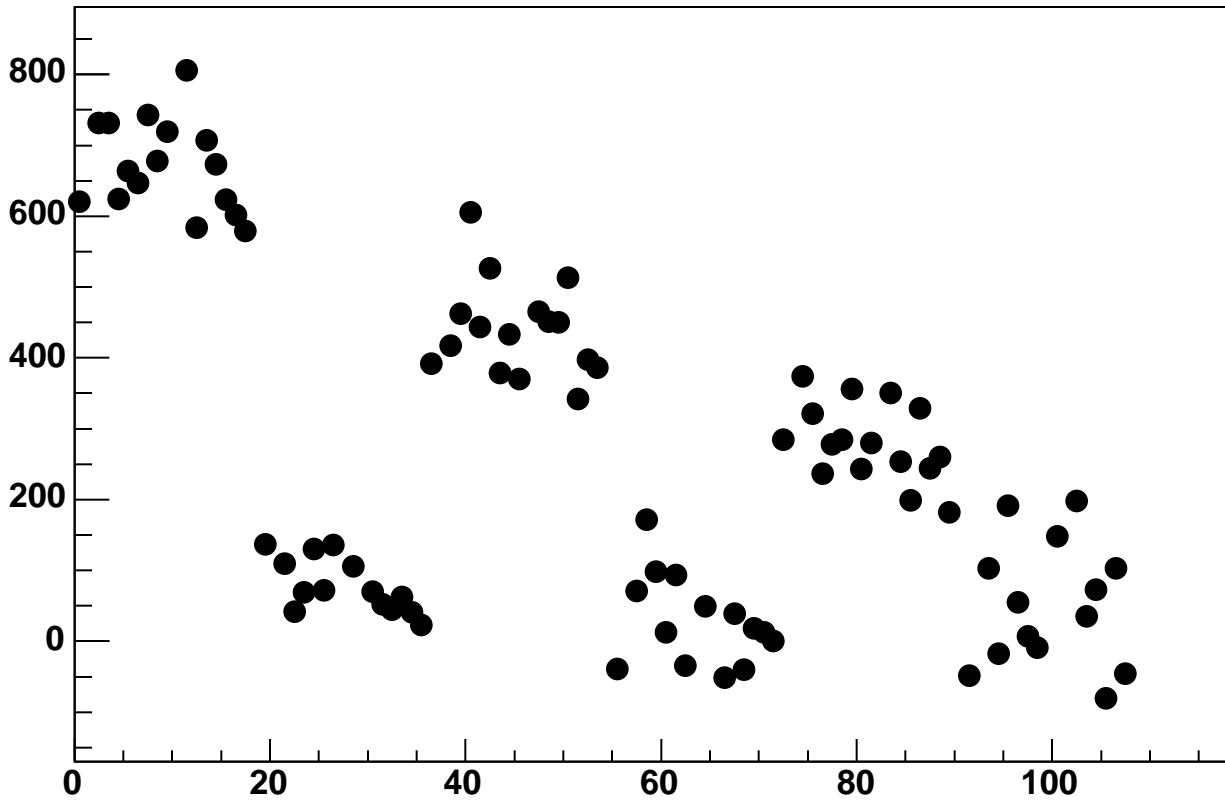
Disabled 2, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



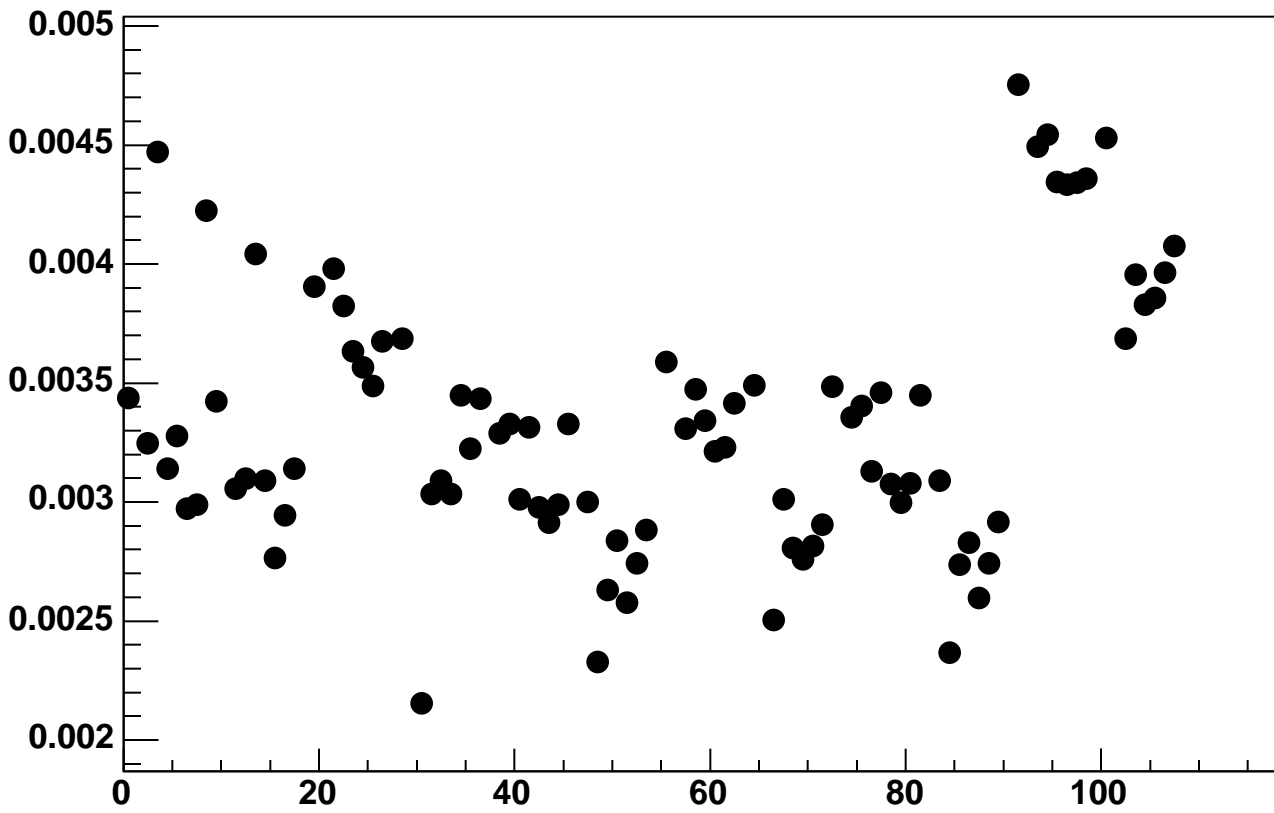
Disabled 2, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



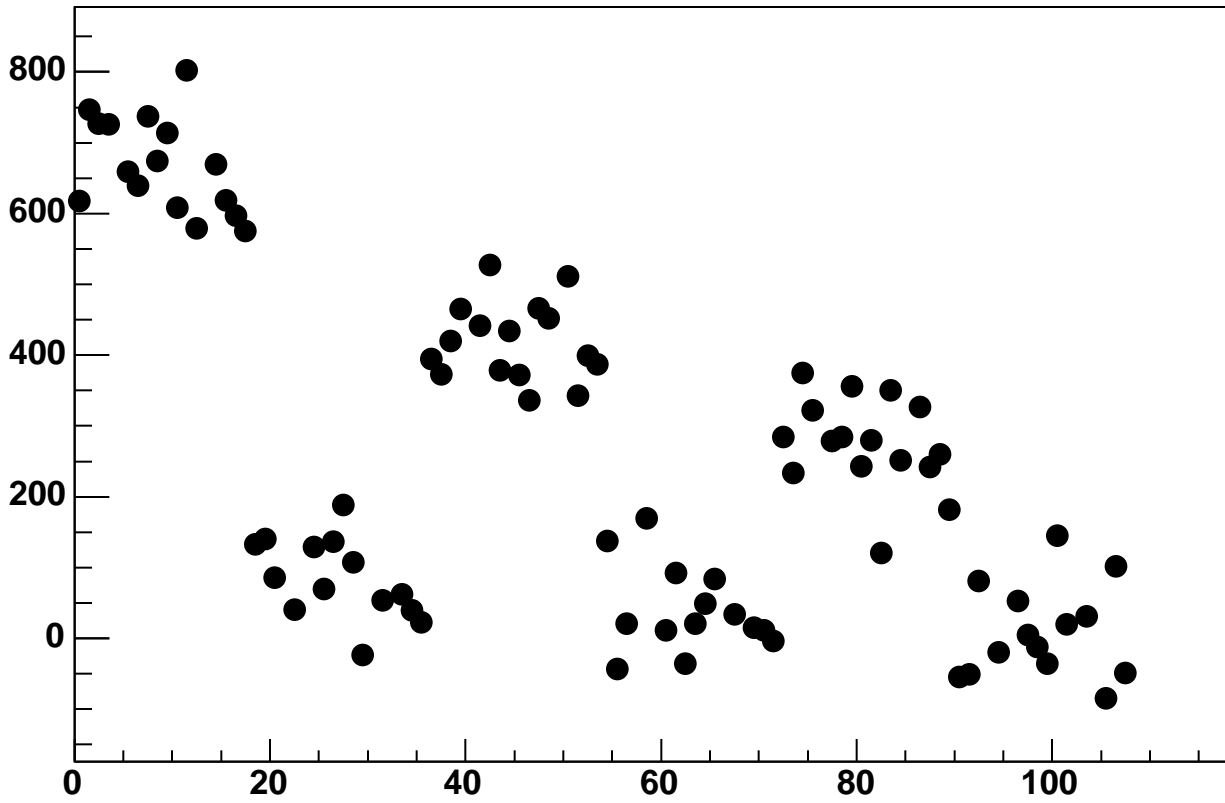
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



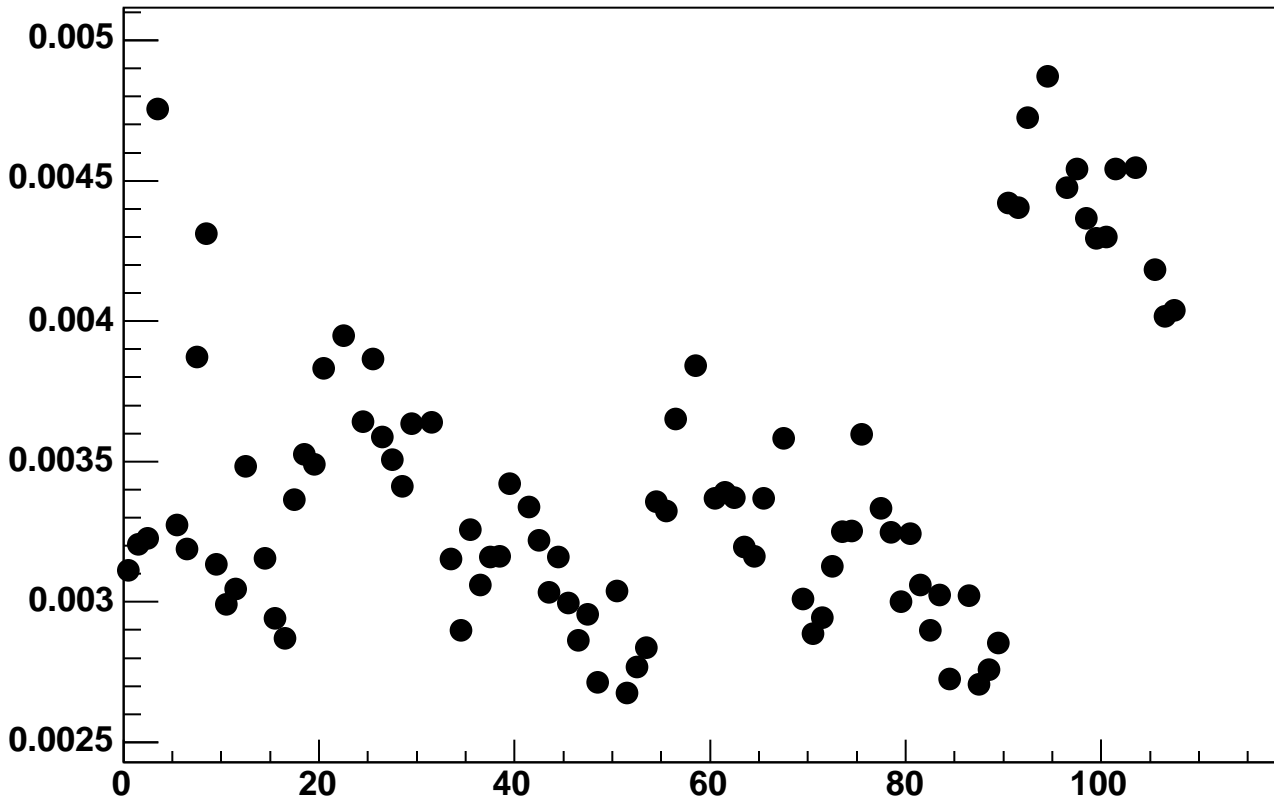
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



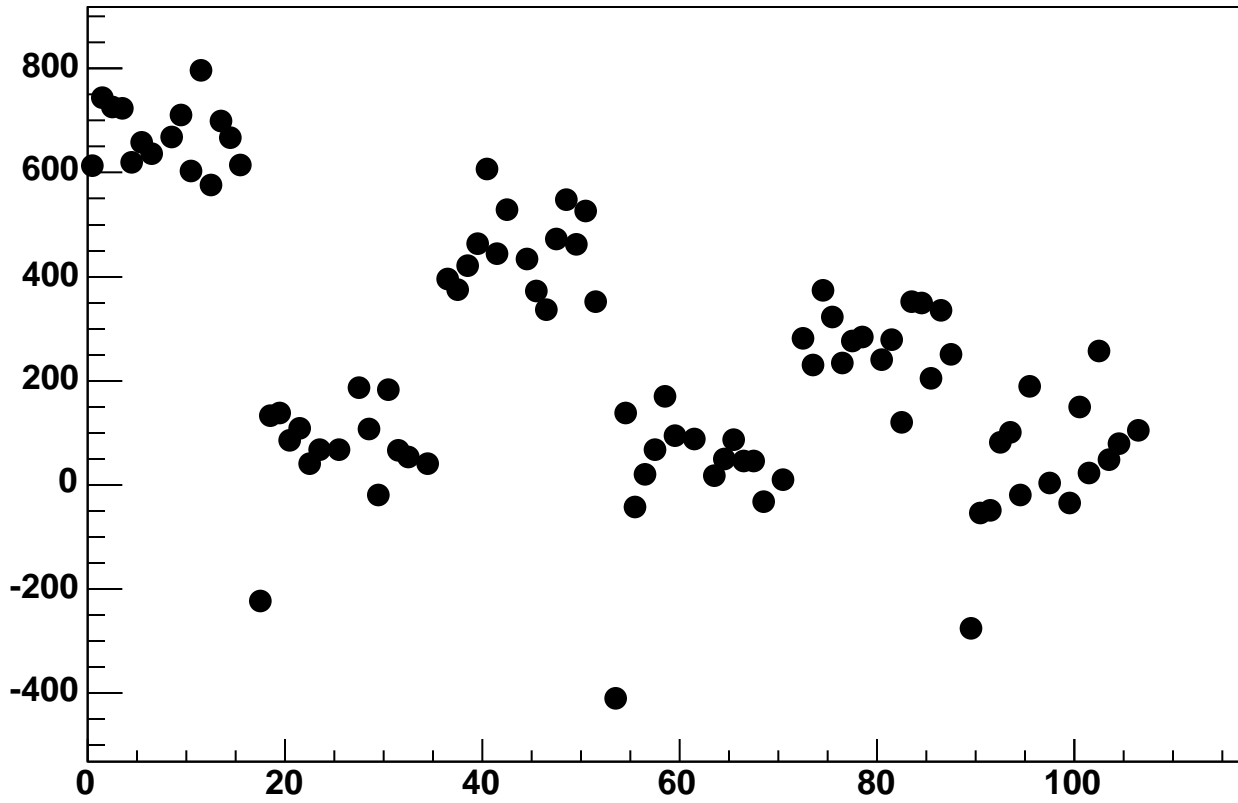
Disabled 4, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



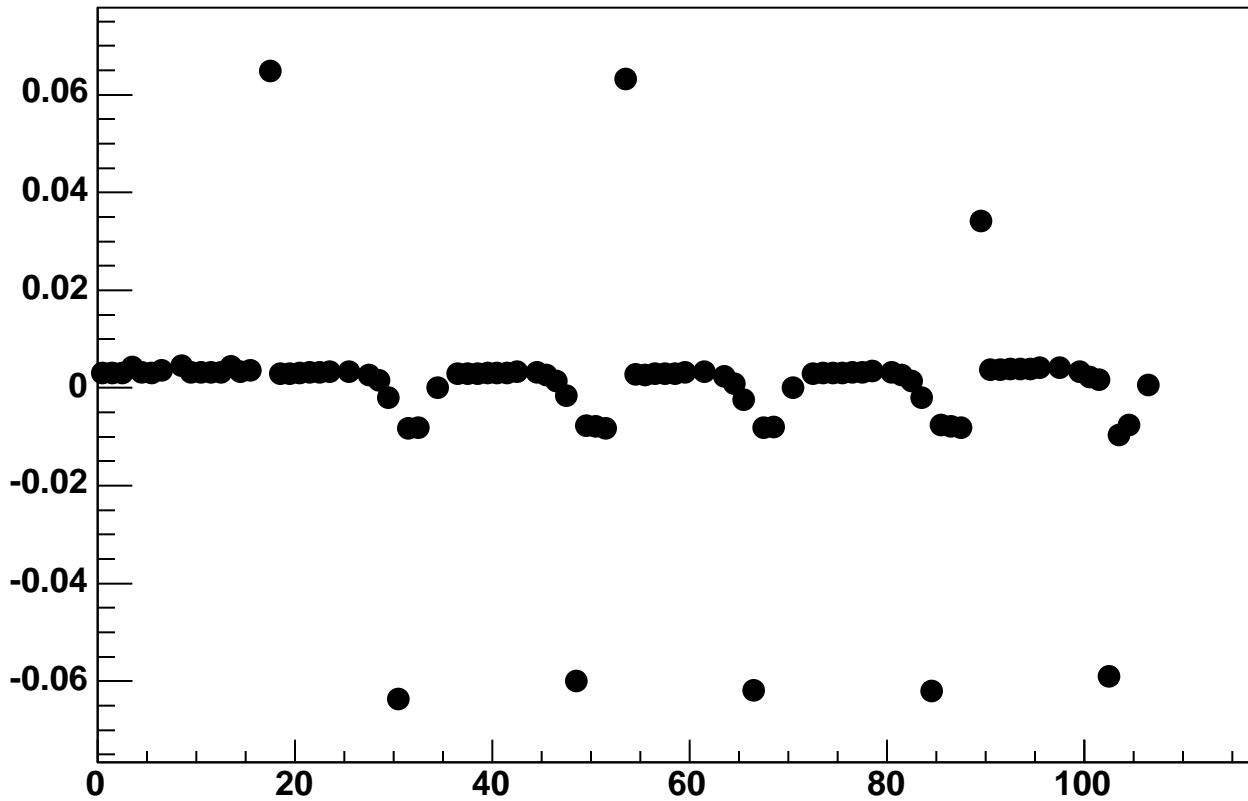
Disabled 4, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



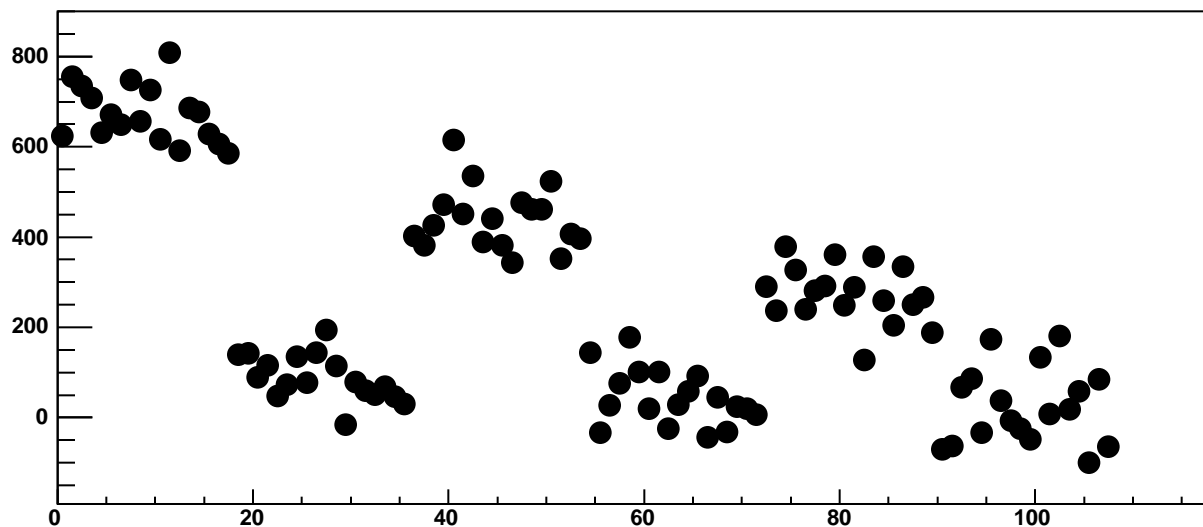
Disabled 5, Hold=35, Fit Intercept vs $18 \cdot \text{Chip} + \text{Chan}$



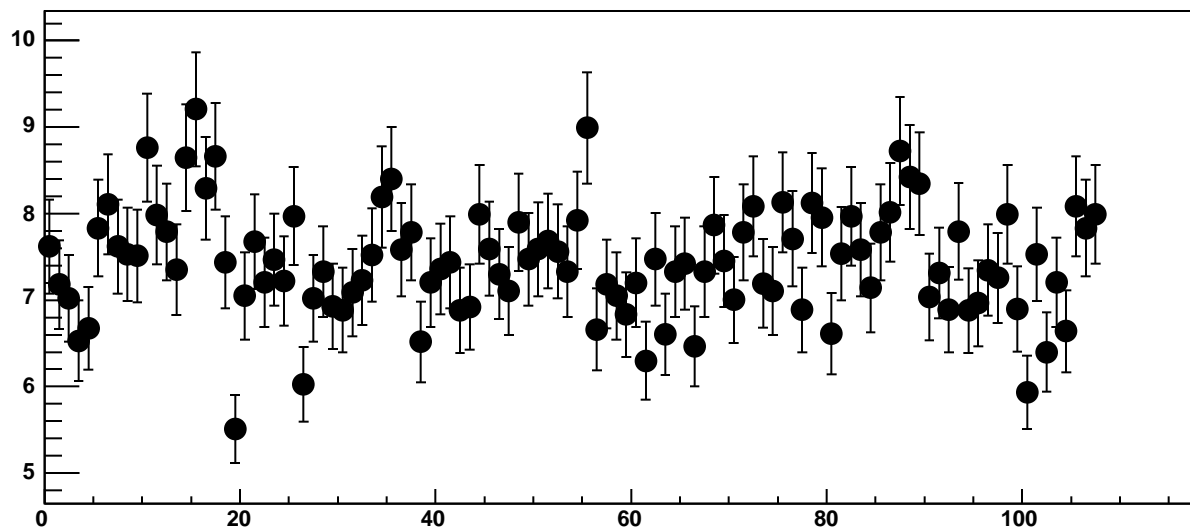
Disabled 5, Hold=35, Fit Slope vs $18 \cdot \text{Chip} + \text{Chan}$



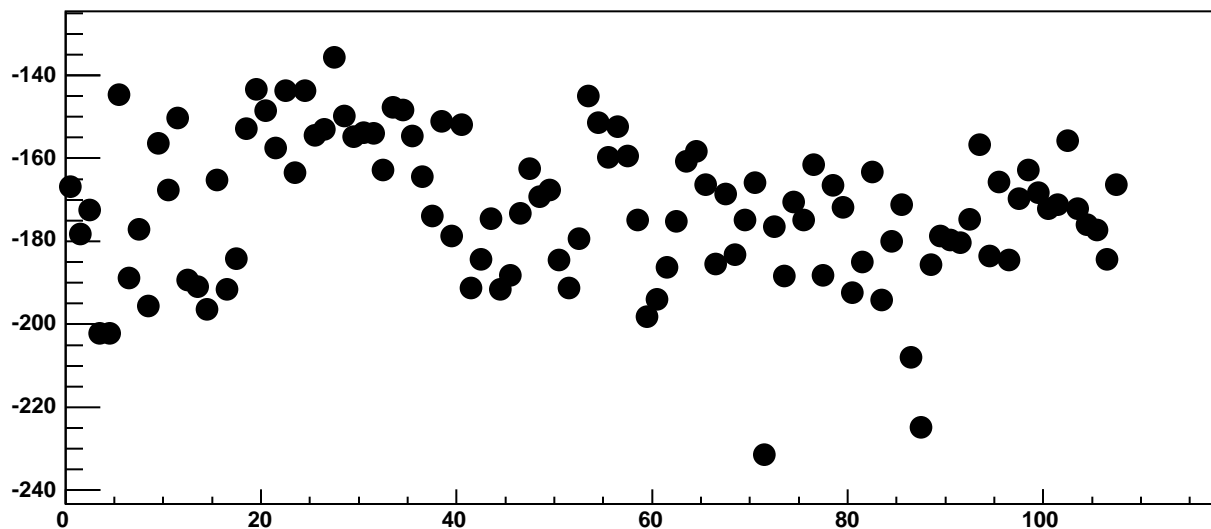
Enables off, ADC Mean vs 18*Chip+Chan



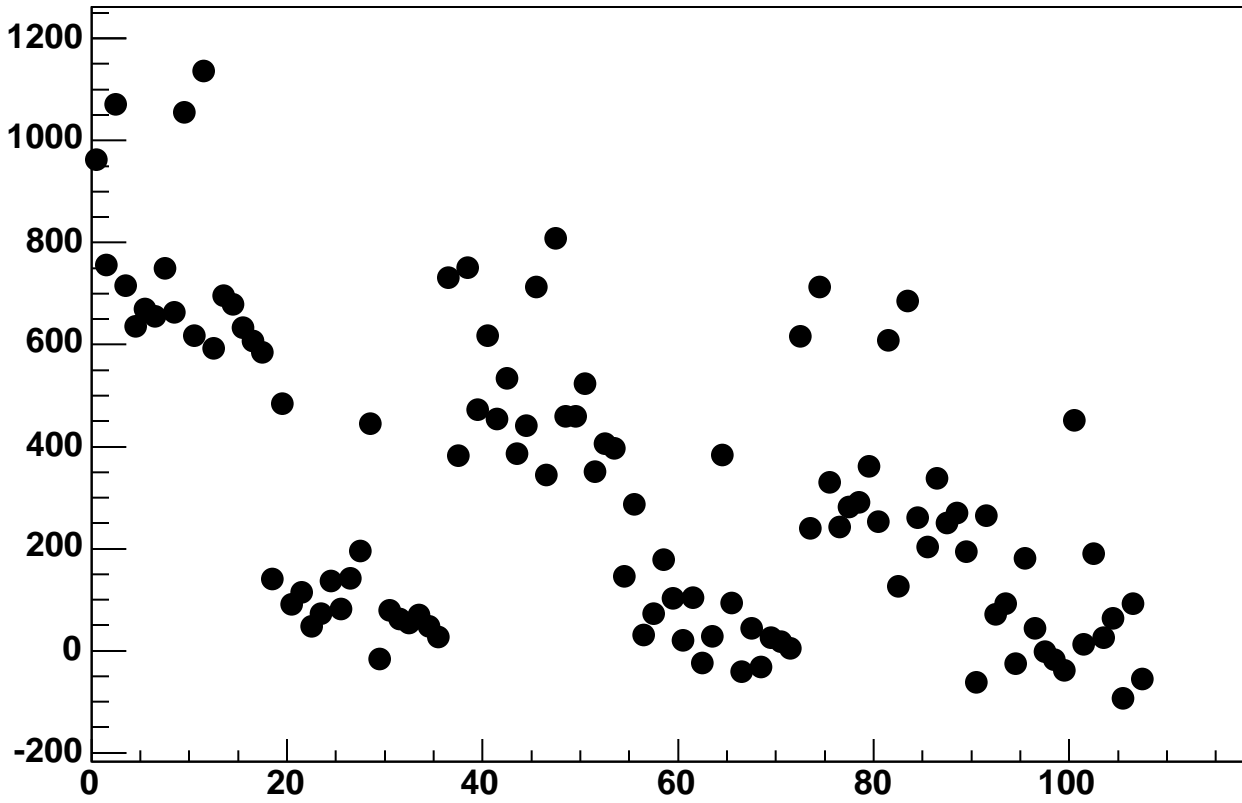
Enables off, ADC Noise vs 18*Chip+Chan



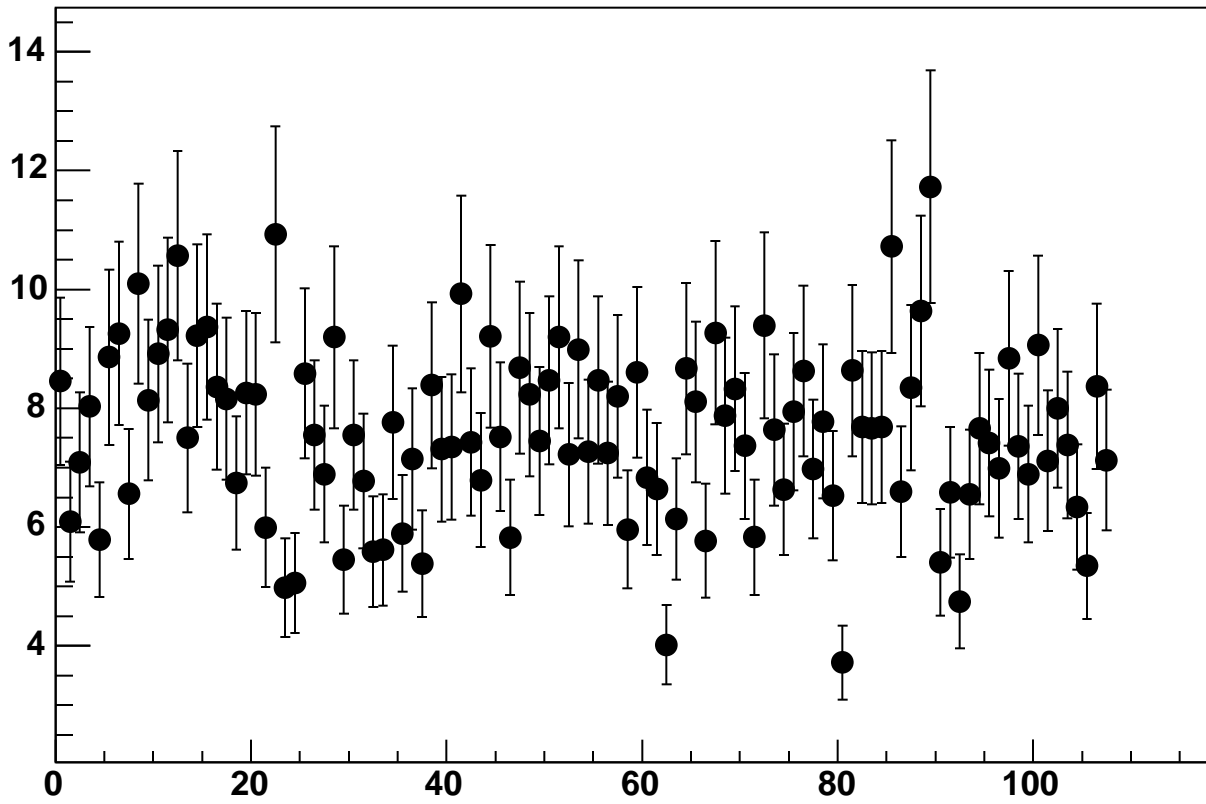
Enables off, ADC Residuals vs 18*Chip+Chan



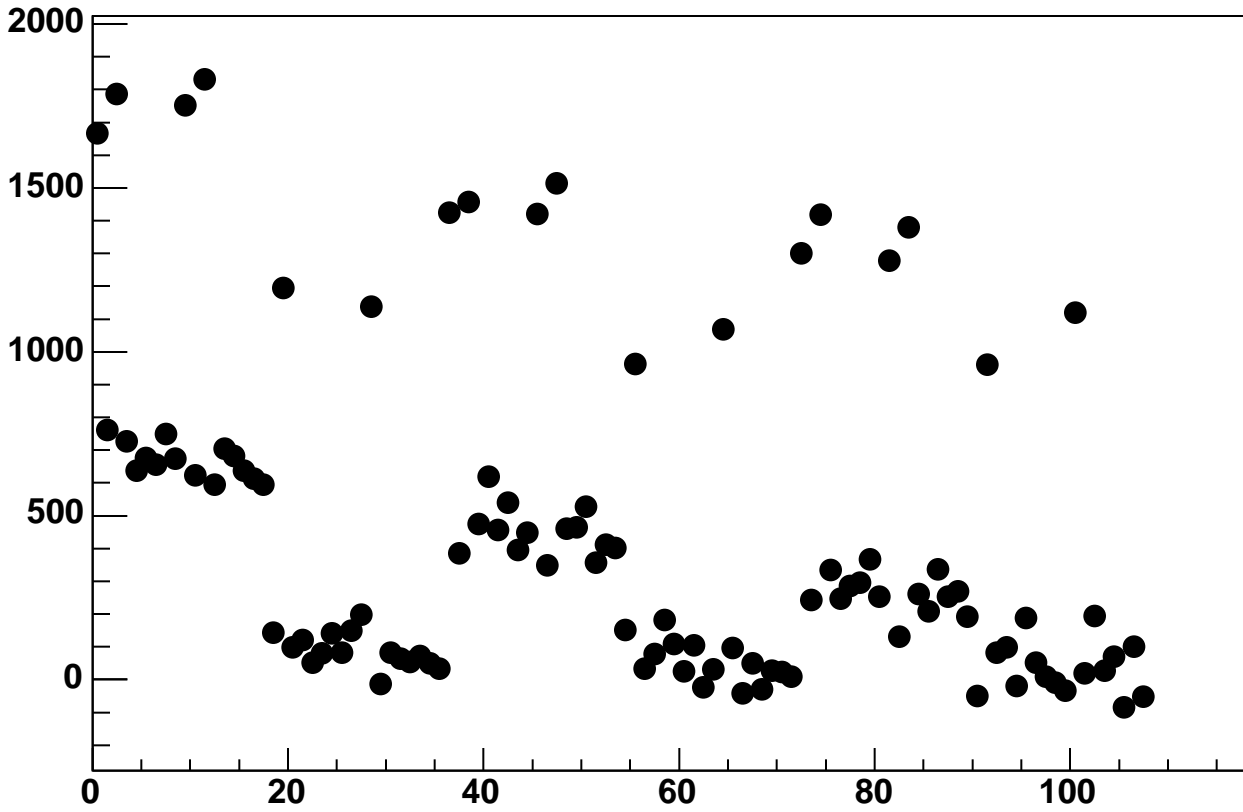
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



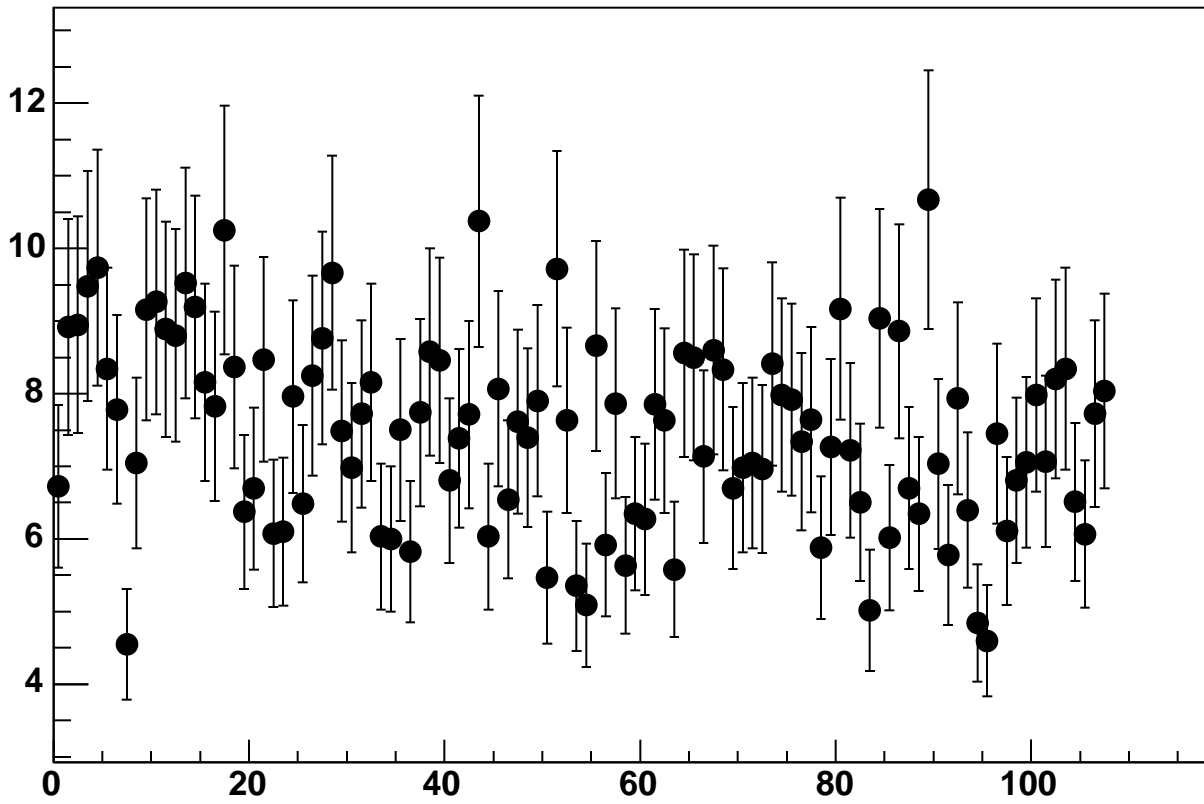
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



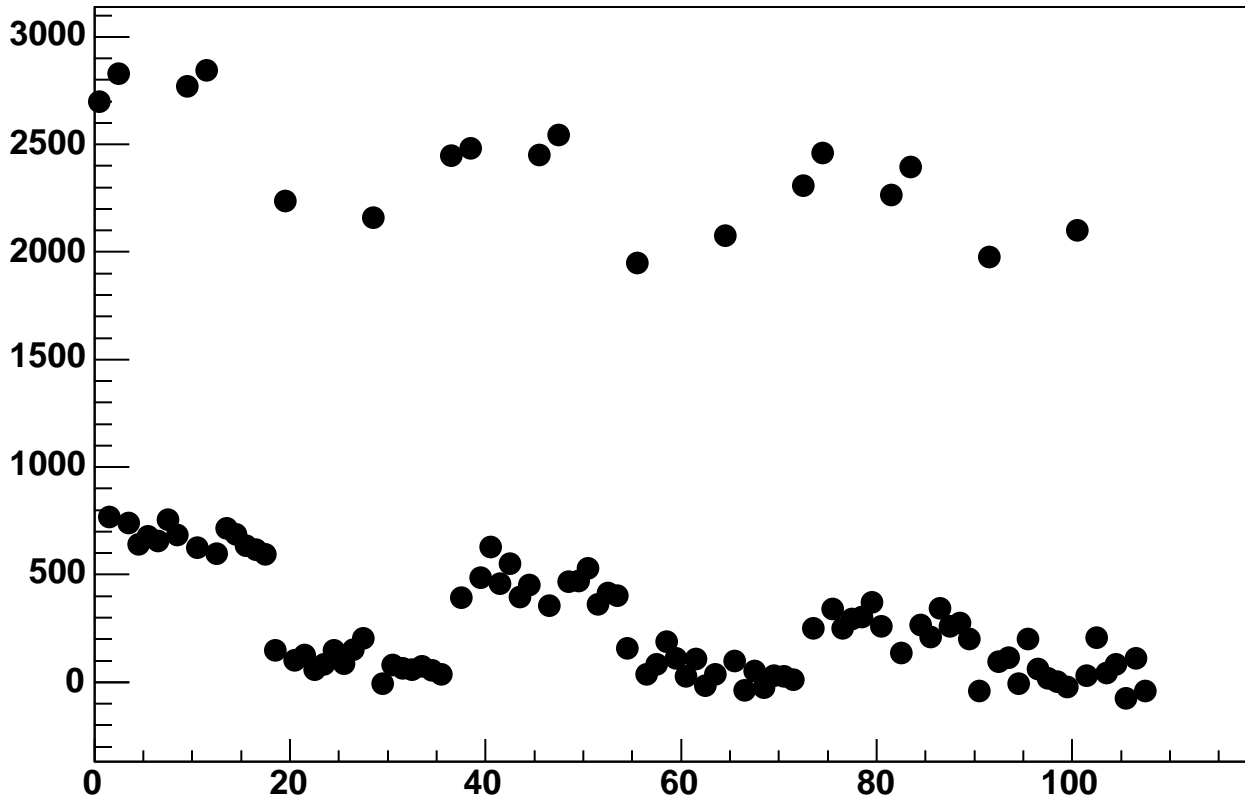
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



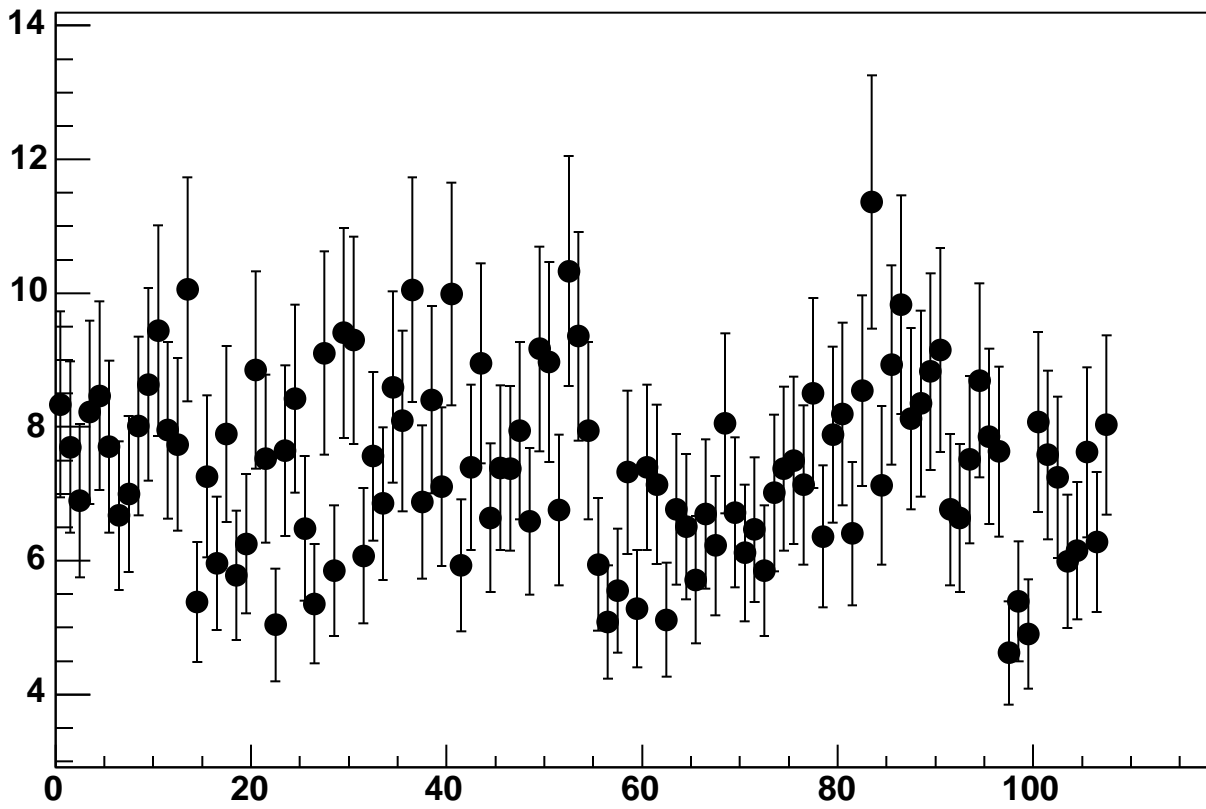
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



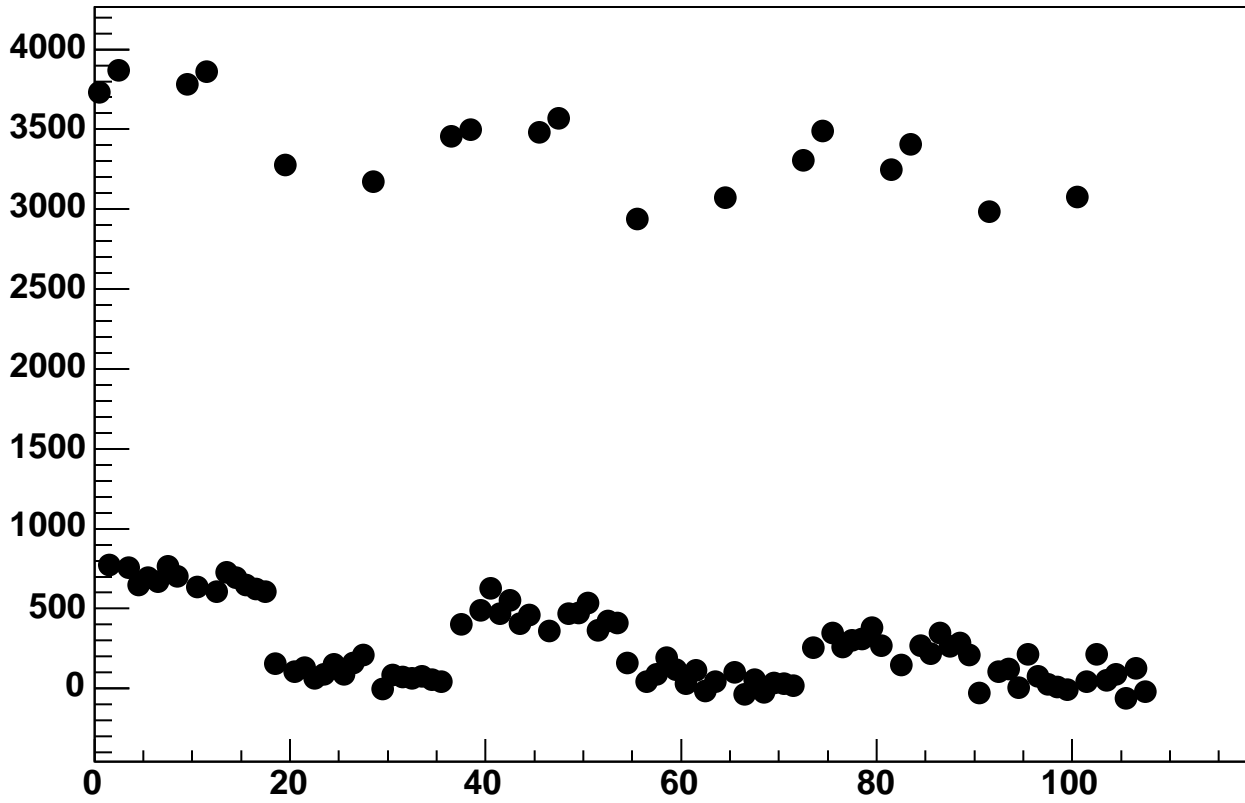
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



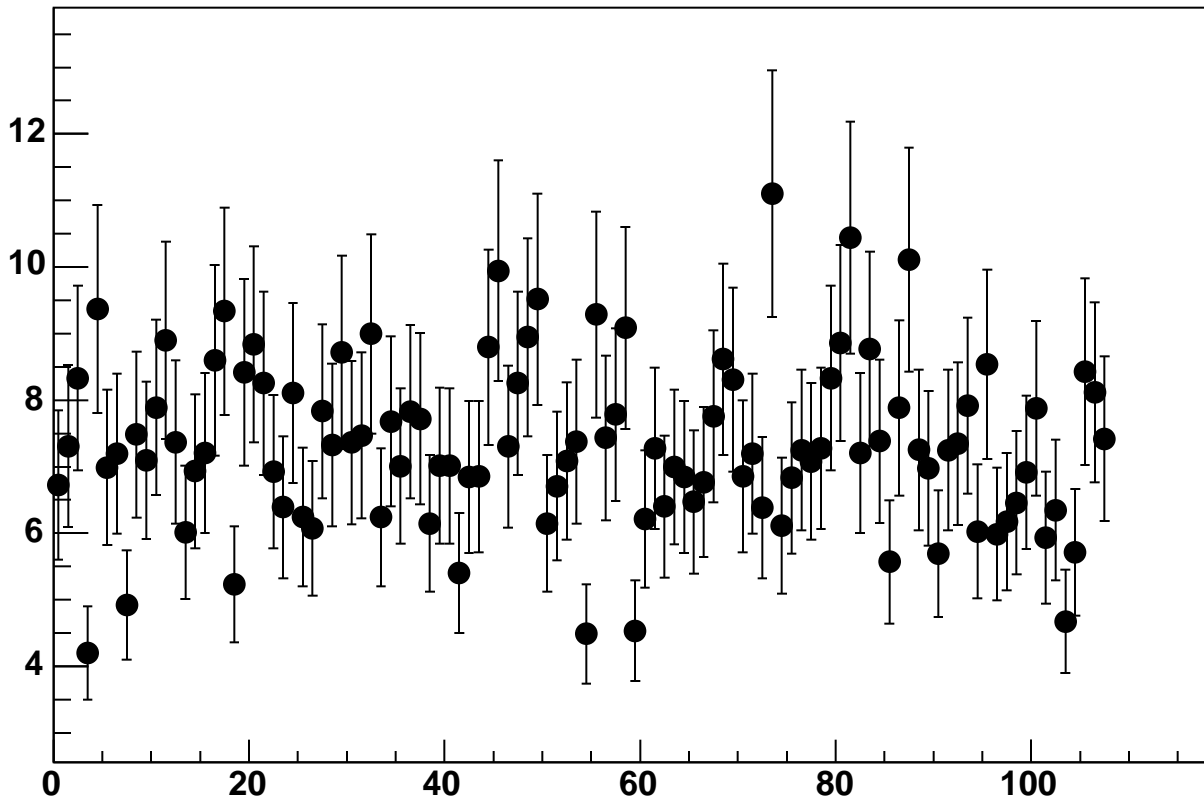
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



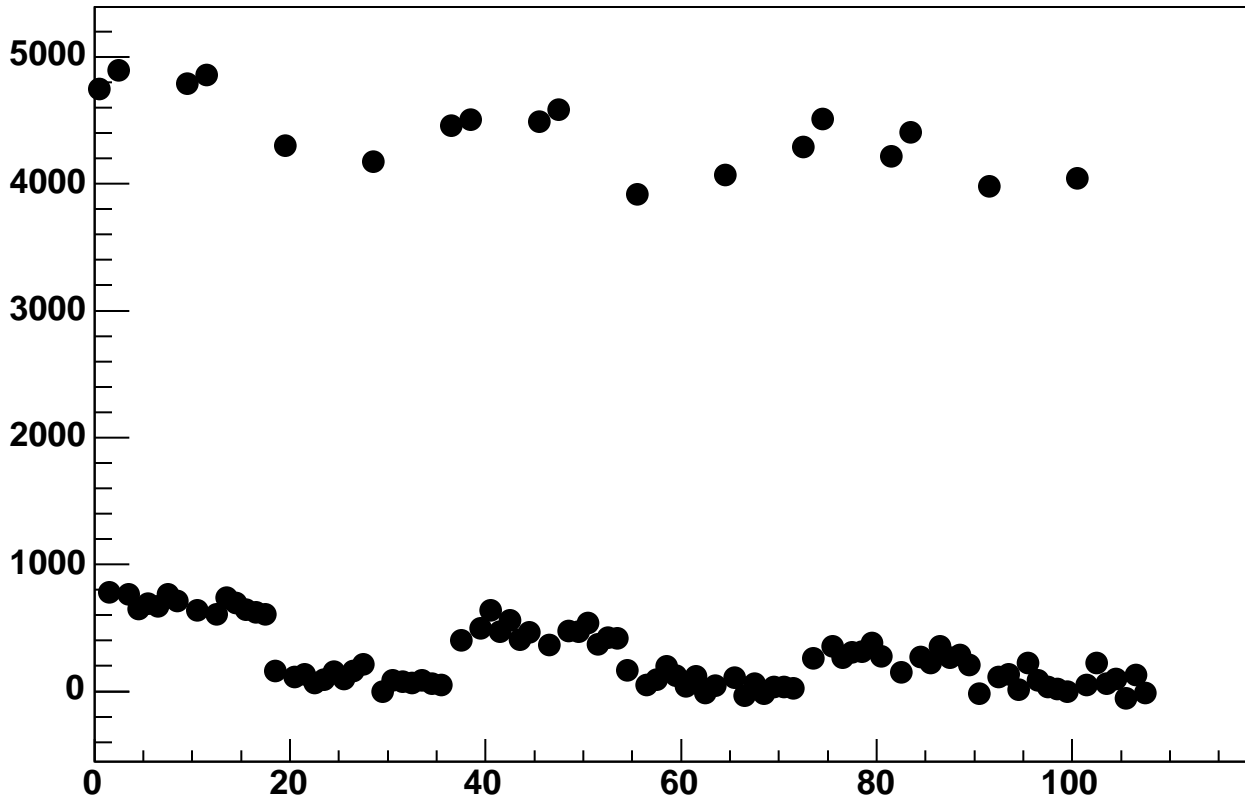
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



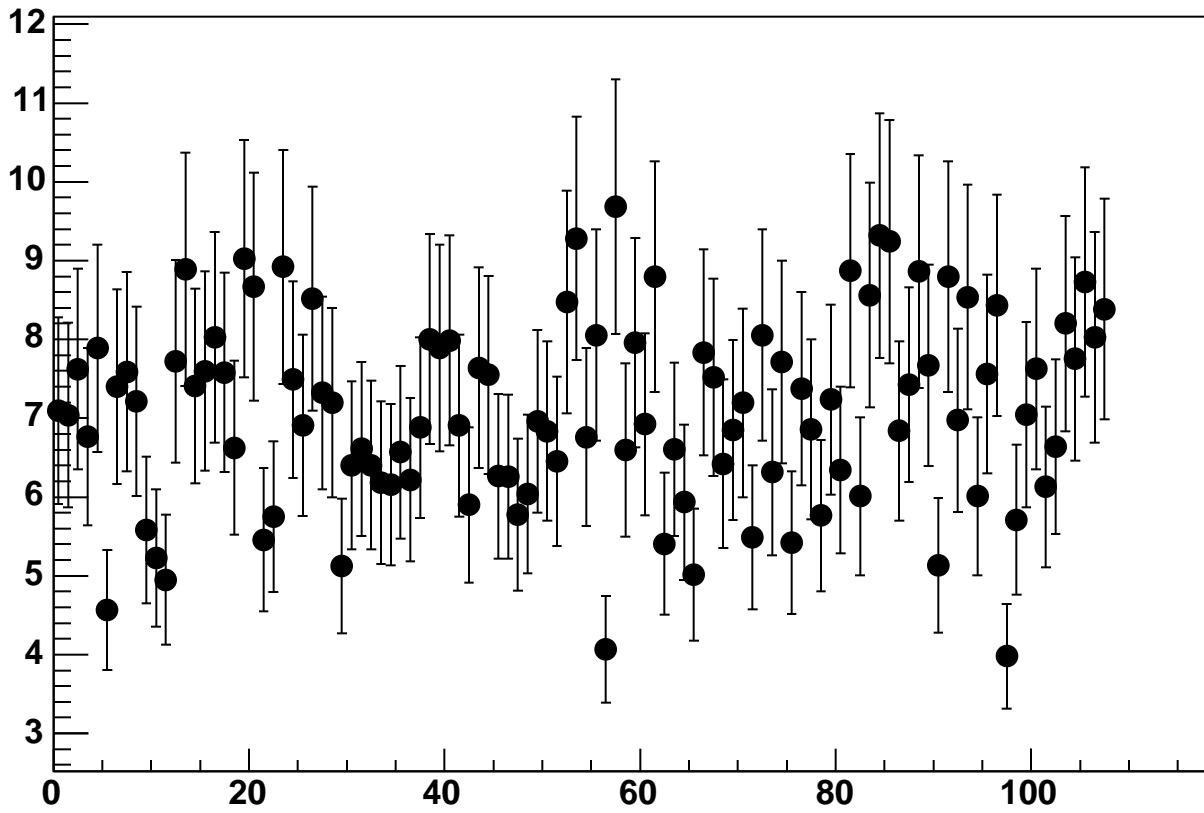
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



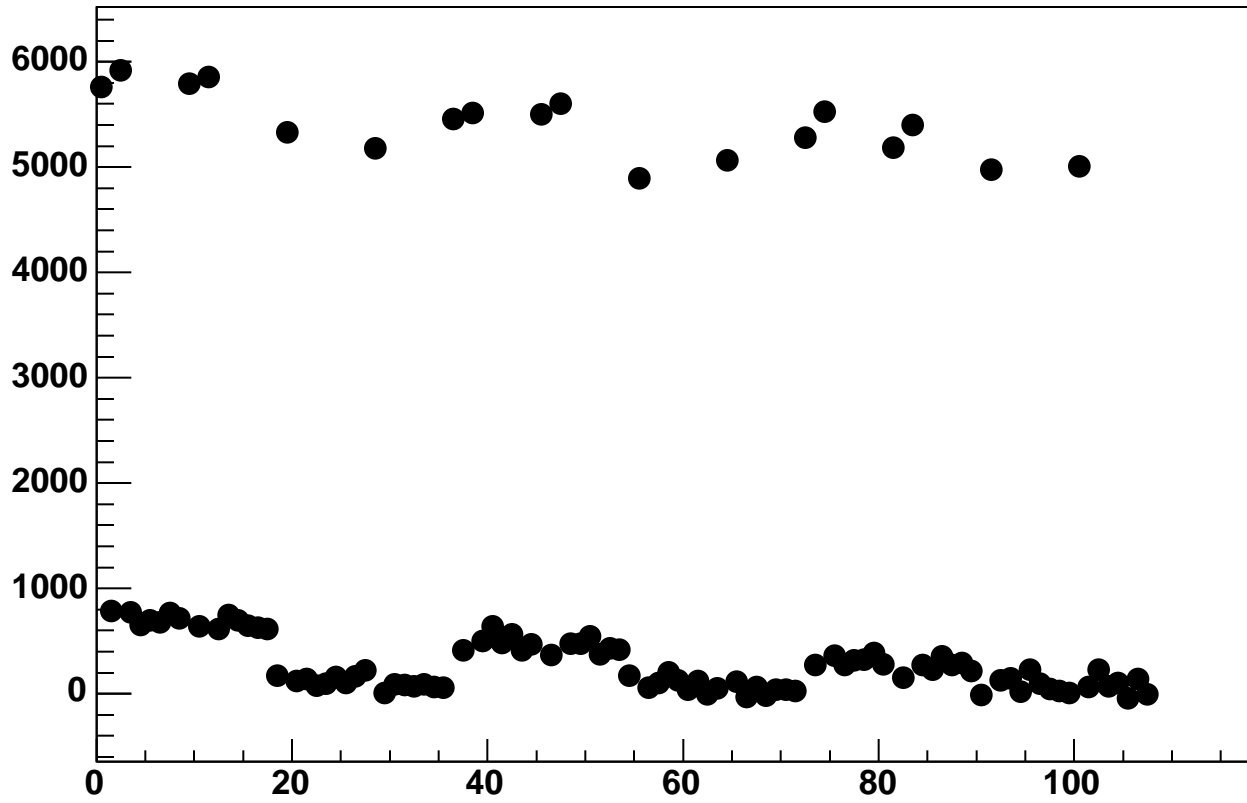
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



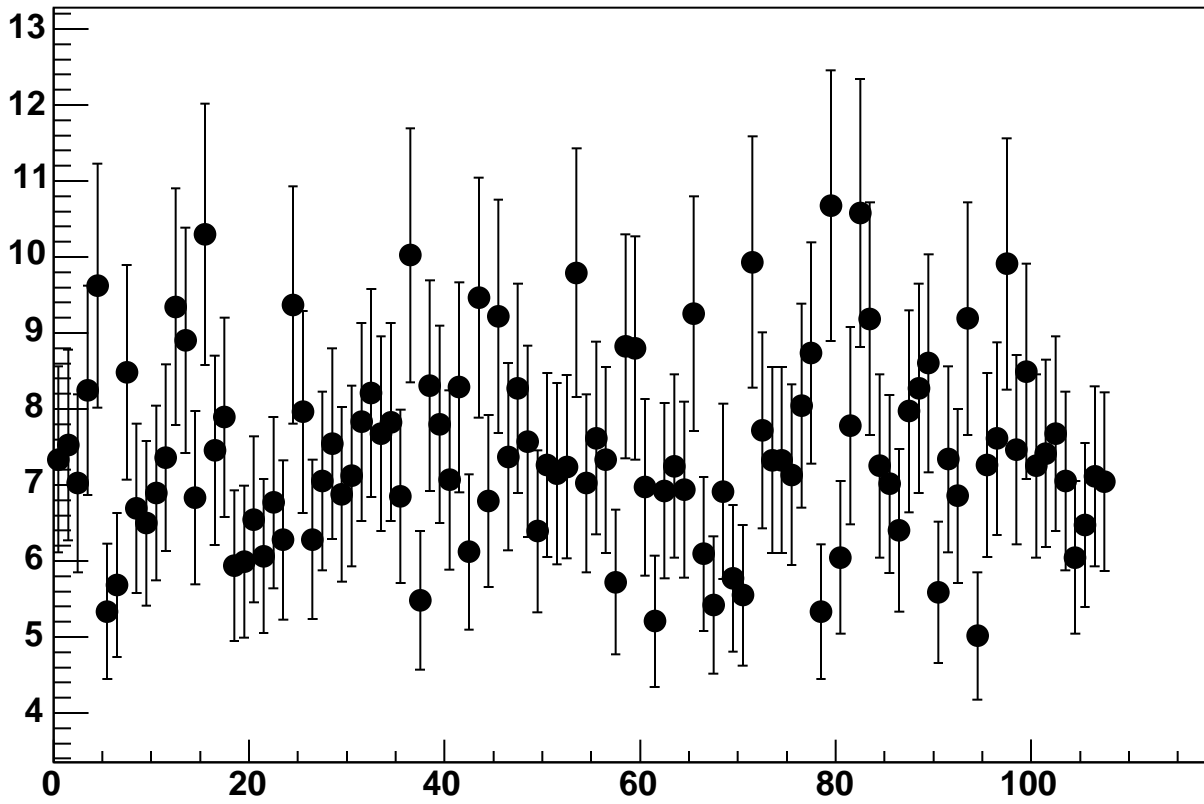
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



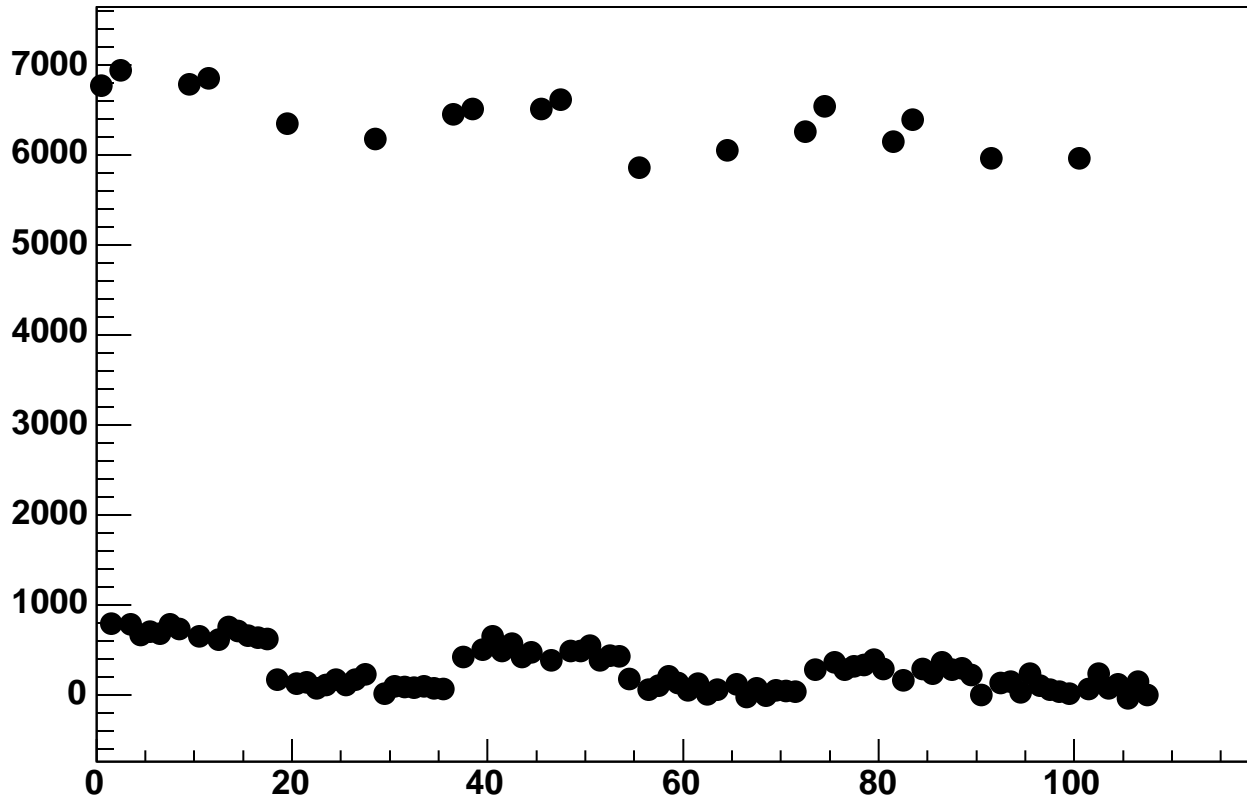
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



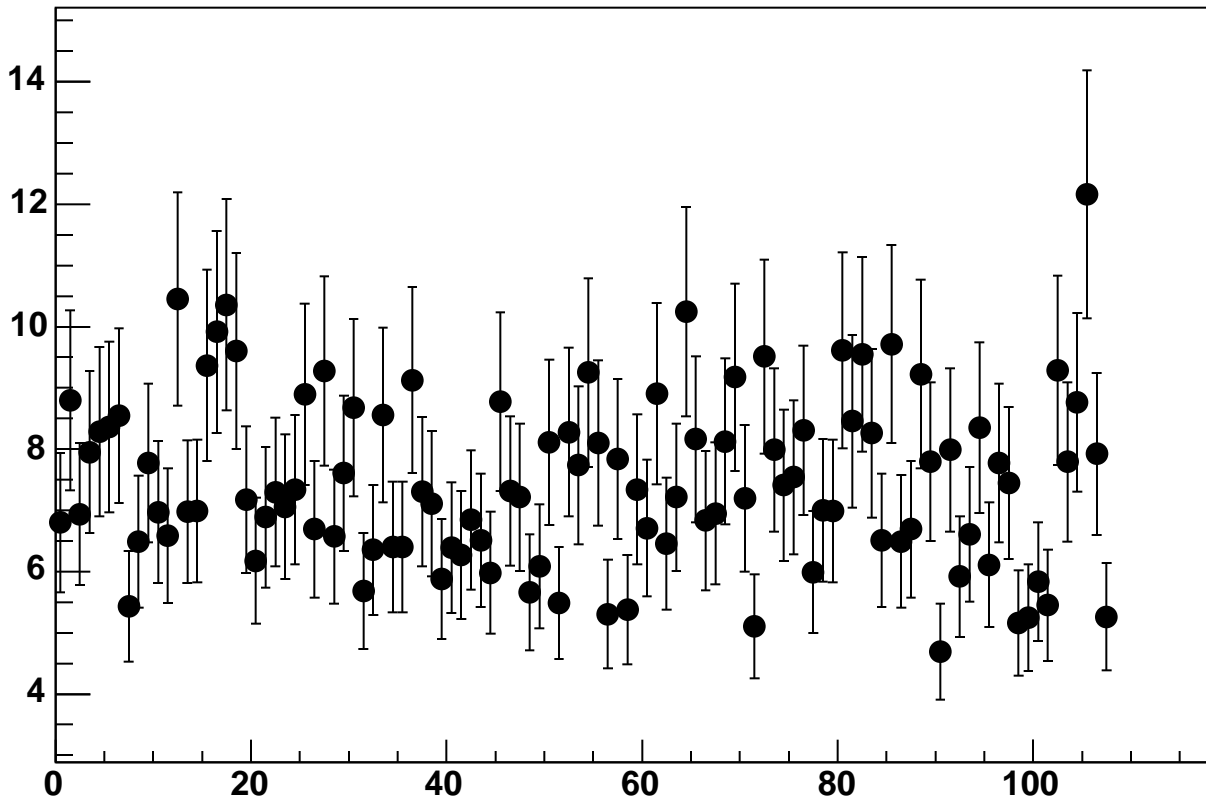
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



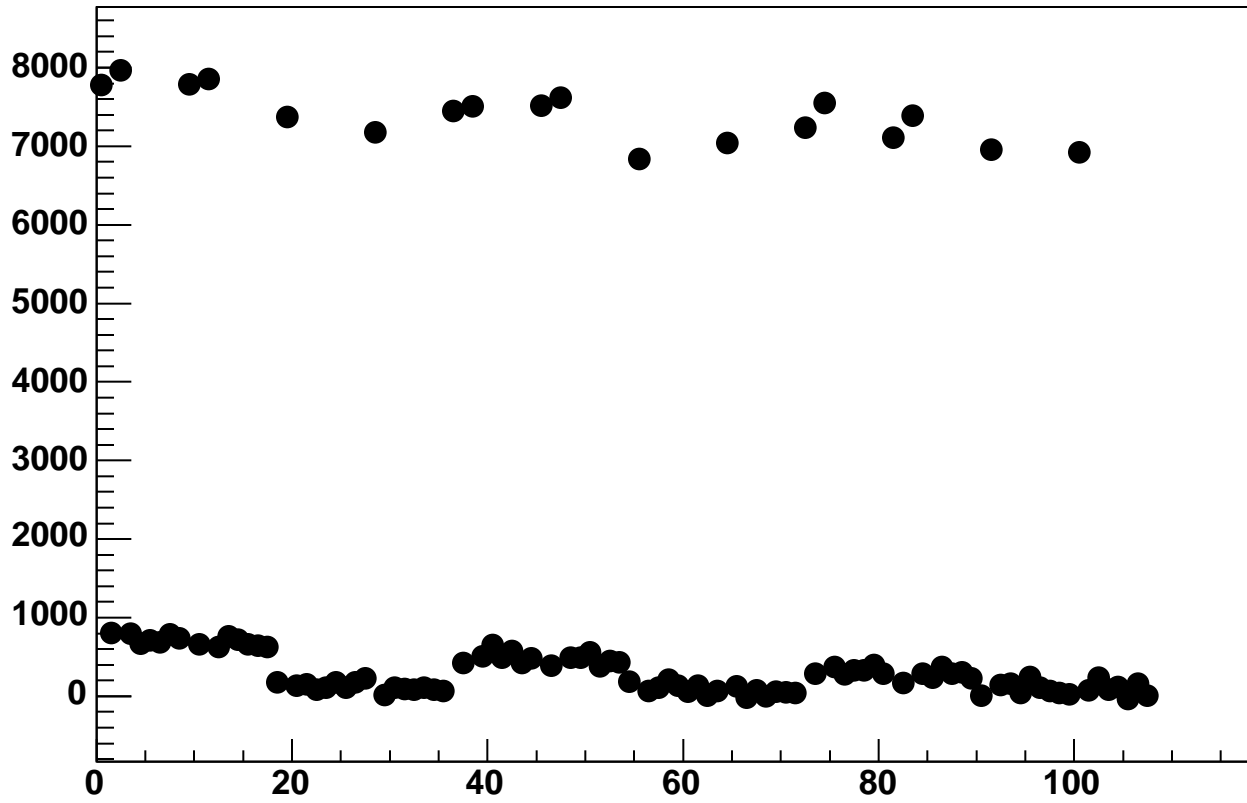
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



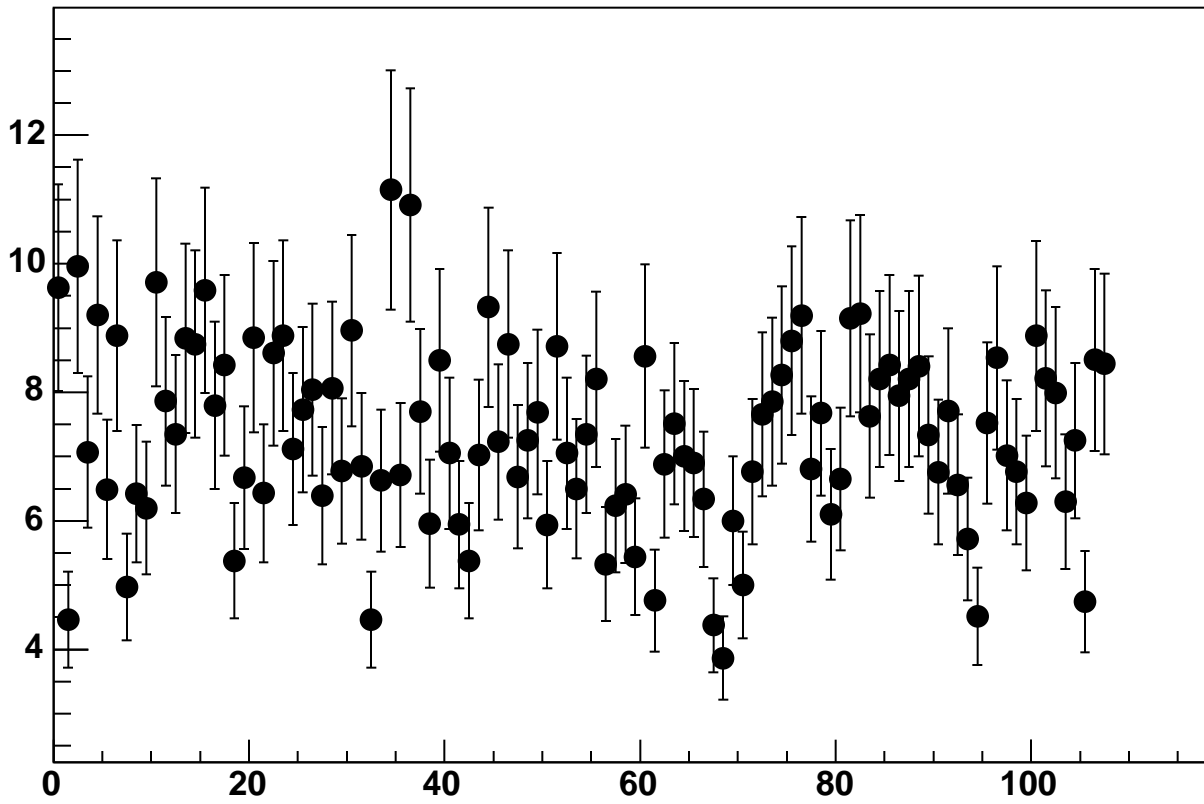
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



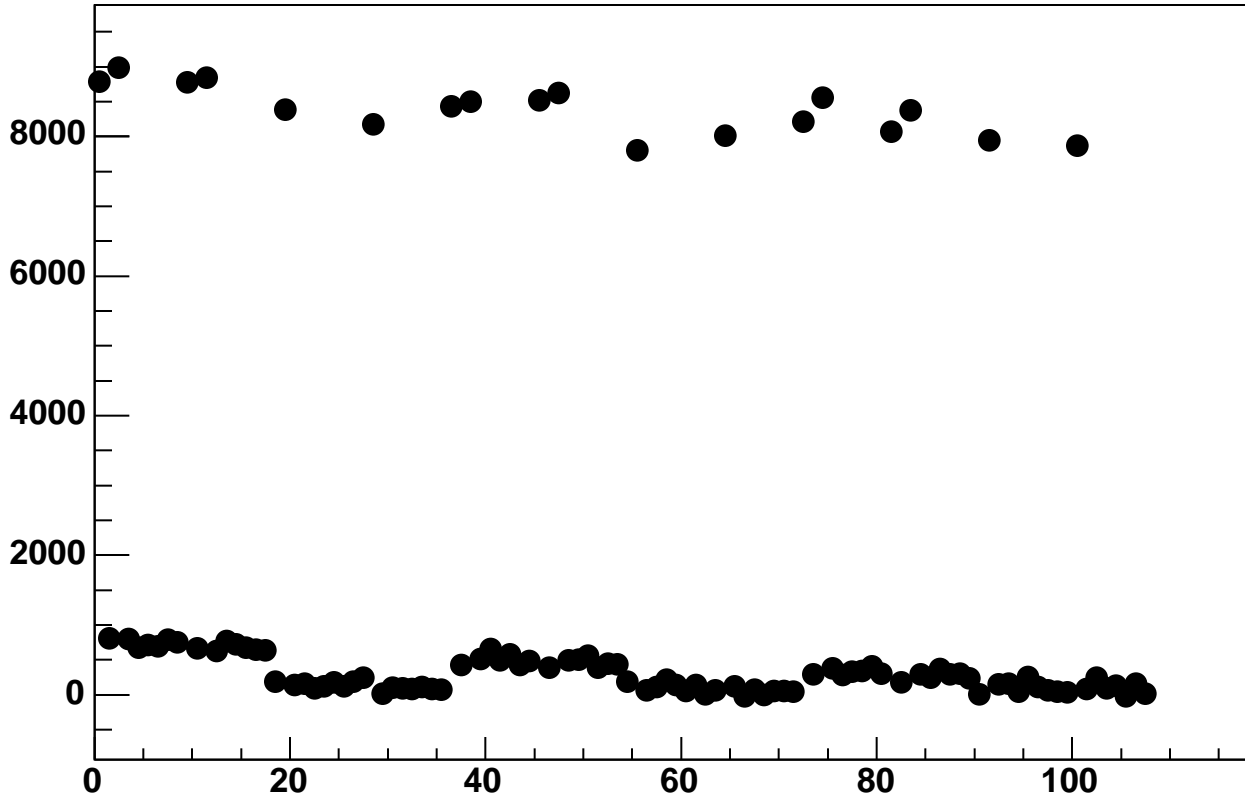
Enable 0, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



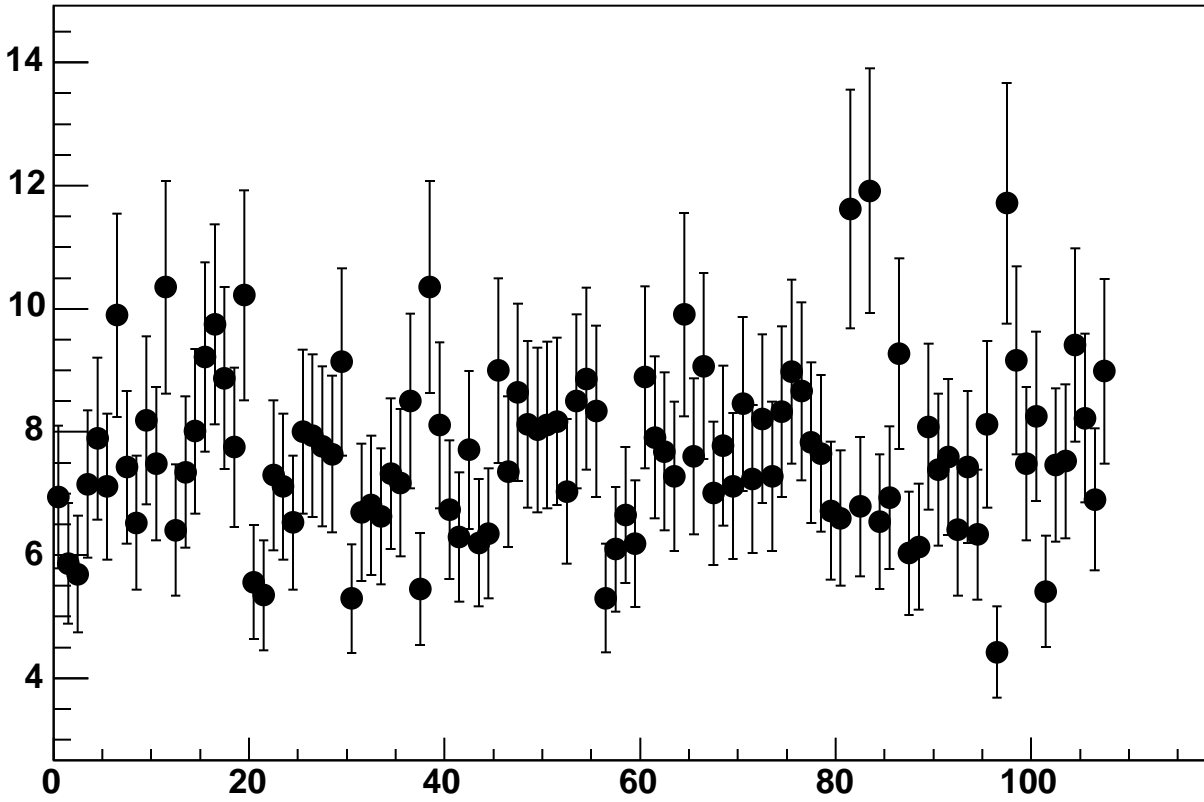
Enable 0, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



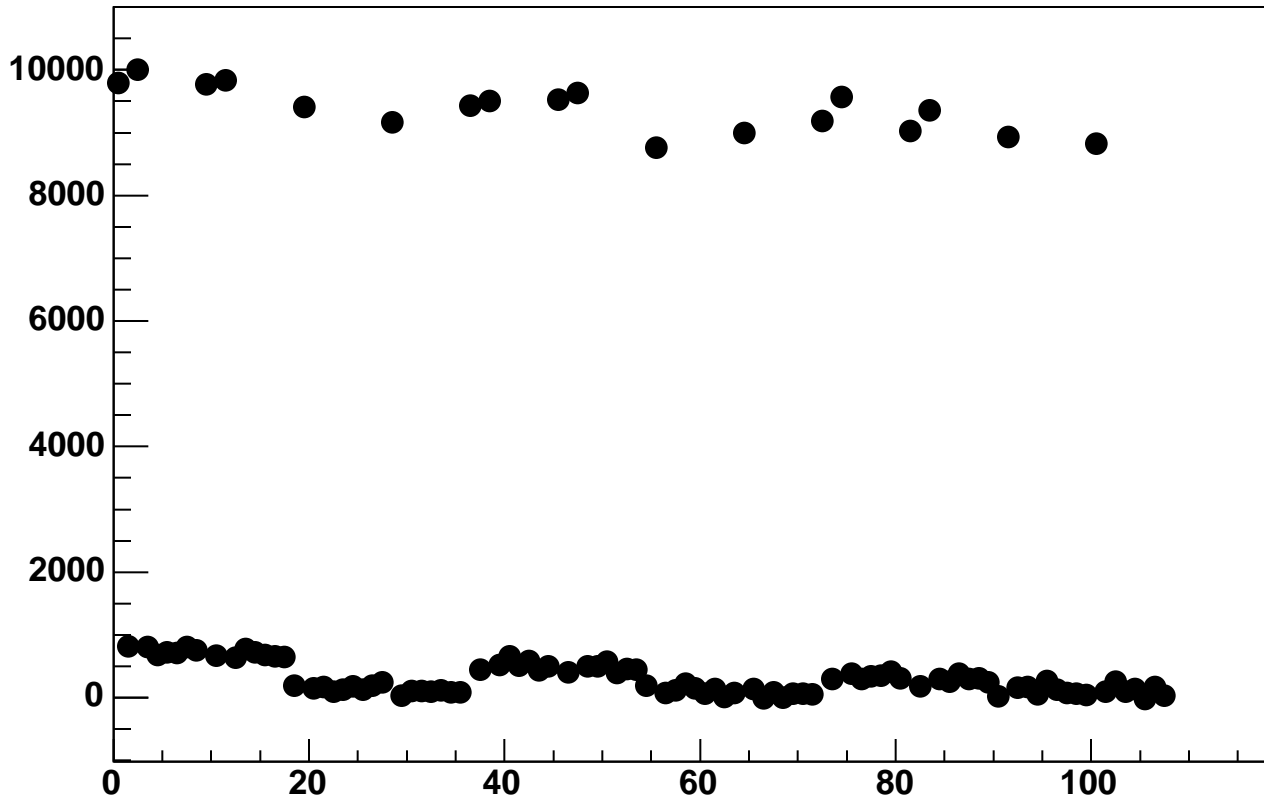
Enable 0, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



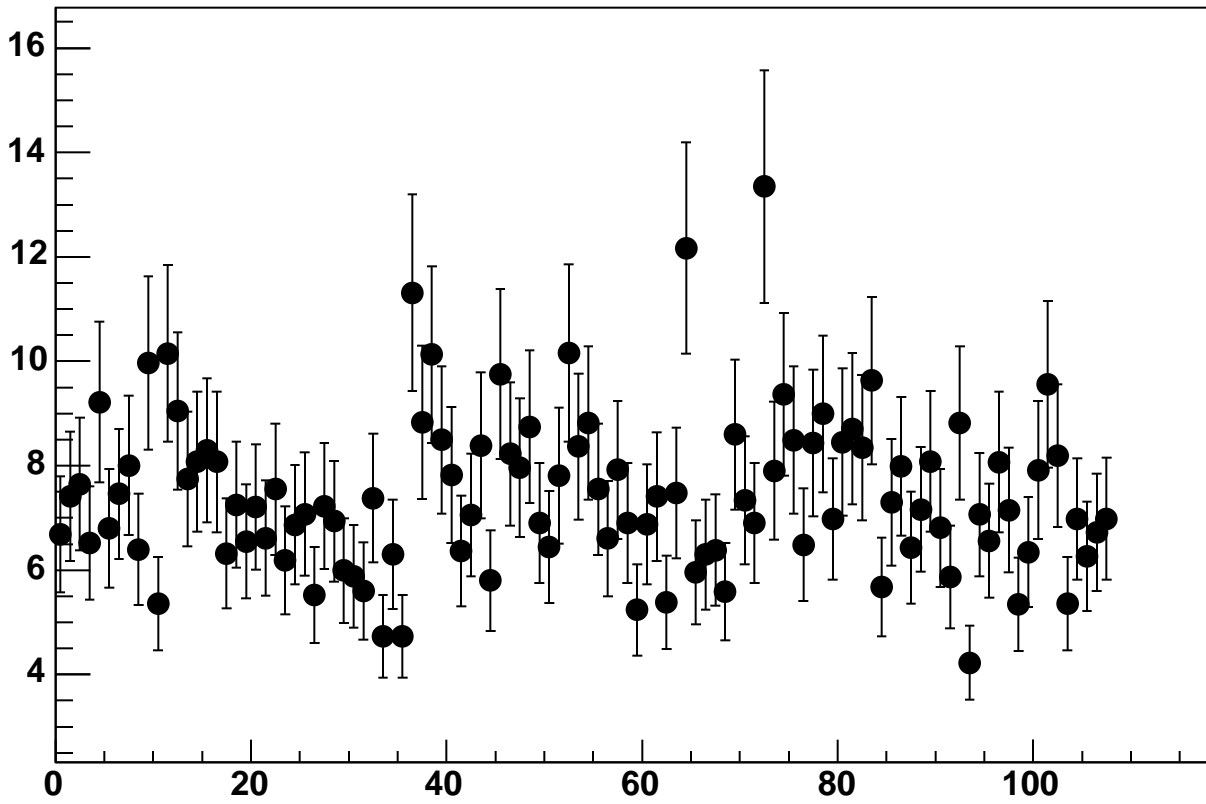
Enable 0, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



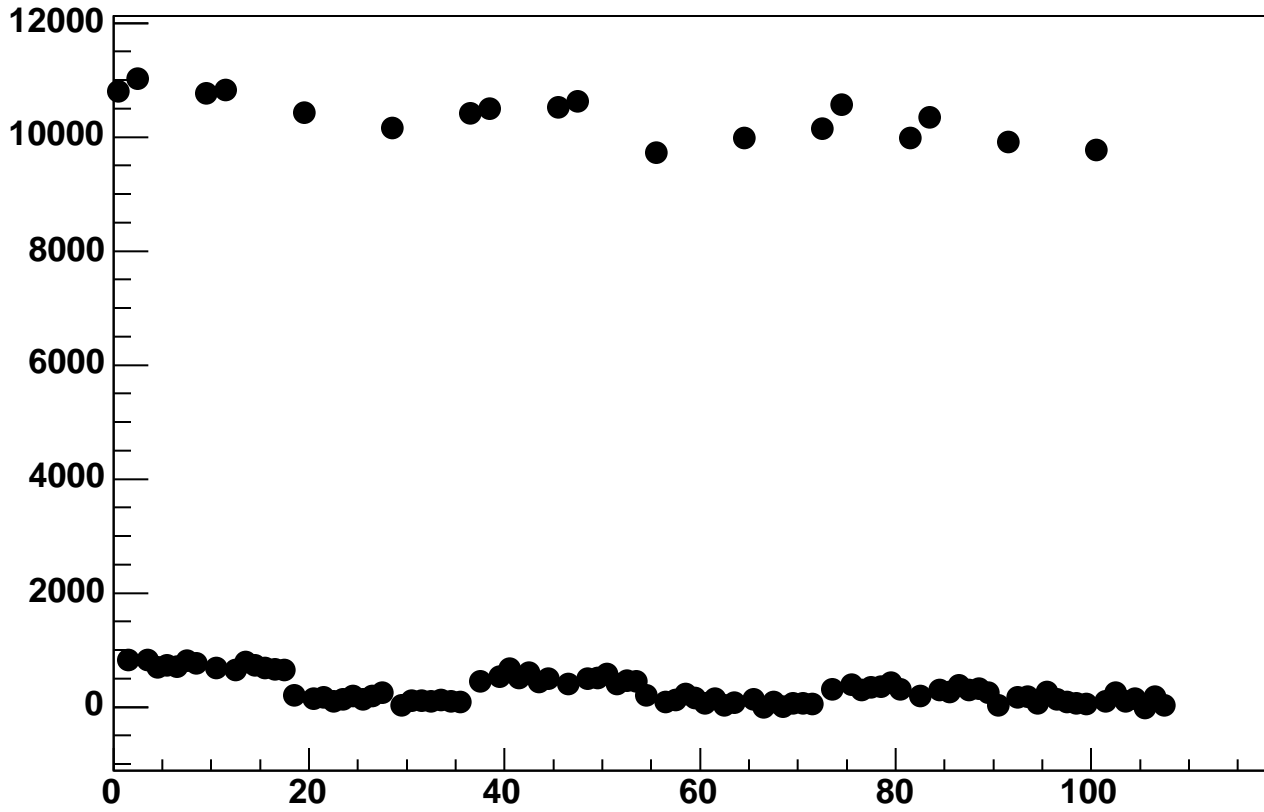
Enable 0, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



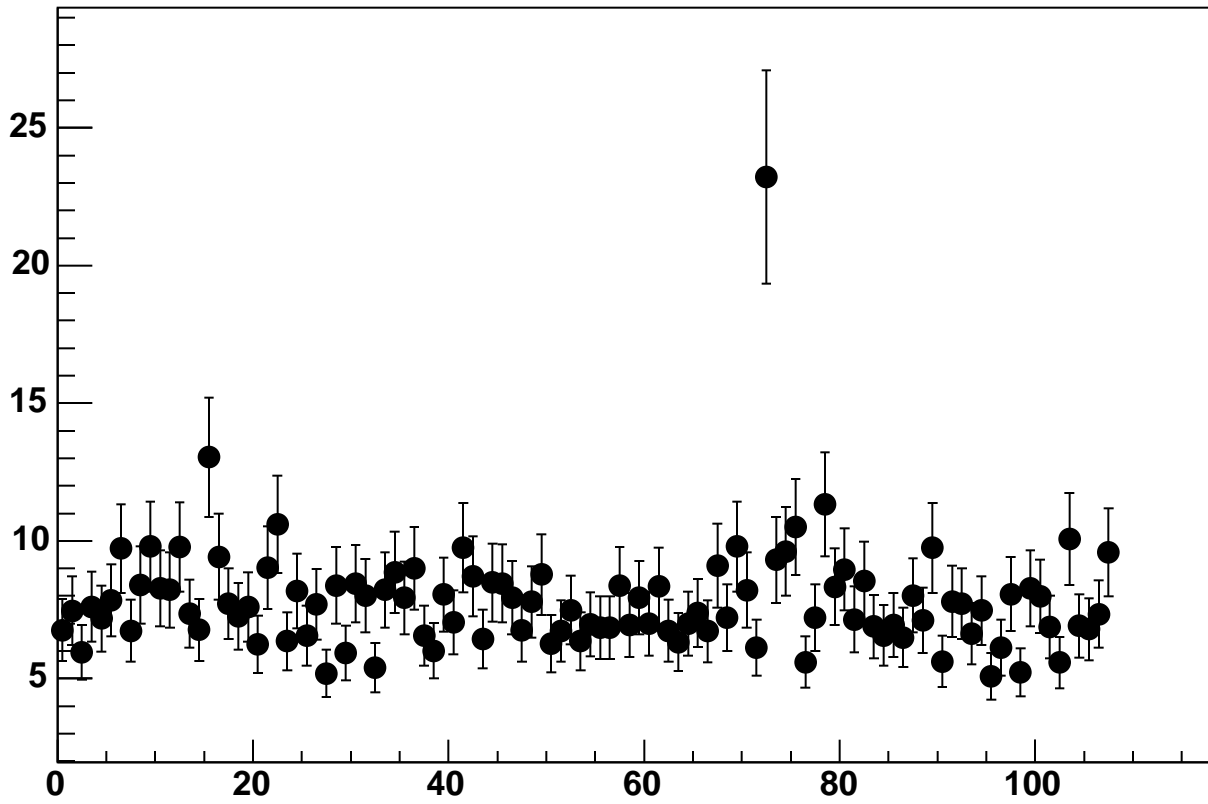
Enable 0, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



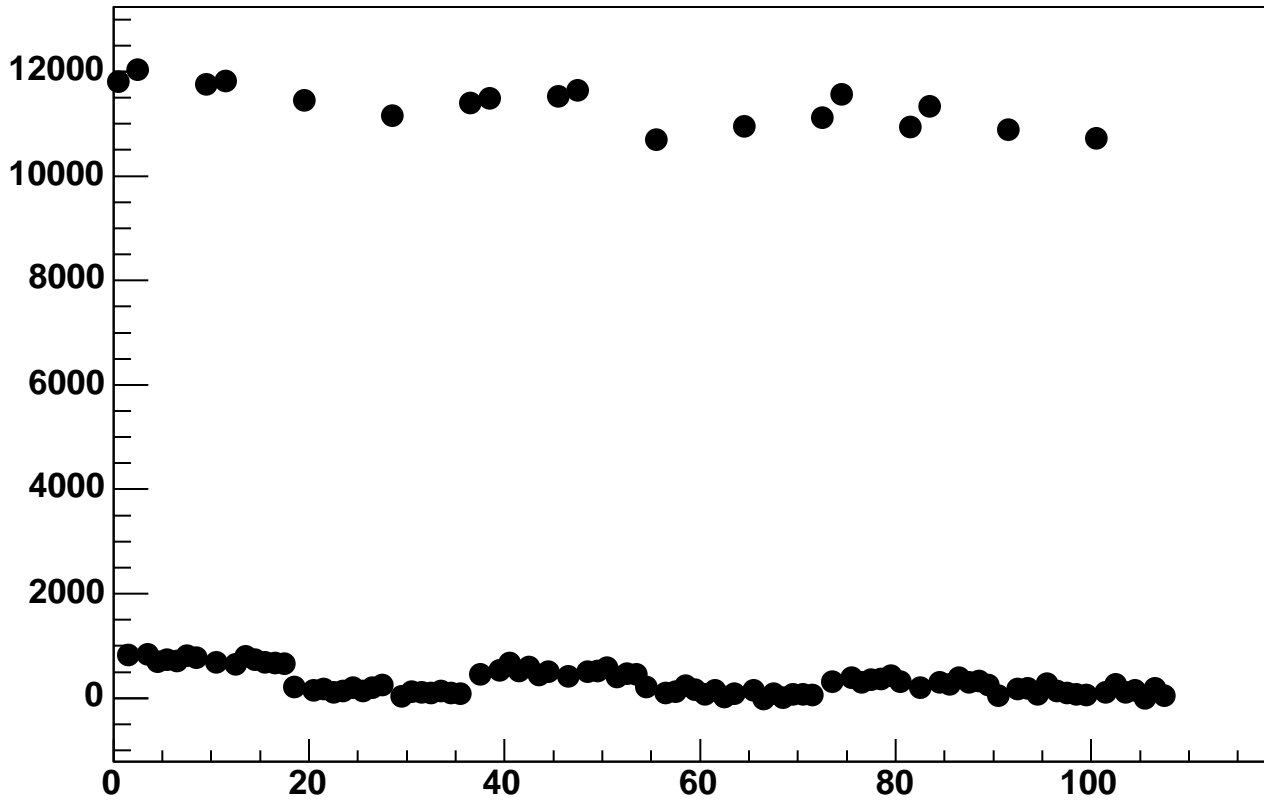
Enable 0, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



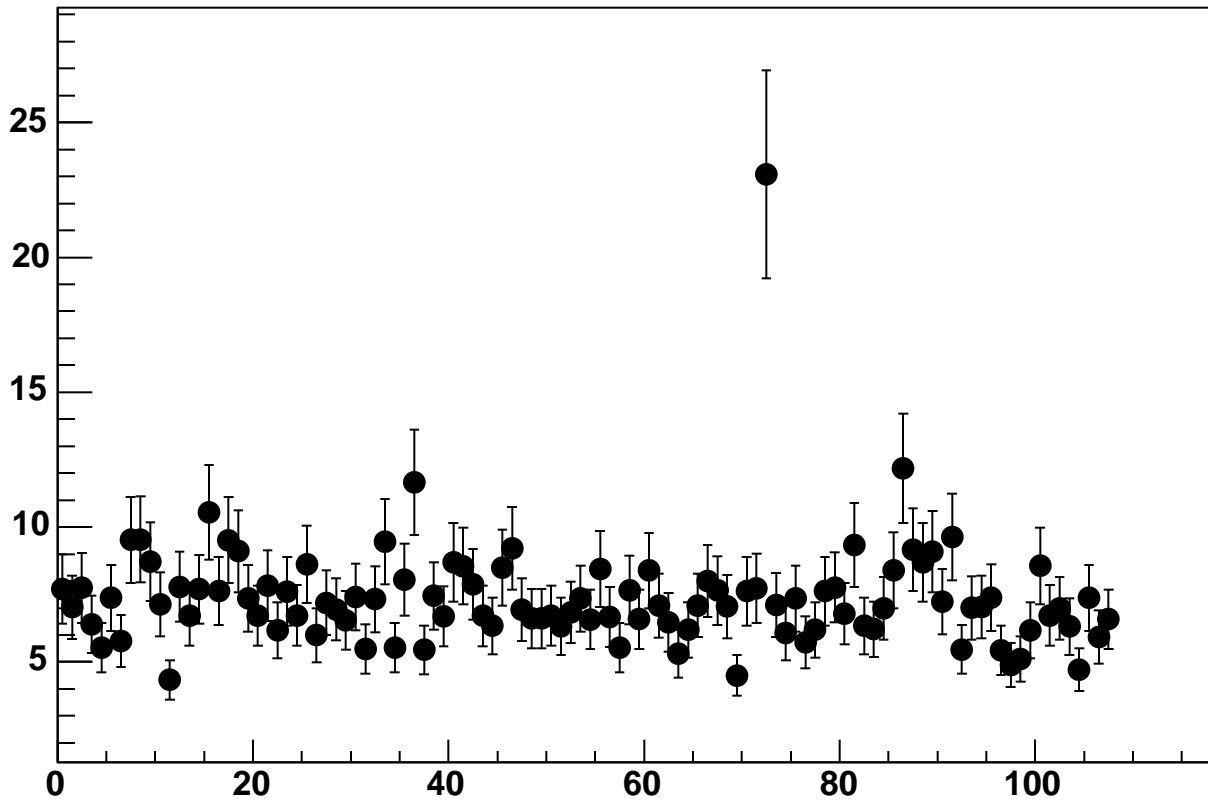
Enable 0, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



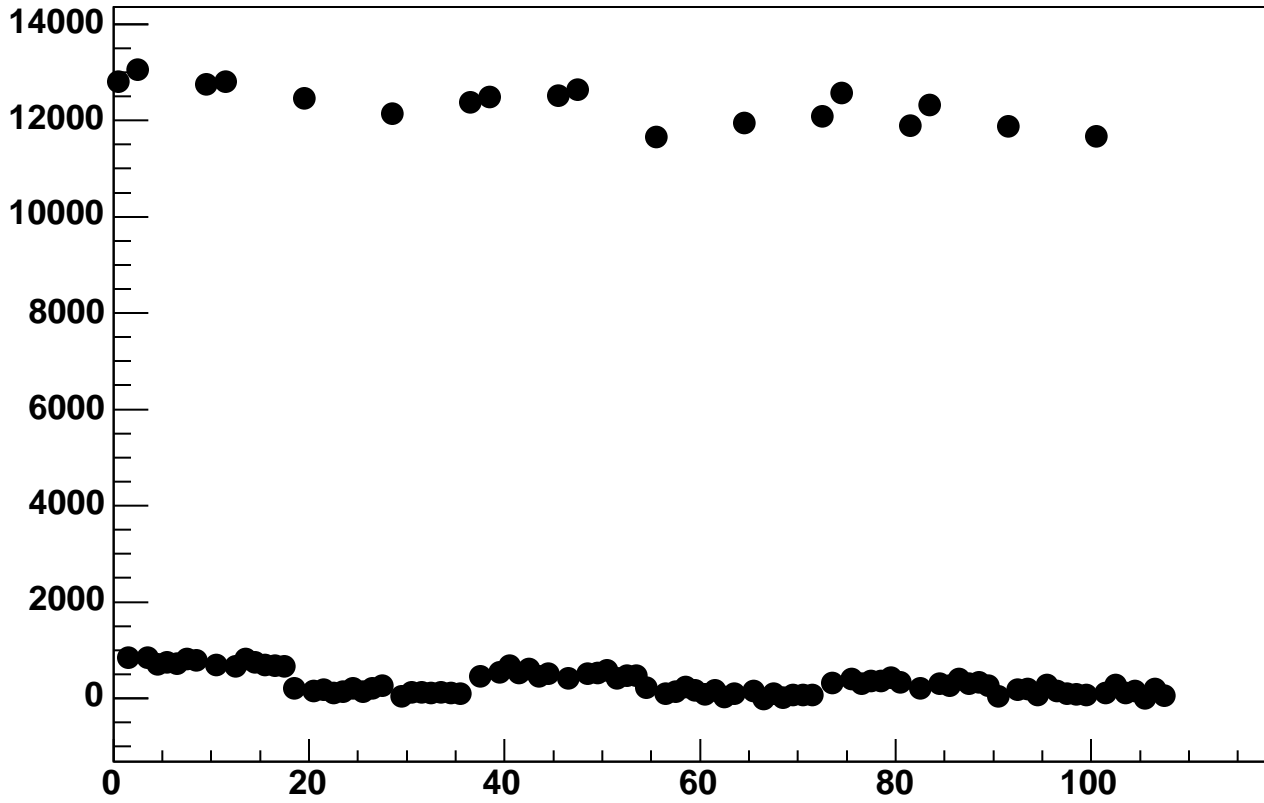
Enable 0, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



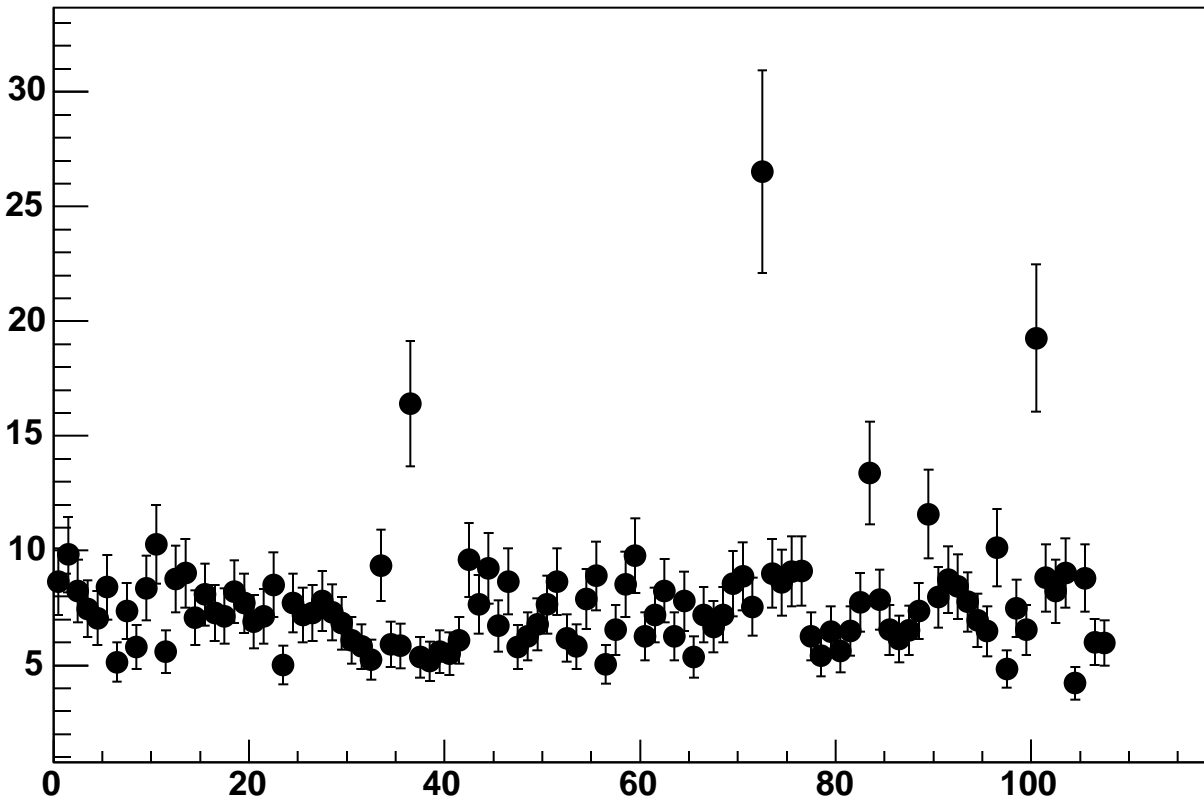
Enable 0, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



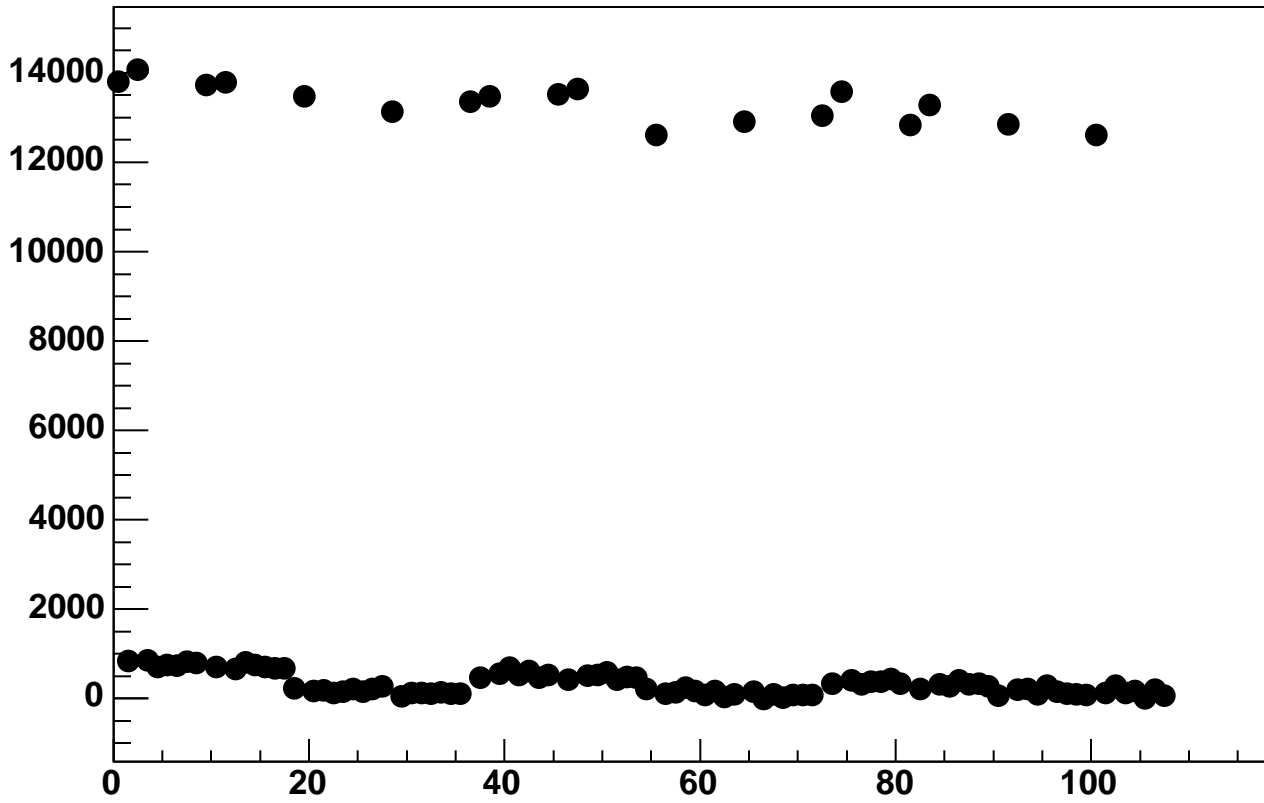
Enable 0, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



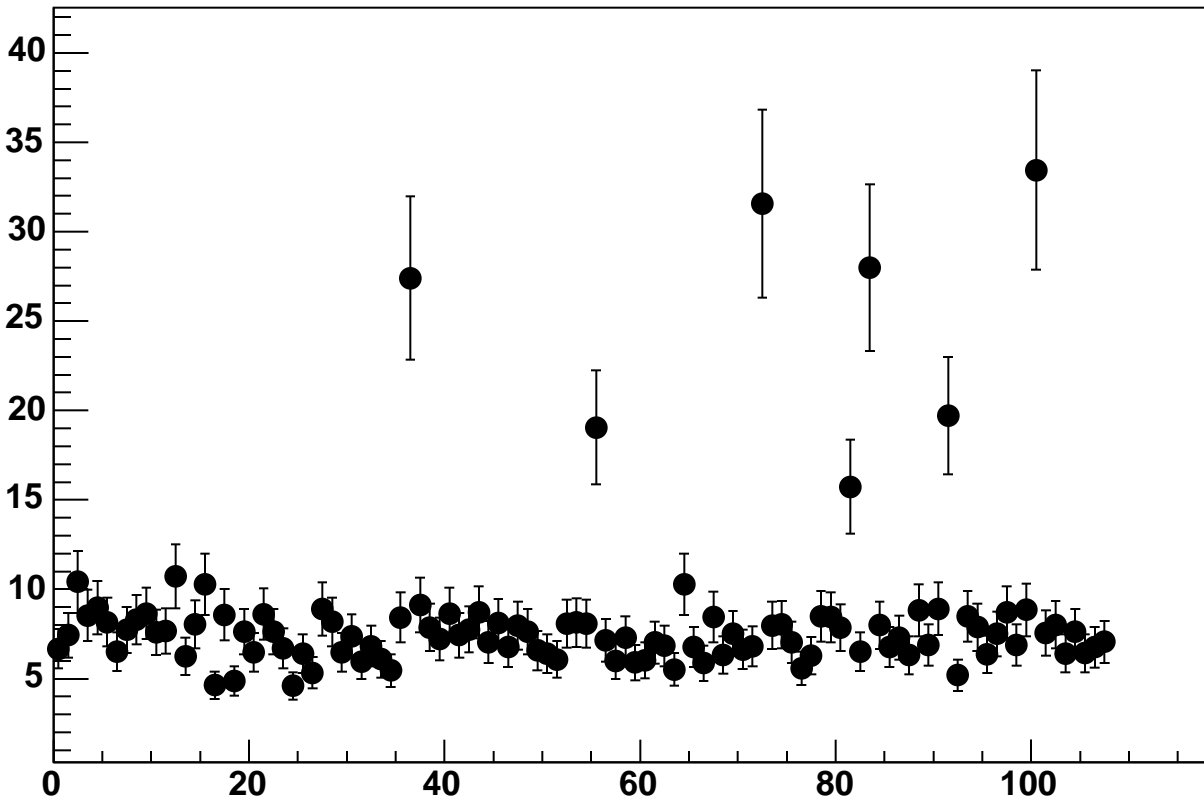
Enable 0, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



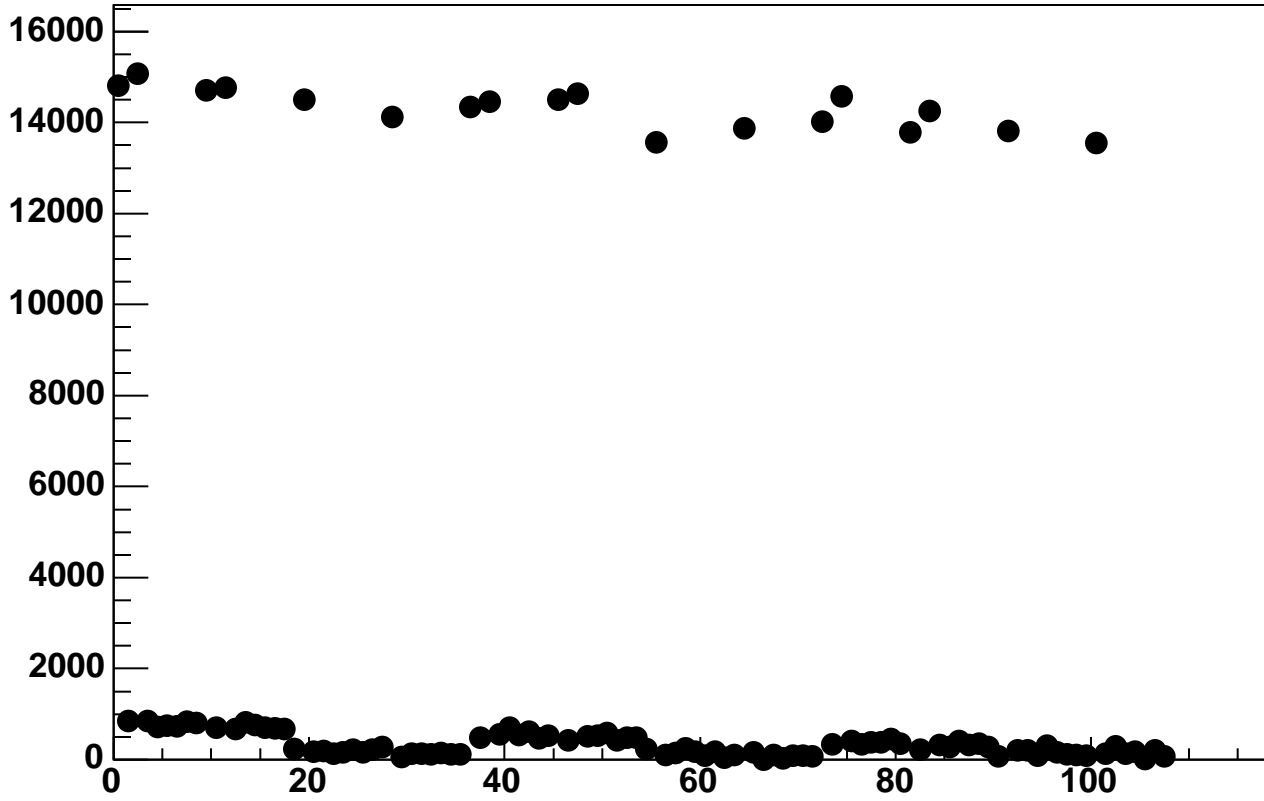
Enable 0, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



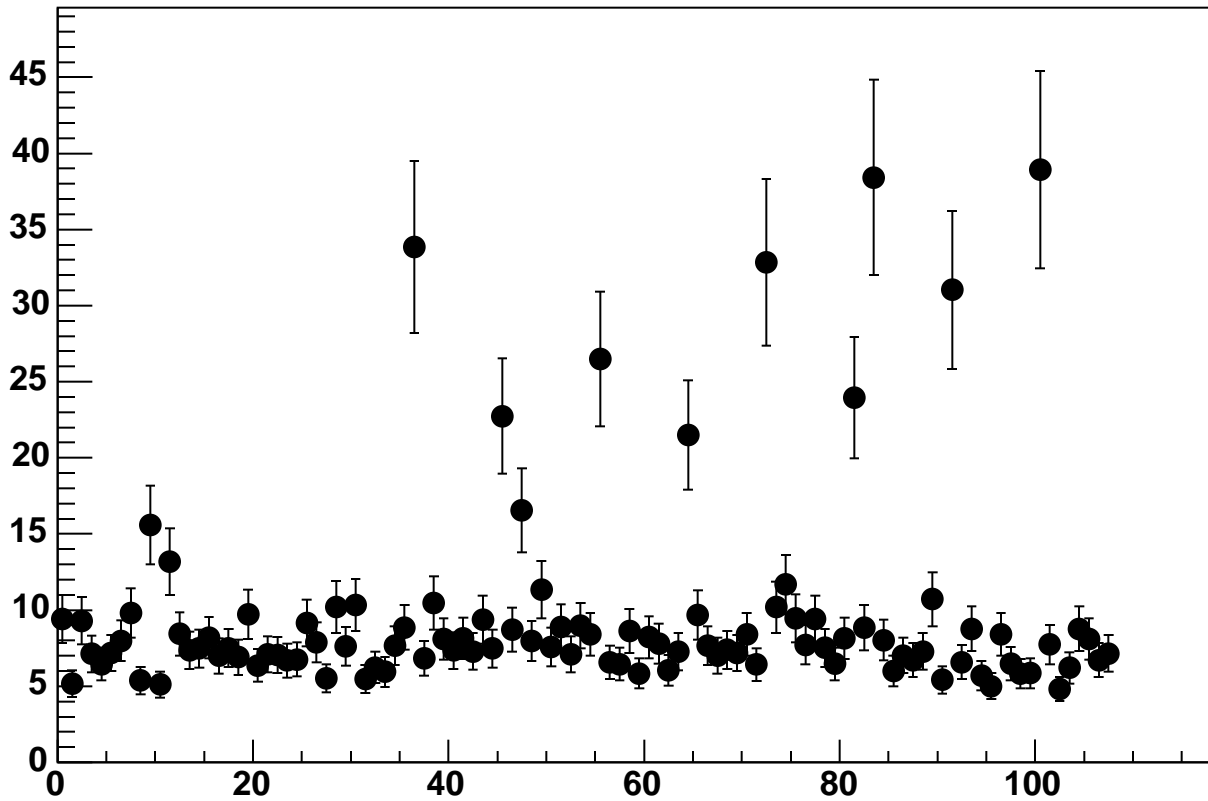
Enable 0, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



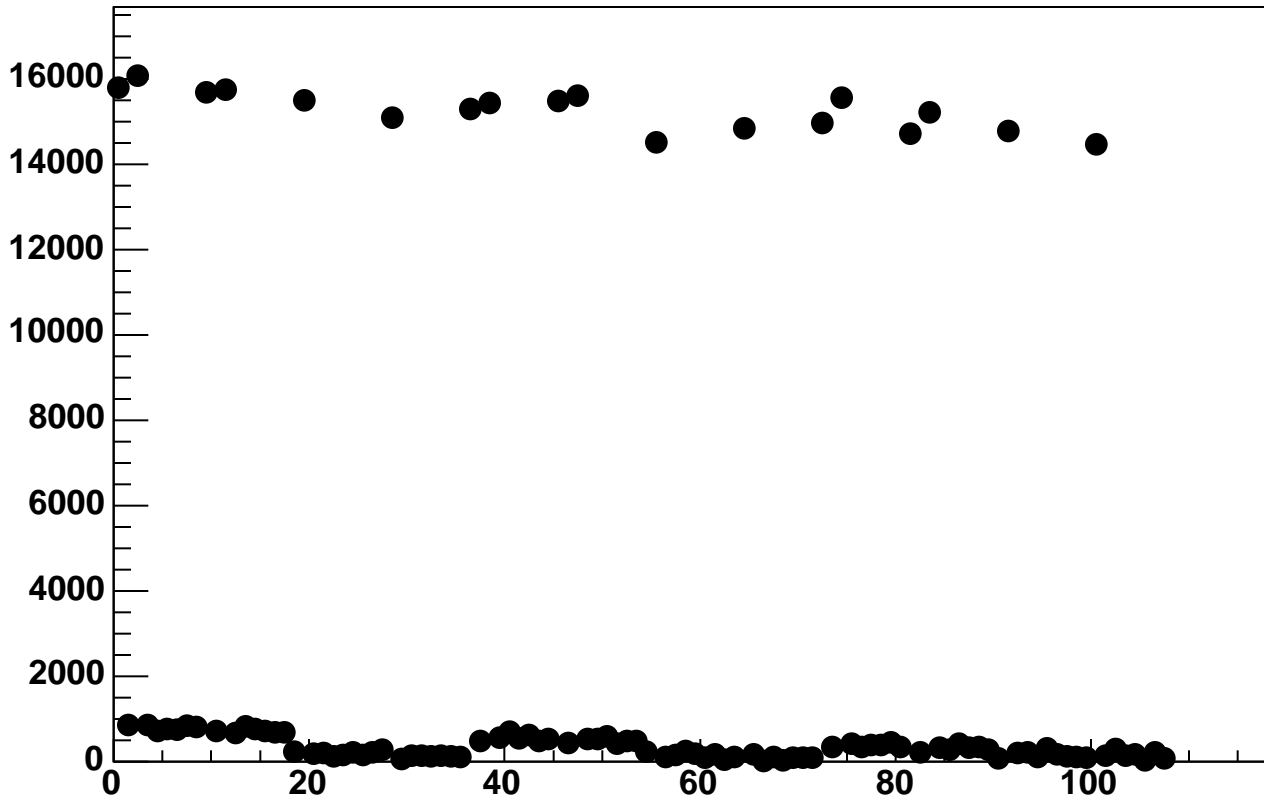
Enable 0, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



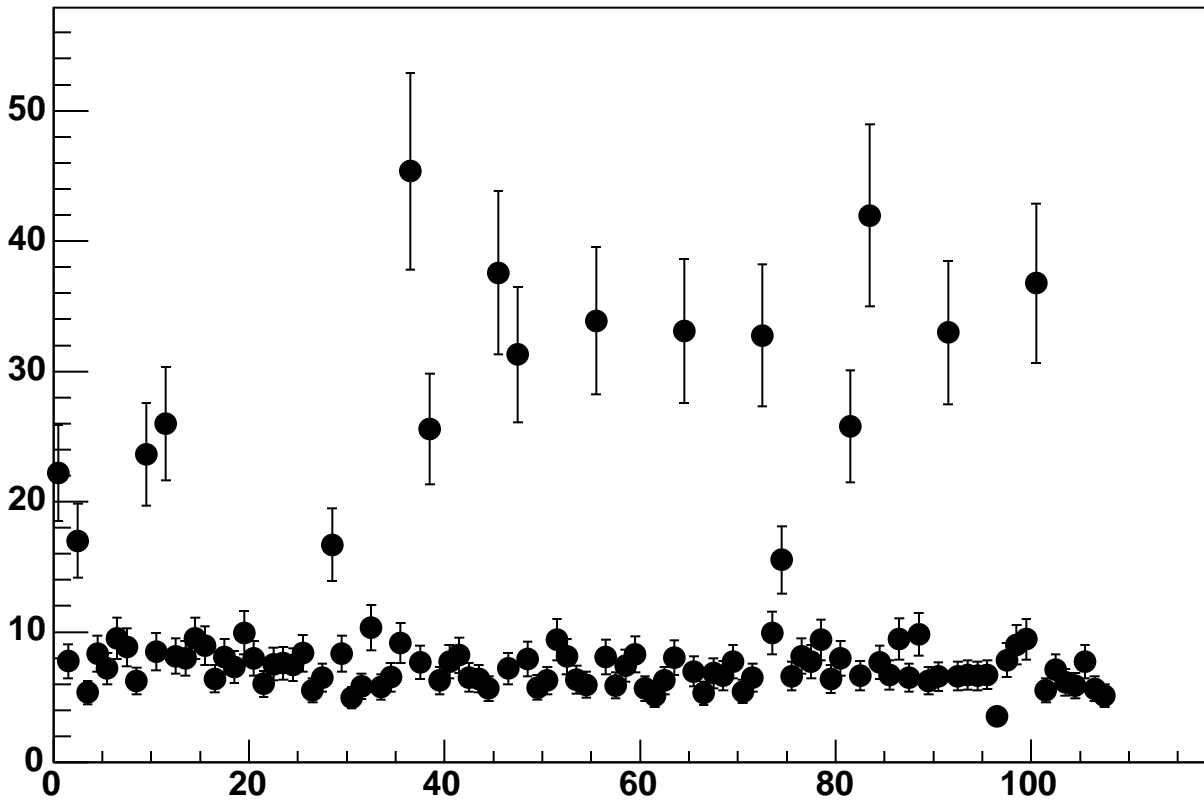
Enable 0, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



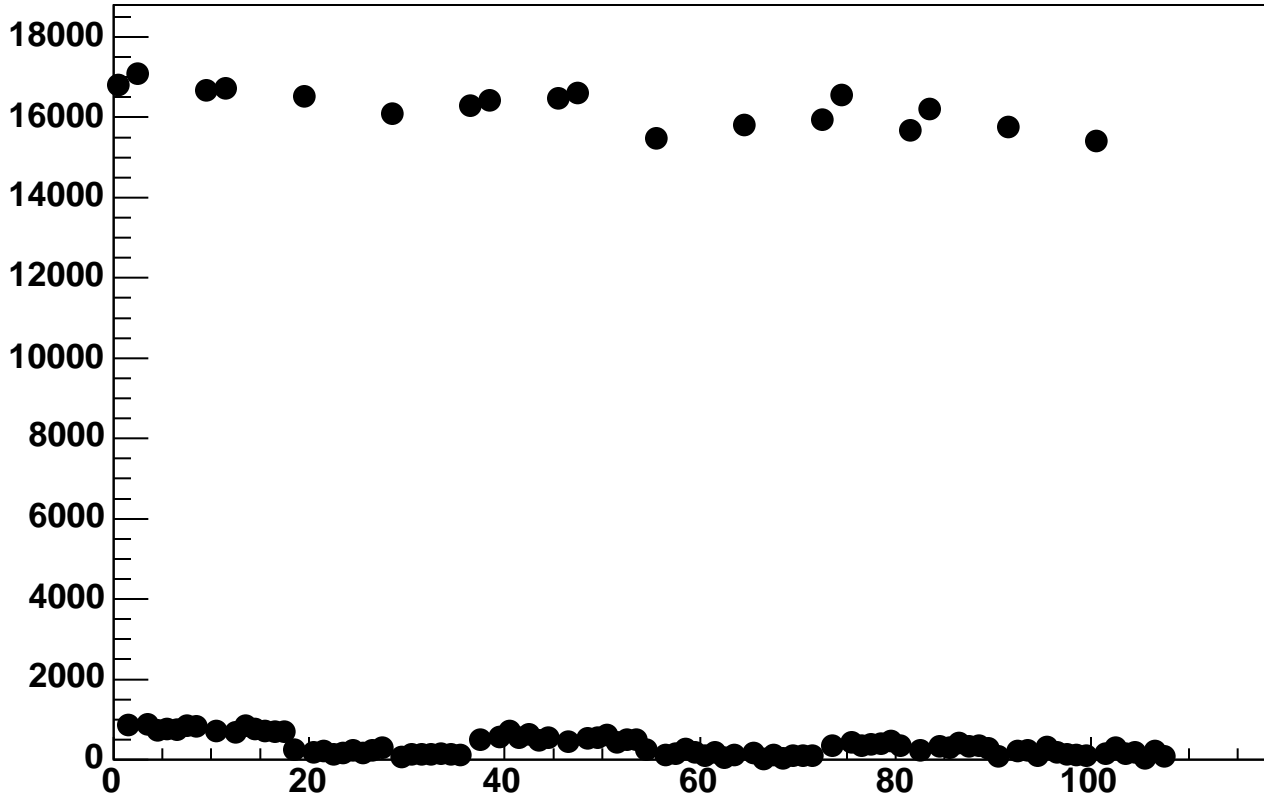
Enable 0, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



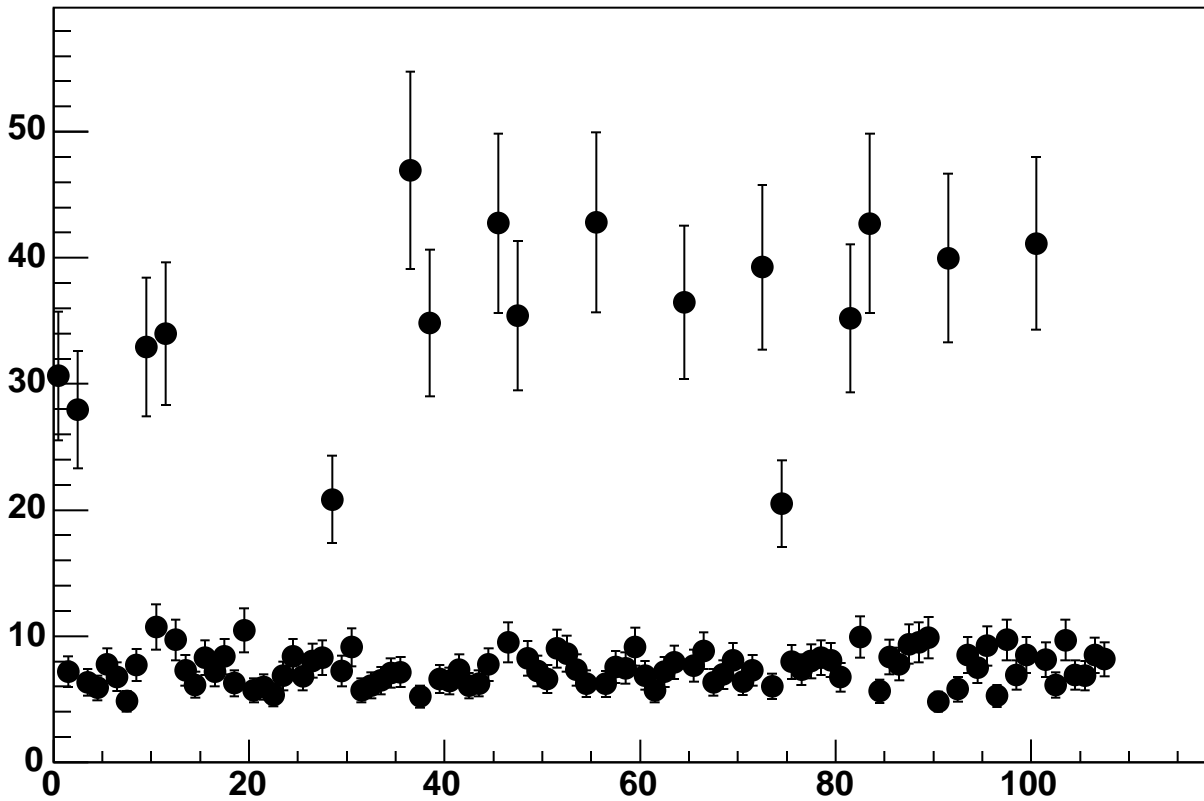
Enable 0, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



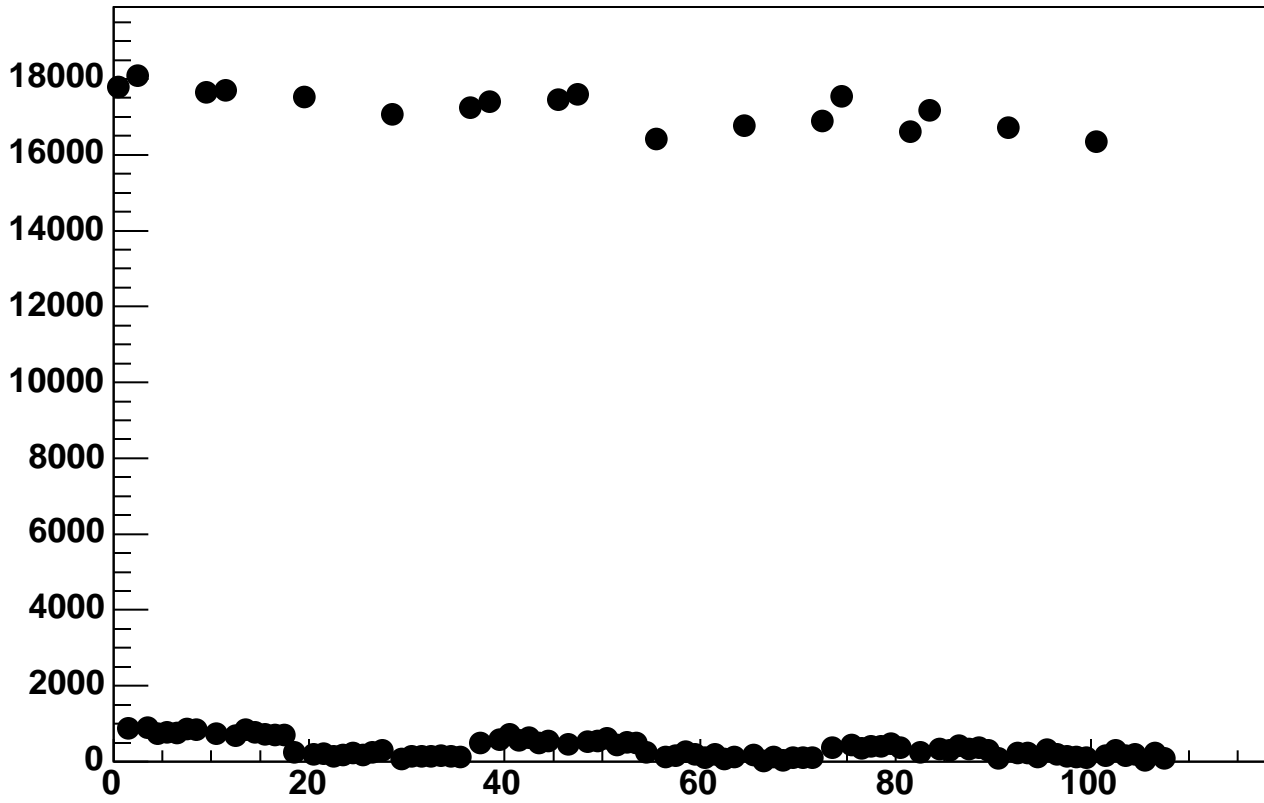
Enable 0, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



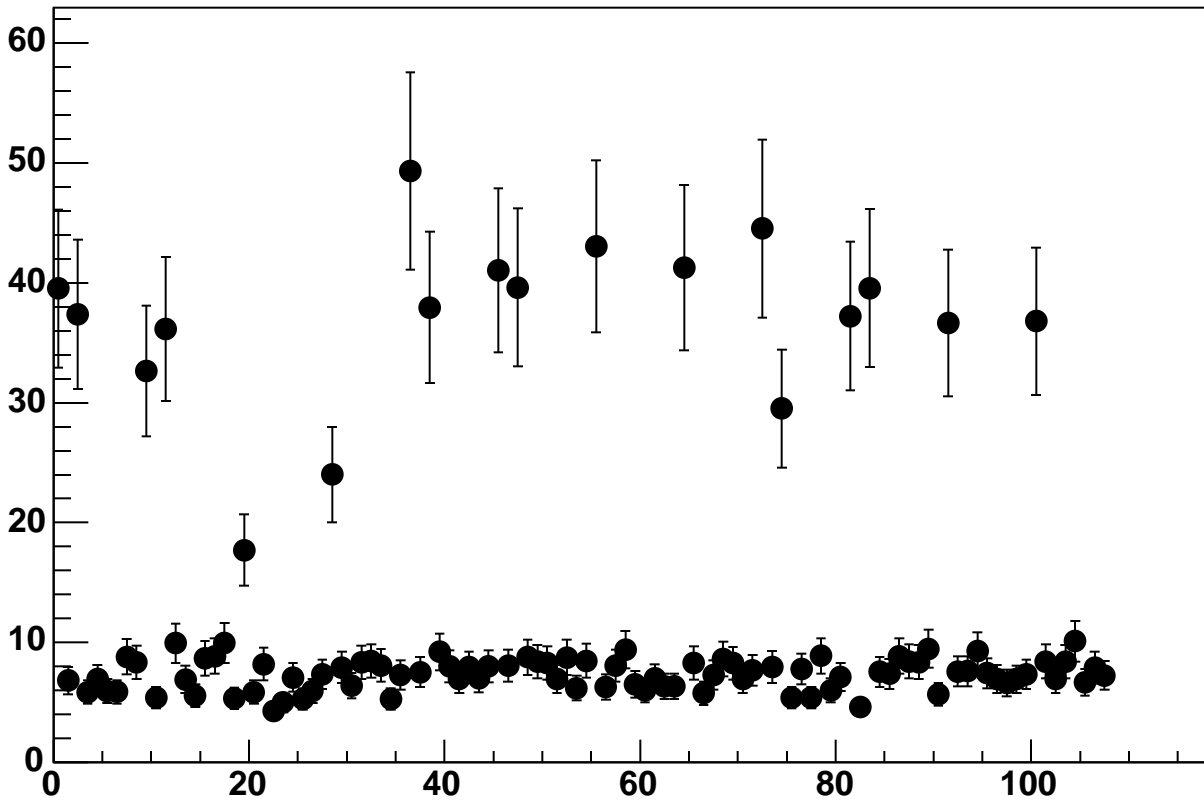
Enable 0, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



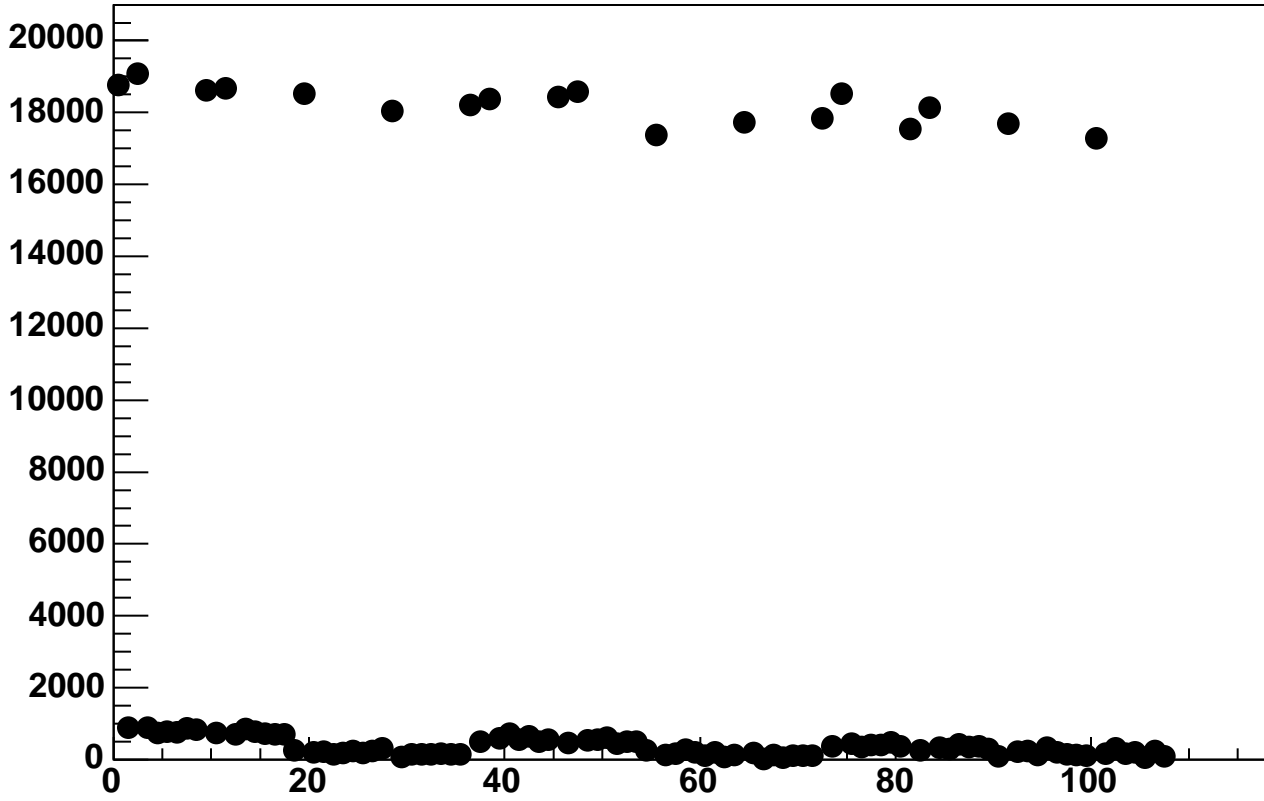
Enable 0, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



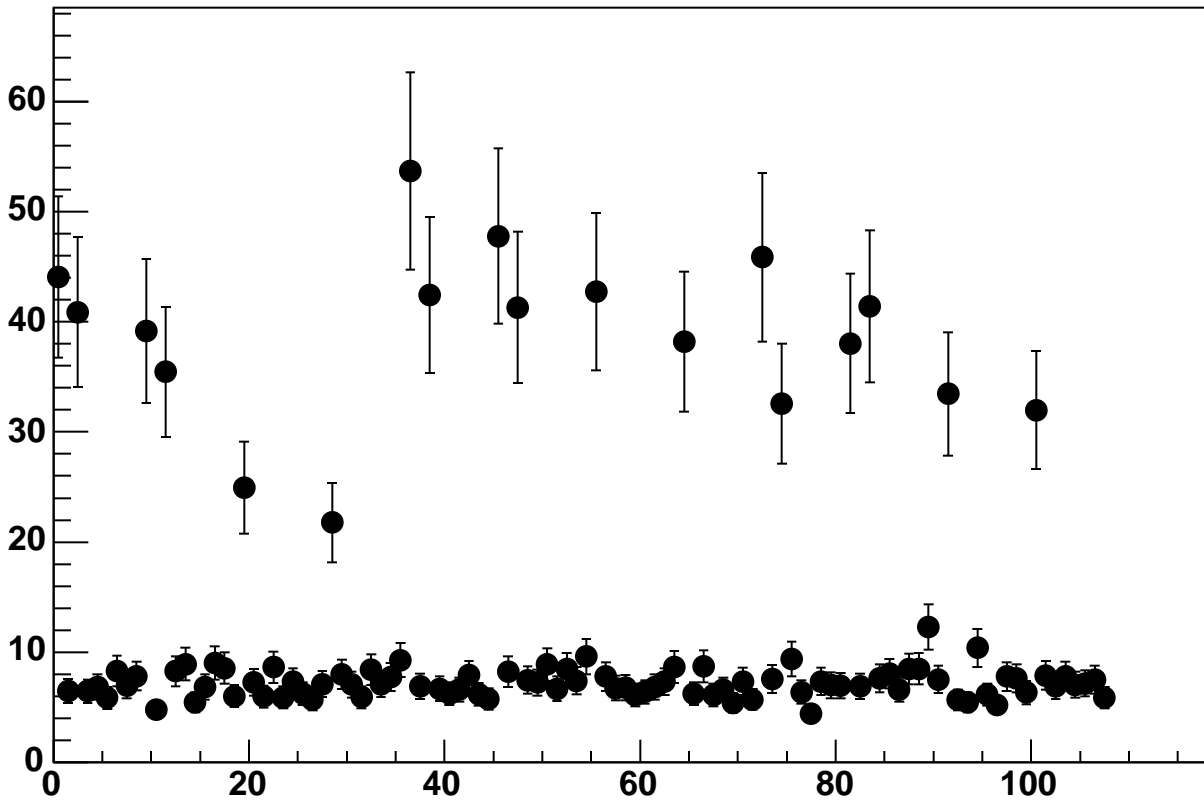
Enable 0, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



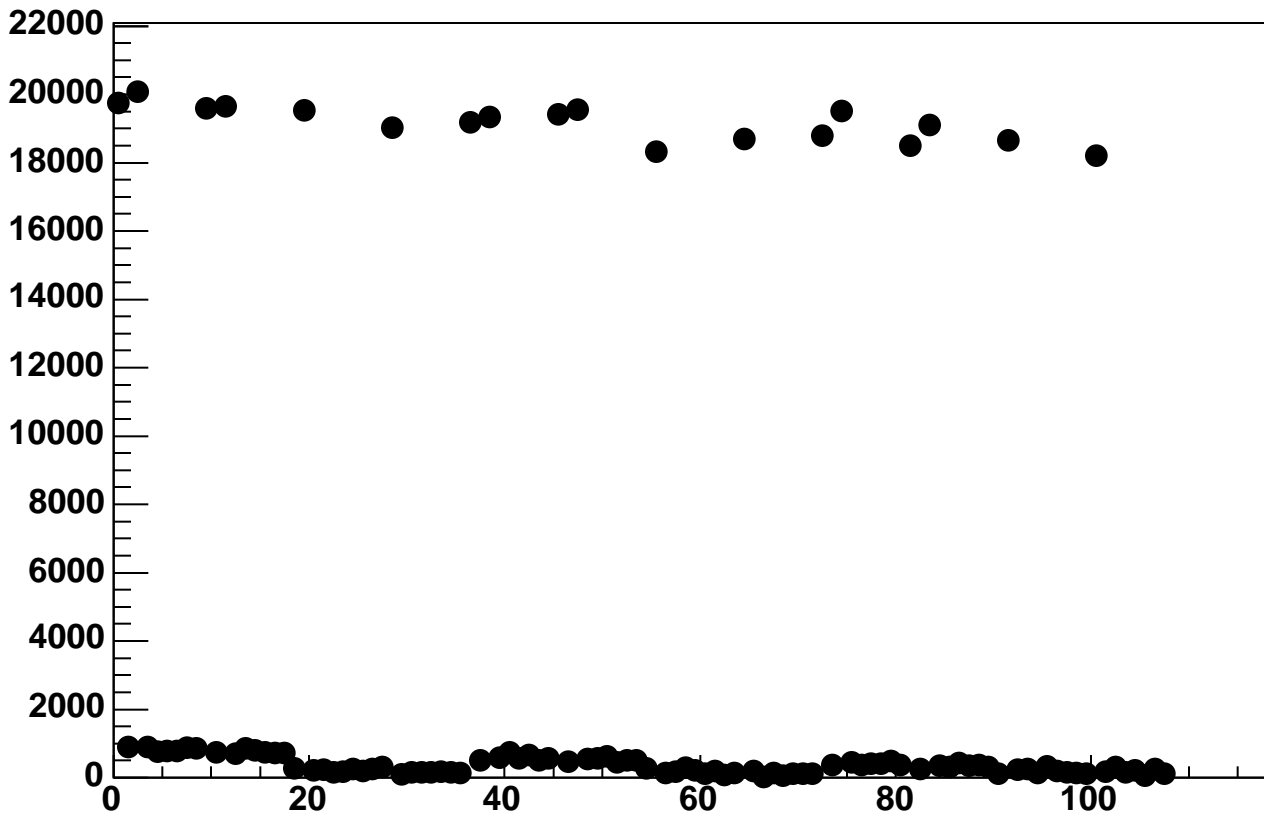
Enable 0, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



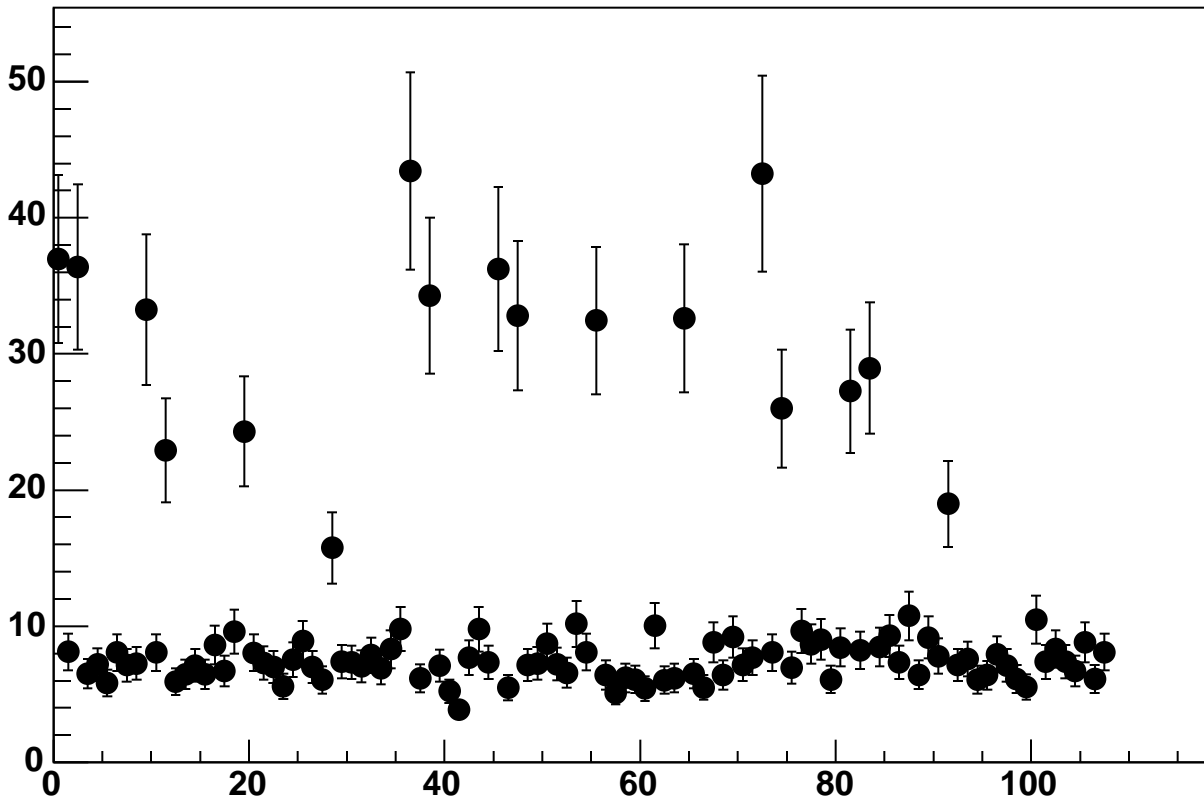
Enable 0, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



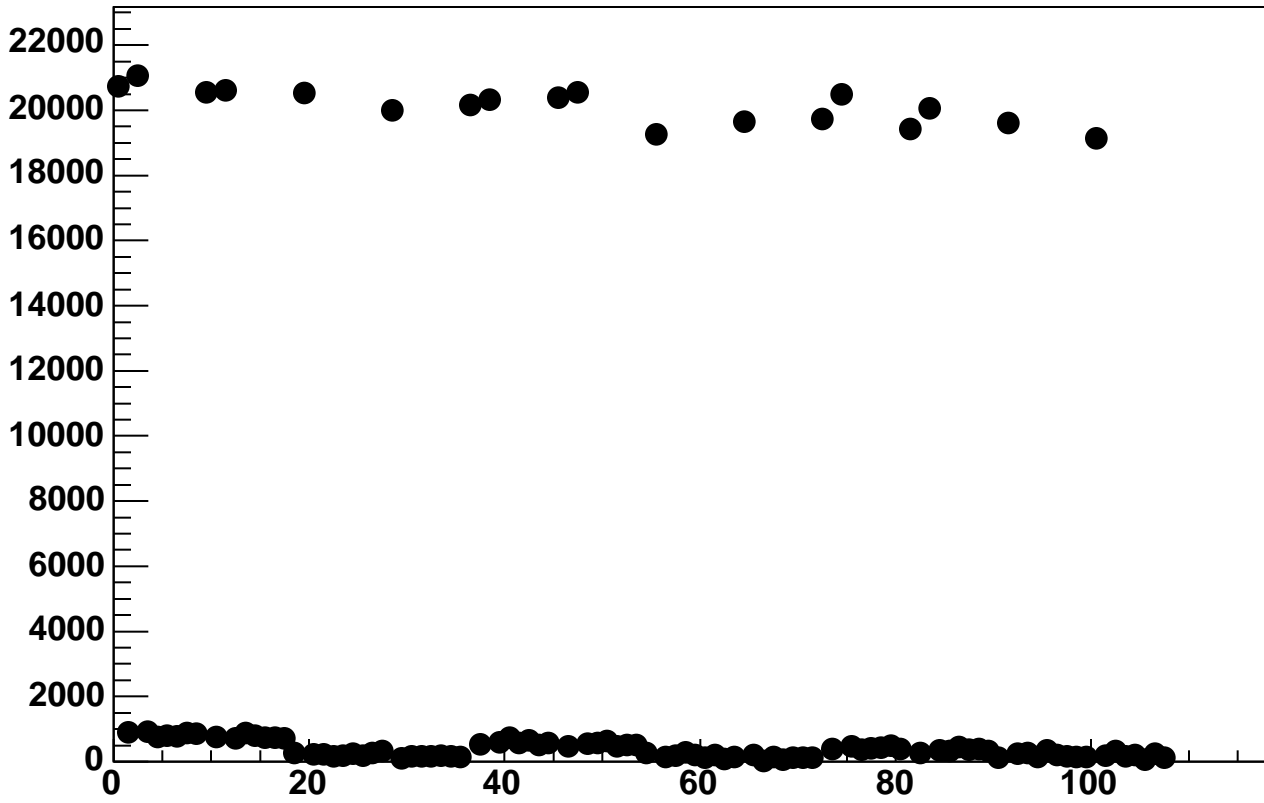
Enable 0, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



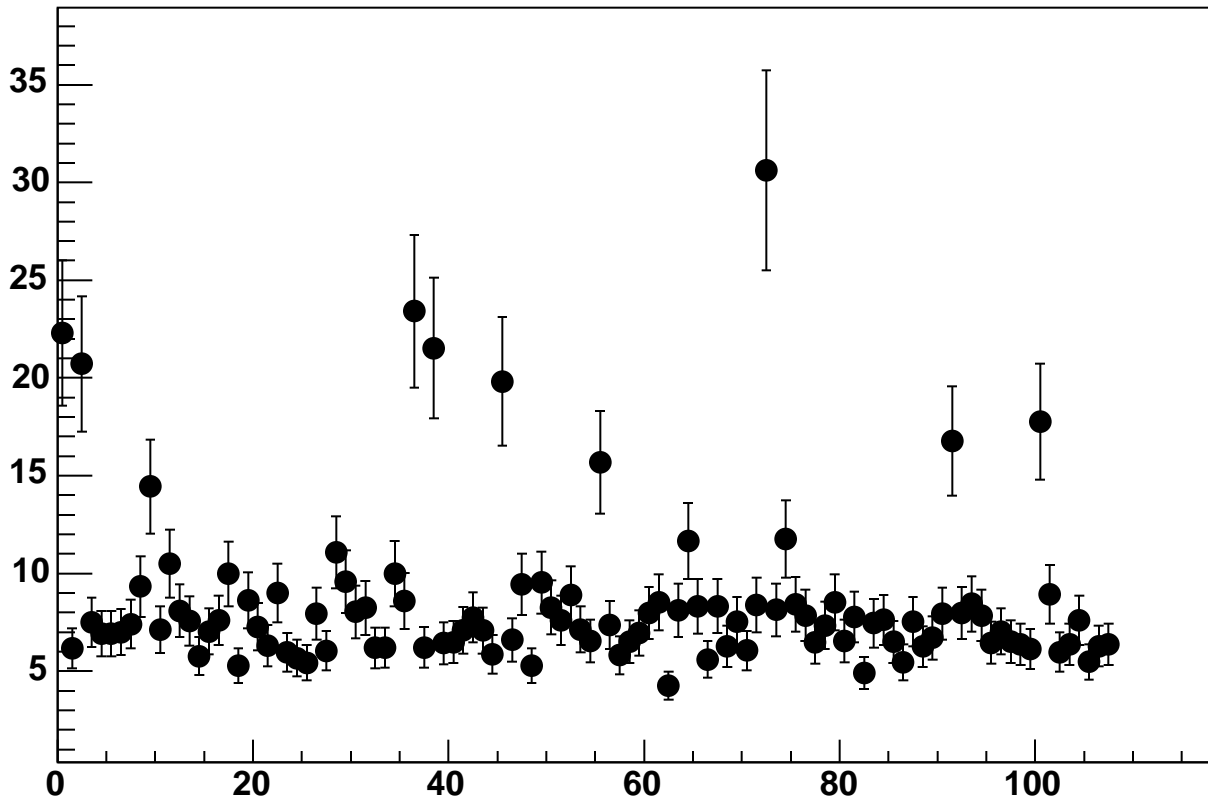
Enable 0, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



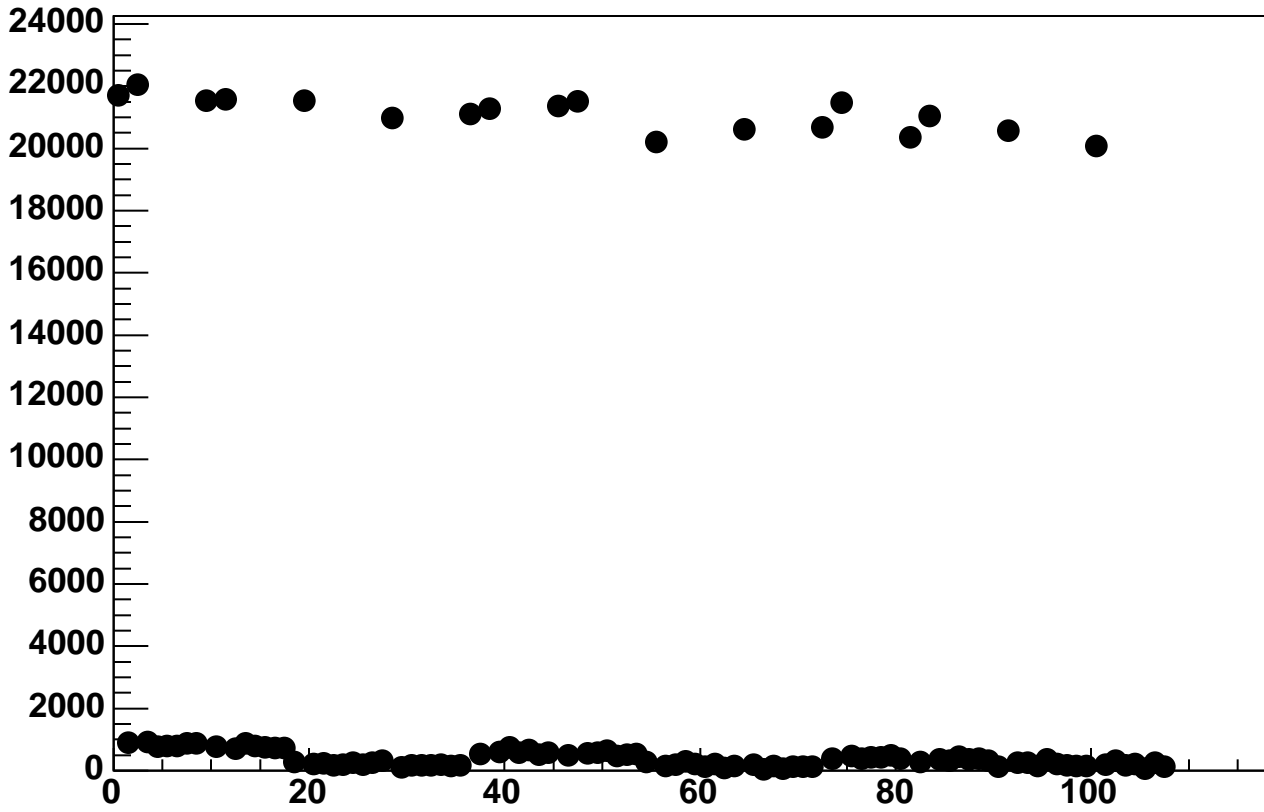
Enable 0, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



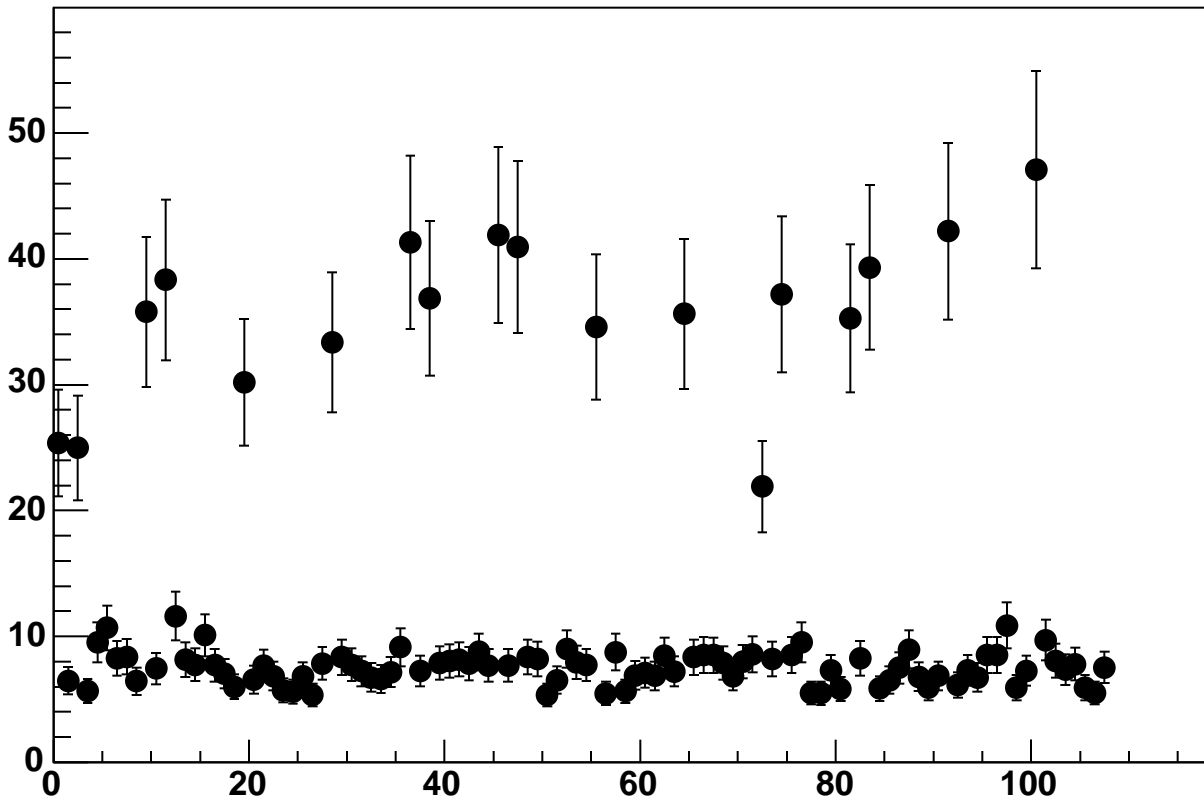
Enable 0, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



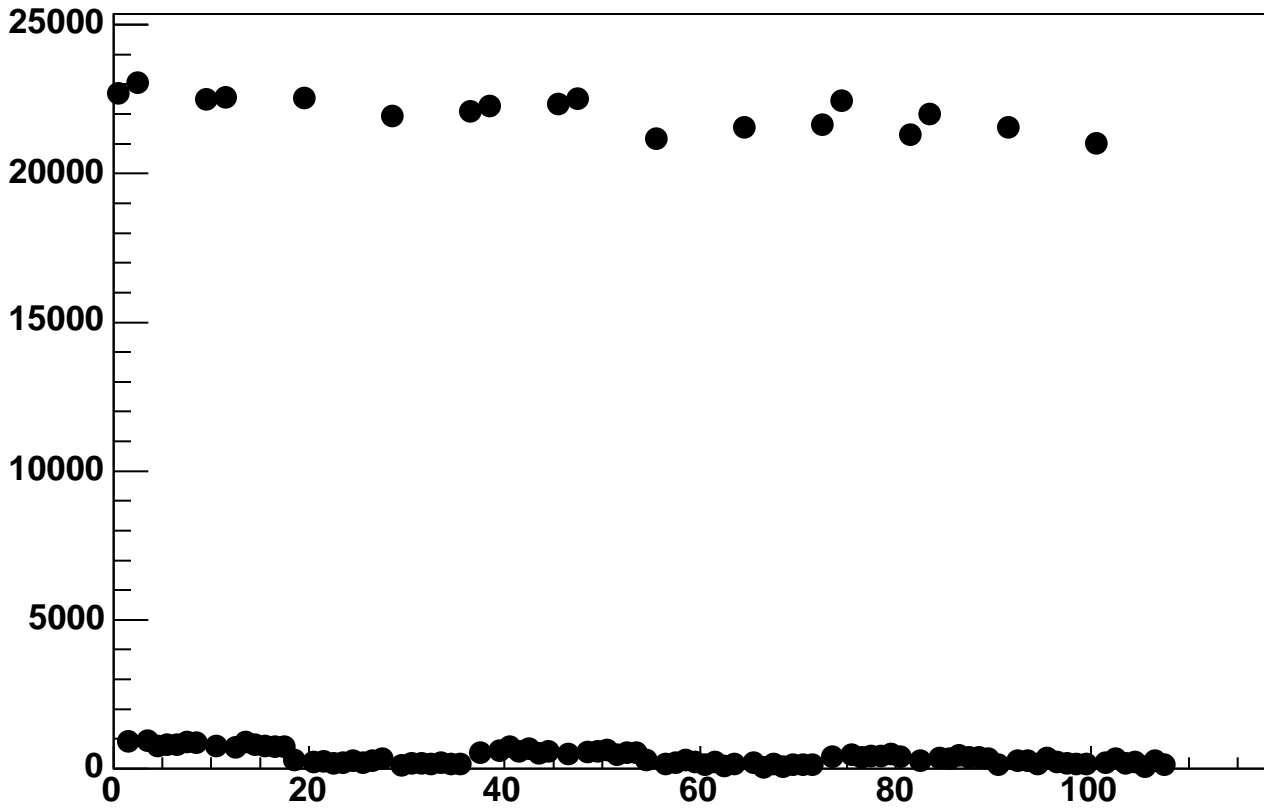
Enable 0, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



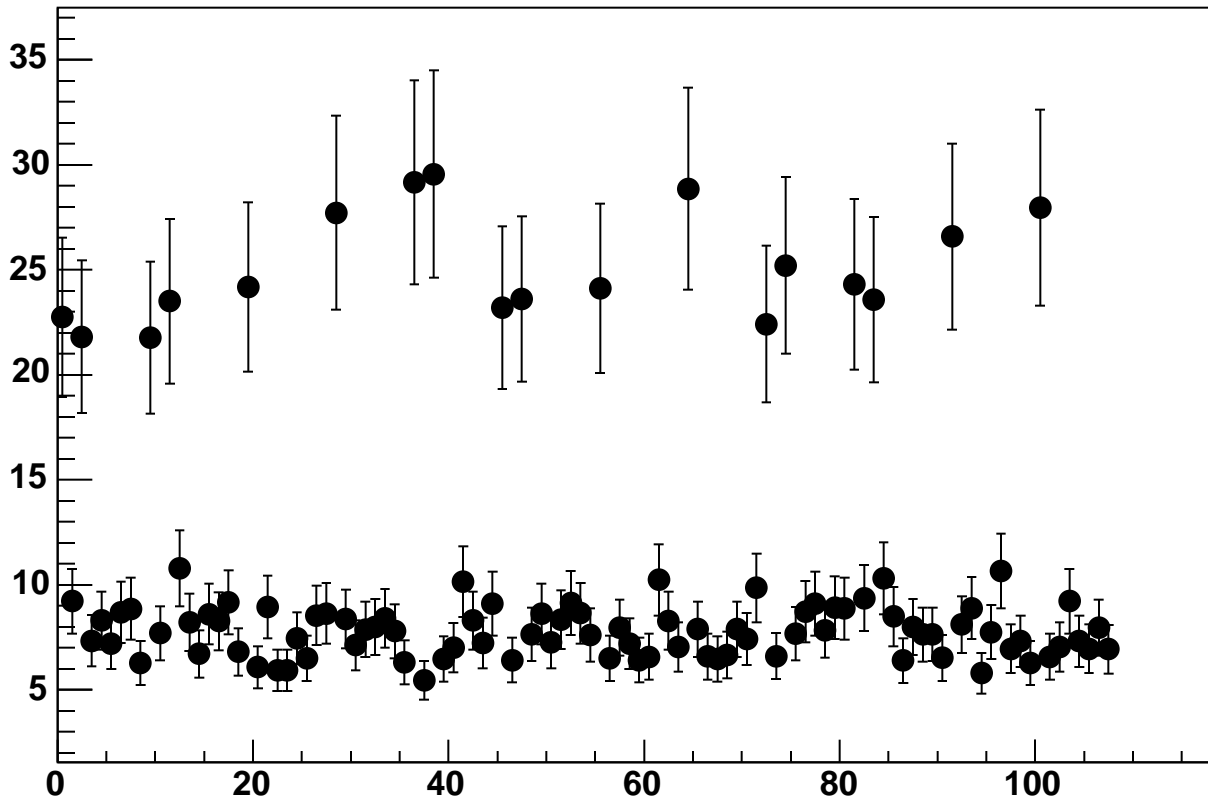
Enable 0, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



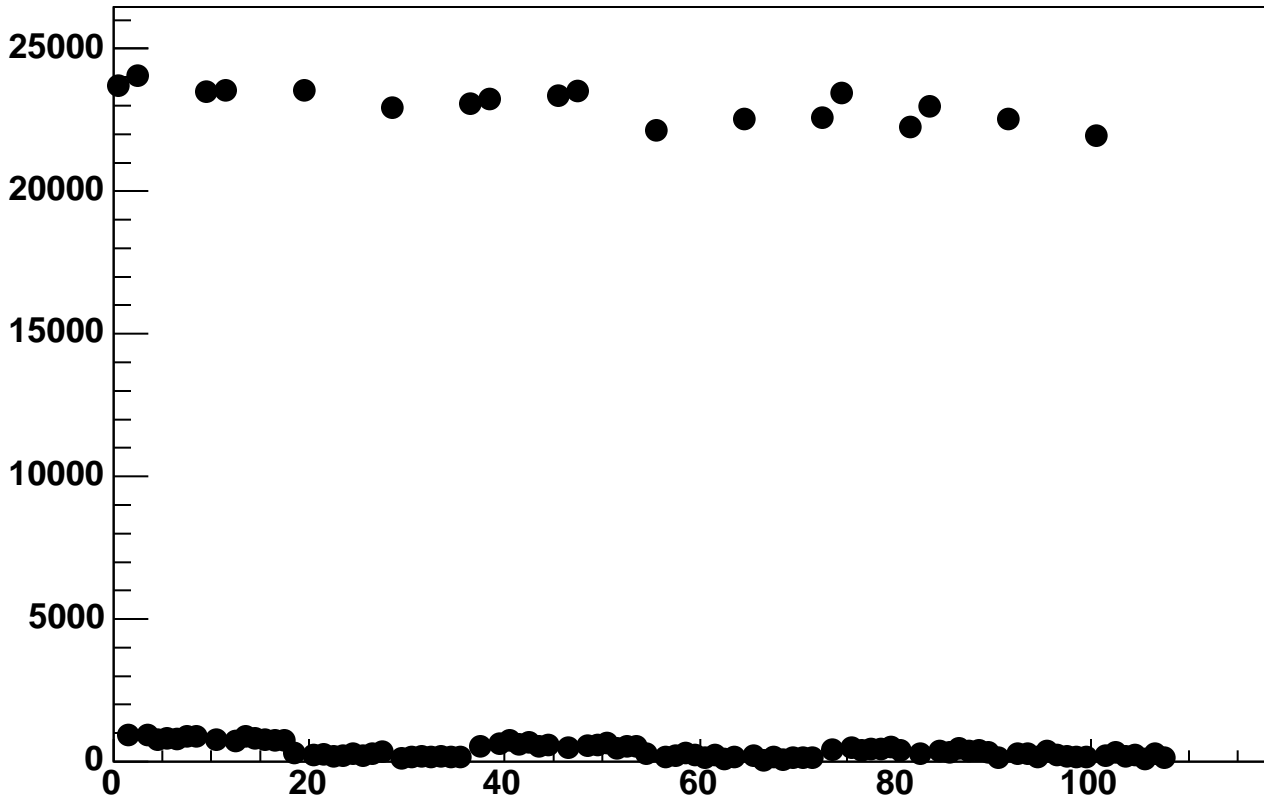
Enable 0, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



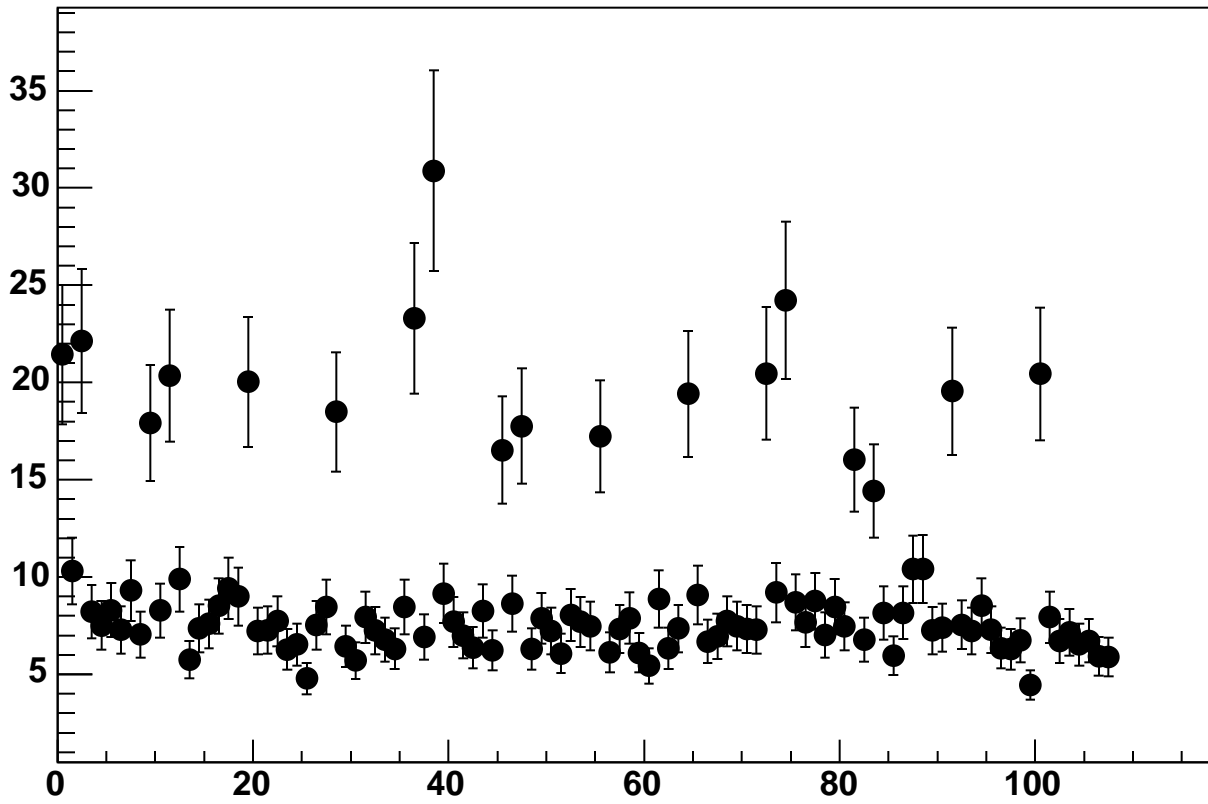
Enable 0, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



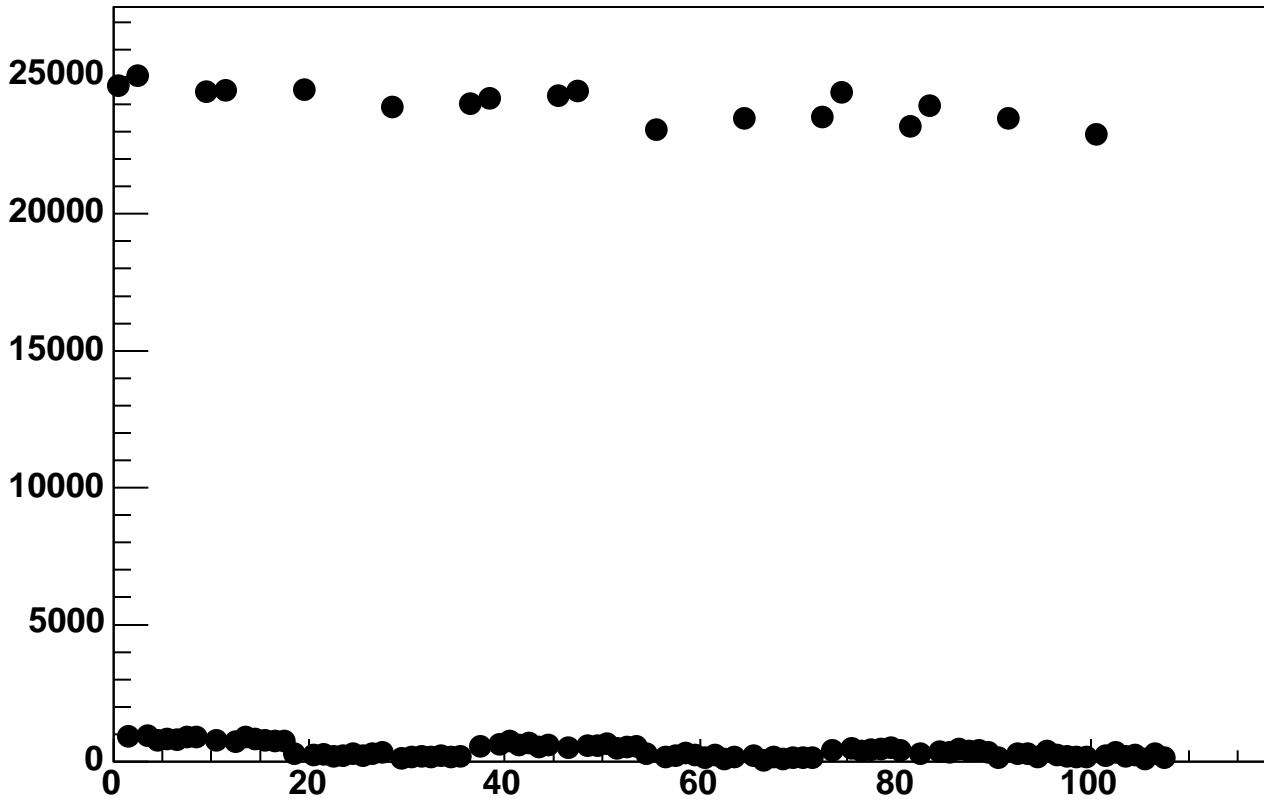
Enable 0, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



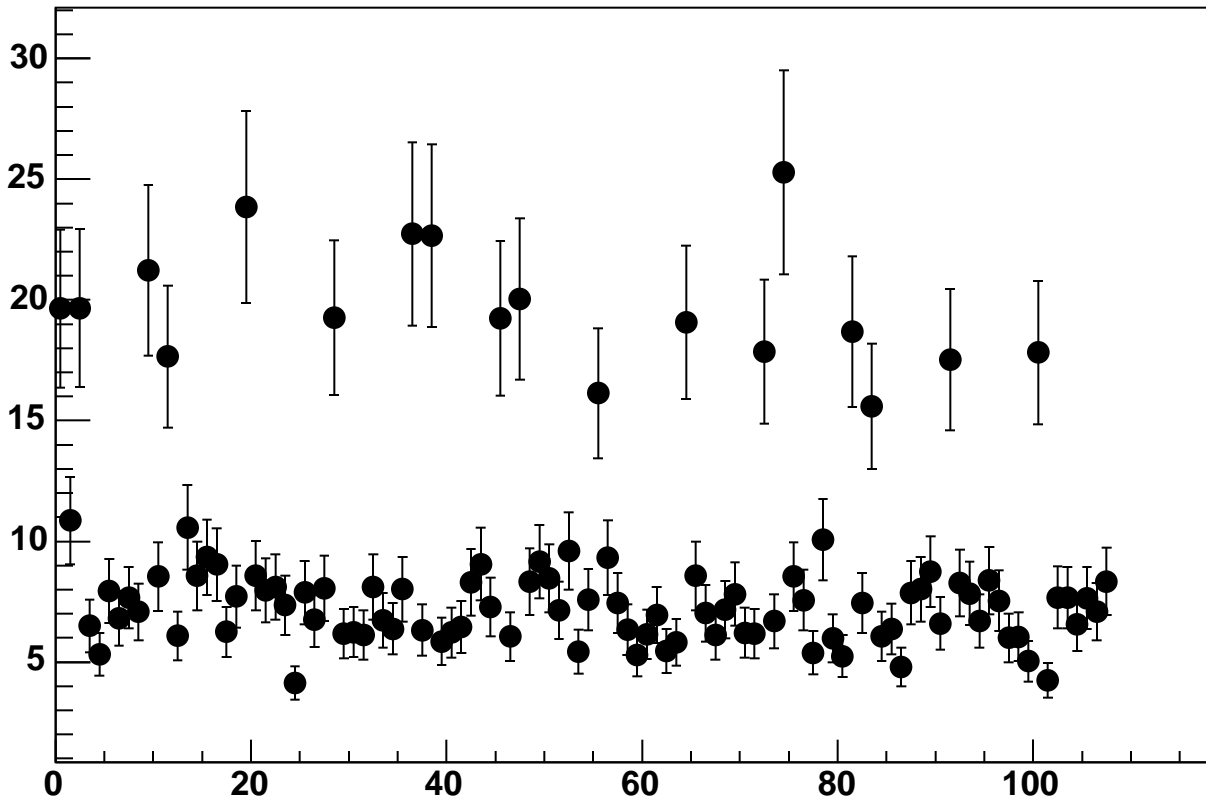
Enable 0, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



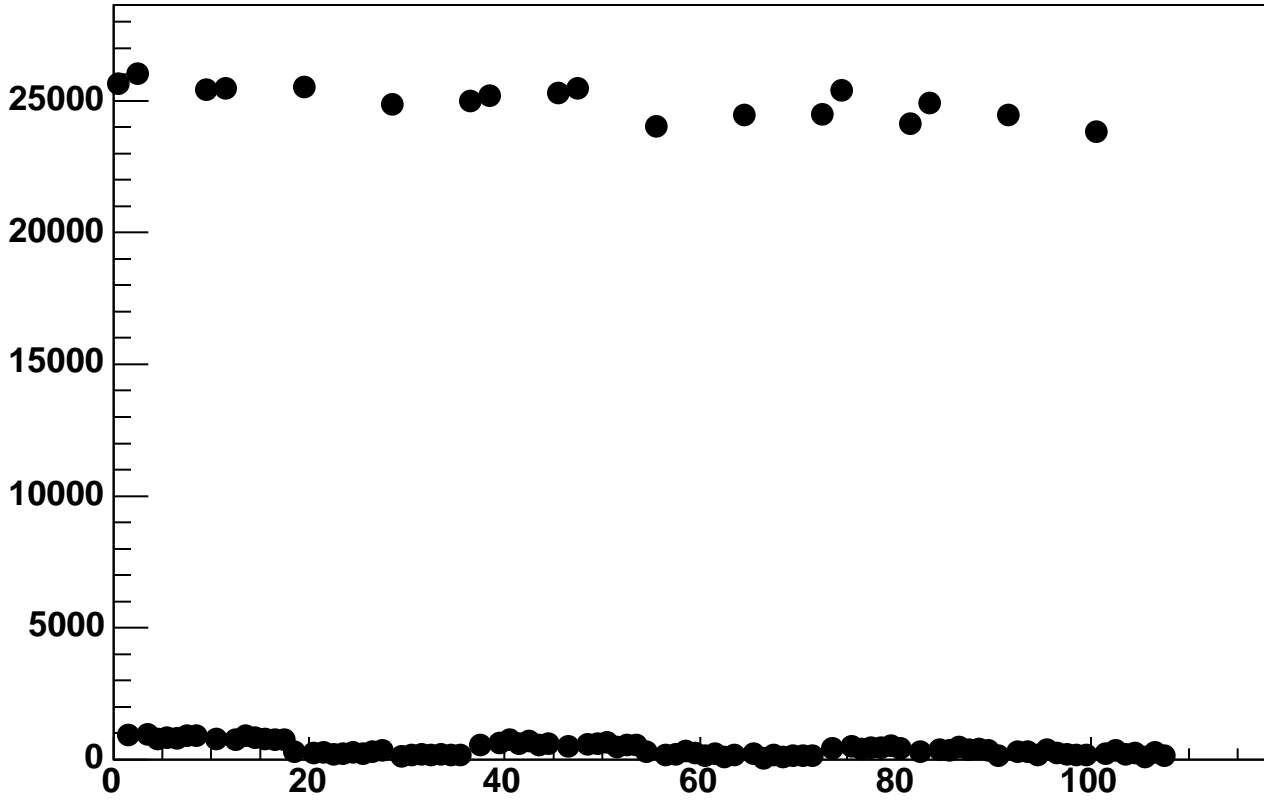
Enable 0, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



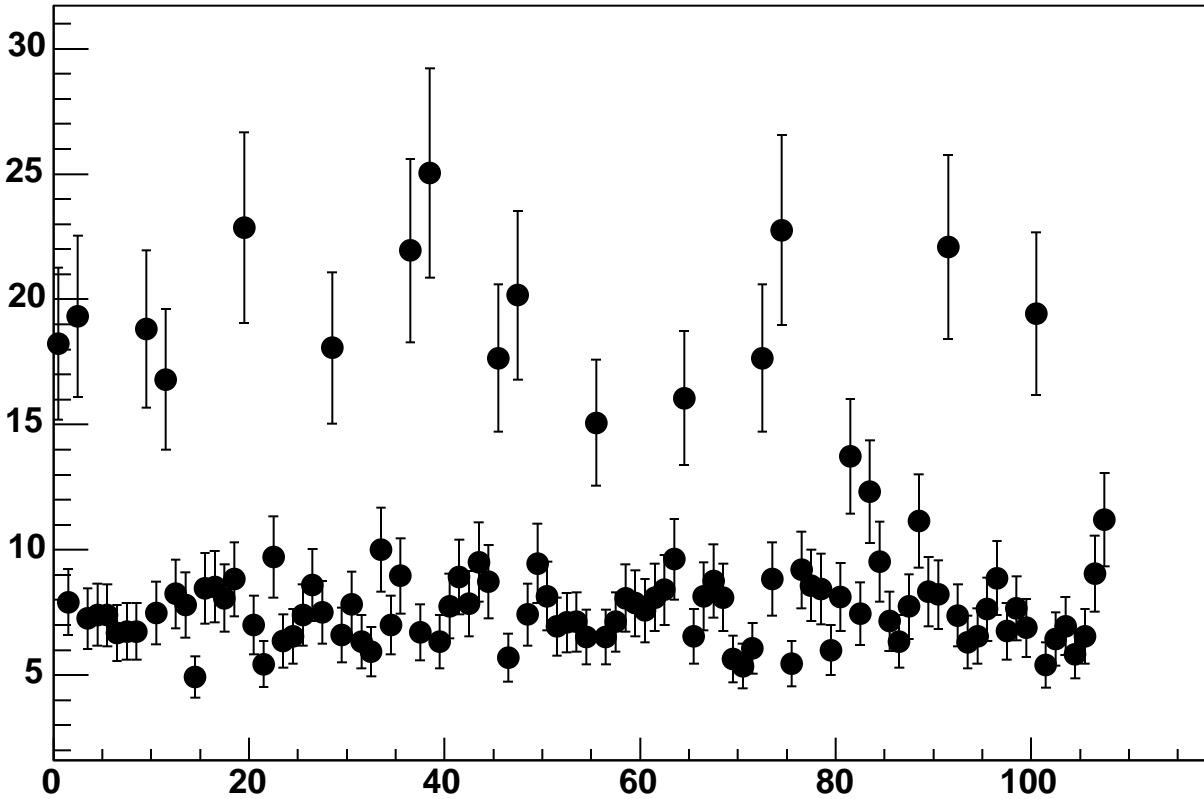
Enable 0, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



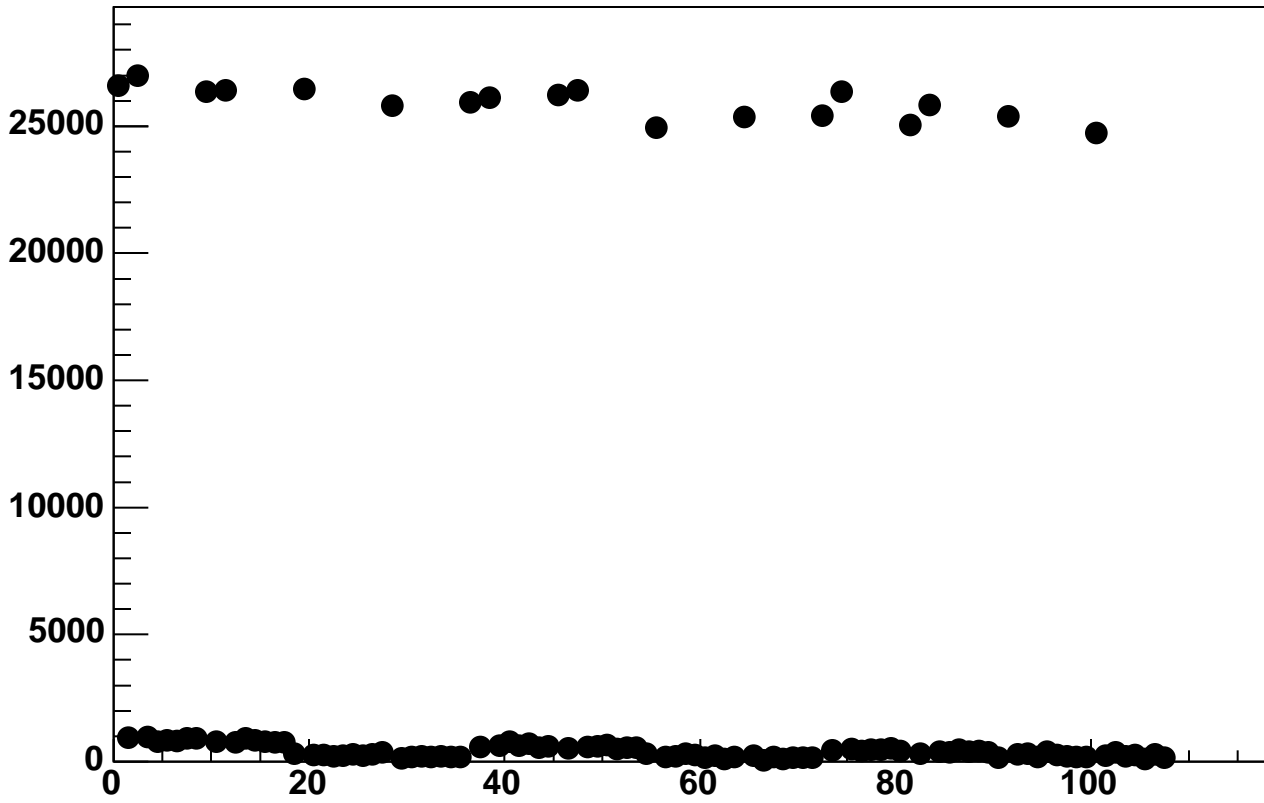
Enable 0, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



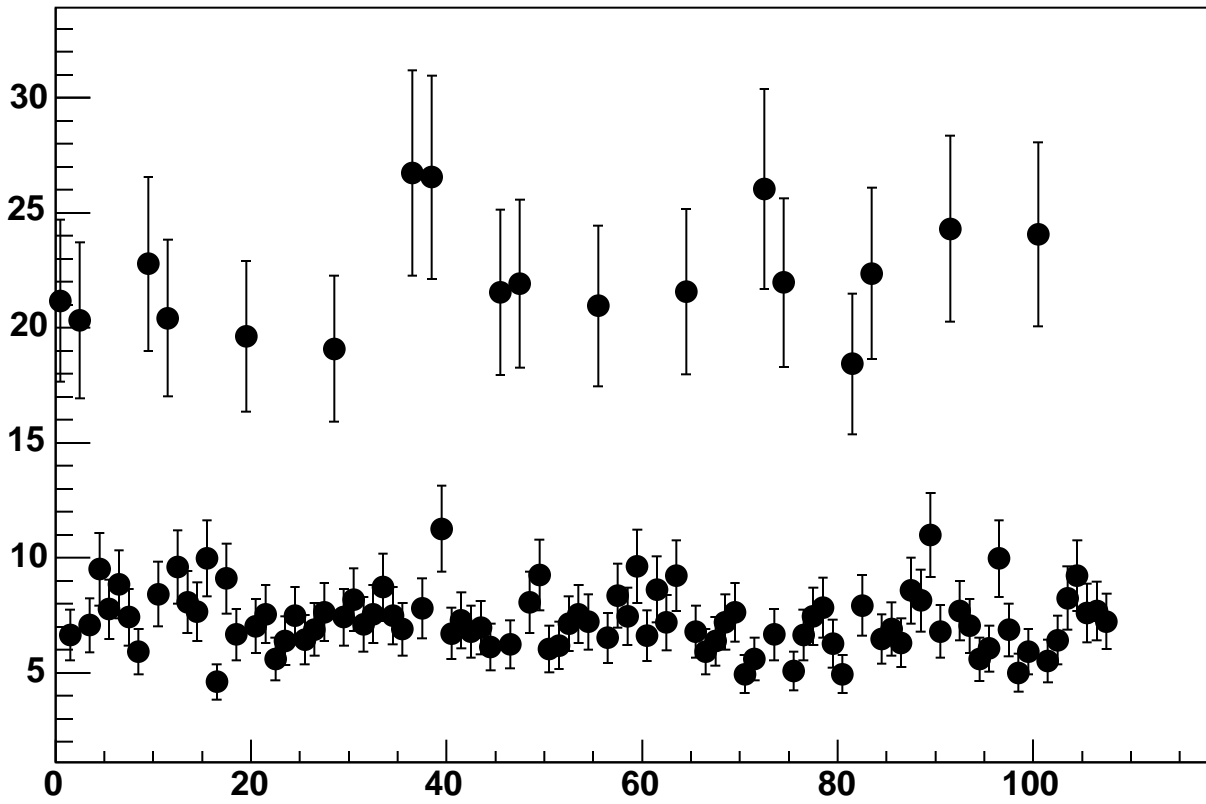
Enable 0, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



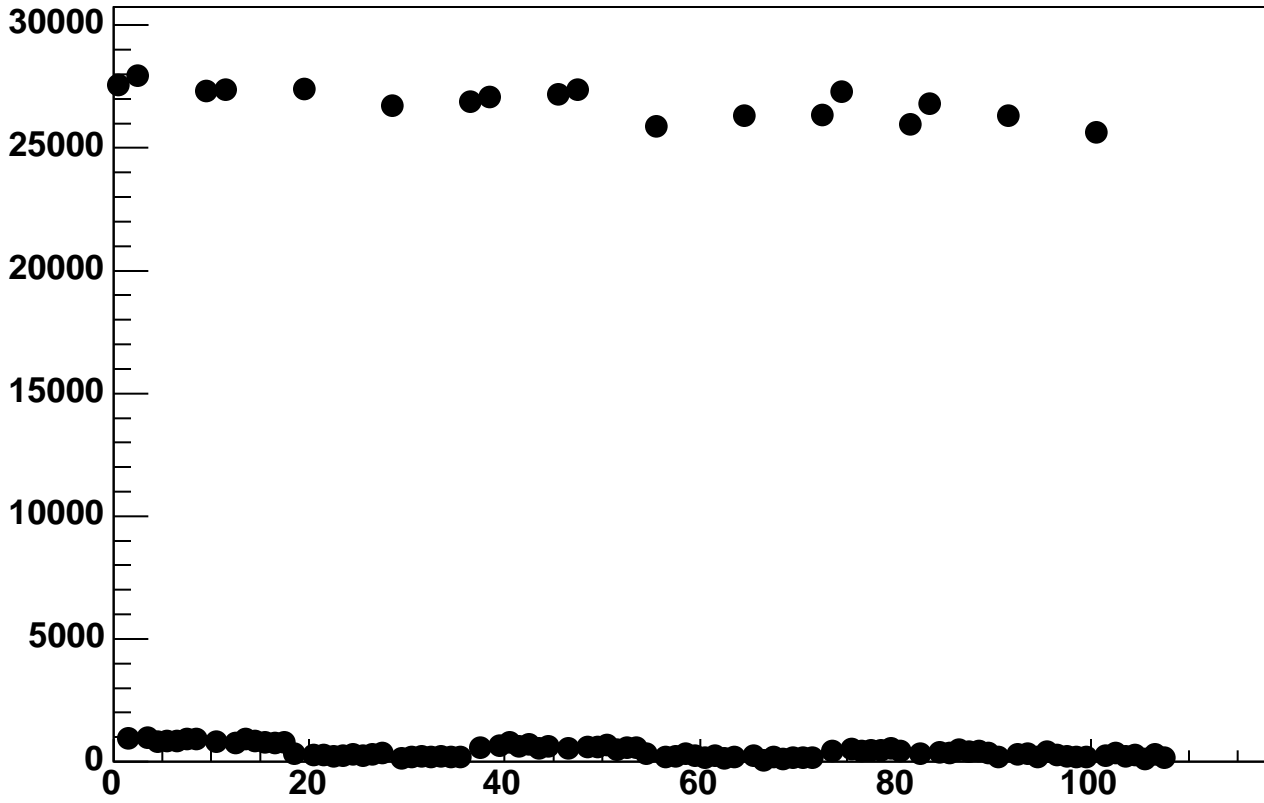
Enable 0, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



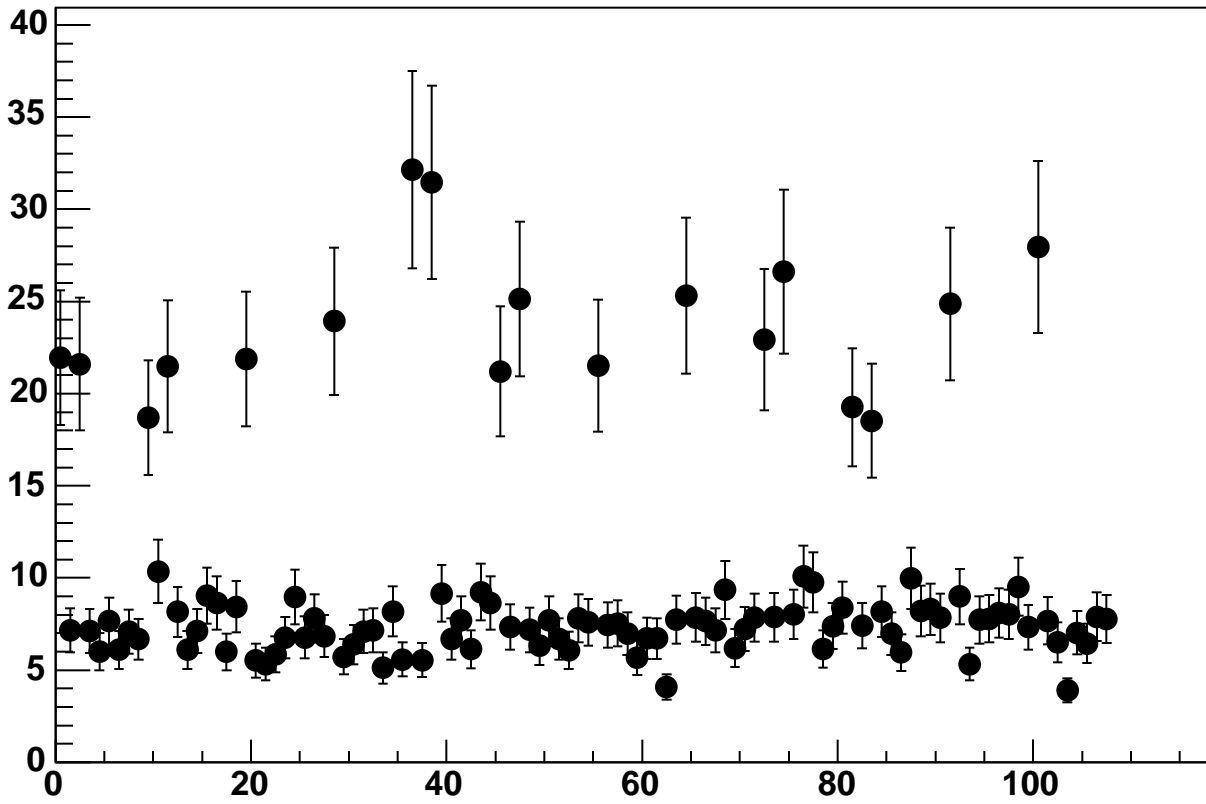
Enable 0, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



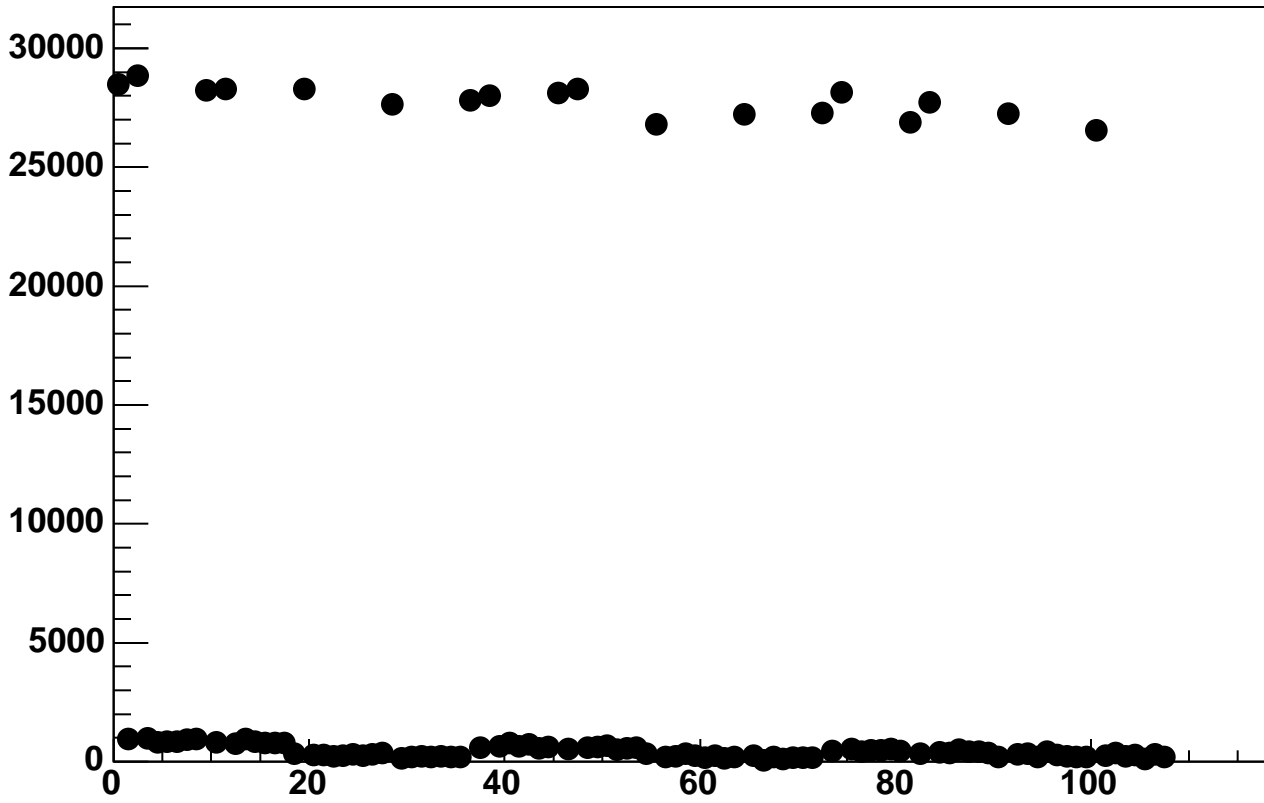
Enable 0, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



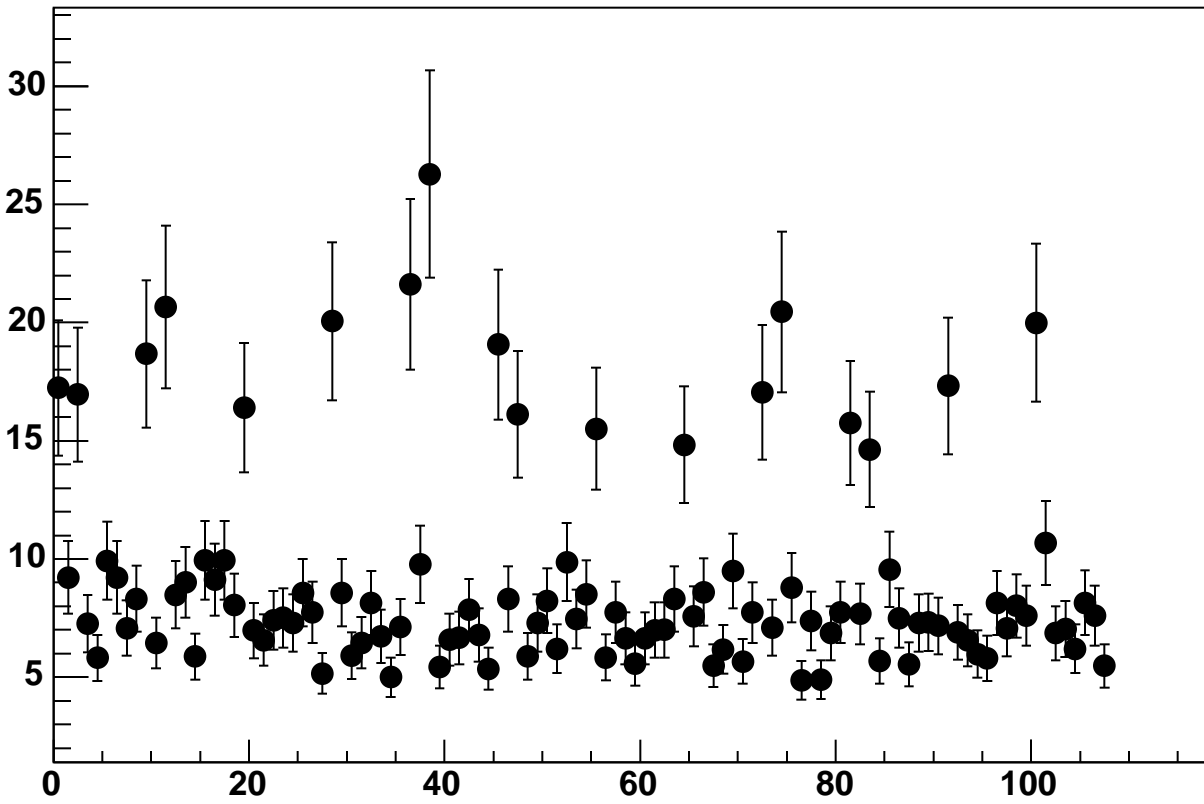
Enable 0, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



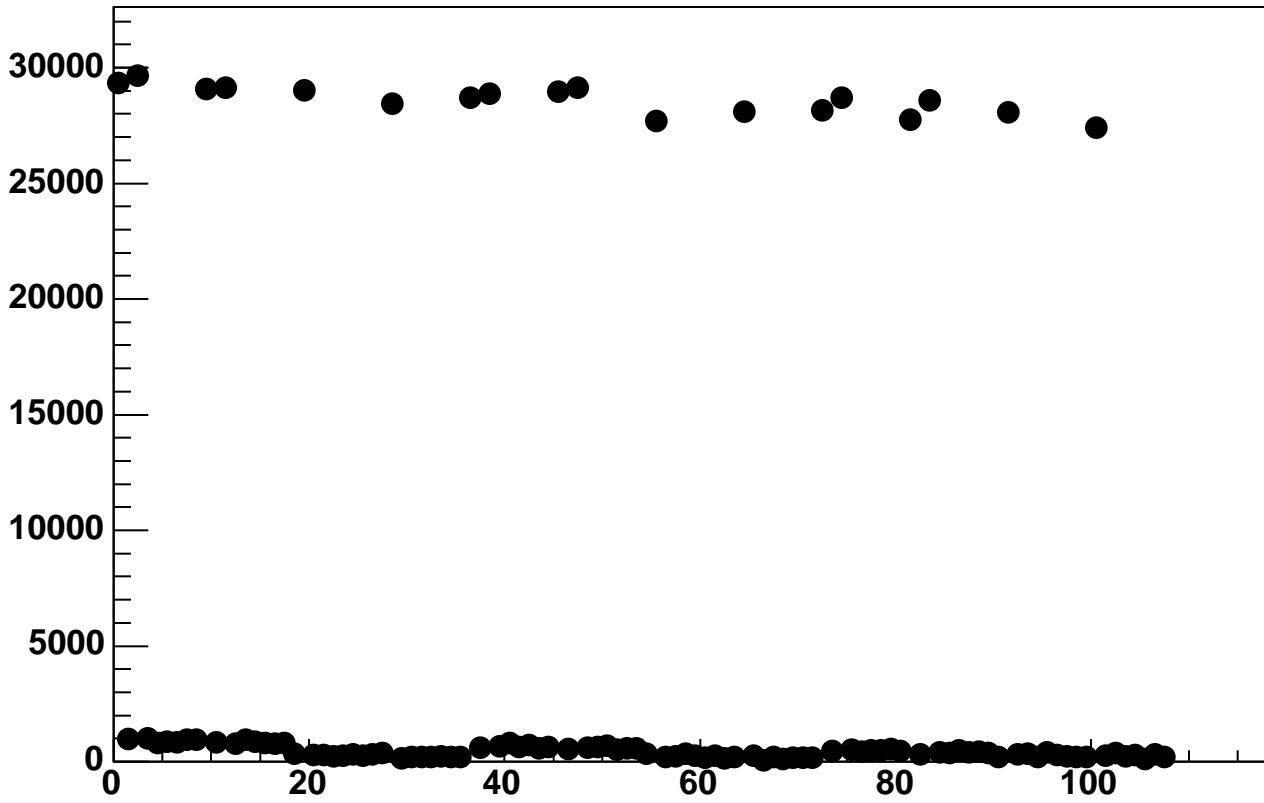
Enable 0, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



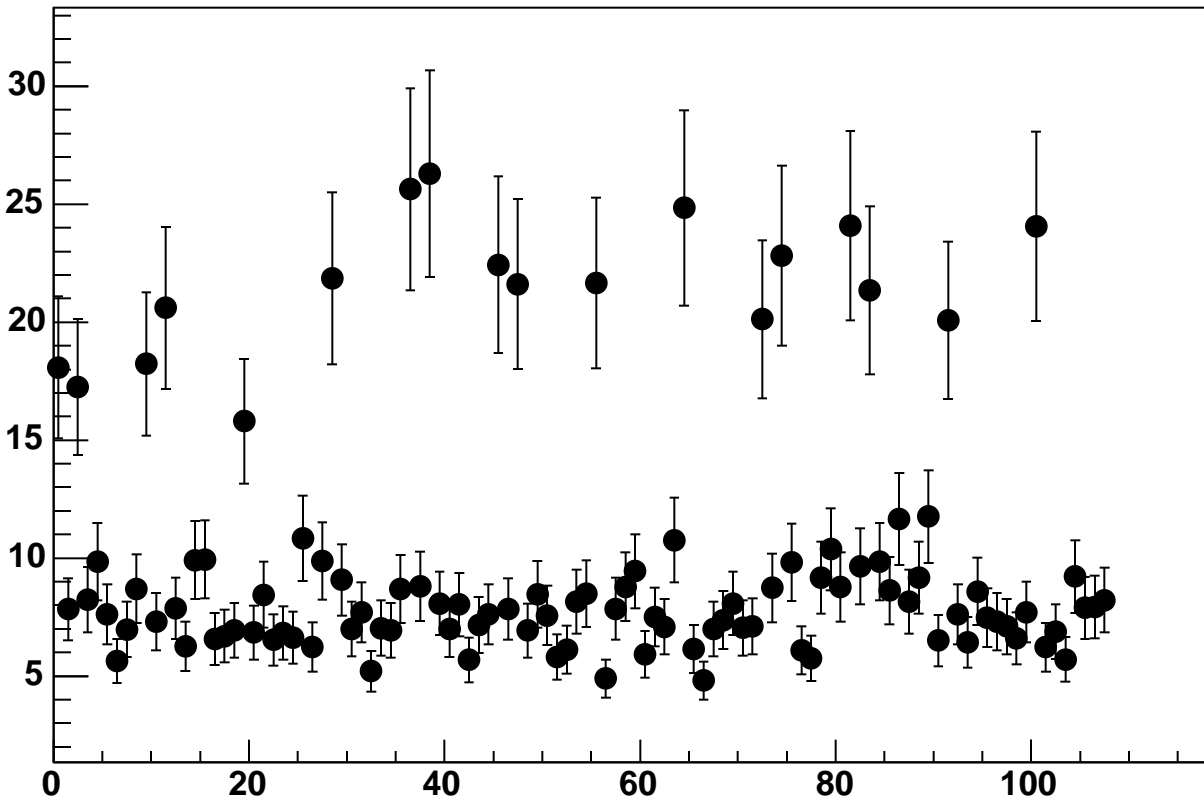
Enable 0, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



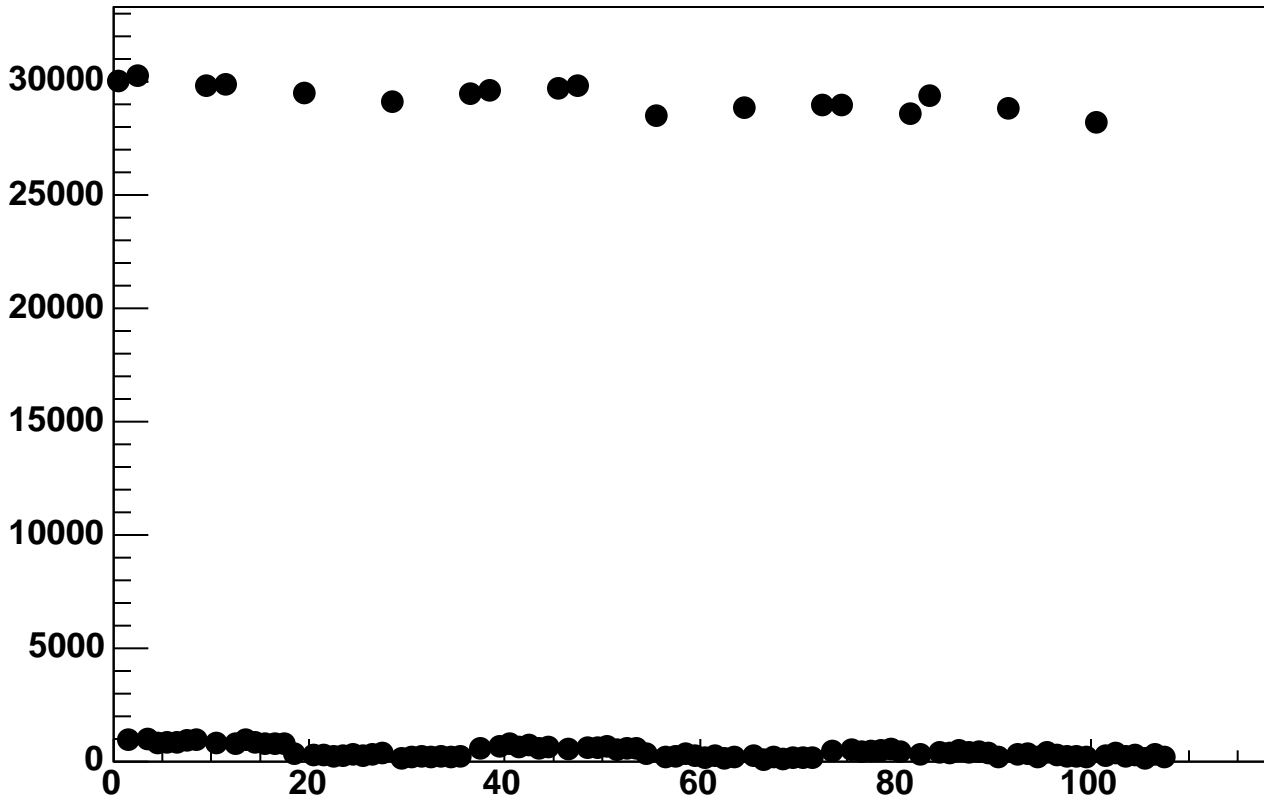
Enable 0, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



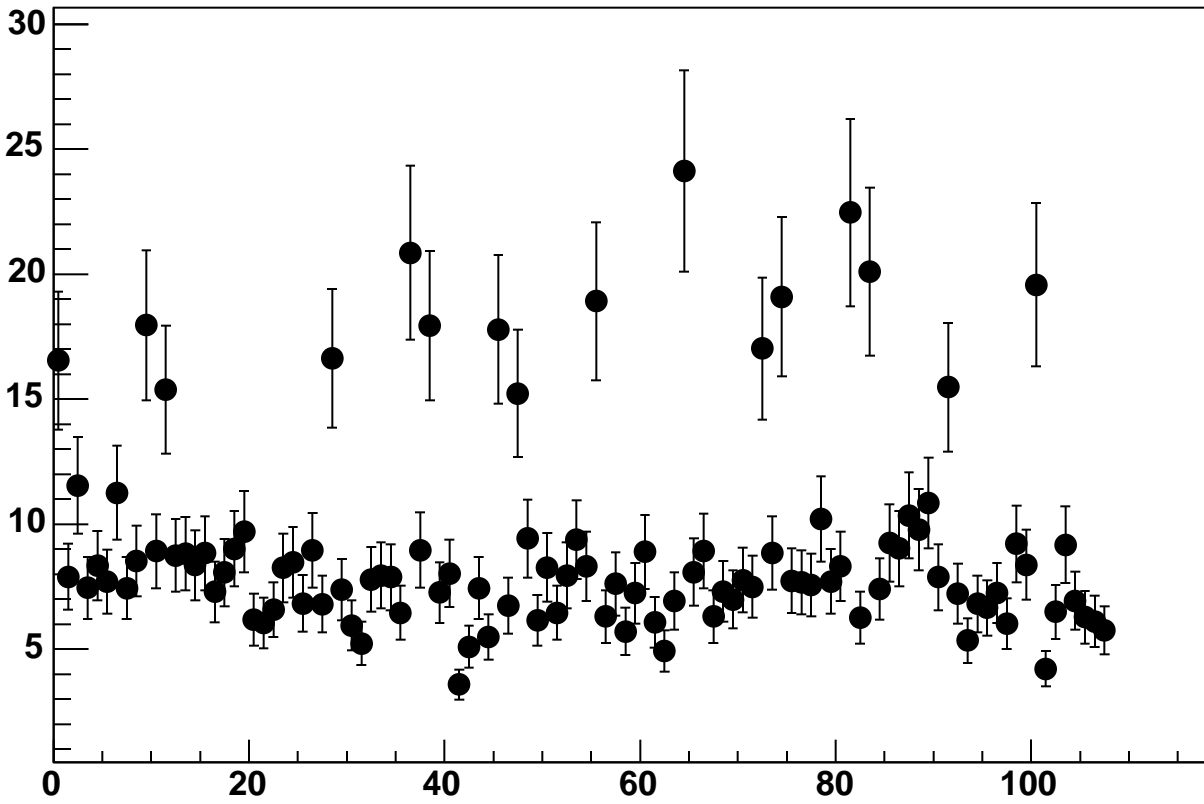
Enable 0, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



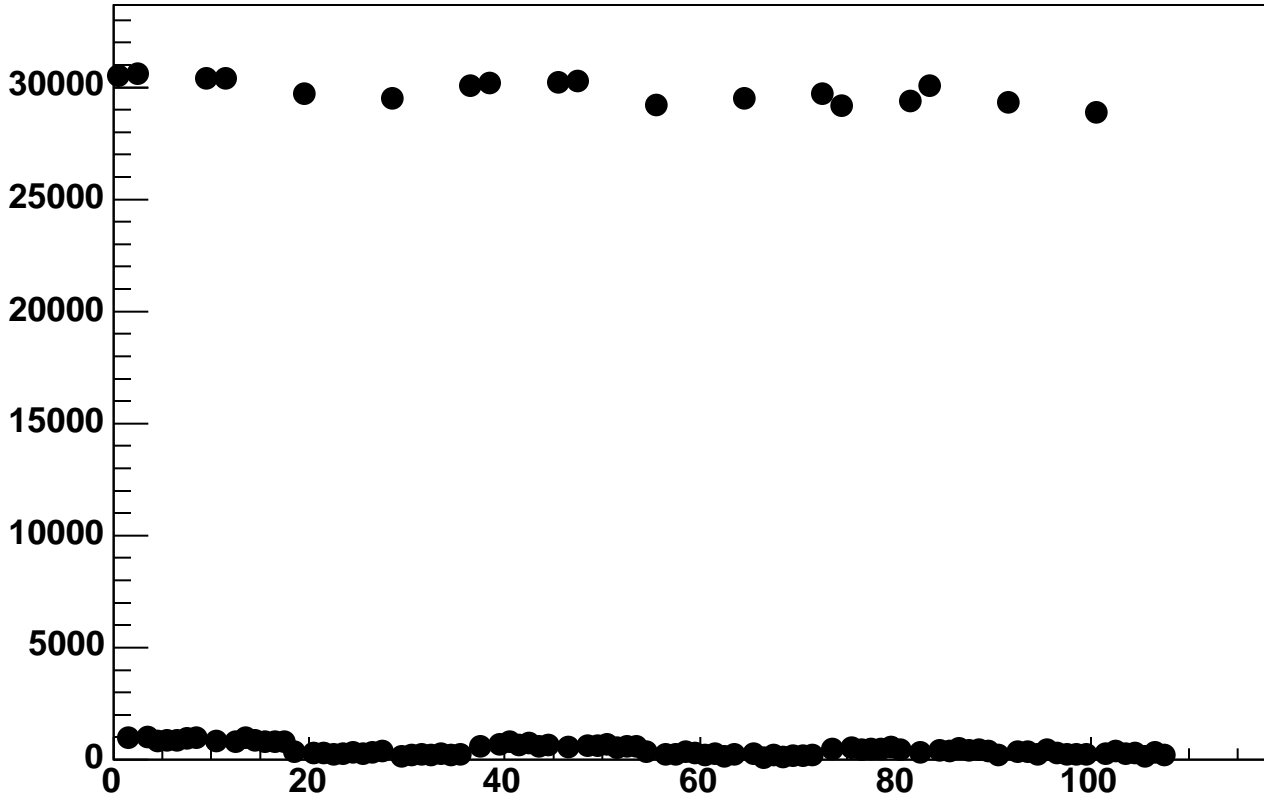
Enable 0, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



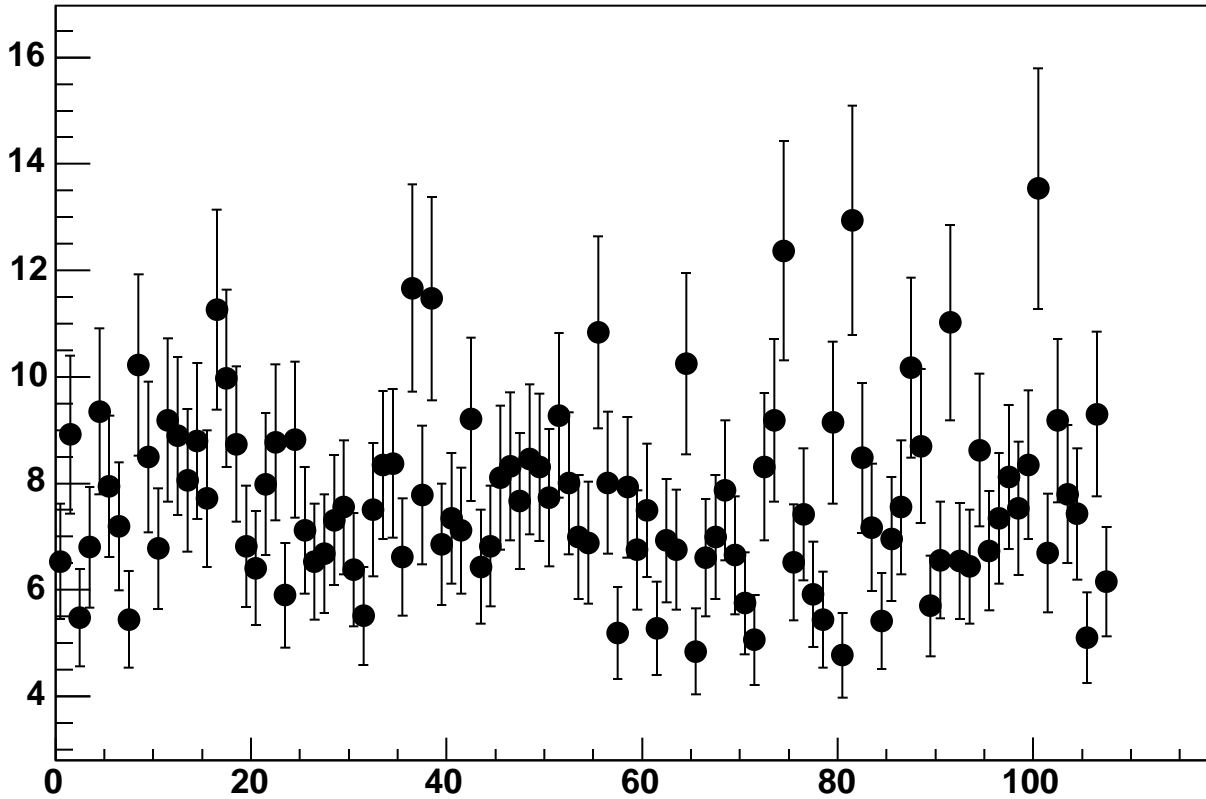
Enable 0, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



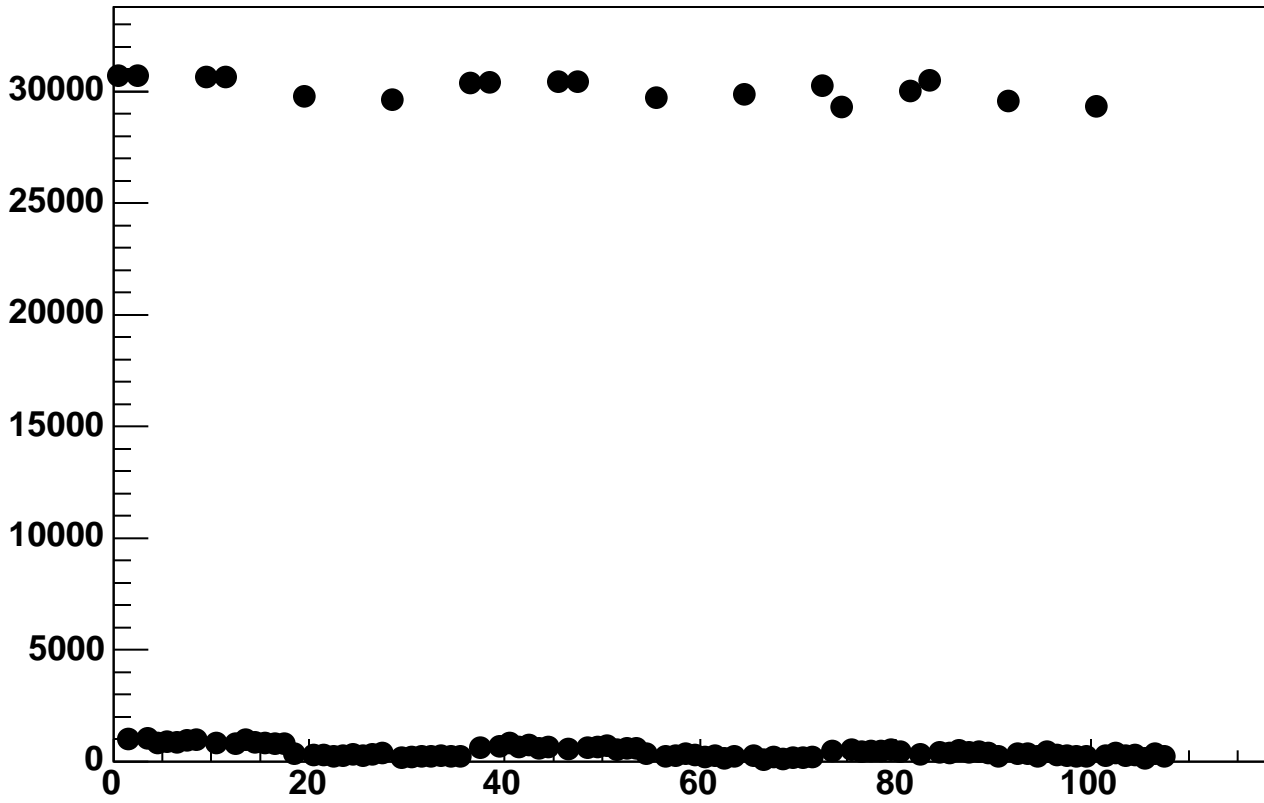
Enable 0, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



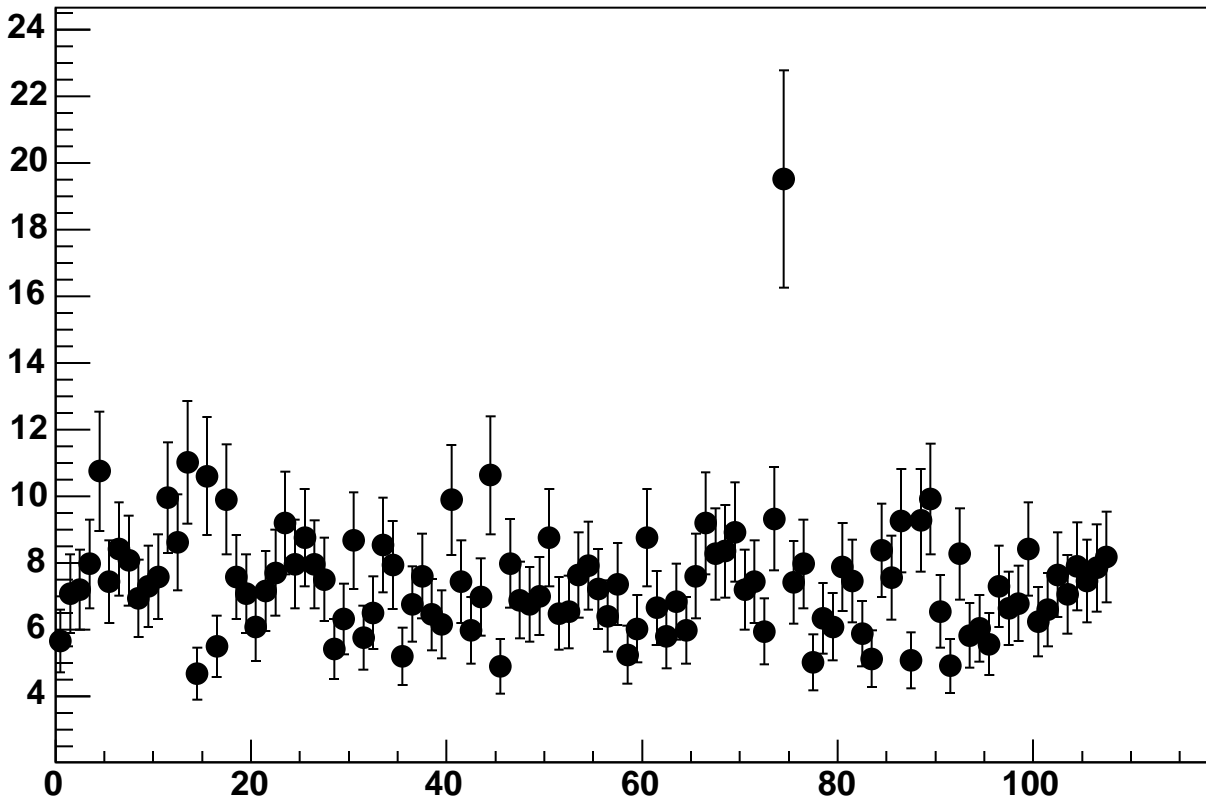
Enable 0, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



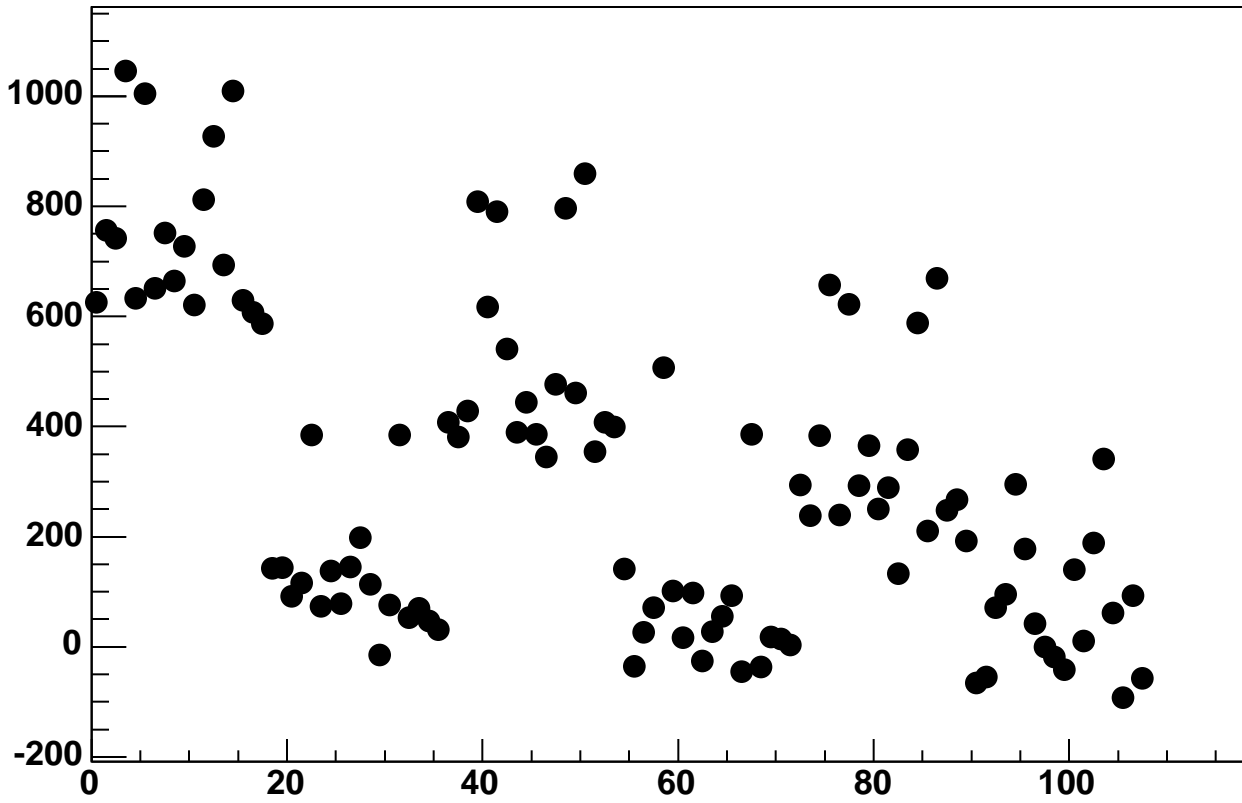
Enable 0, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



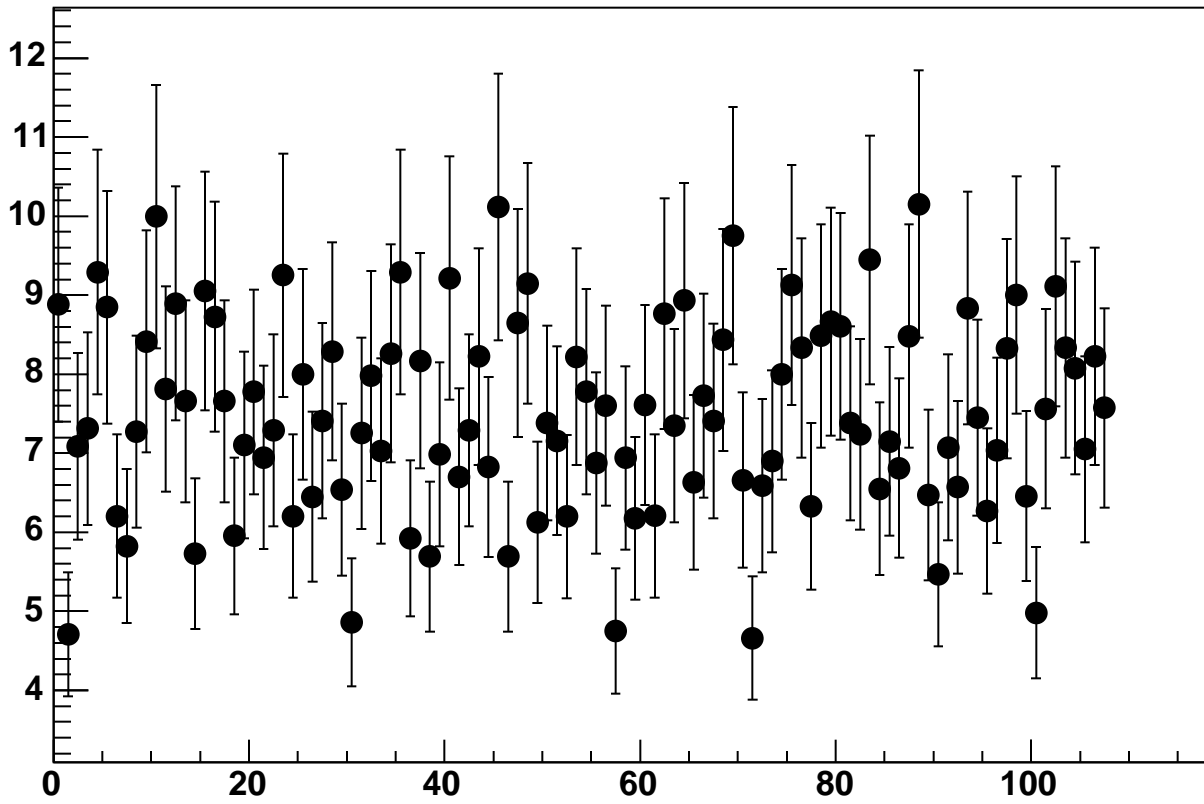
Enable 0, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



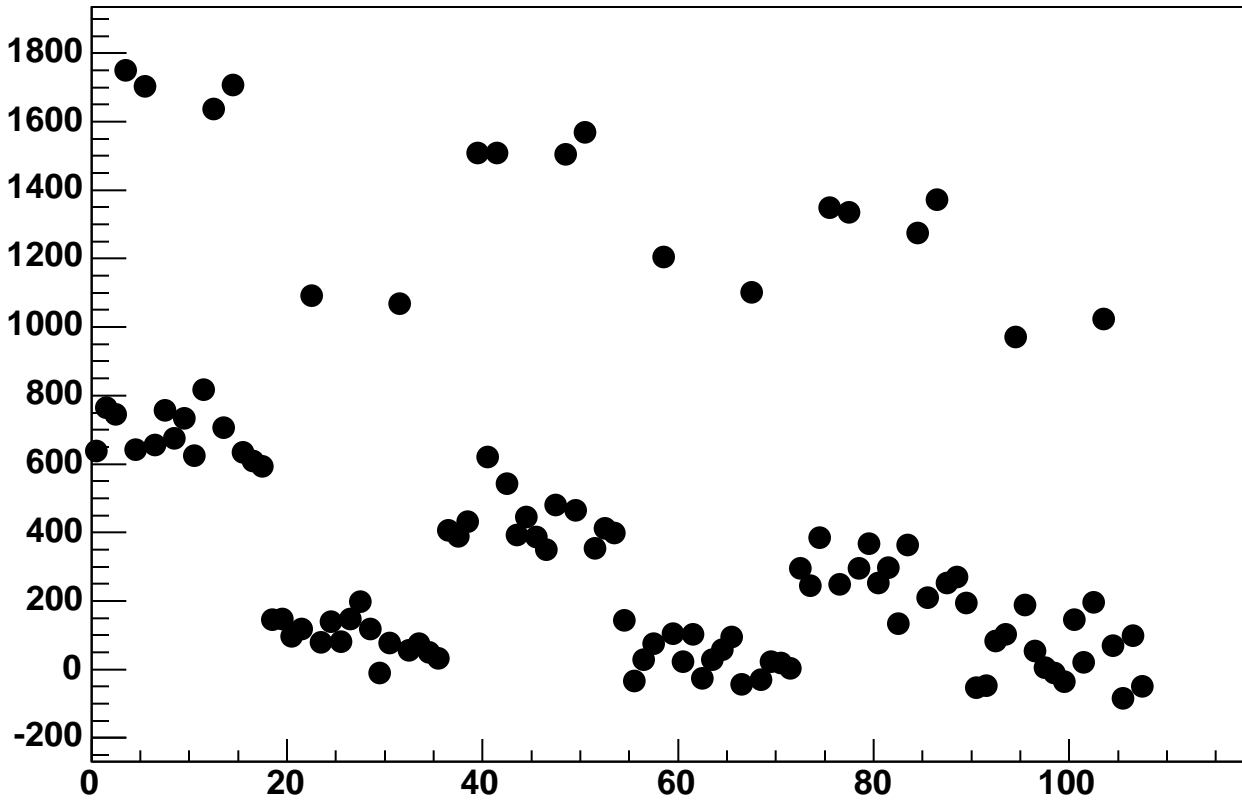
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



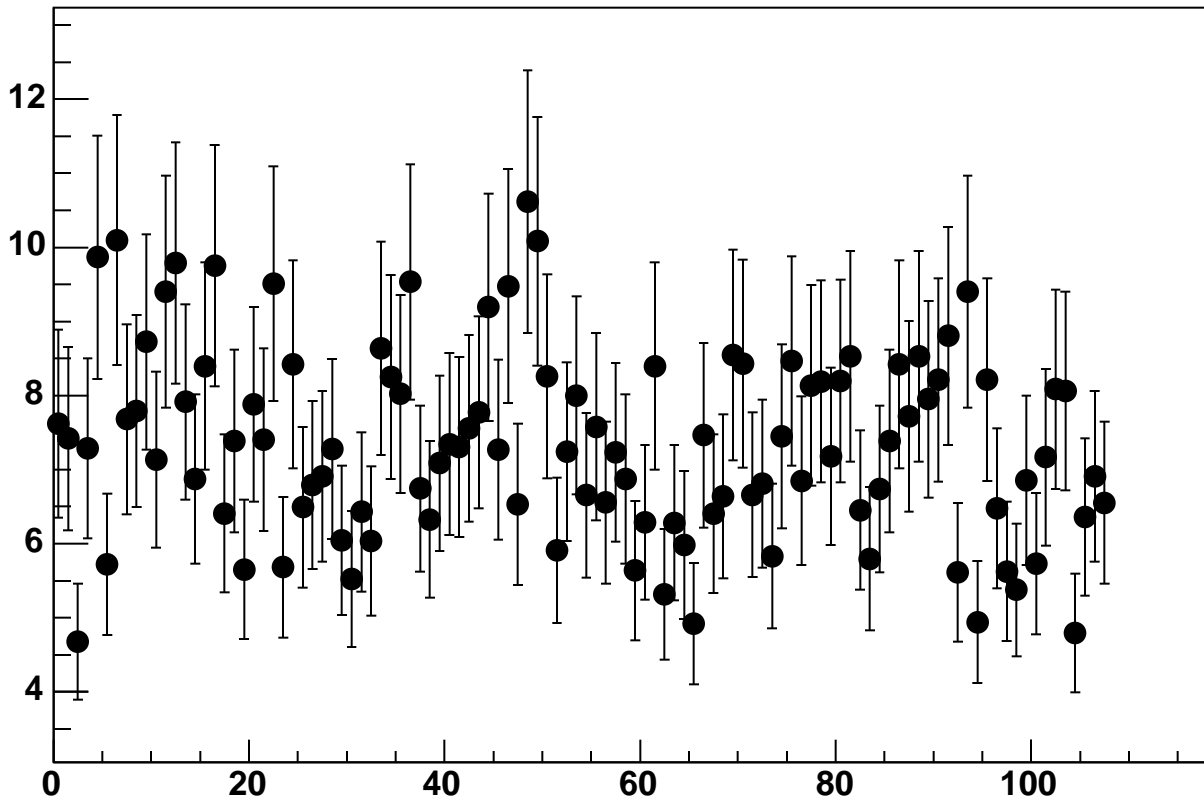
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



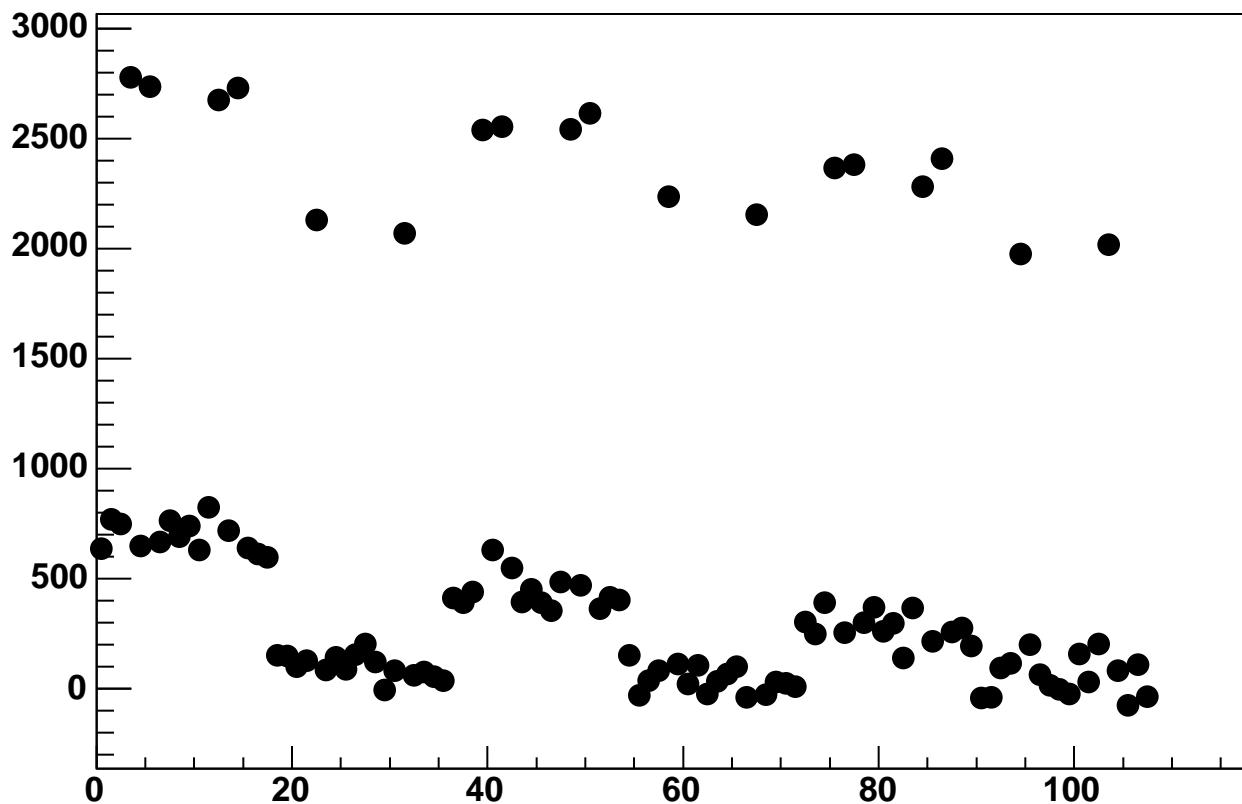
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



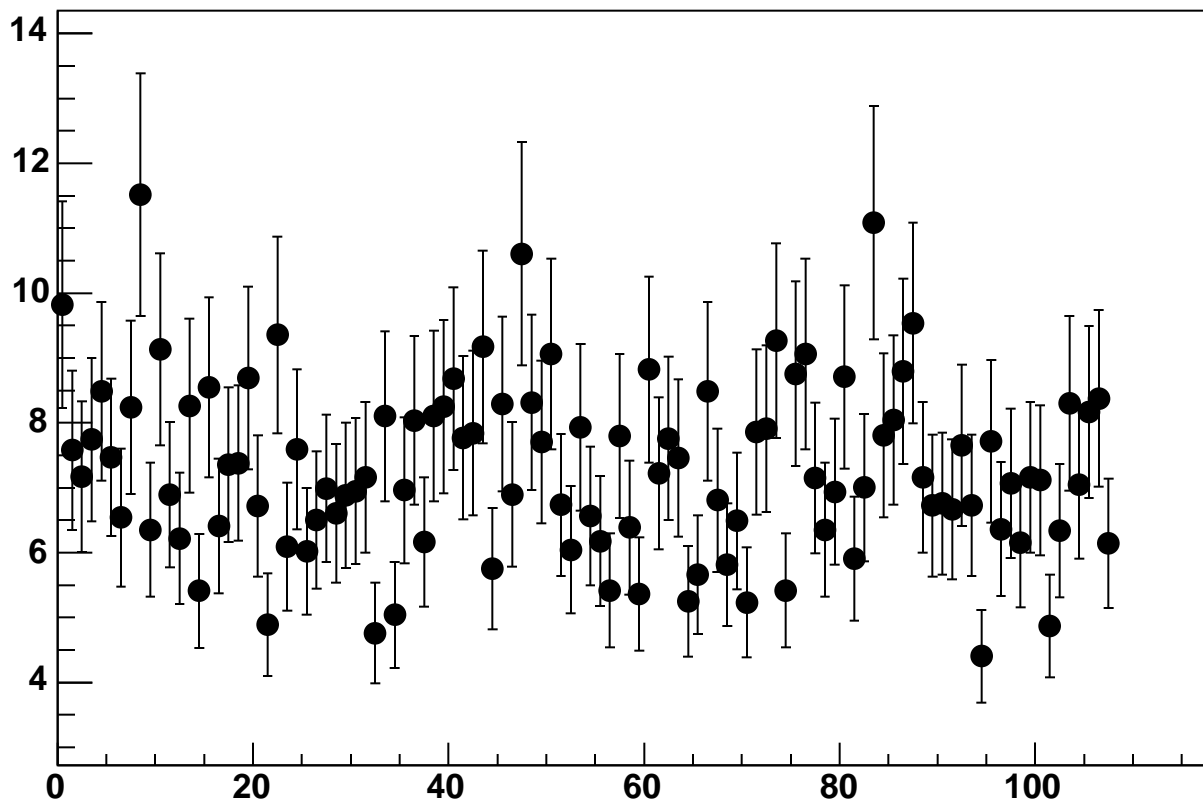
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



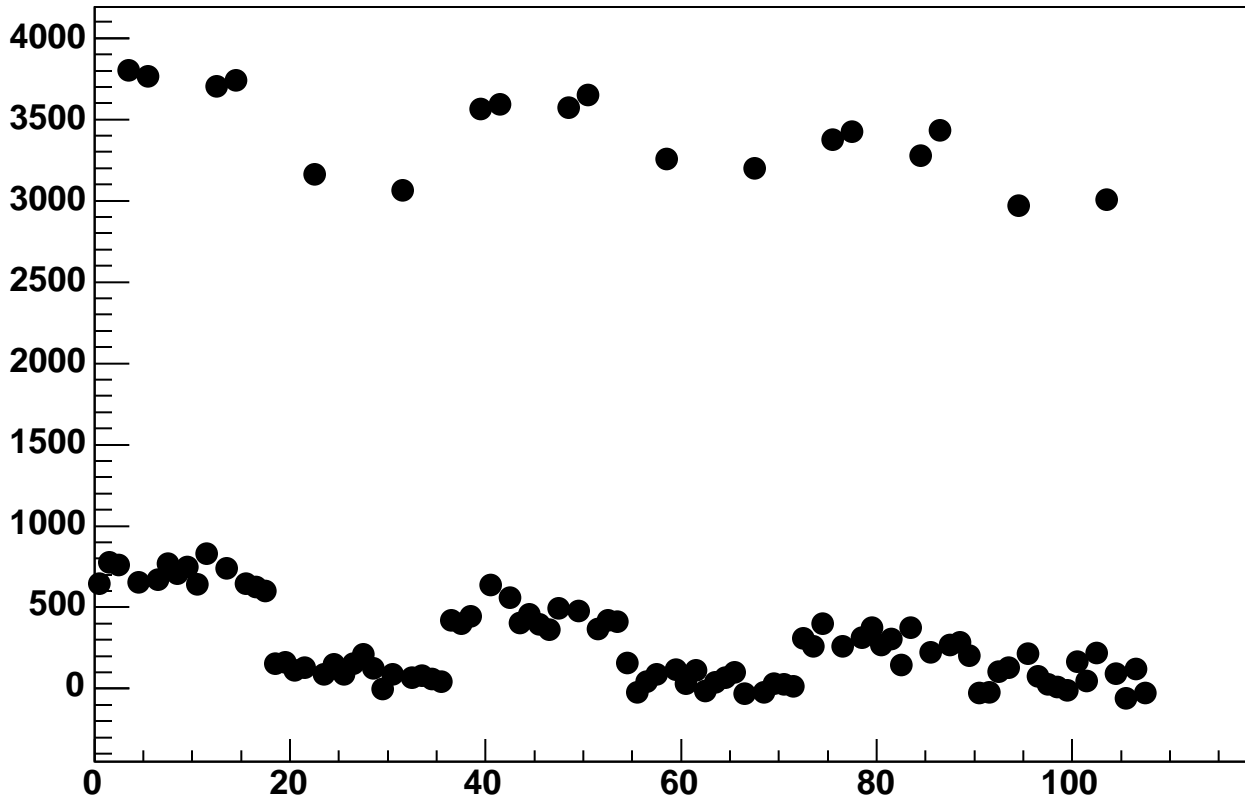
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



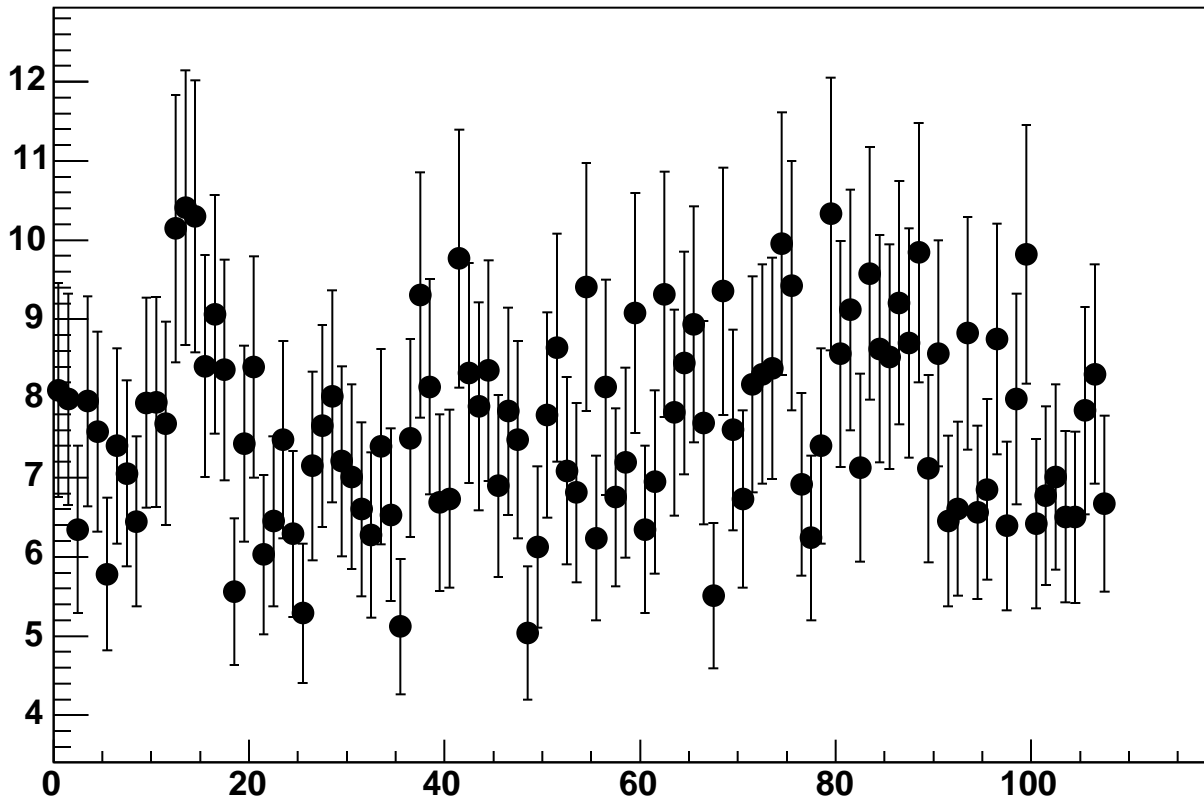
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



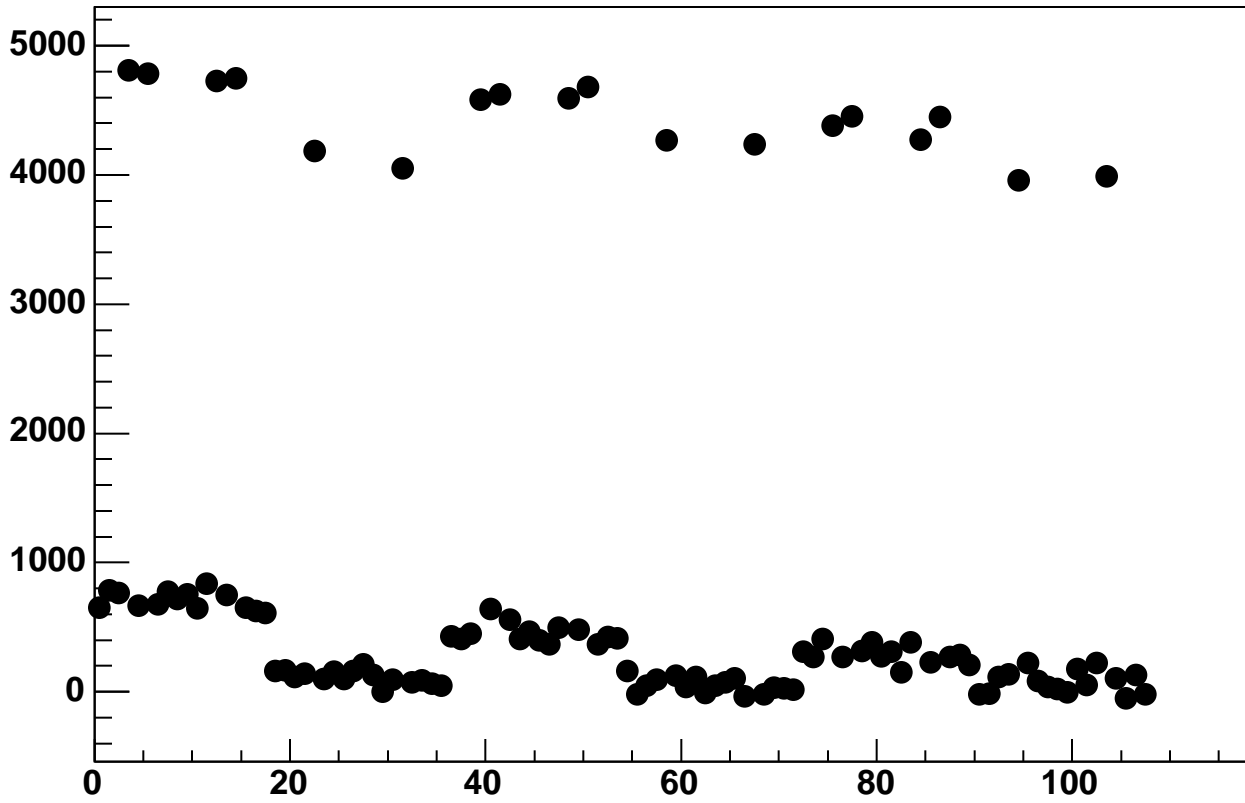
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



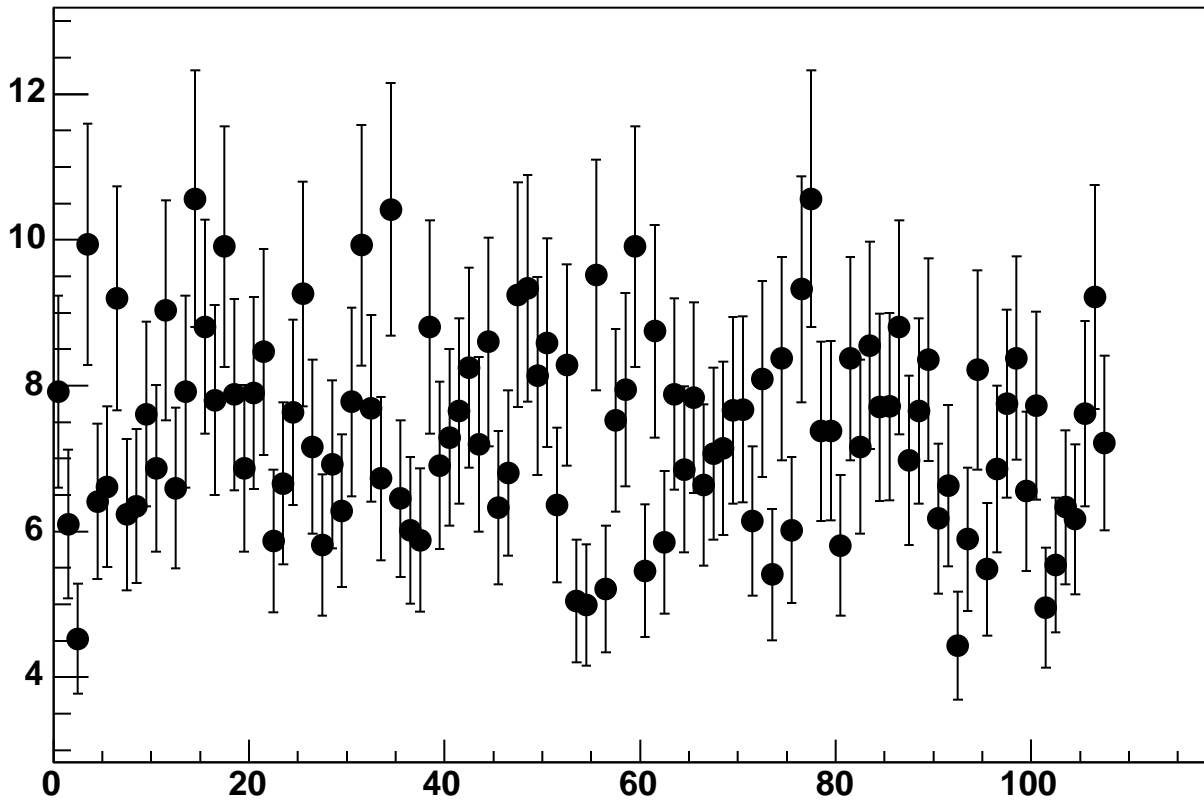
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



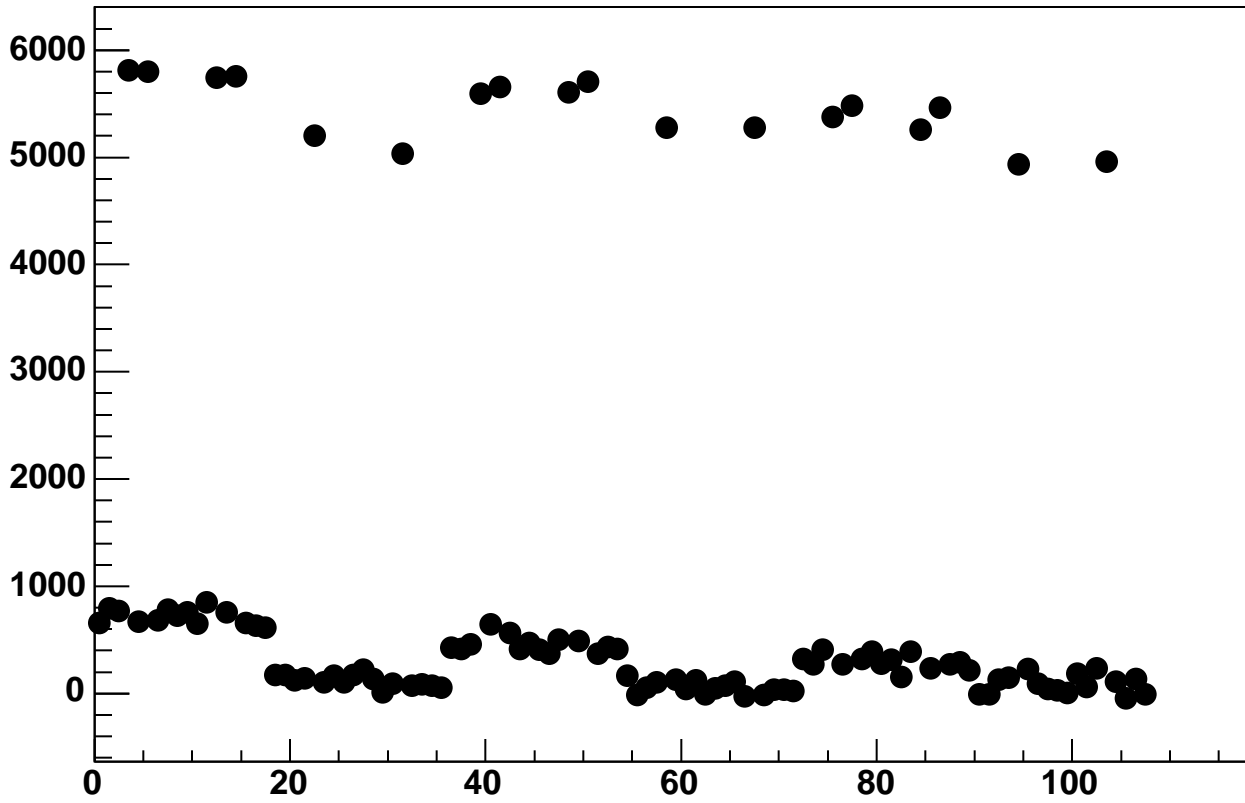
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



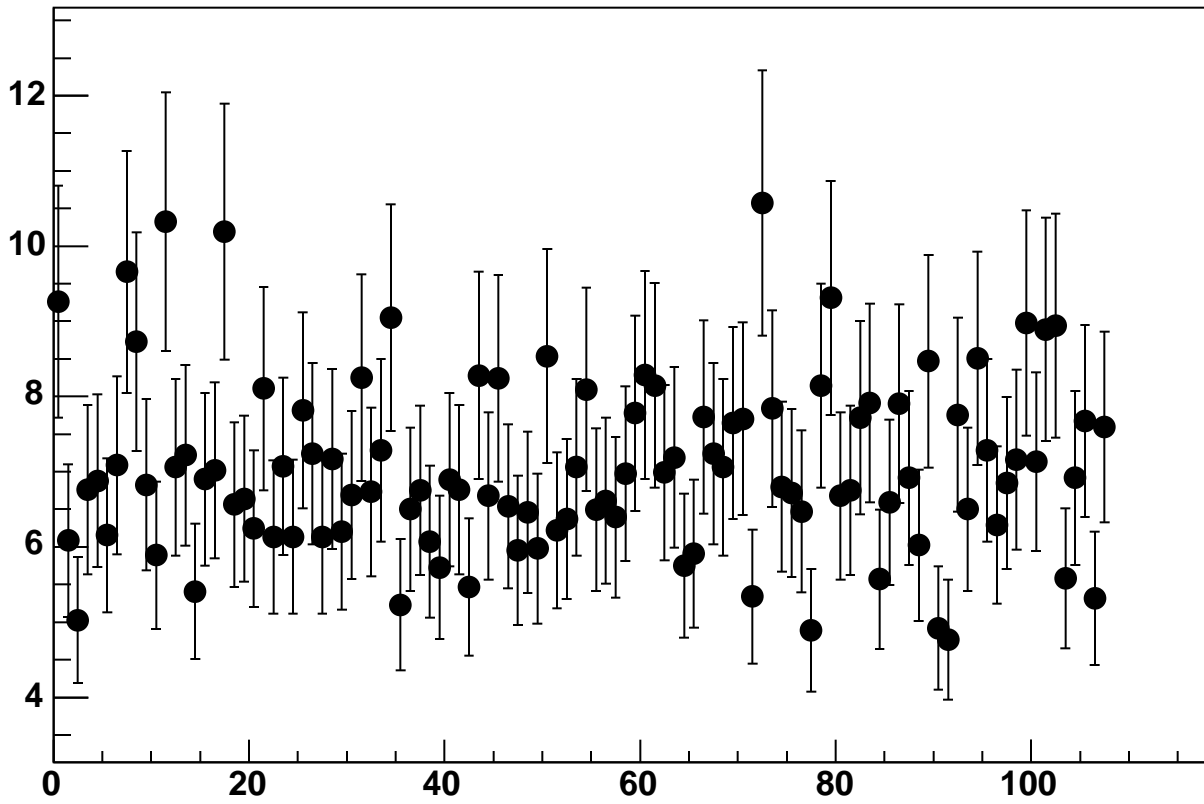
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



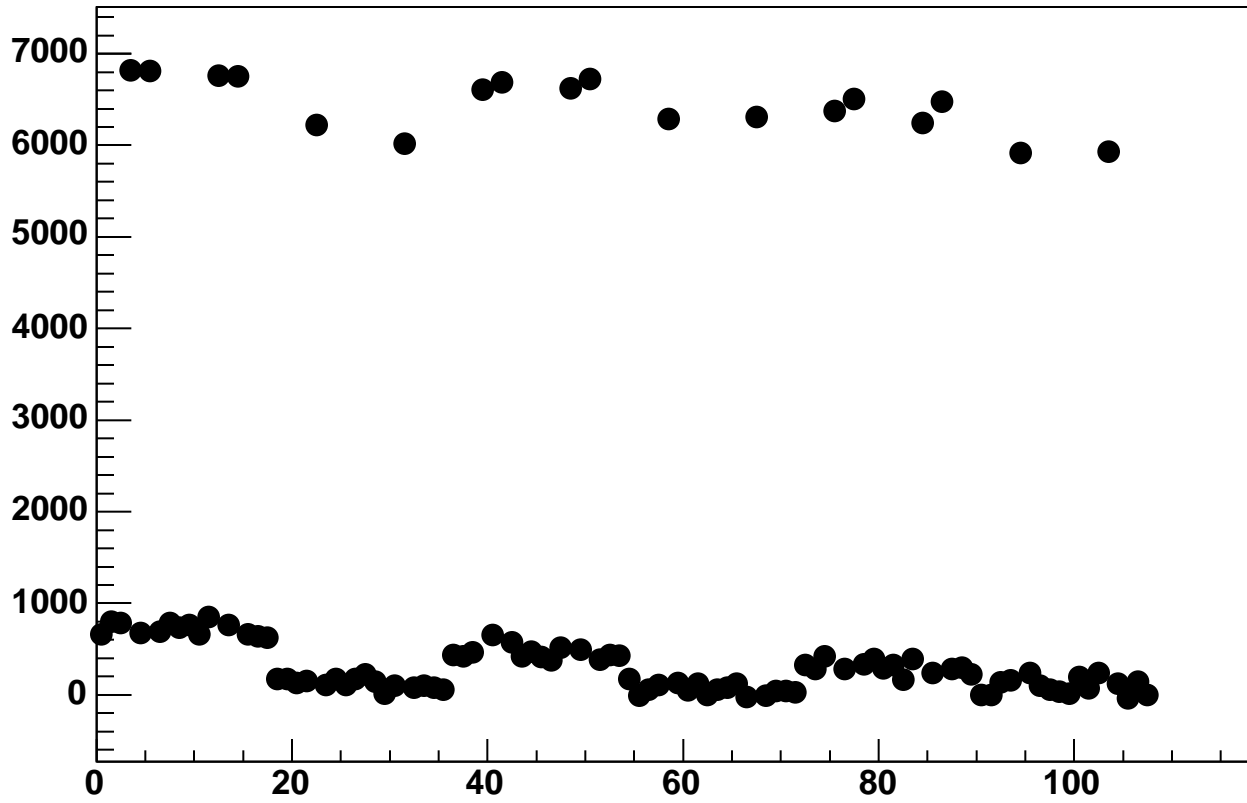
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



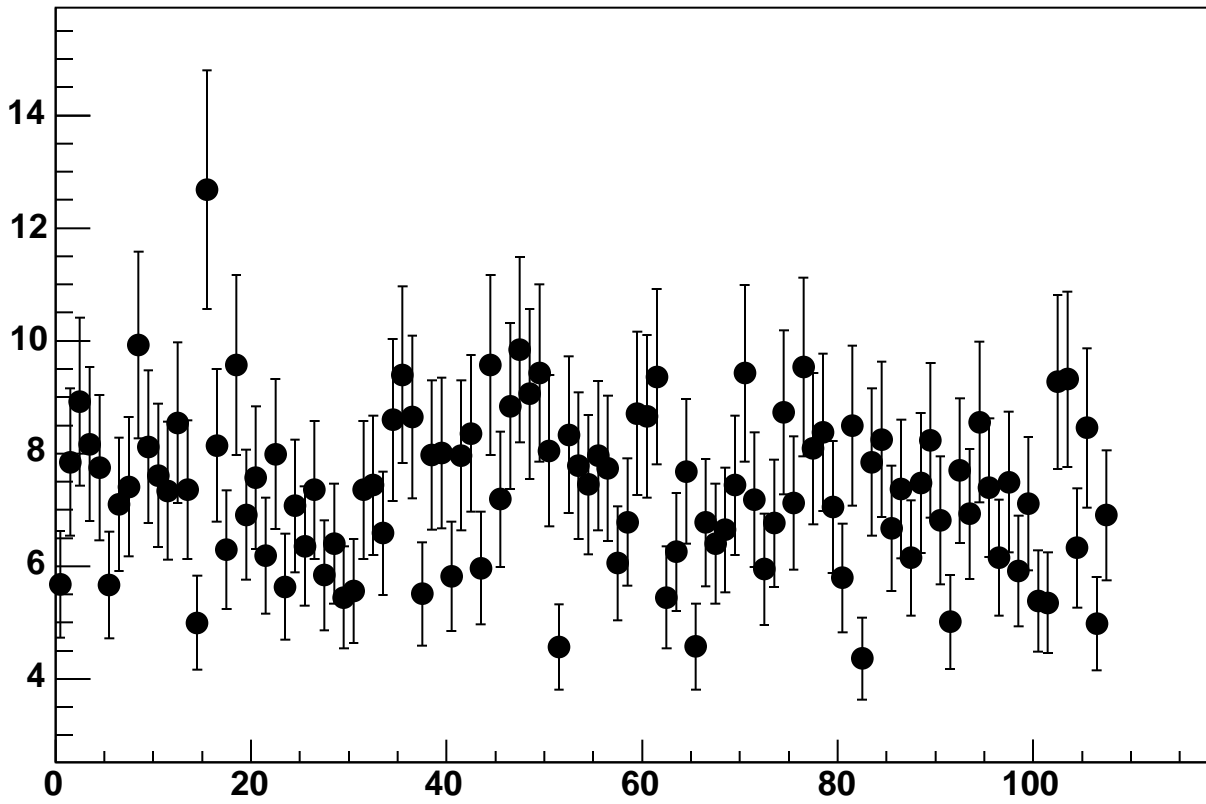
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



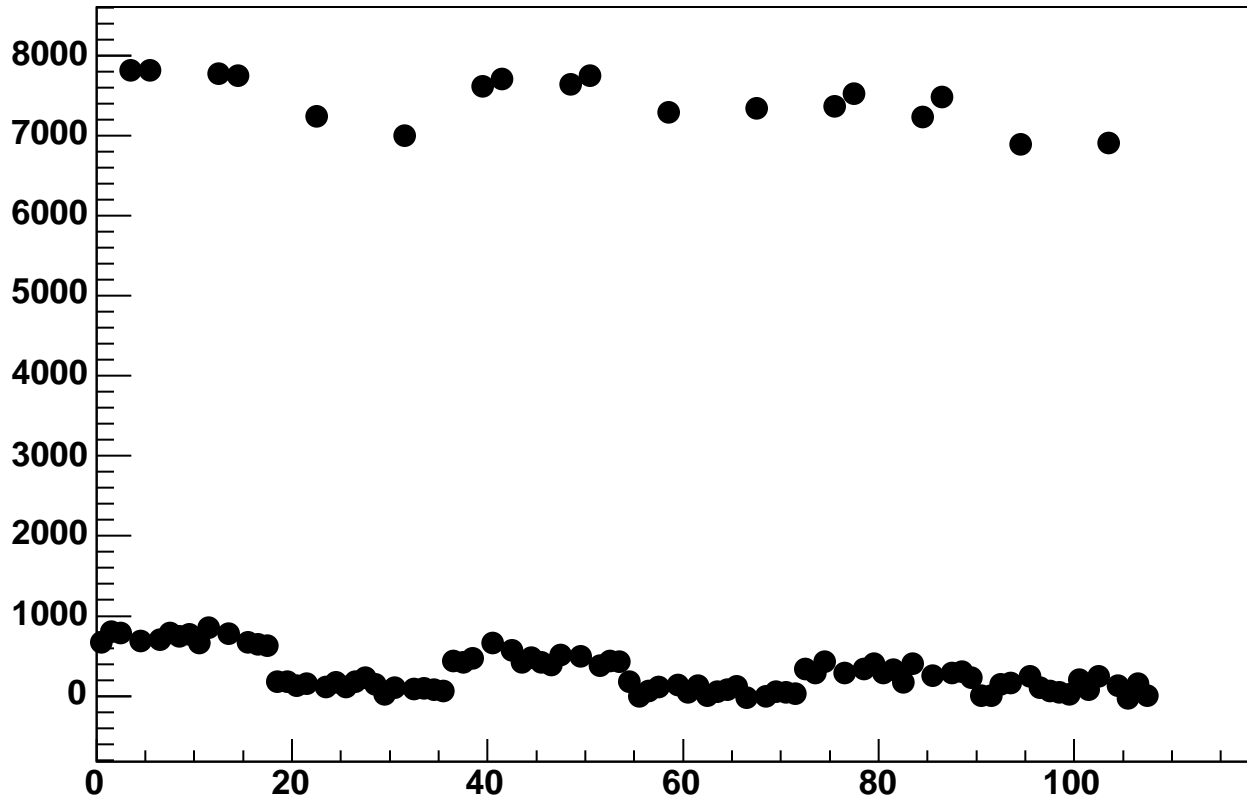
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



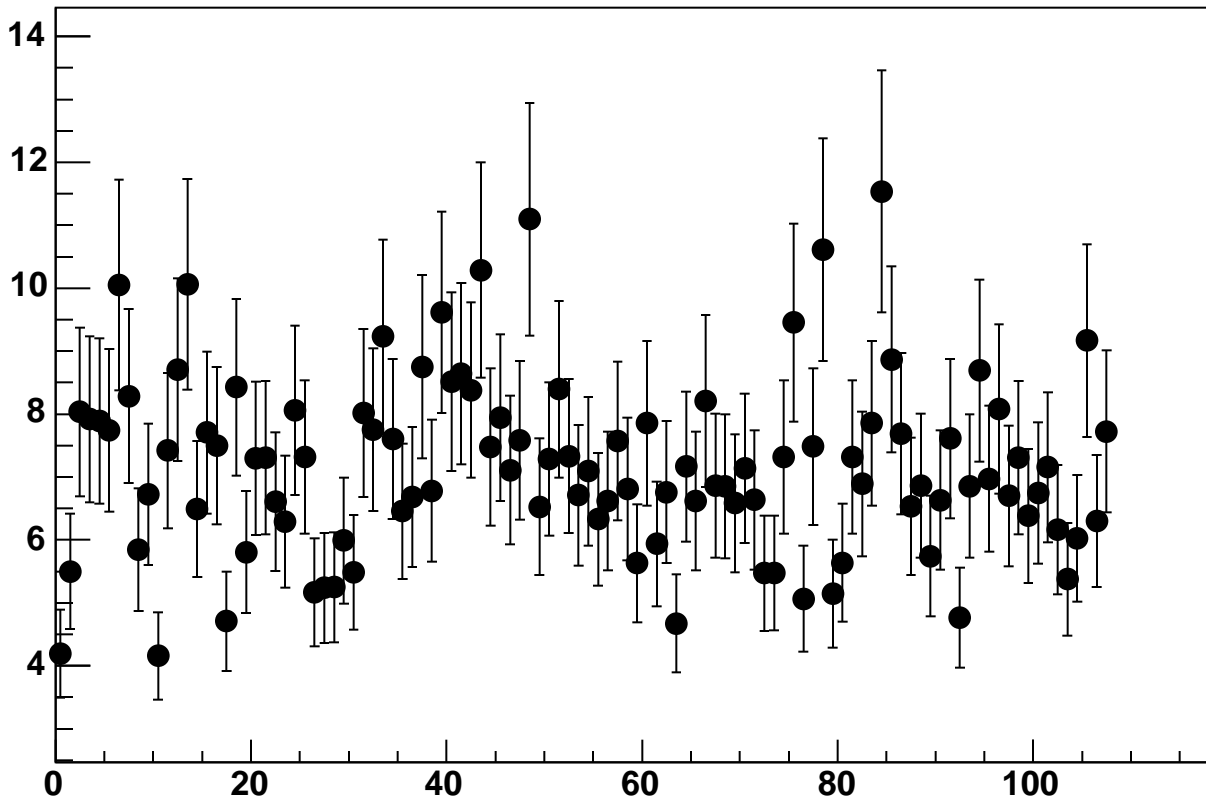
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



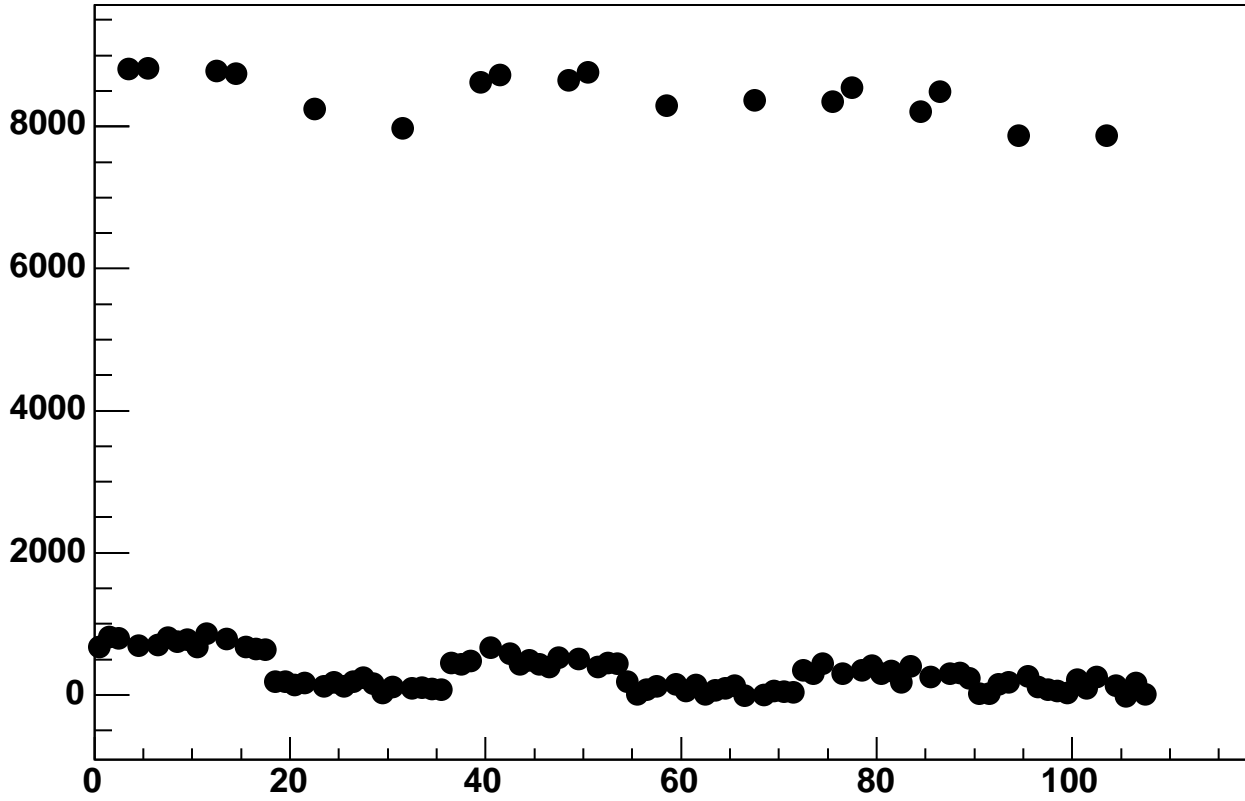
Enable 1, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



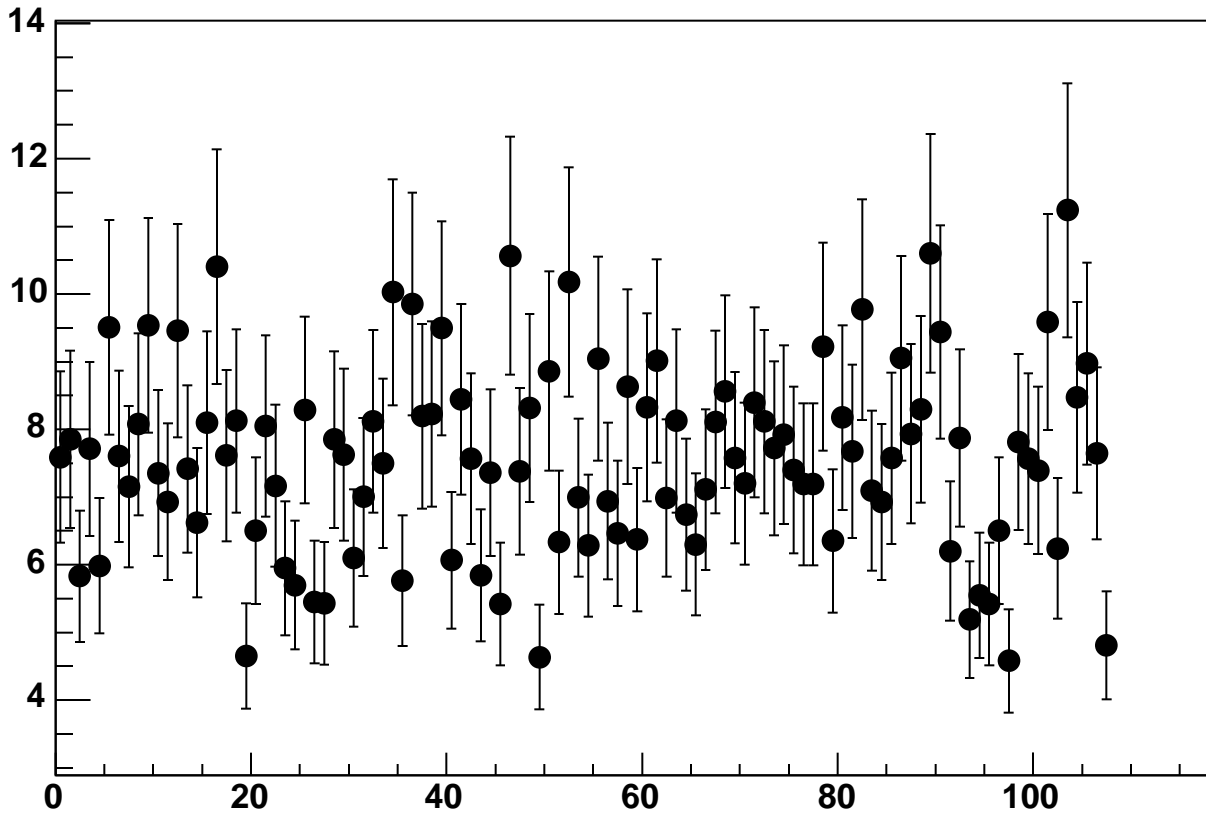
Enable 1, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



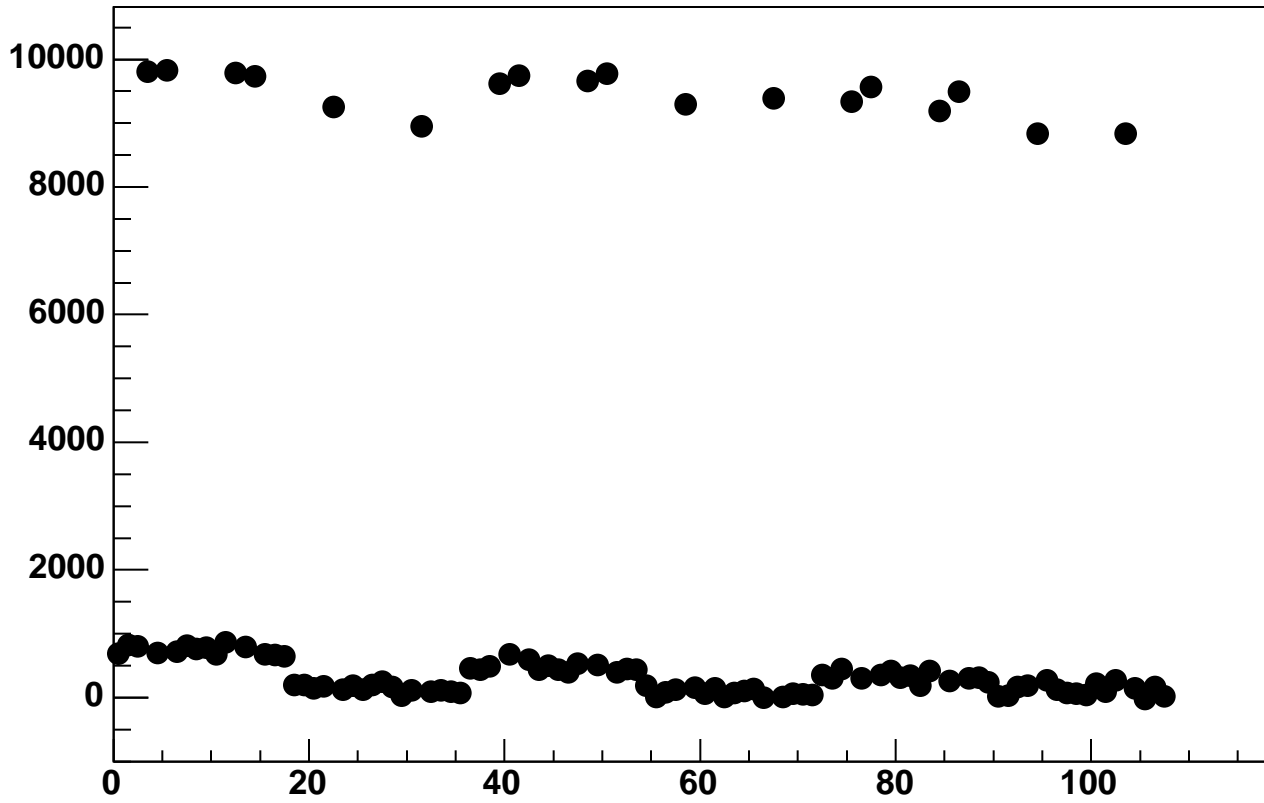
Enable 1, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



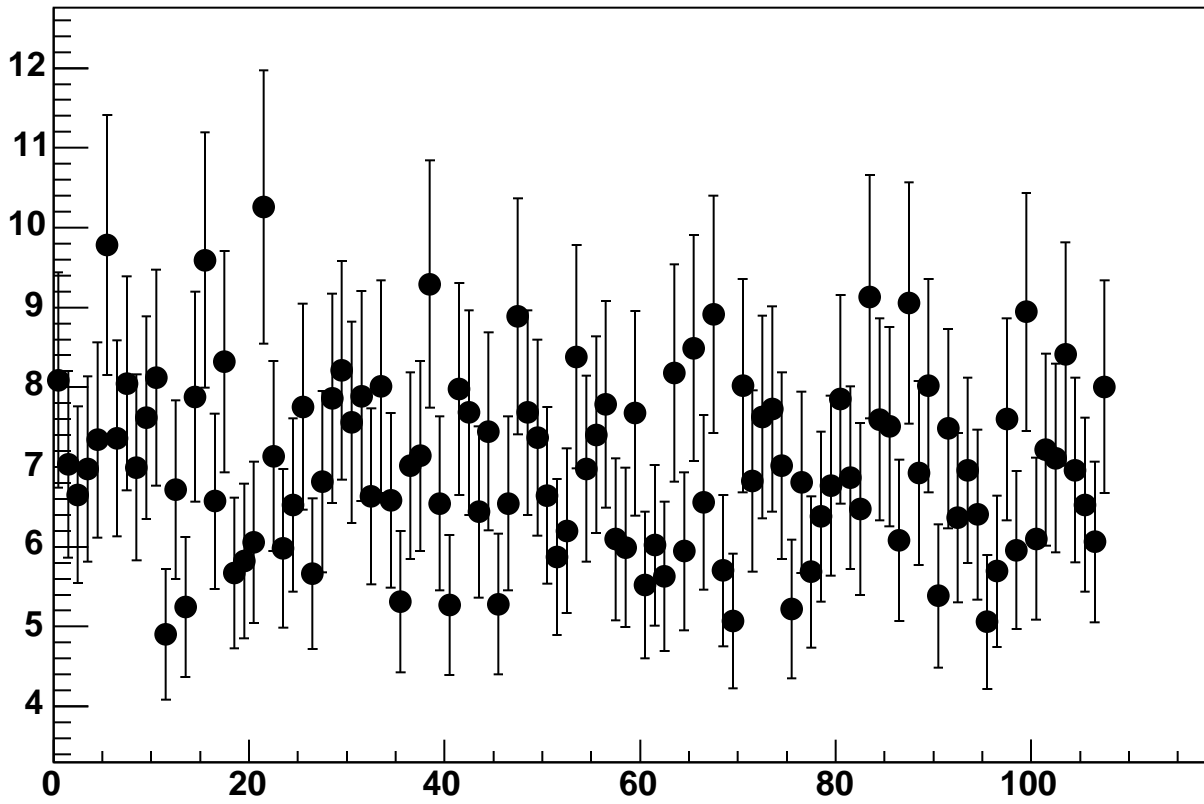
Enable 1, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



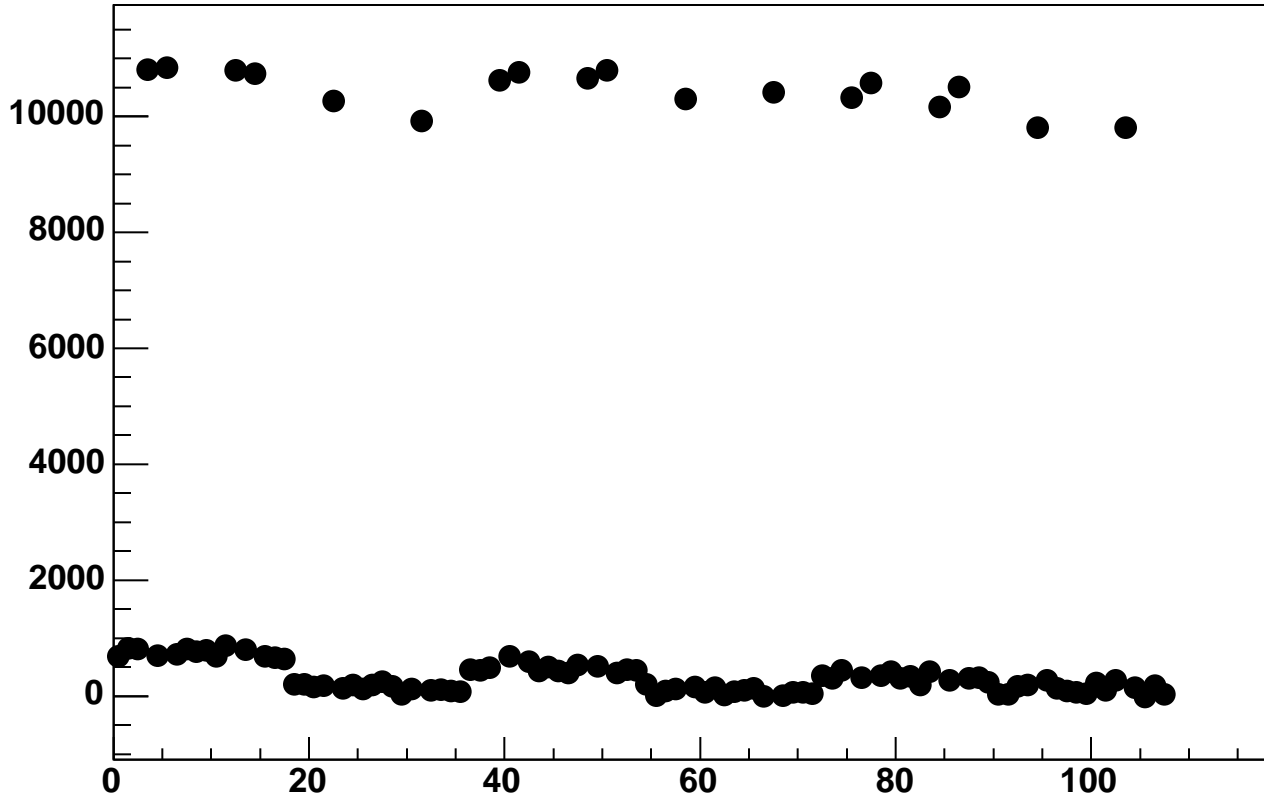
Enable 1, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



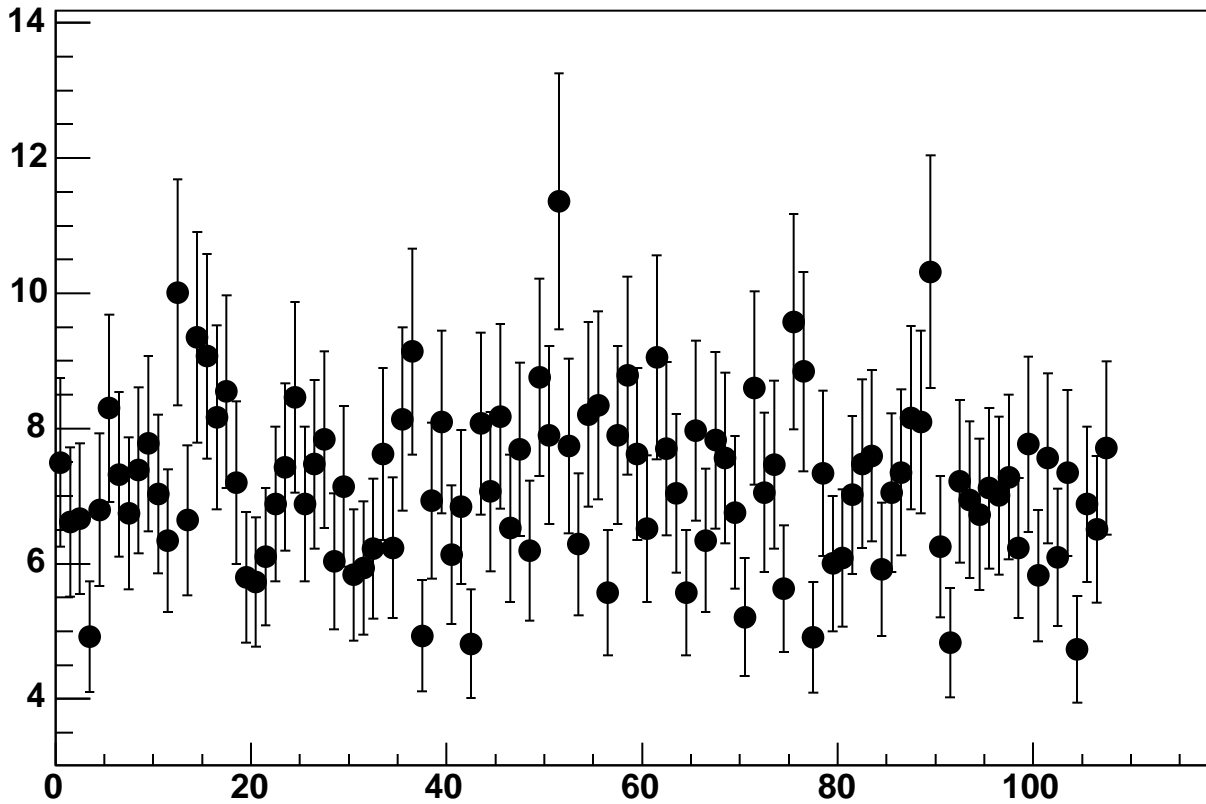
Enable 1, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



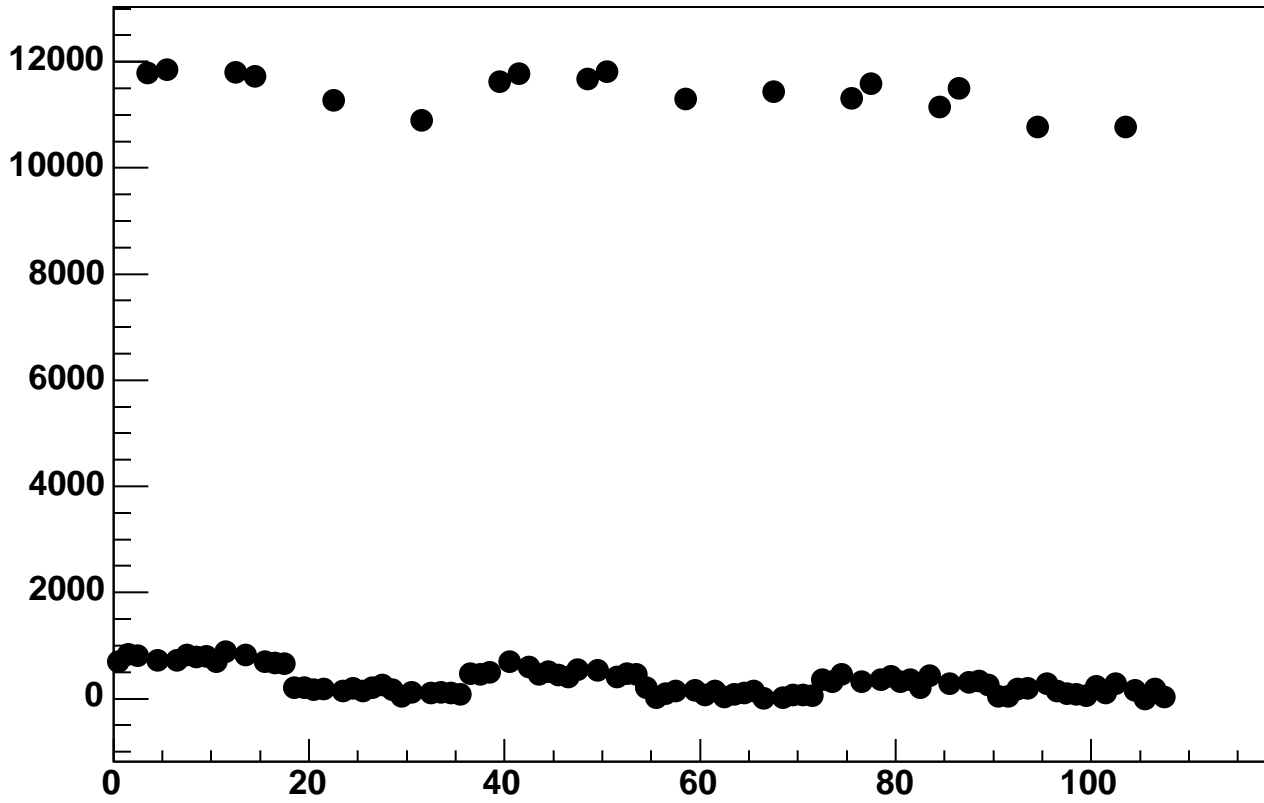
Enable 1, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



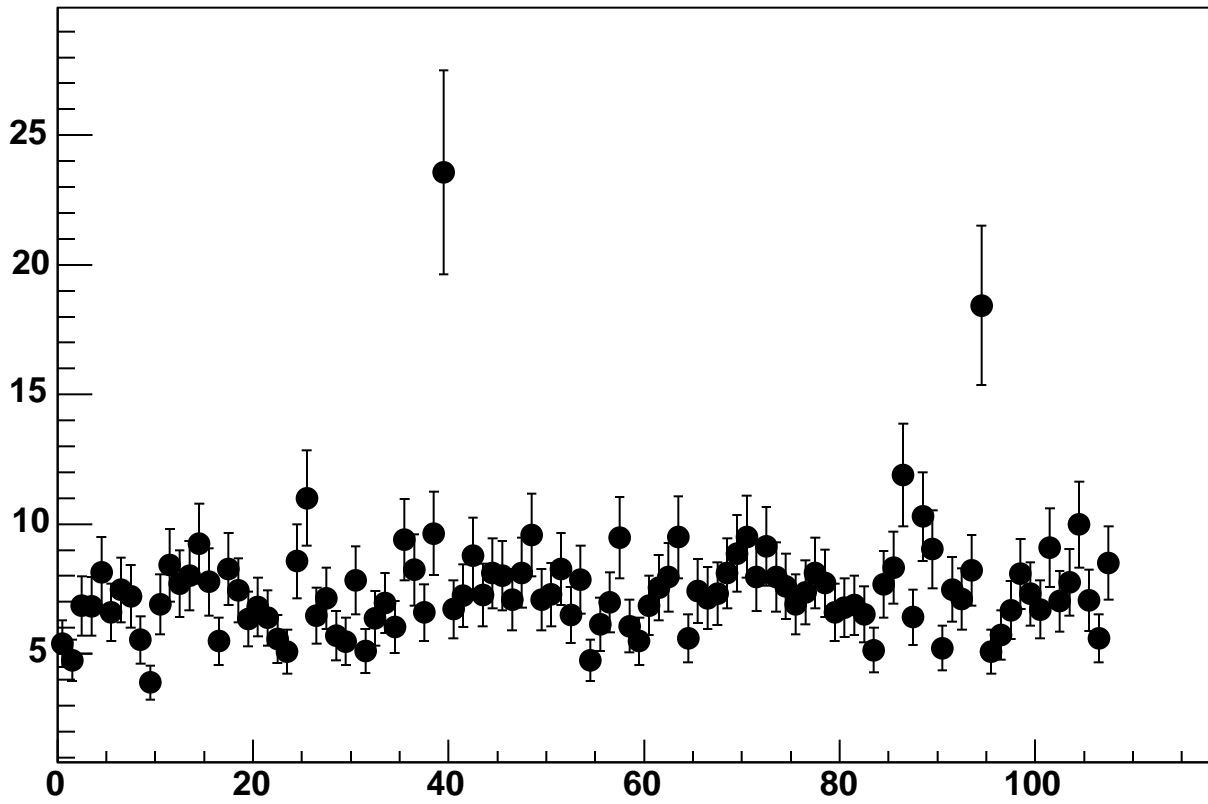
Enable 1, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



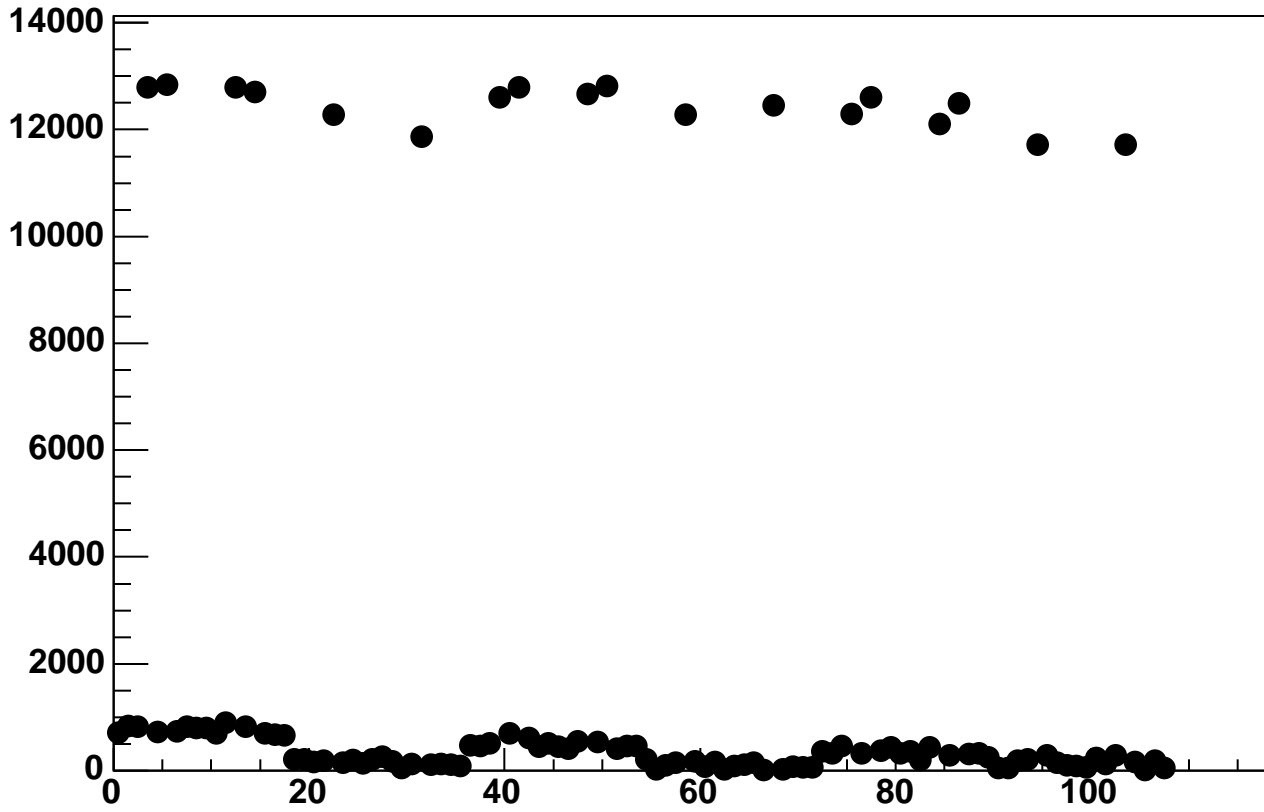
Enable 1, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



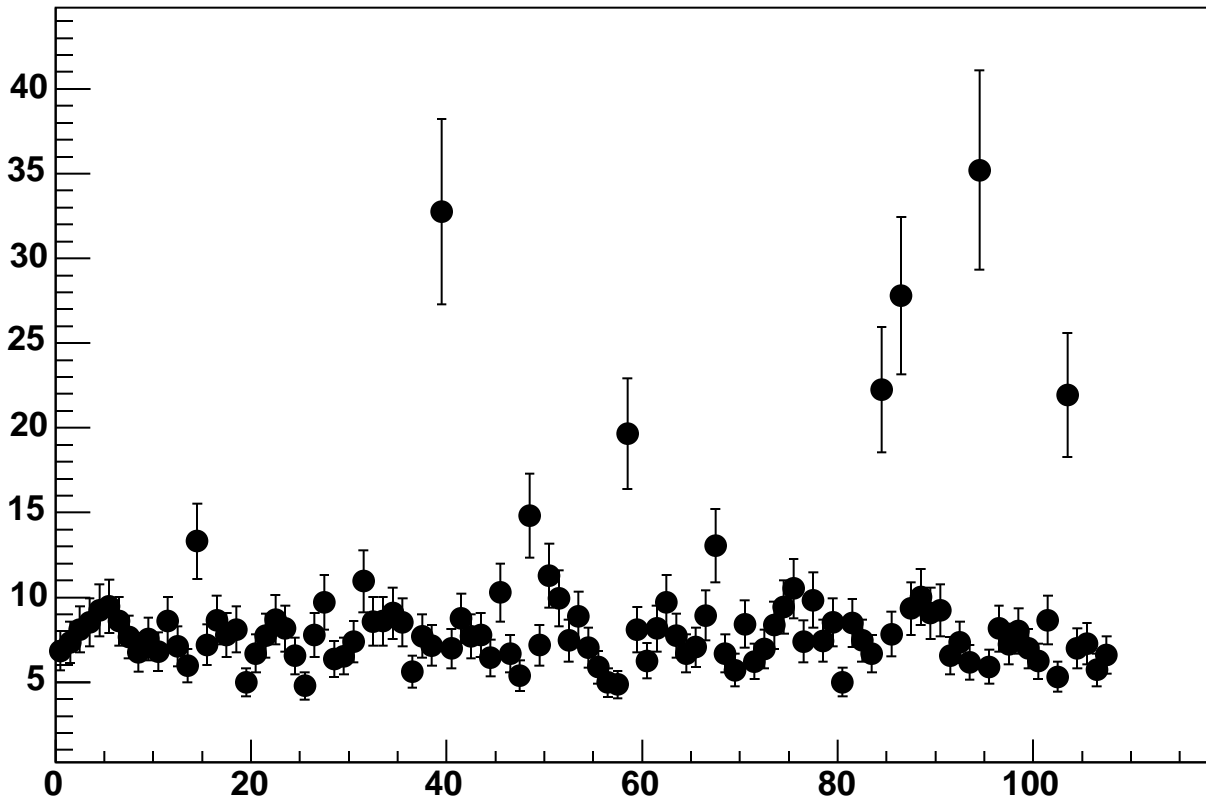
Enable 1, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



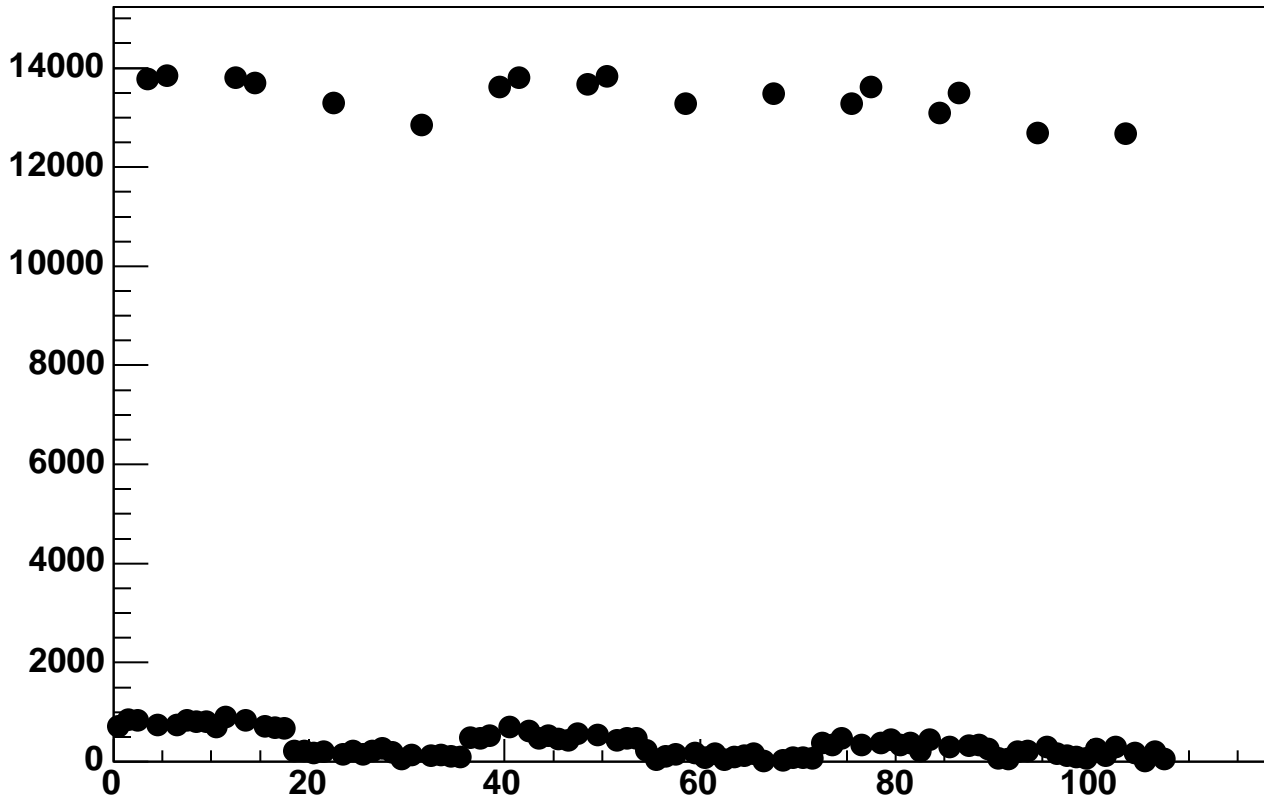
Enable 1, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



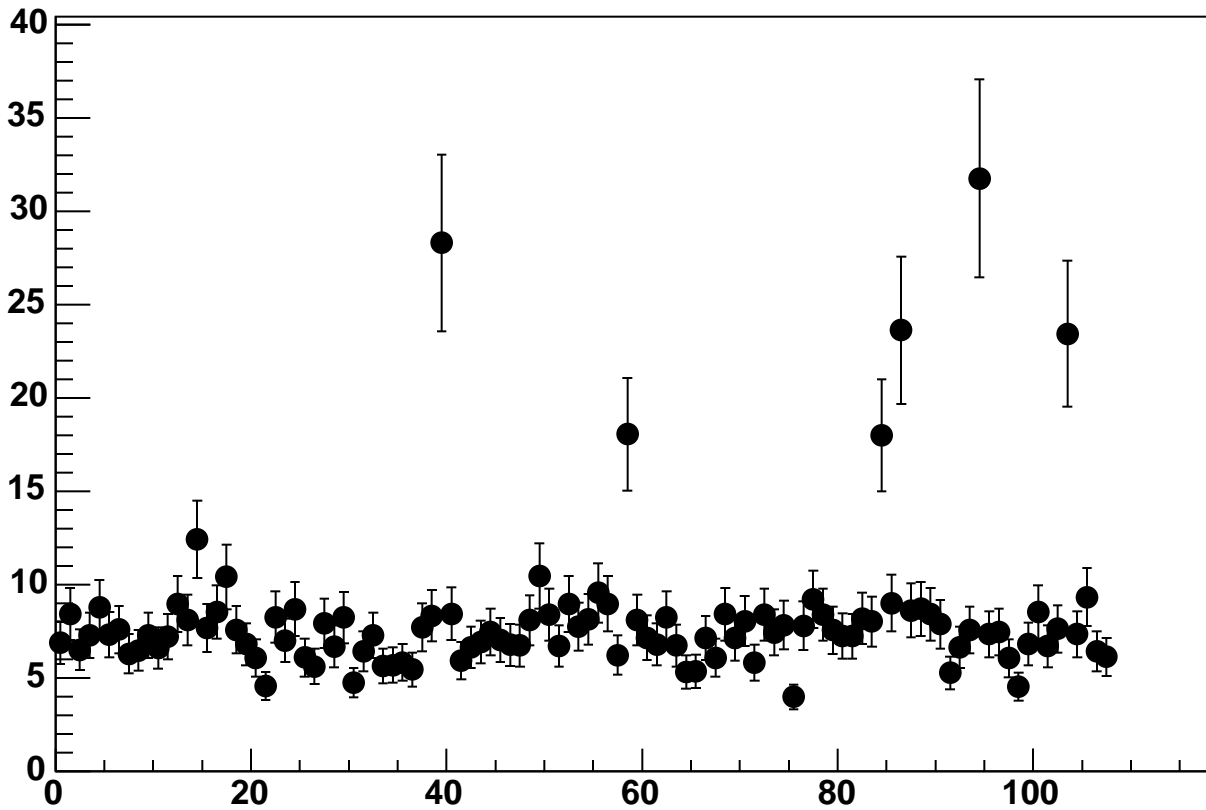
Enable 1, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



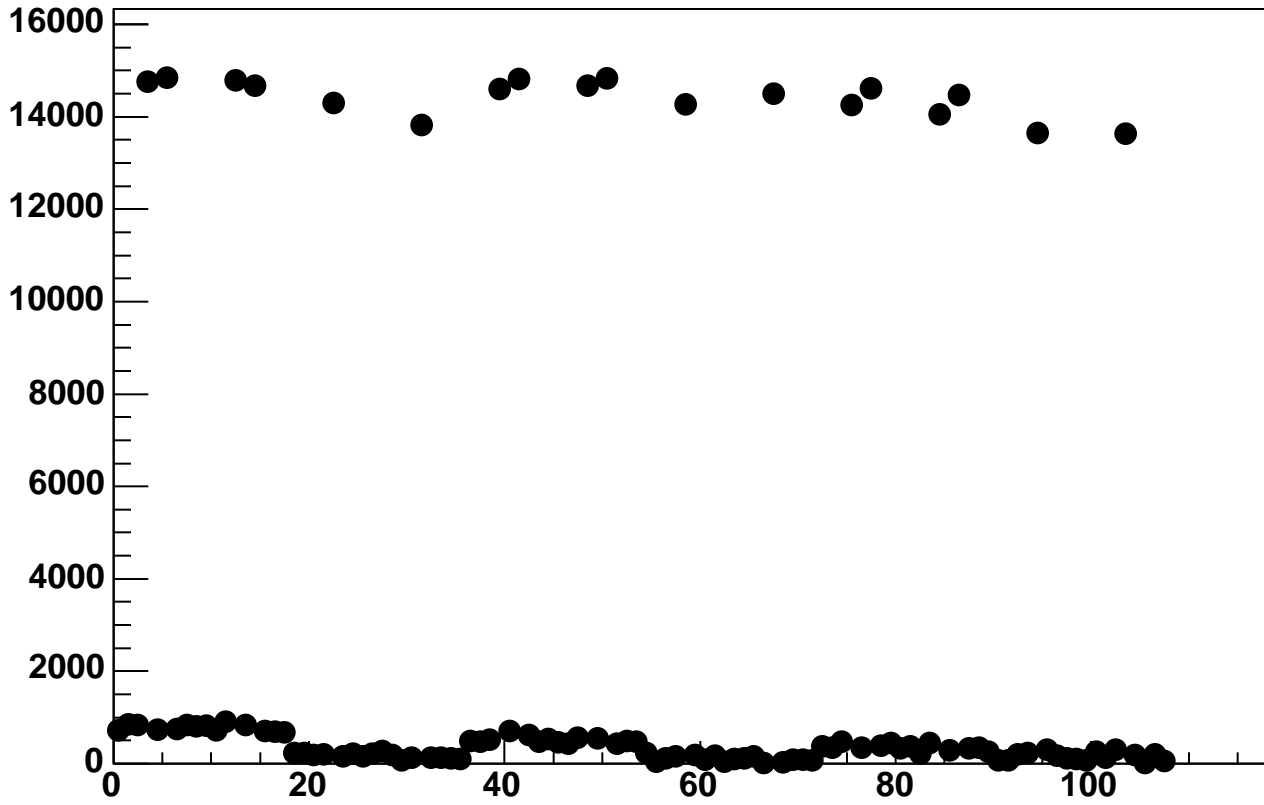
Enable 1, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



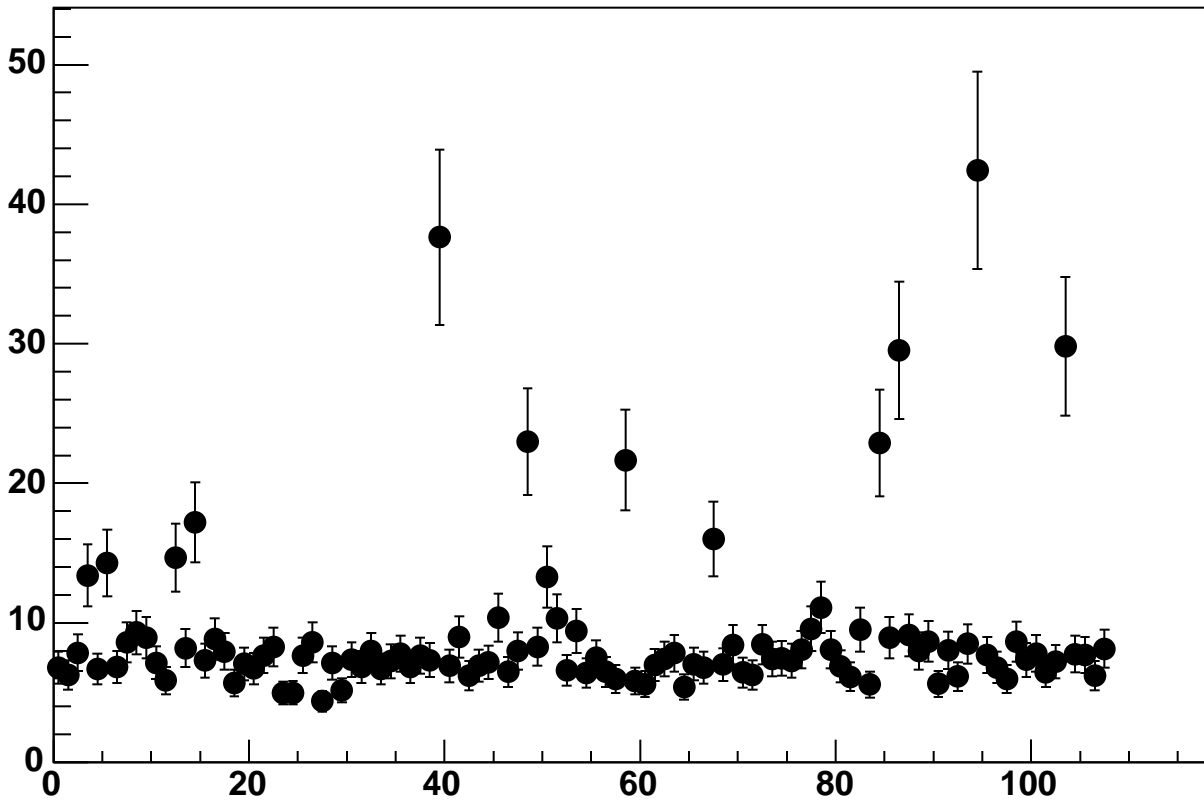
Enable 1, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



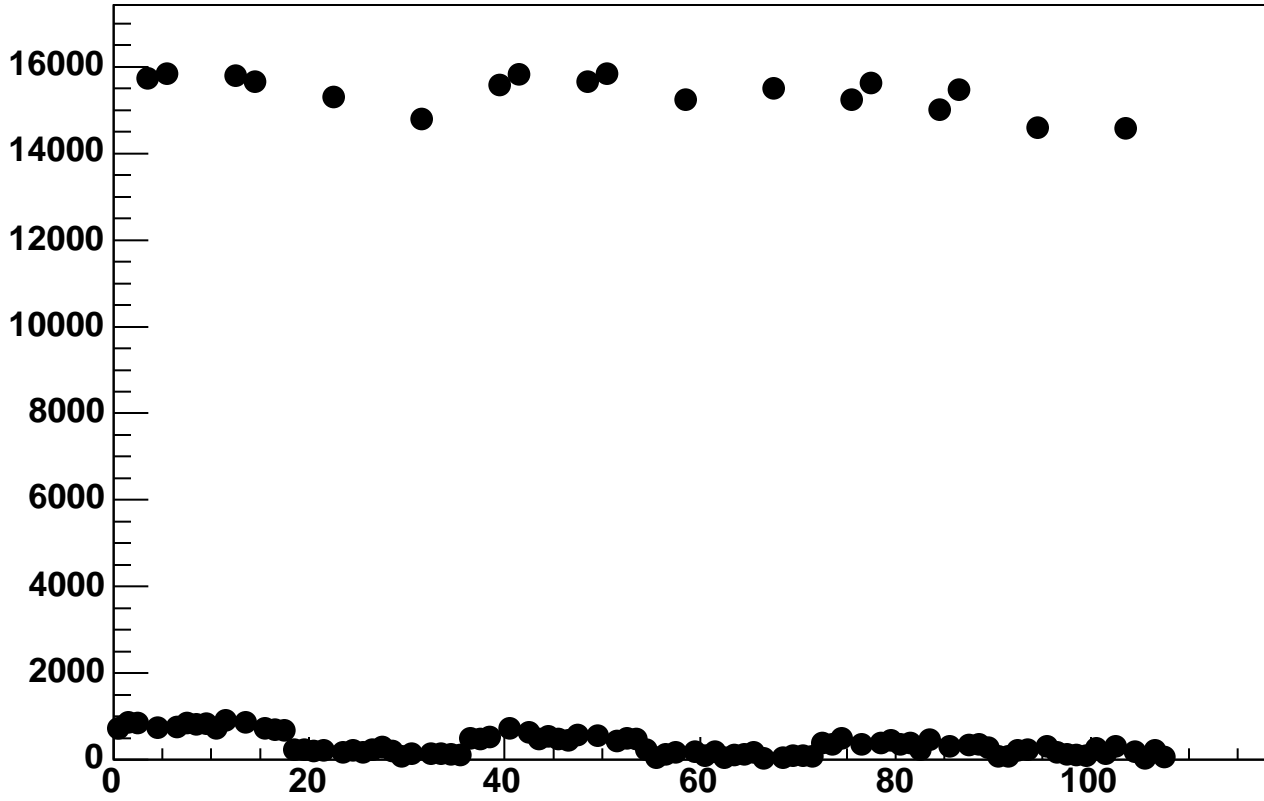
Enable 1, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



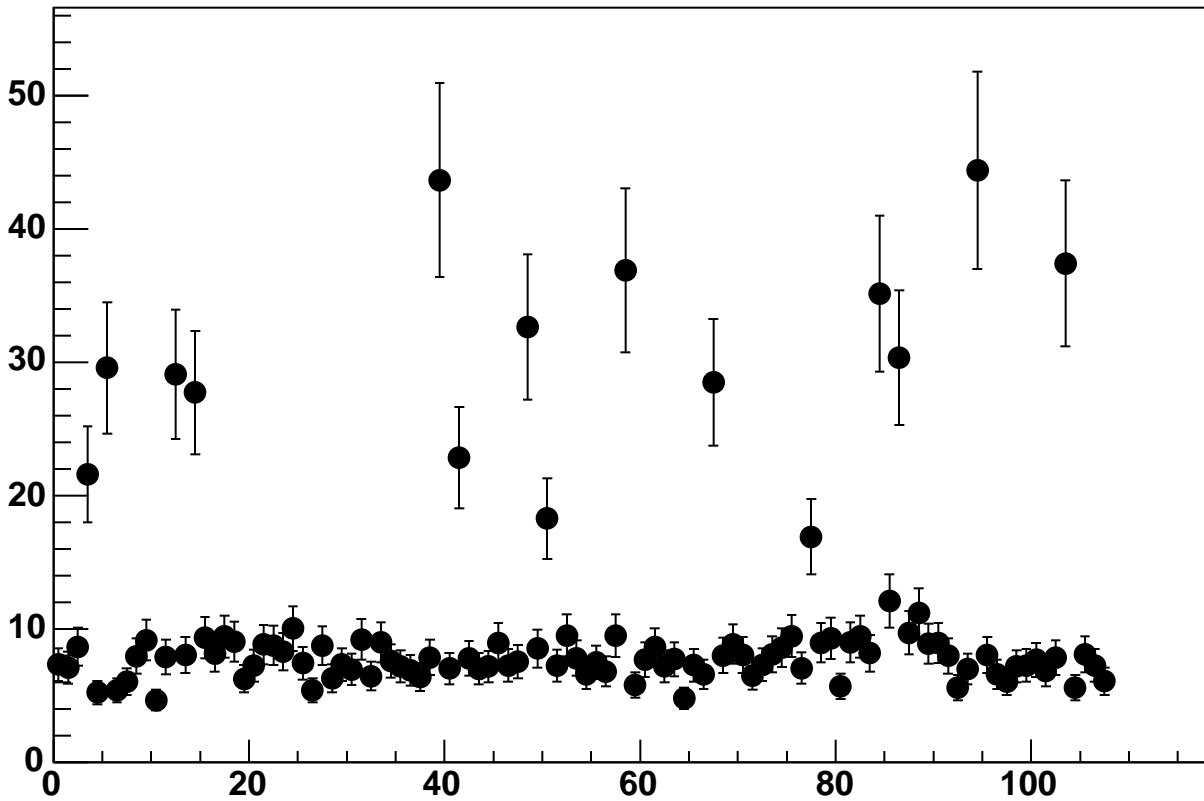
Enable 1, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



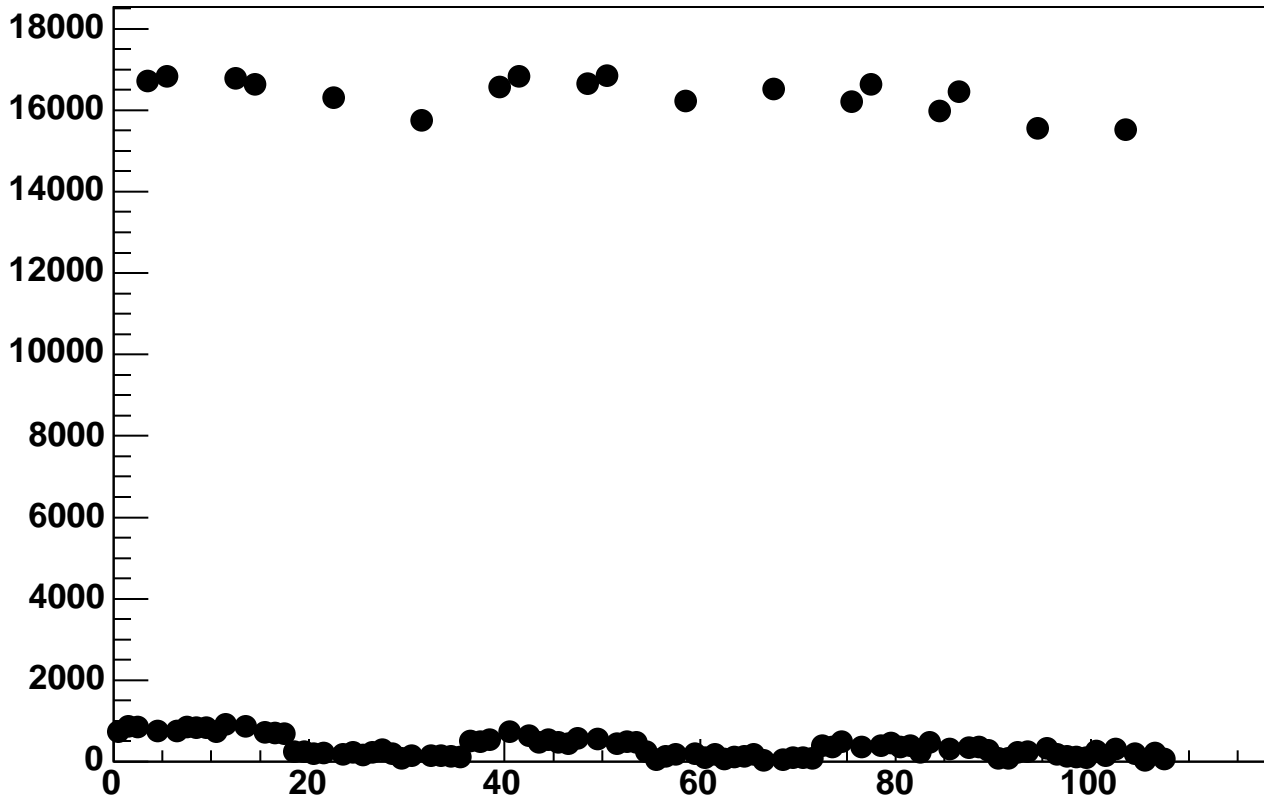
Enable 1, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



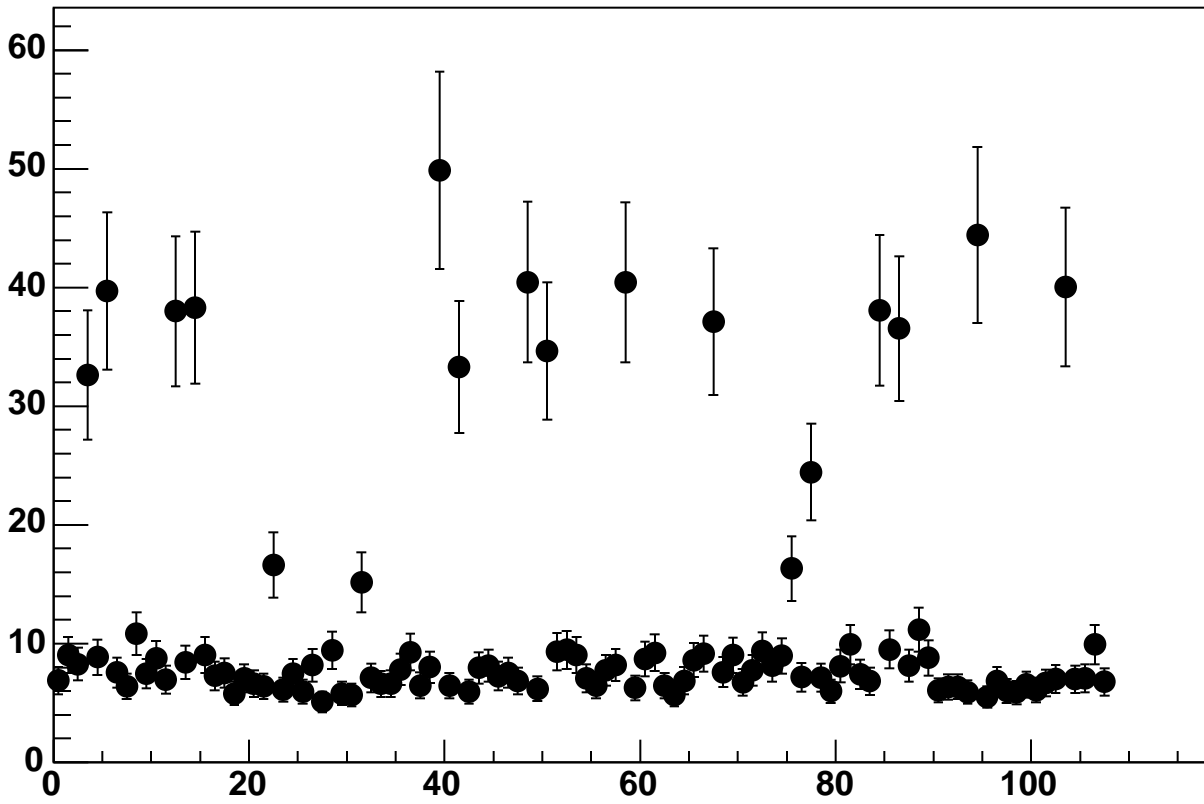
Enable 1, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



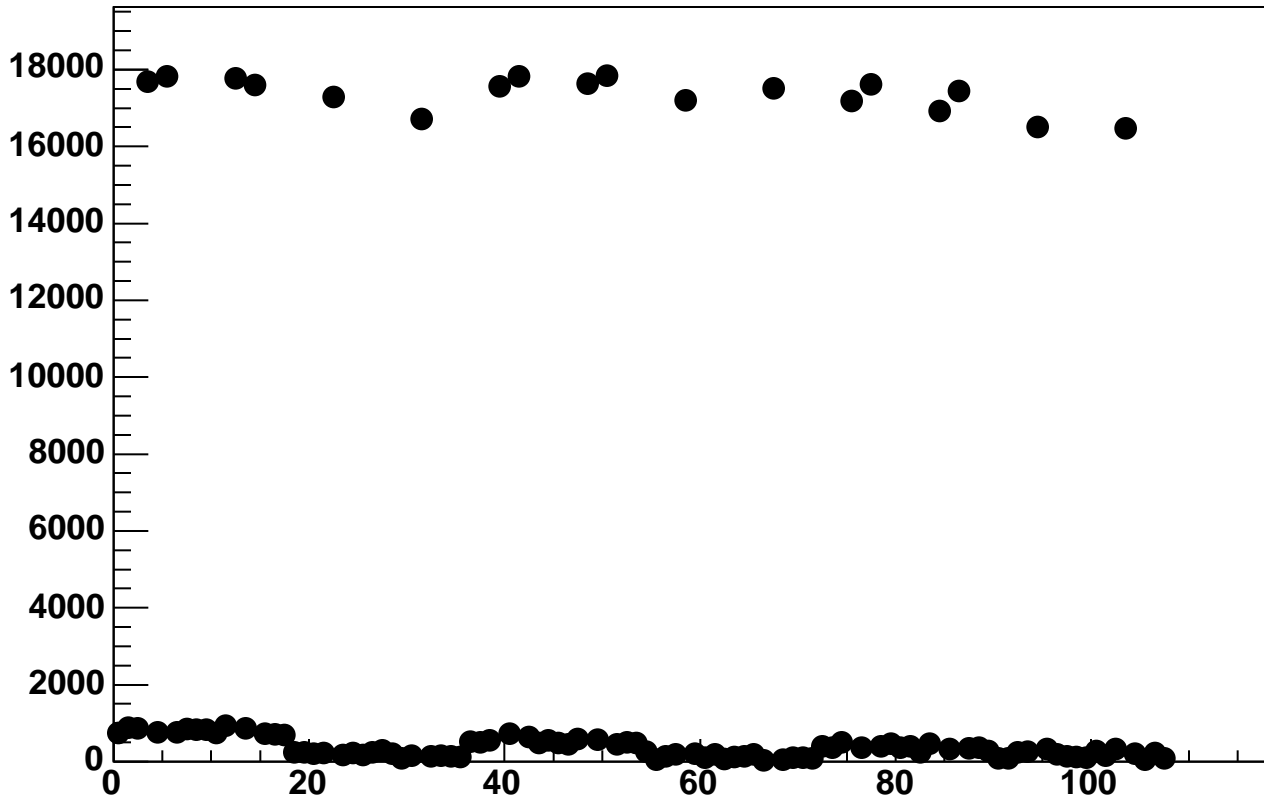
Enable 1, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



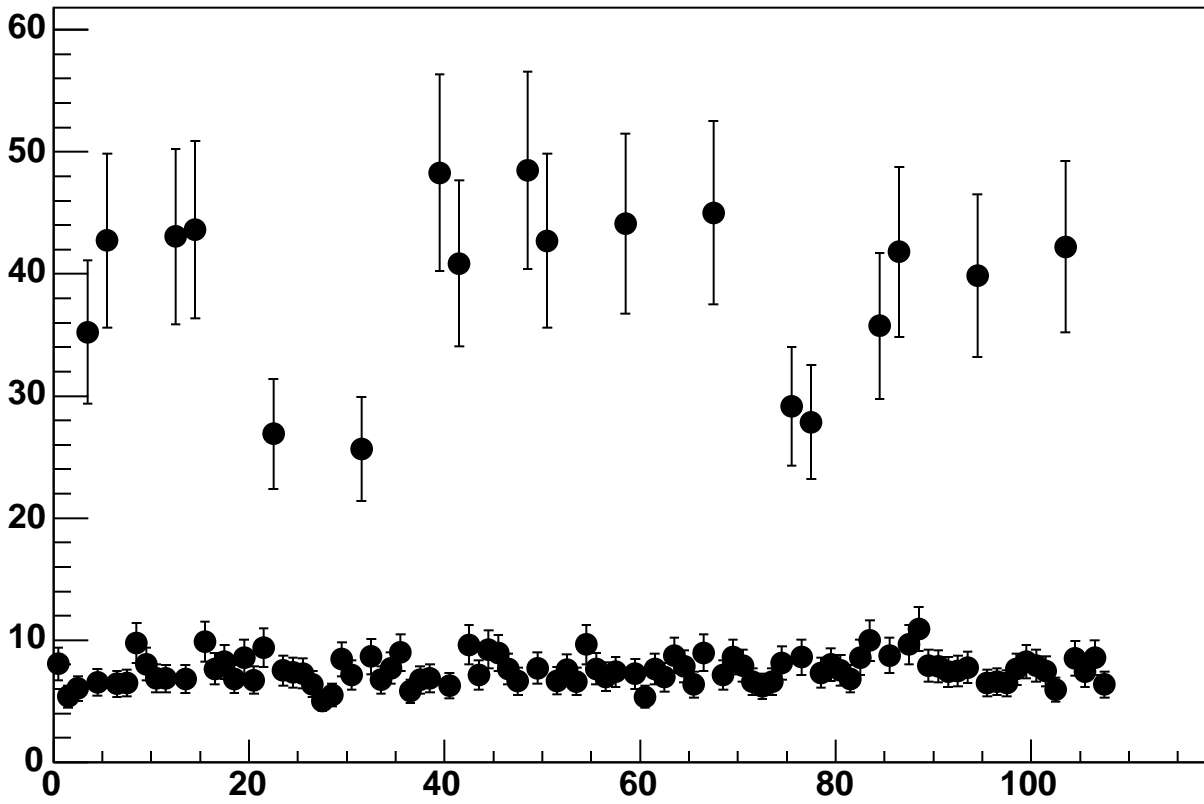
Enable 1, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



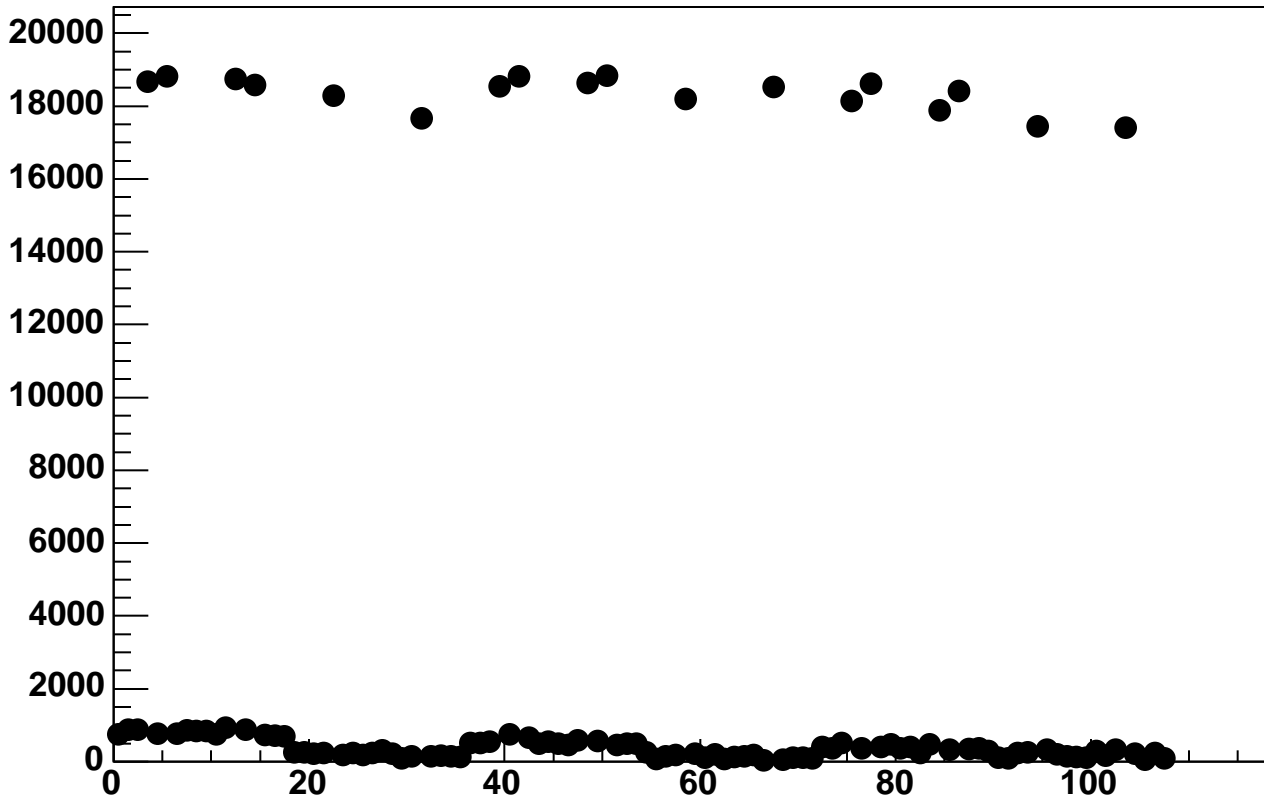
Enable 1, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



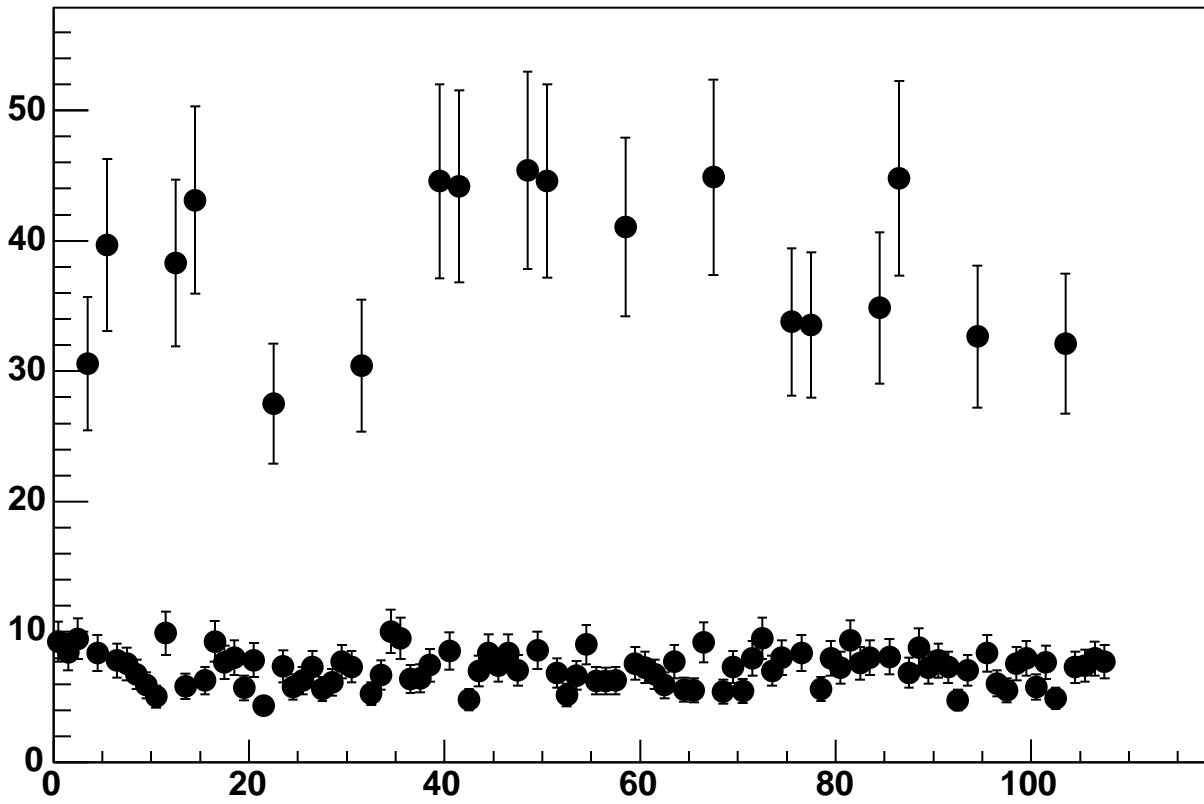
Enable 1, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



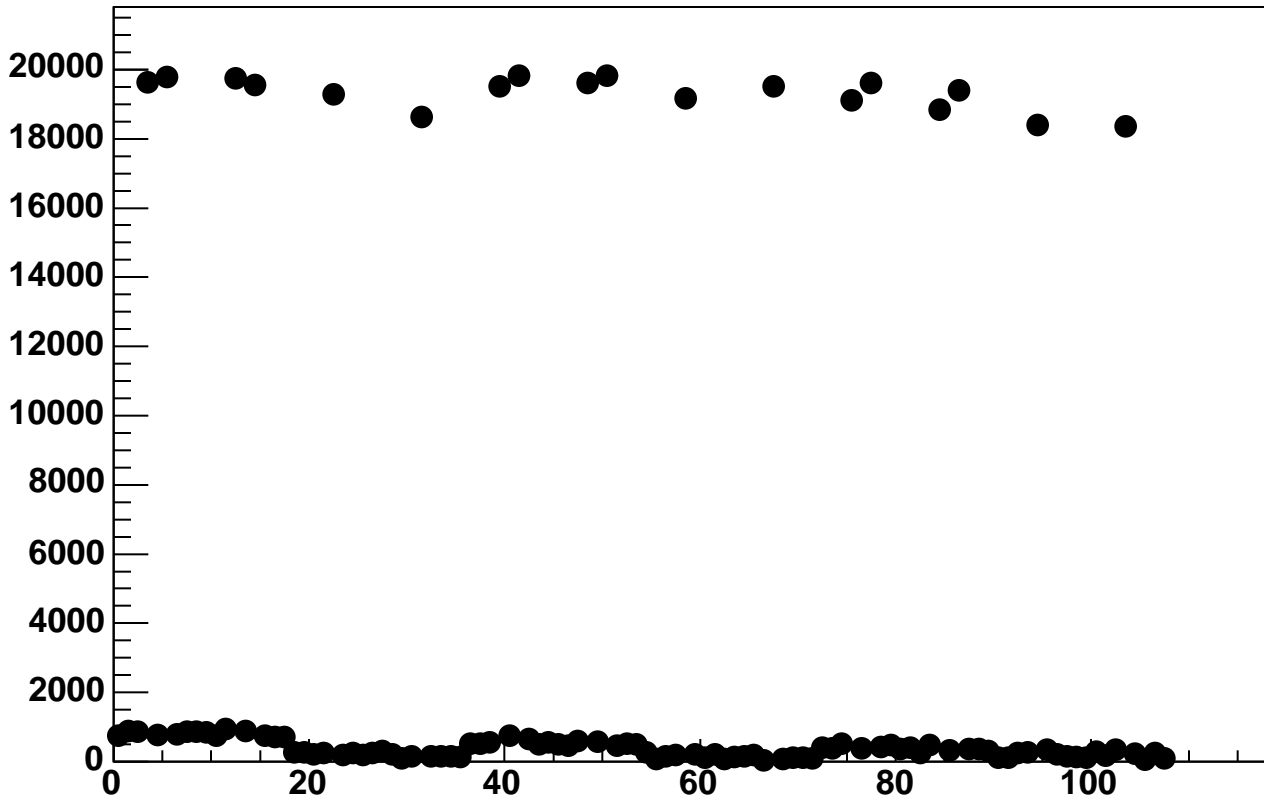
Enable 1, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



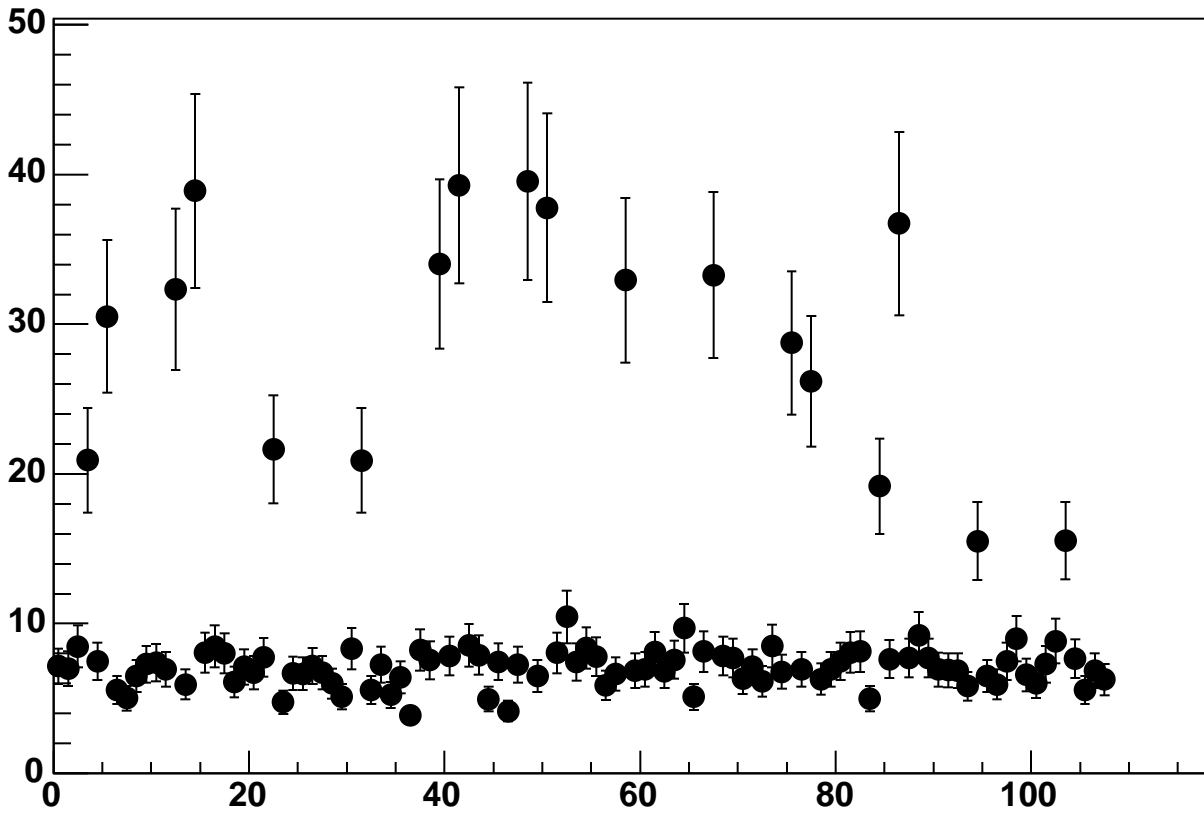
Enable 1, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



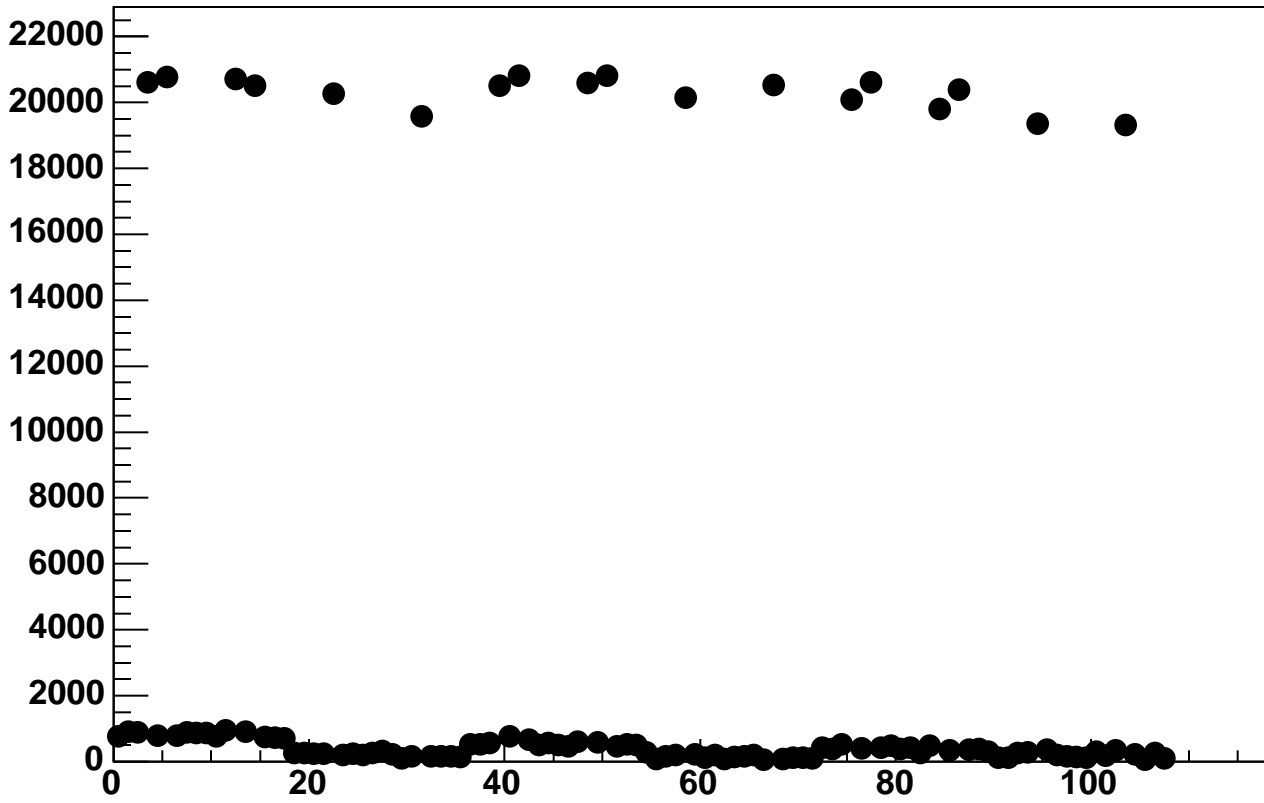
Enable 1, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



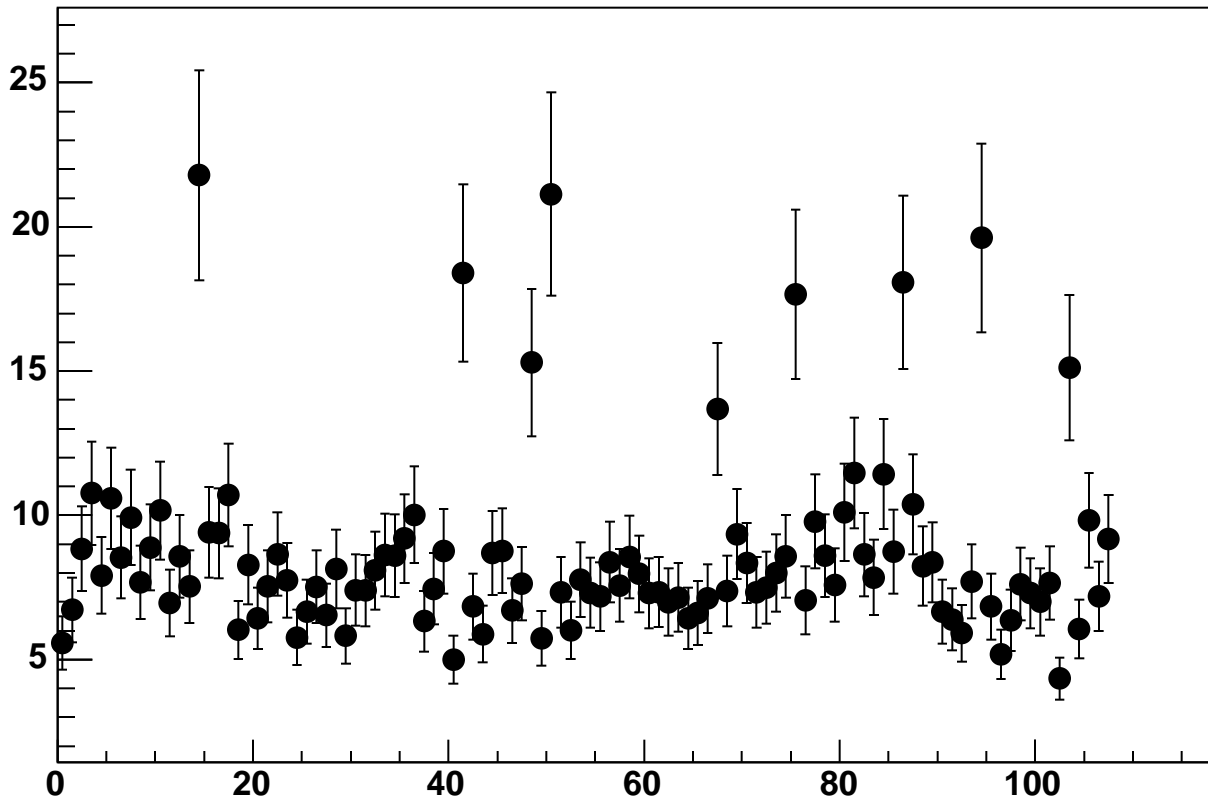
Enable 1, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



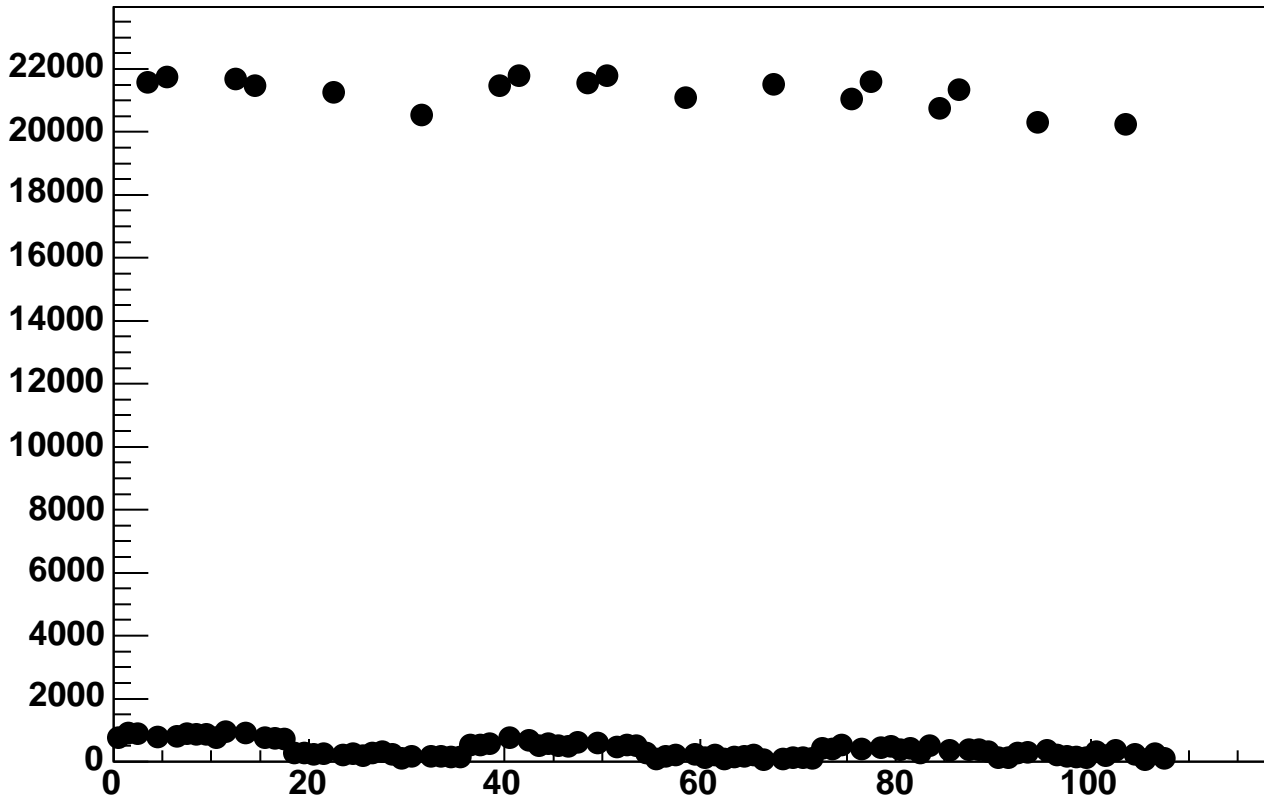
Enable 1, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



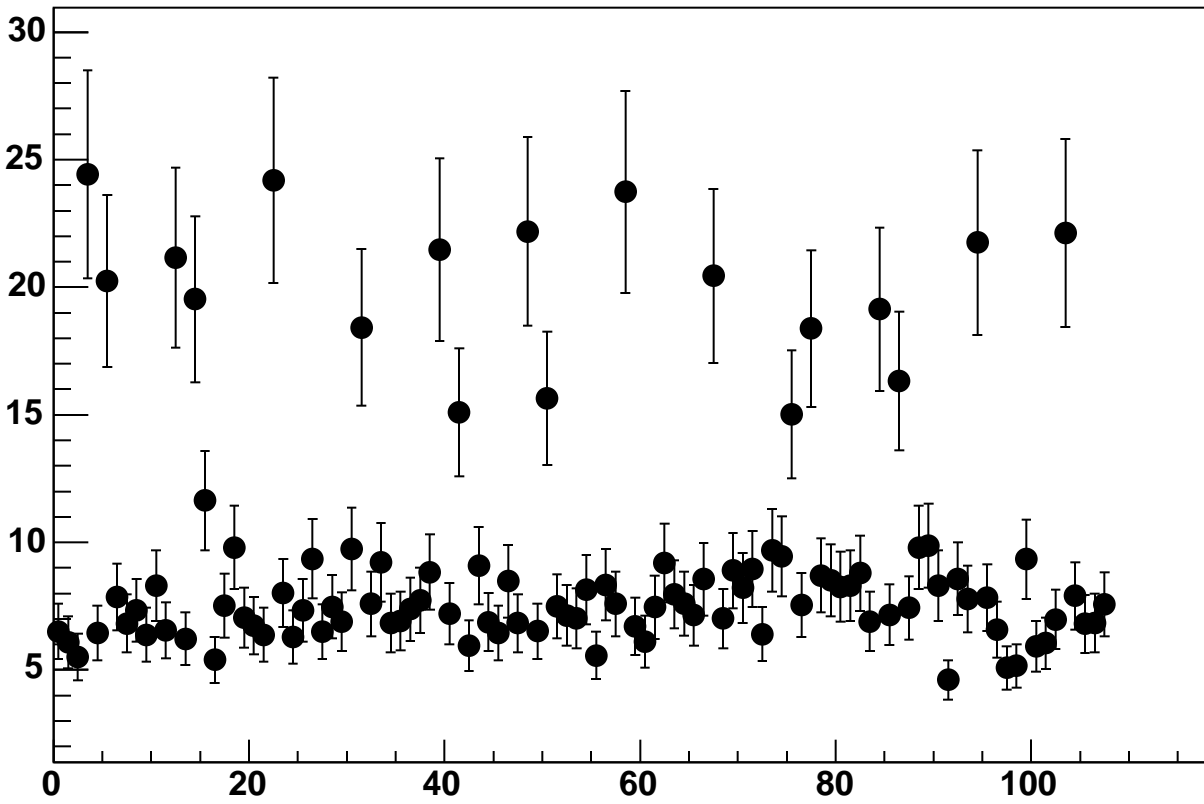
Enable 1, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



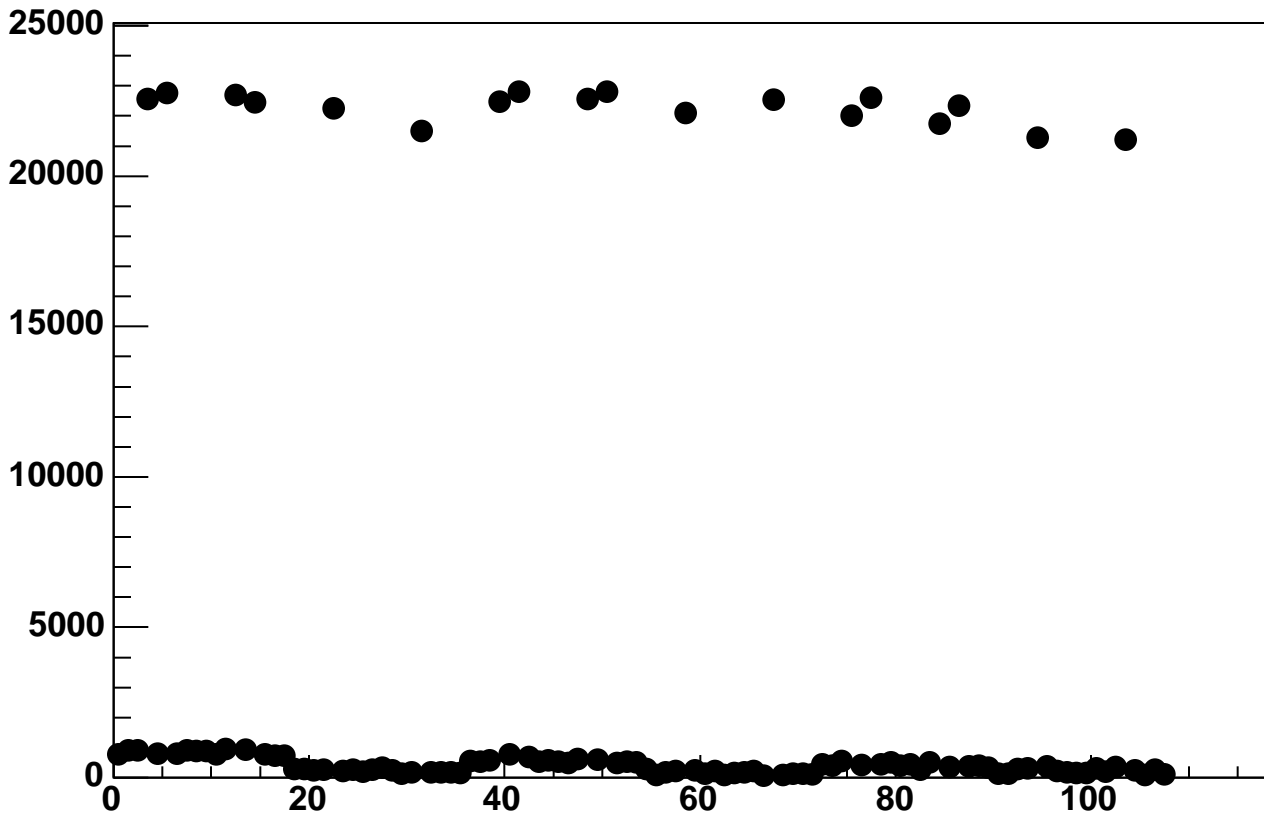
Enable 1, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



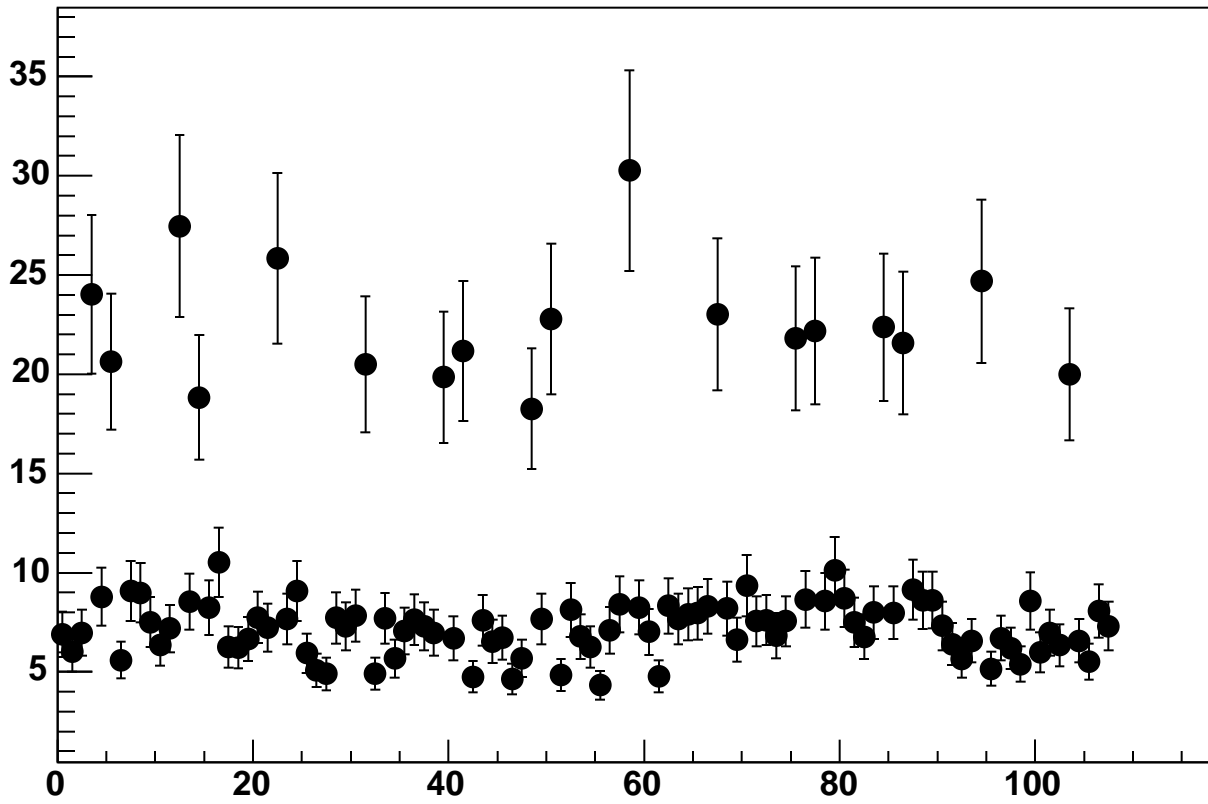
Enable 1, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



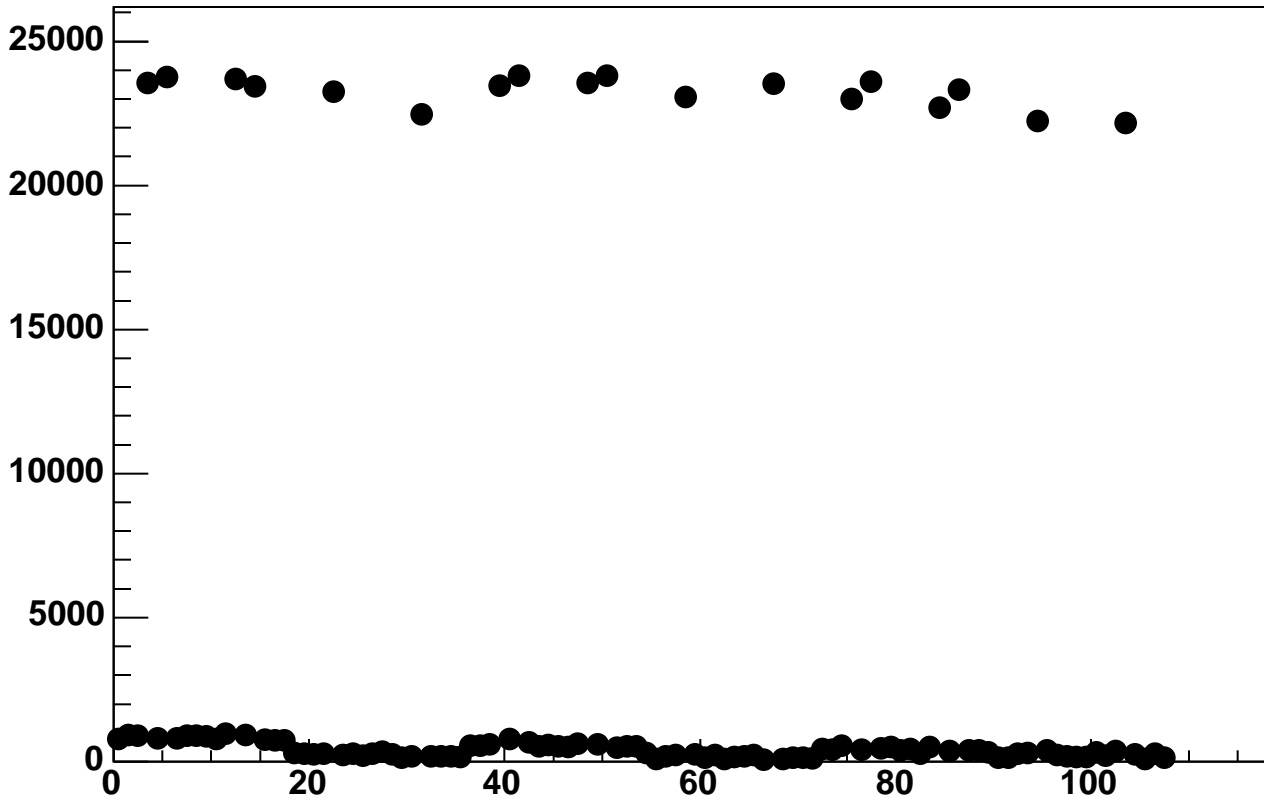
Enable 1, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



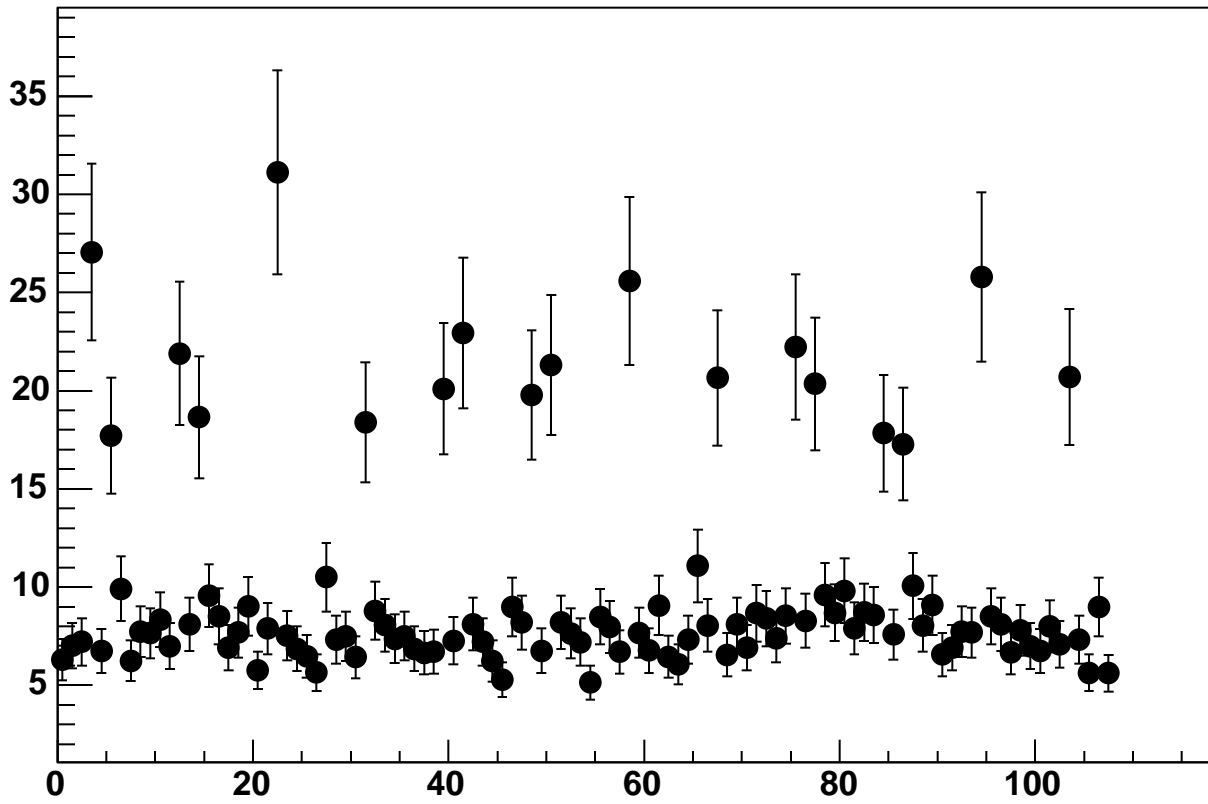
Enable 1, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



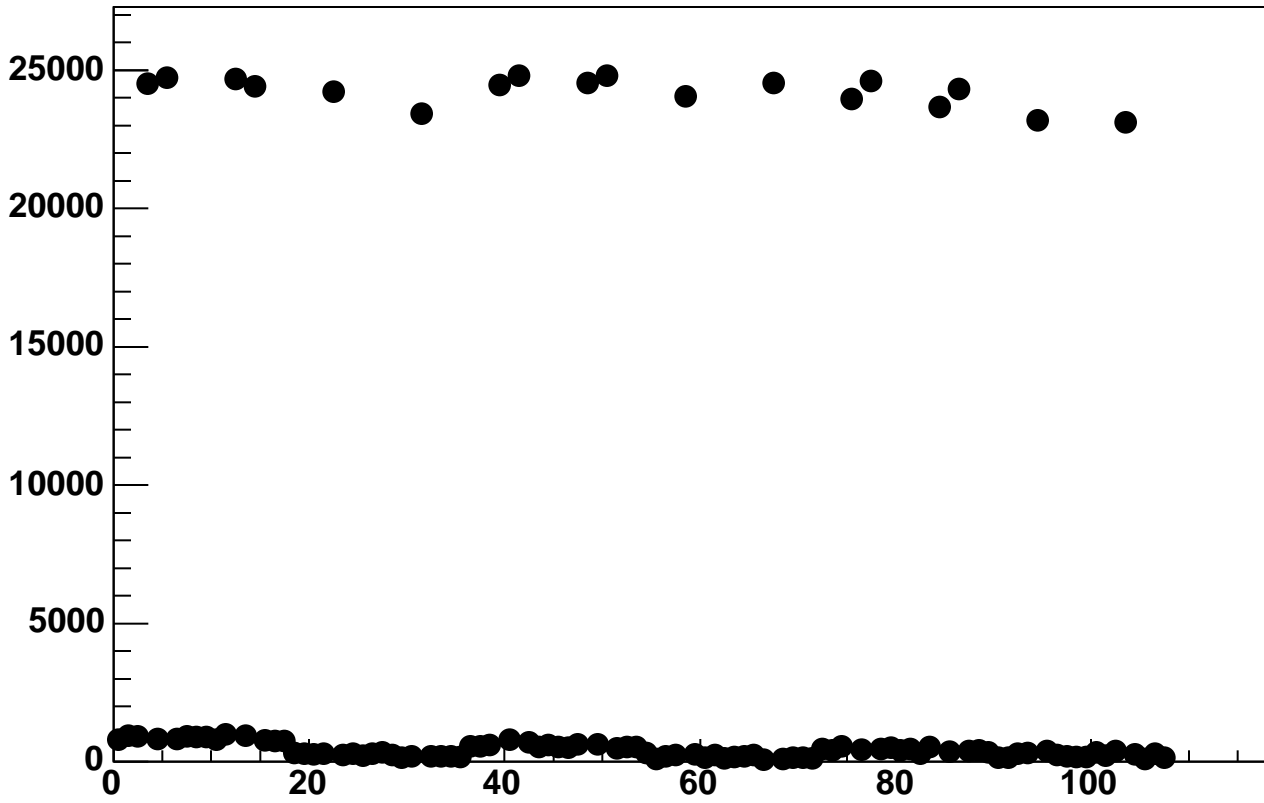
Enable 1, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



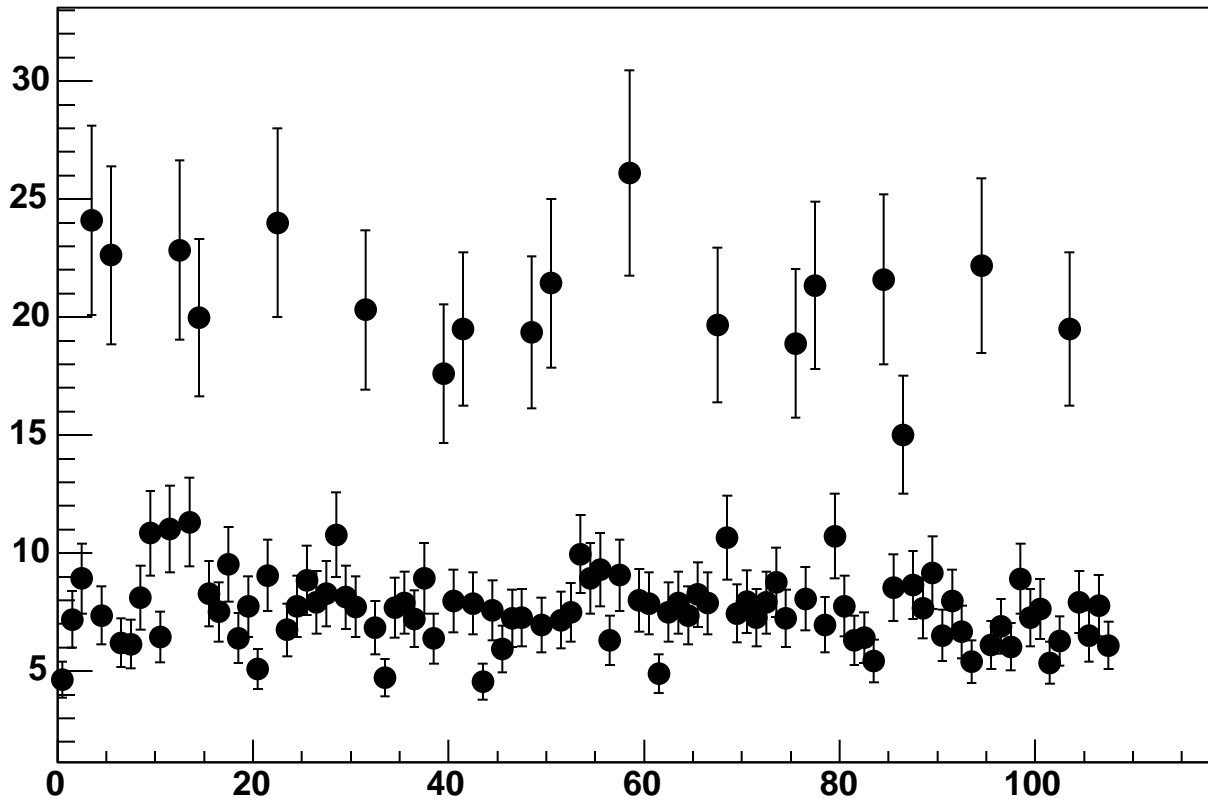
Enable 1, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



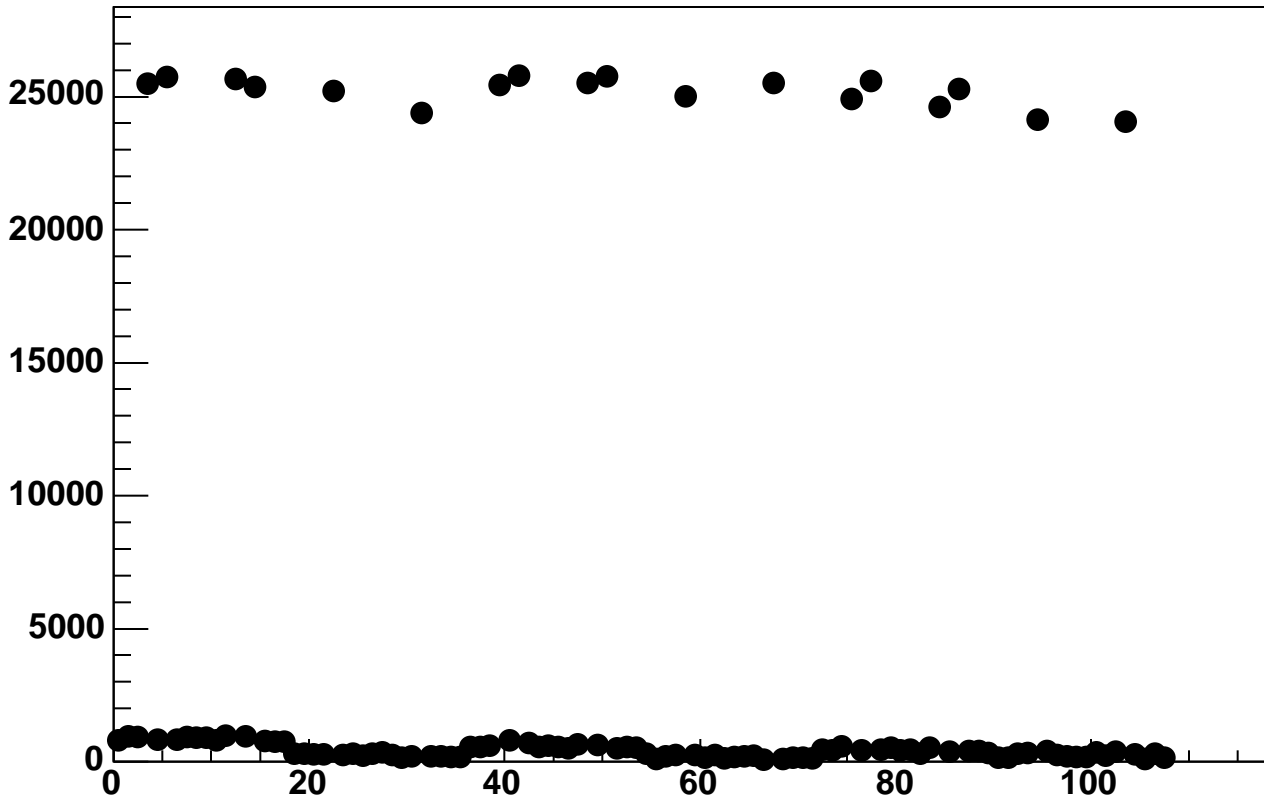
Enable 1, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



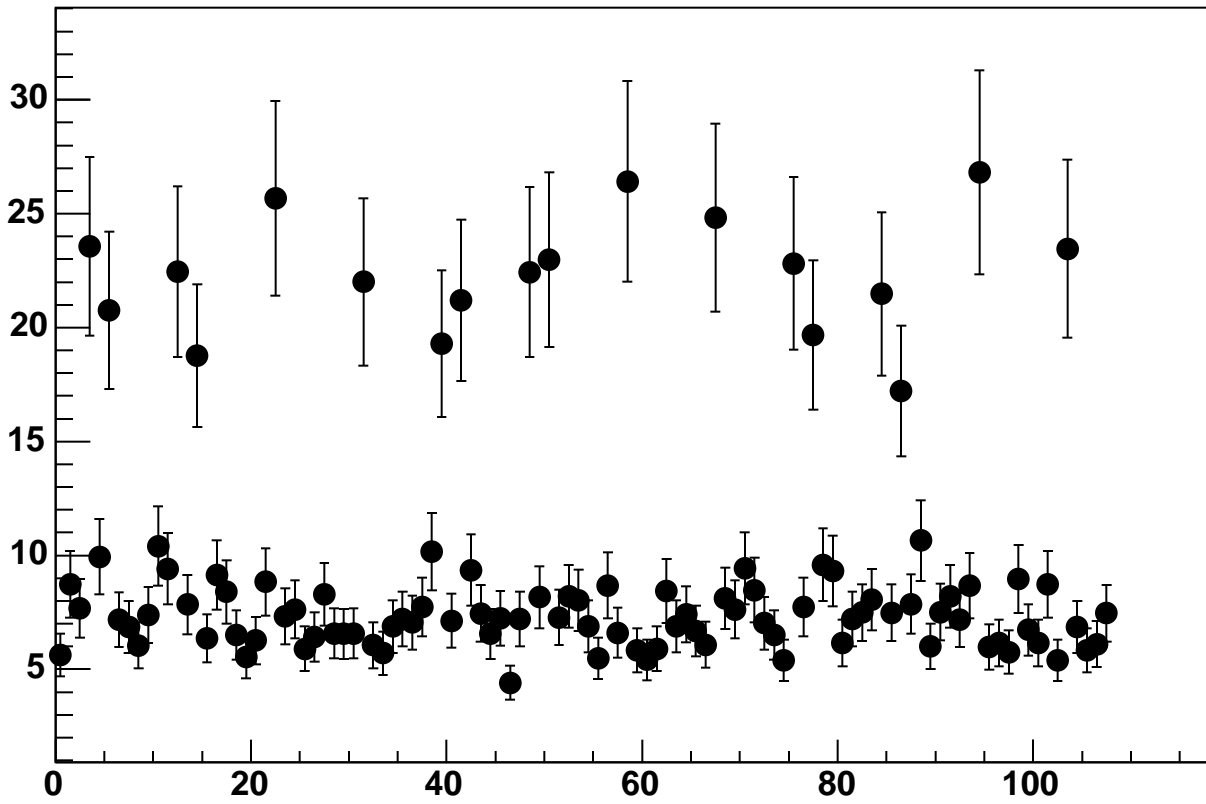
Enable 1, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



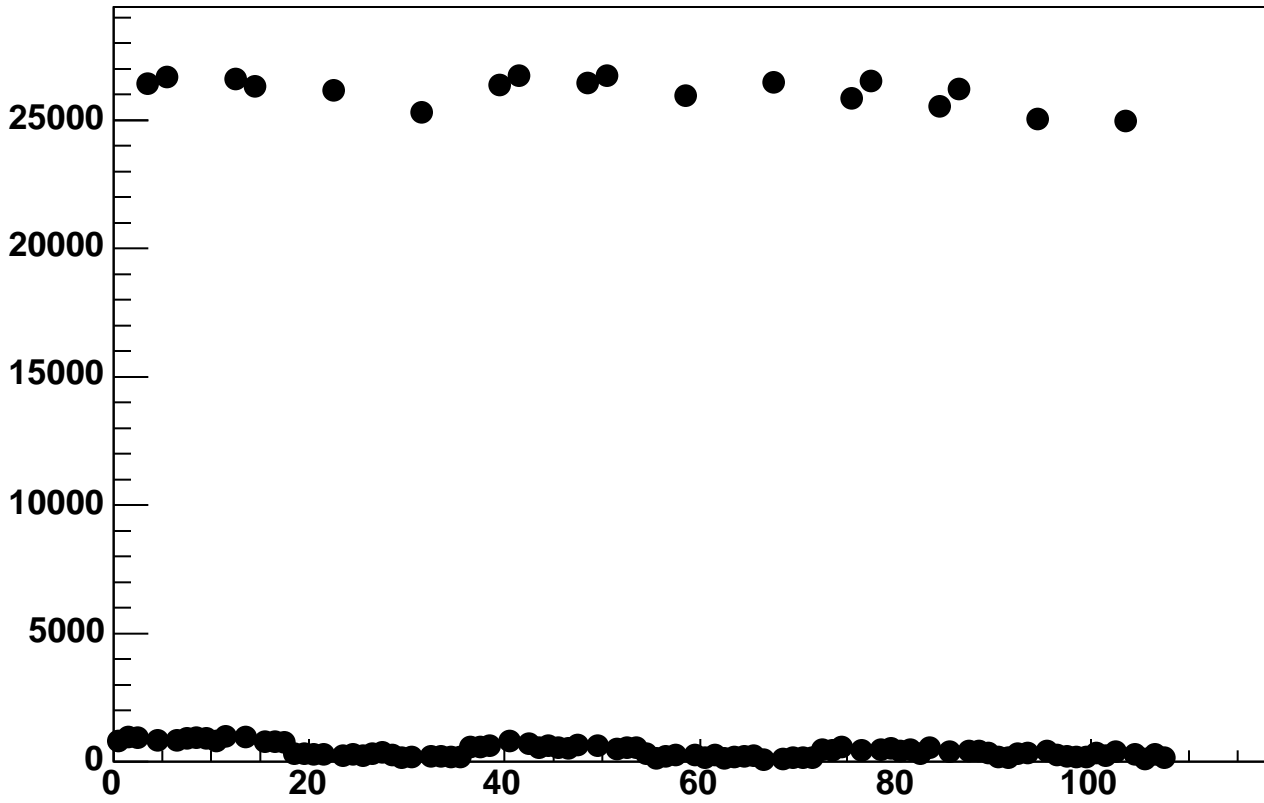
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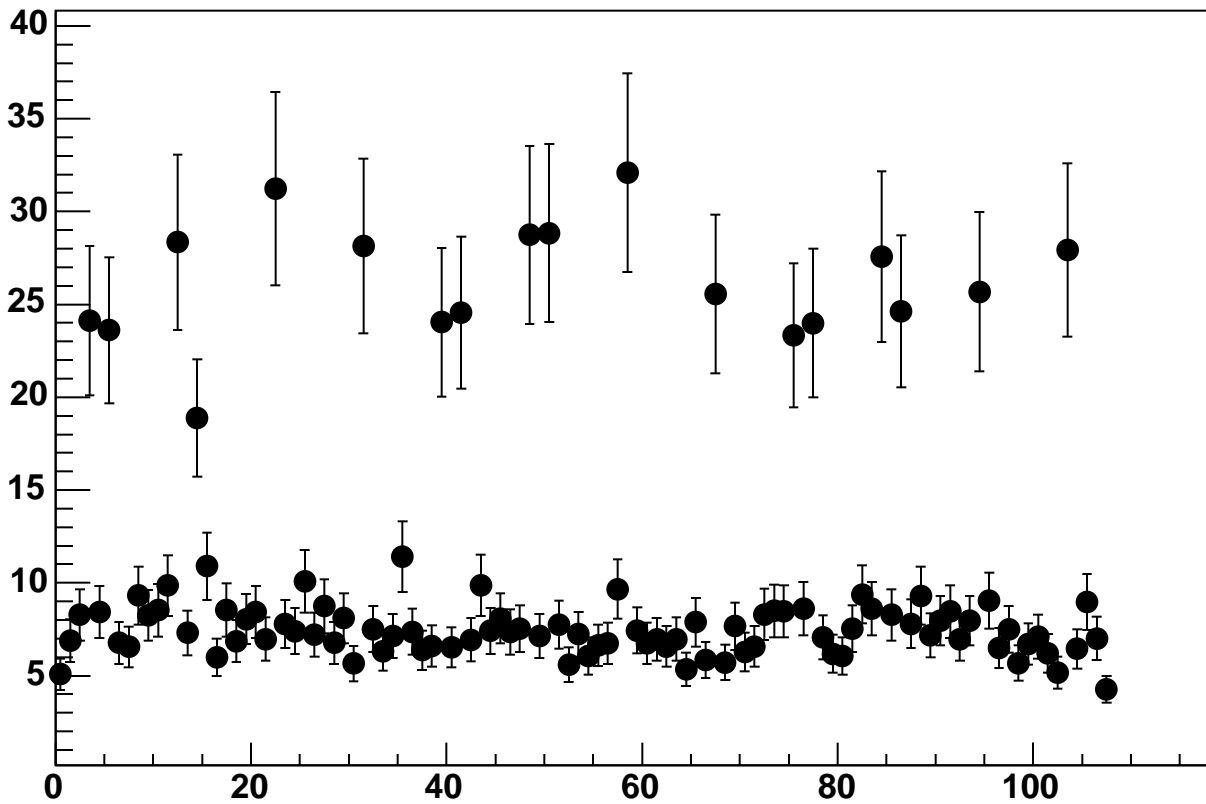
Enable 1, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



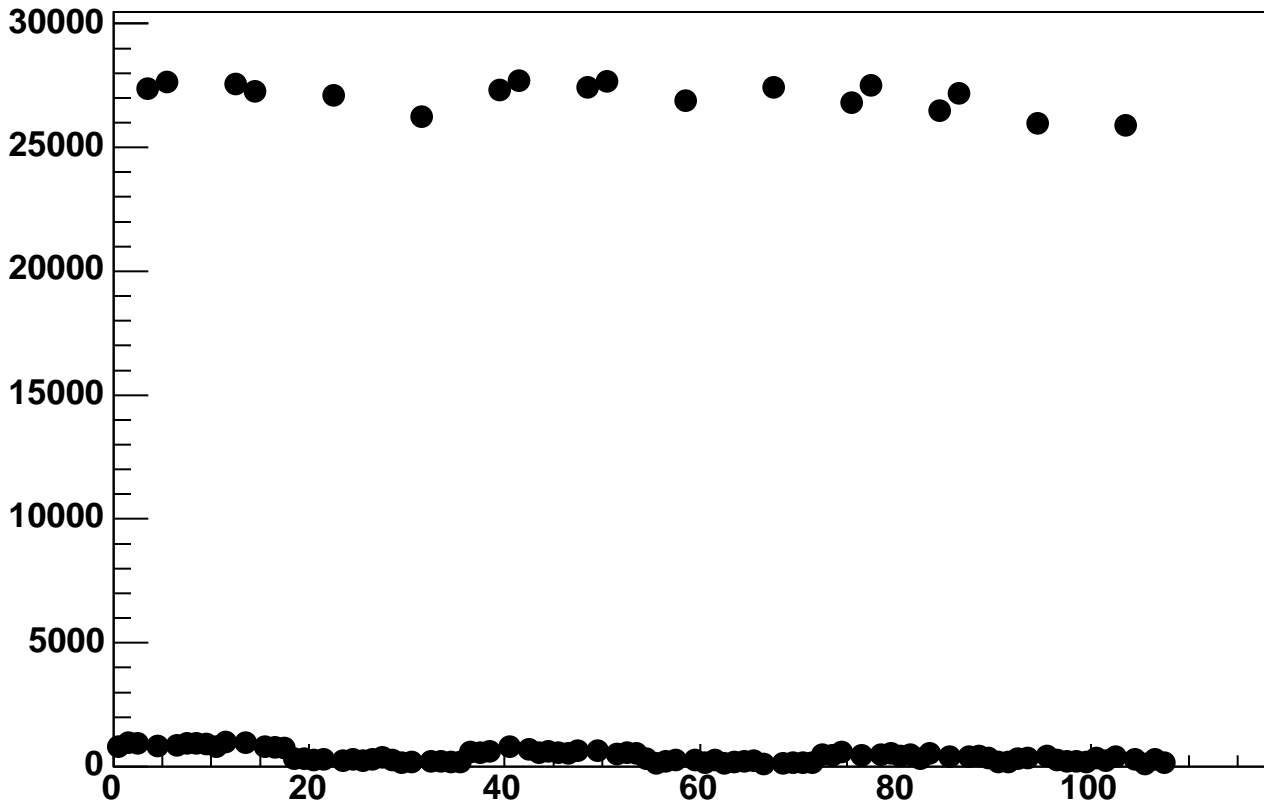
Enable 1, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



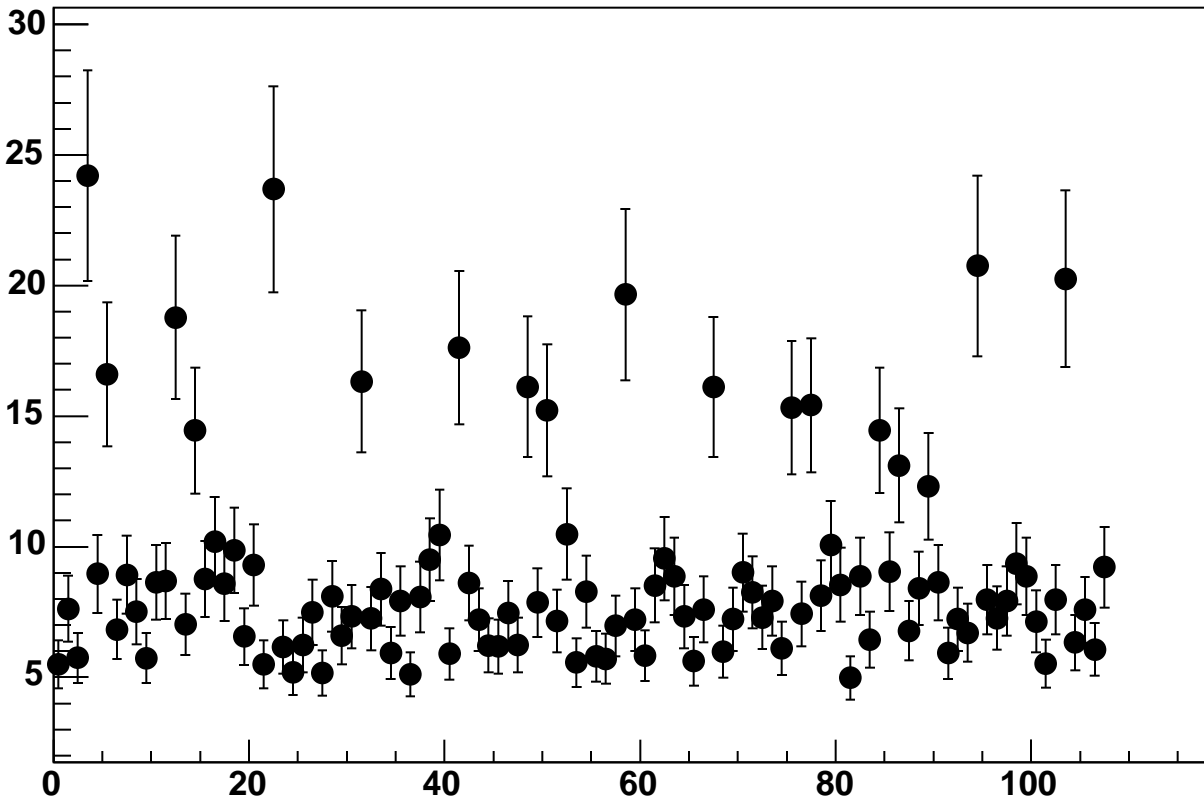
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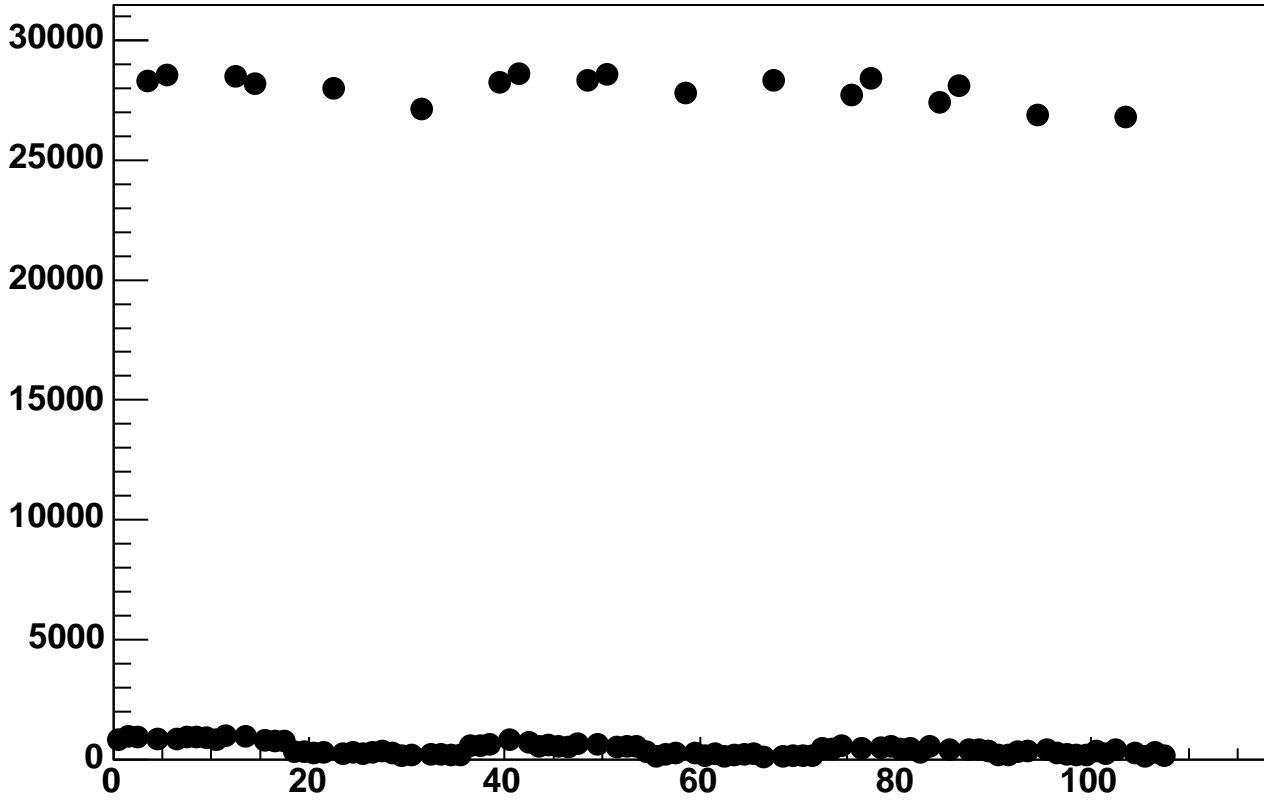
Enable 1, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



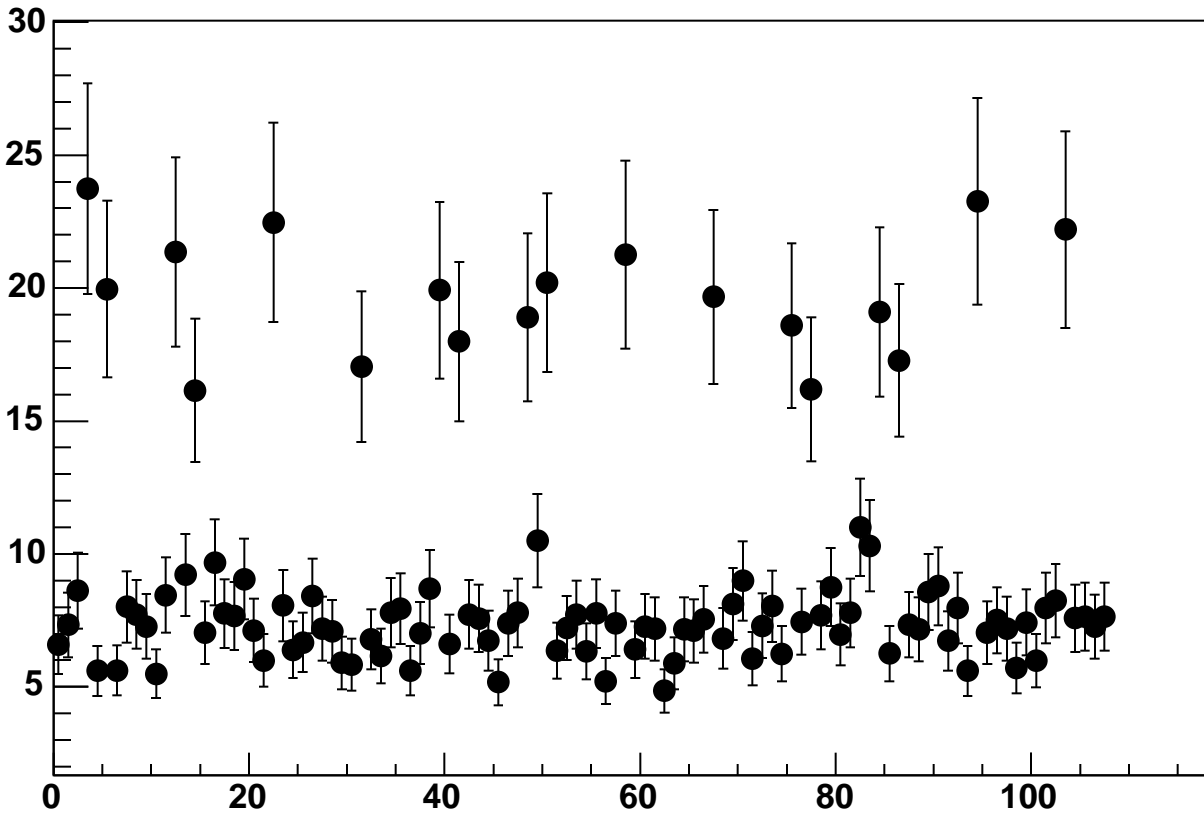
Enable 1, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



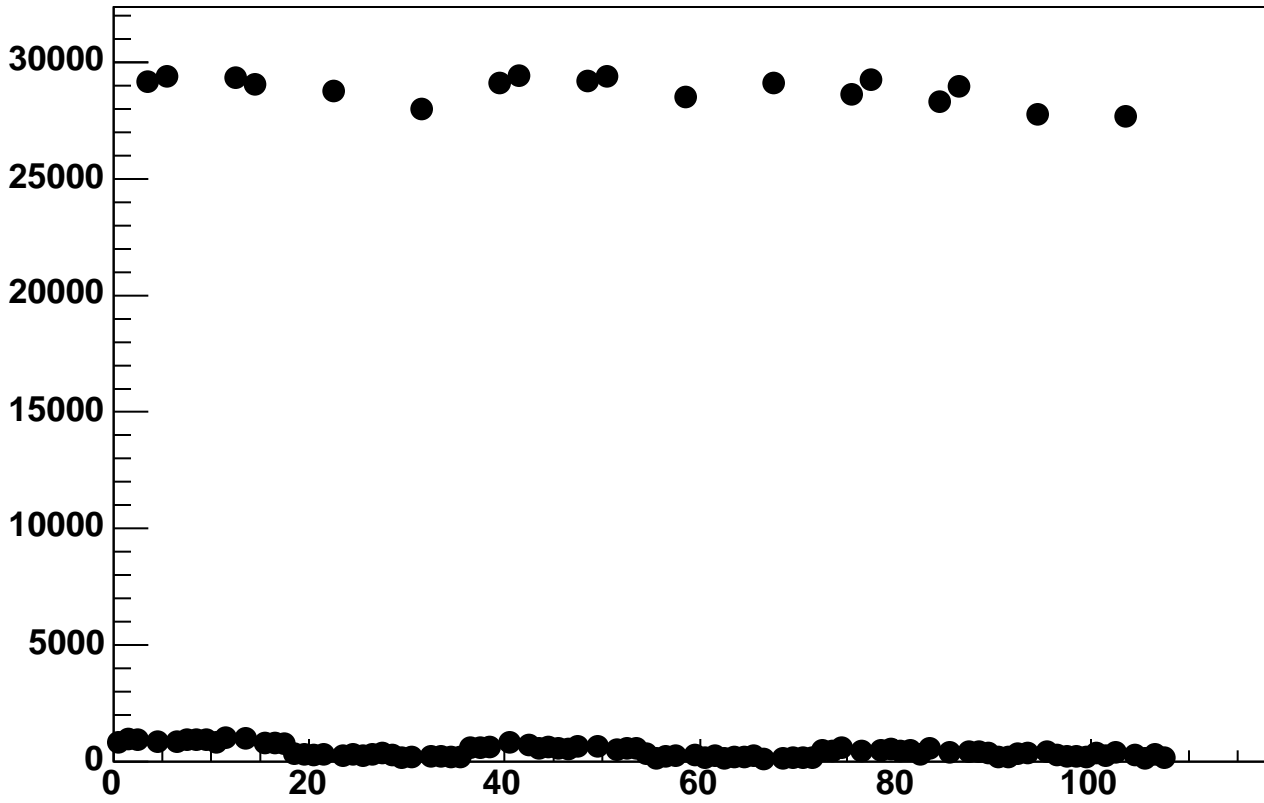
Enable 1, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



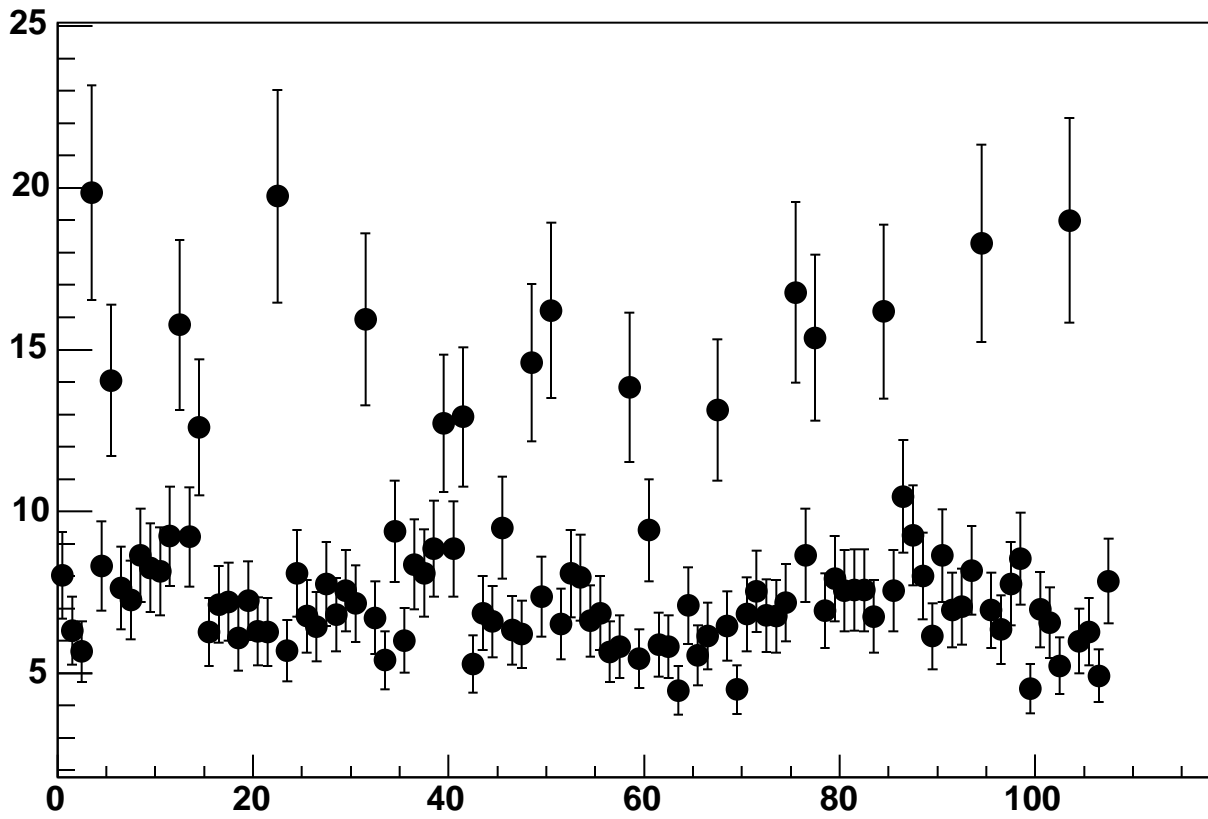
Enable 1, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



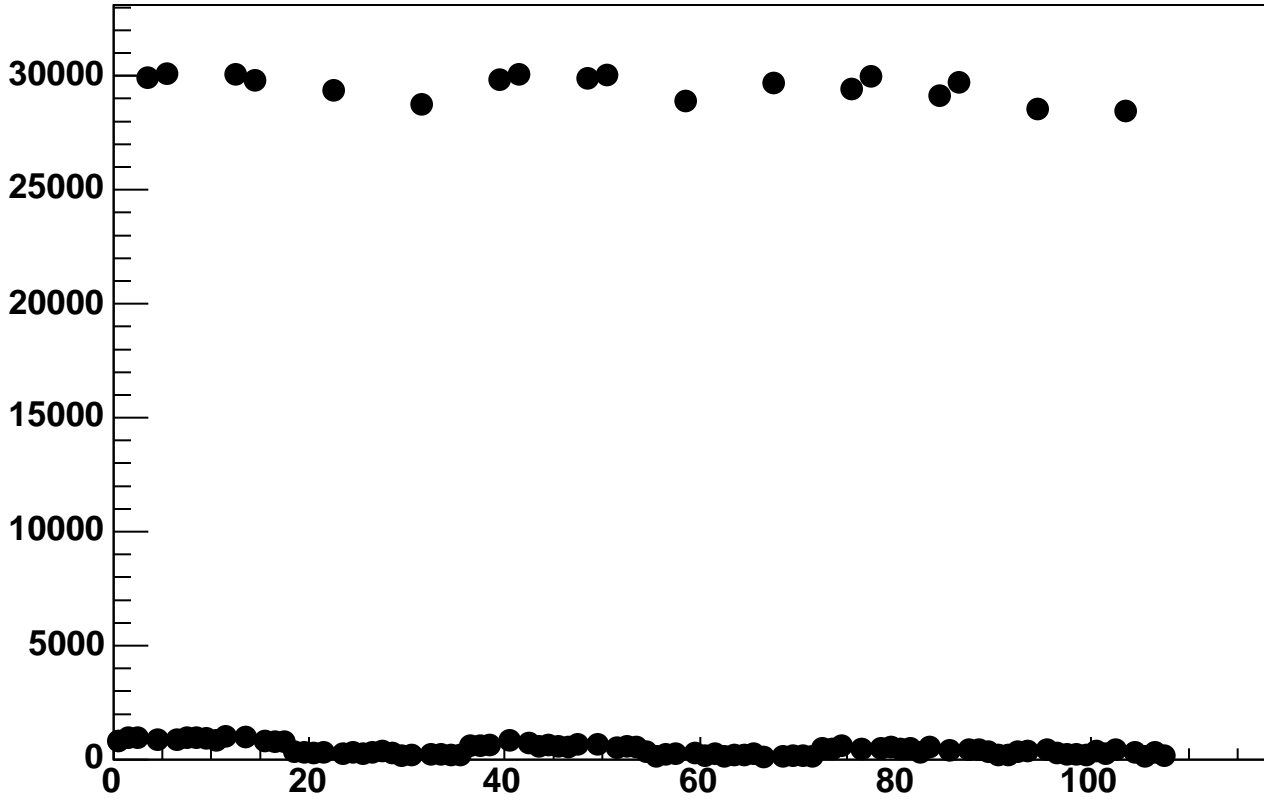
Enable 1, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



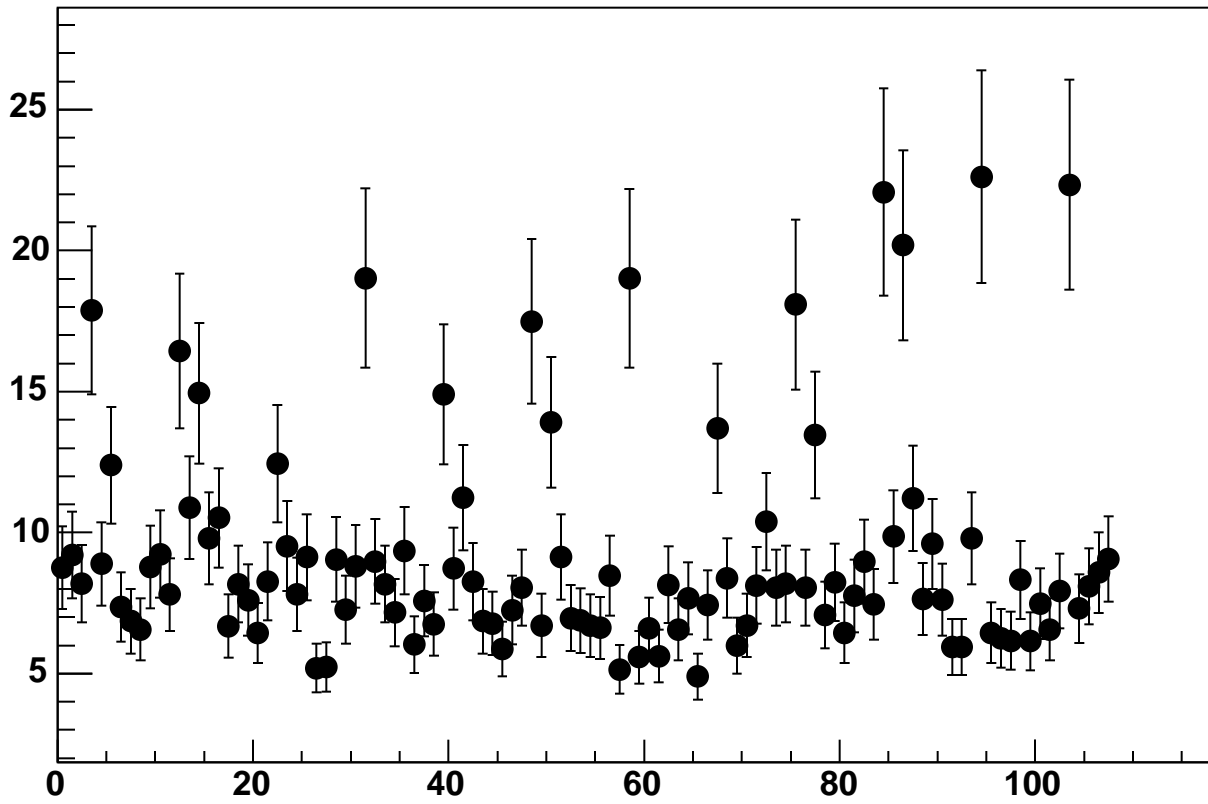
Enable 1, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



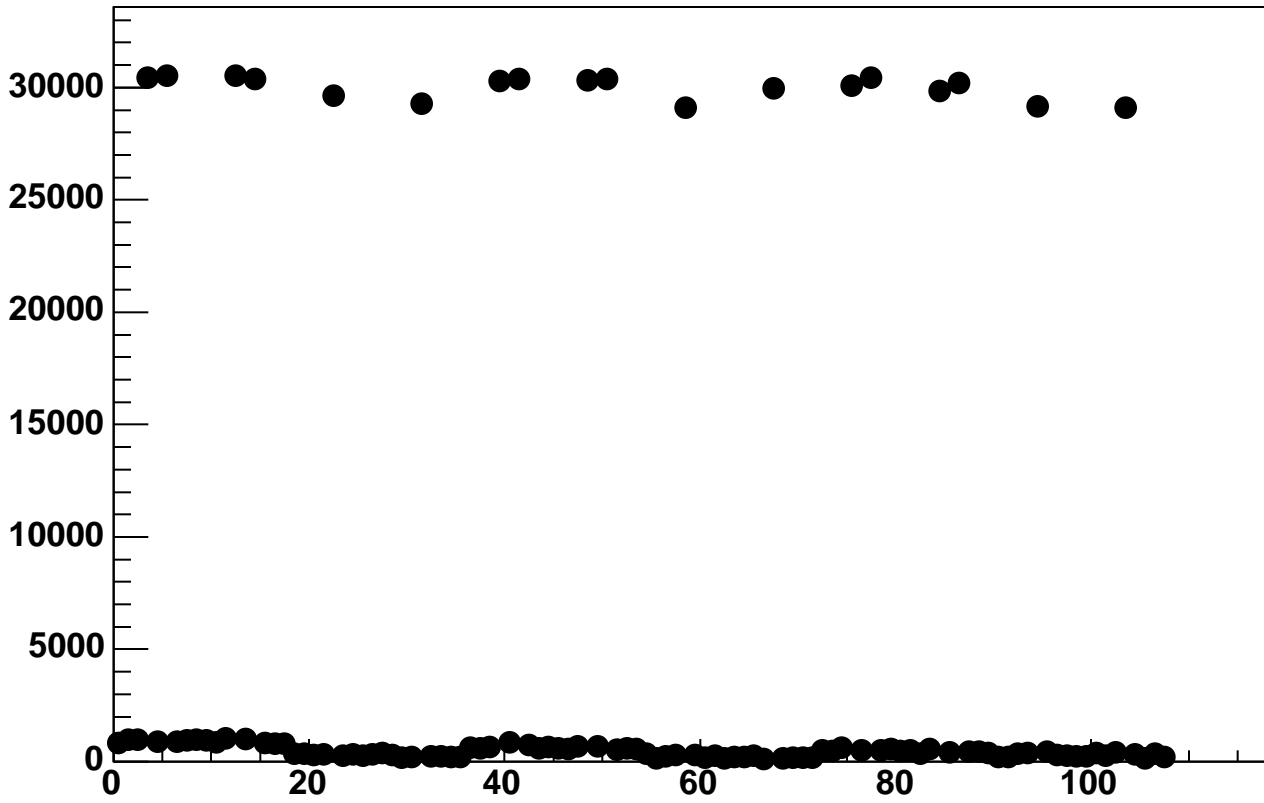
Enable 1, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



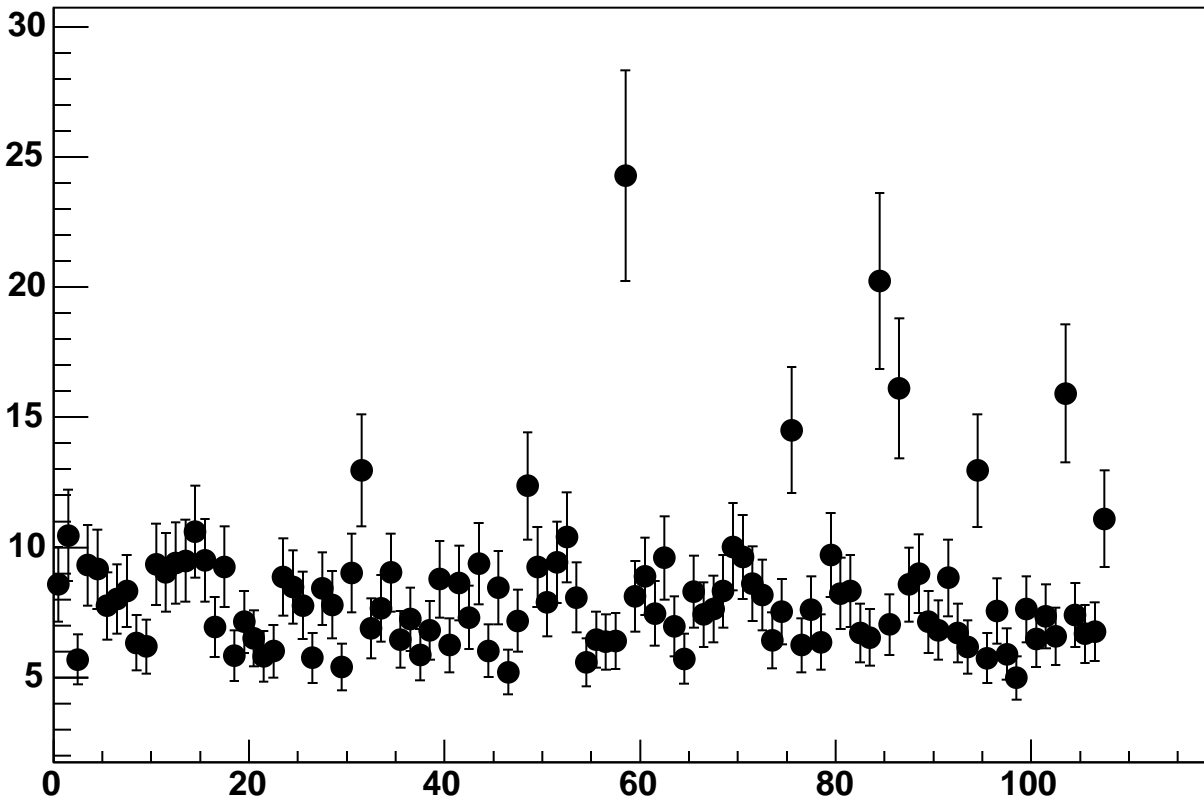
Enable 1, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



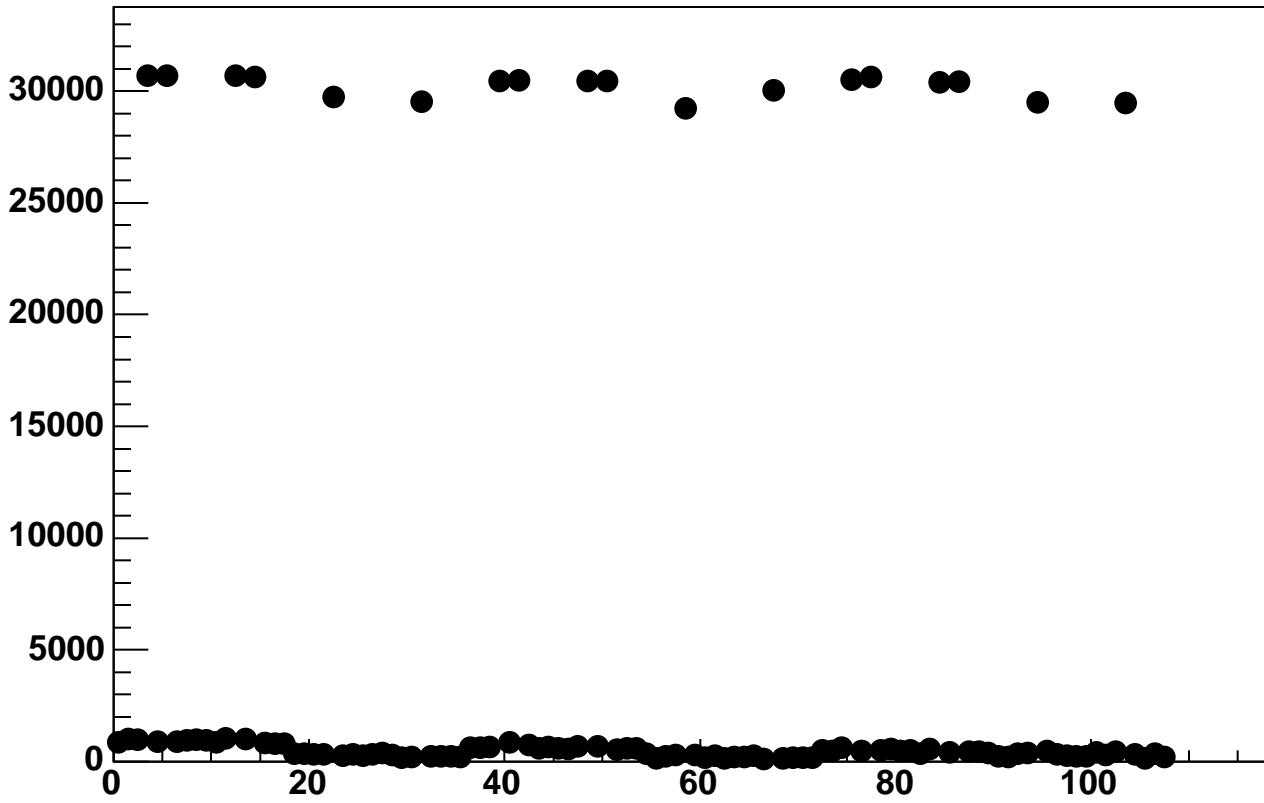
Enable 1, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



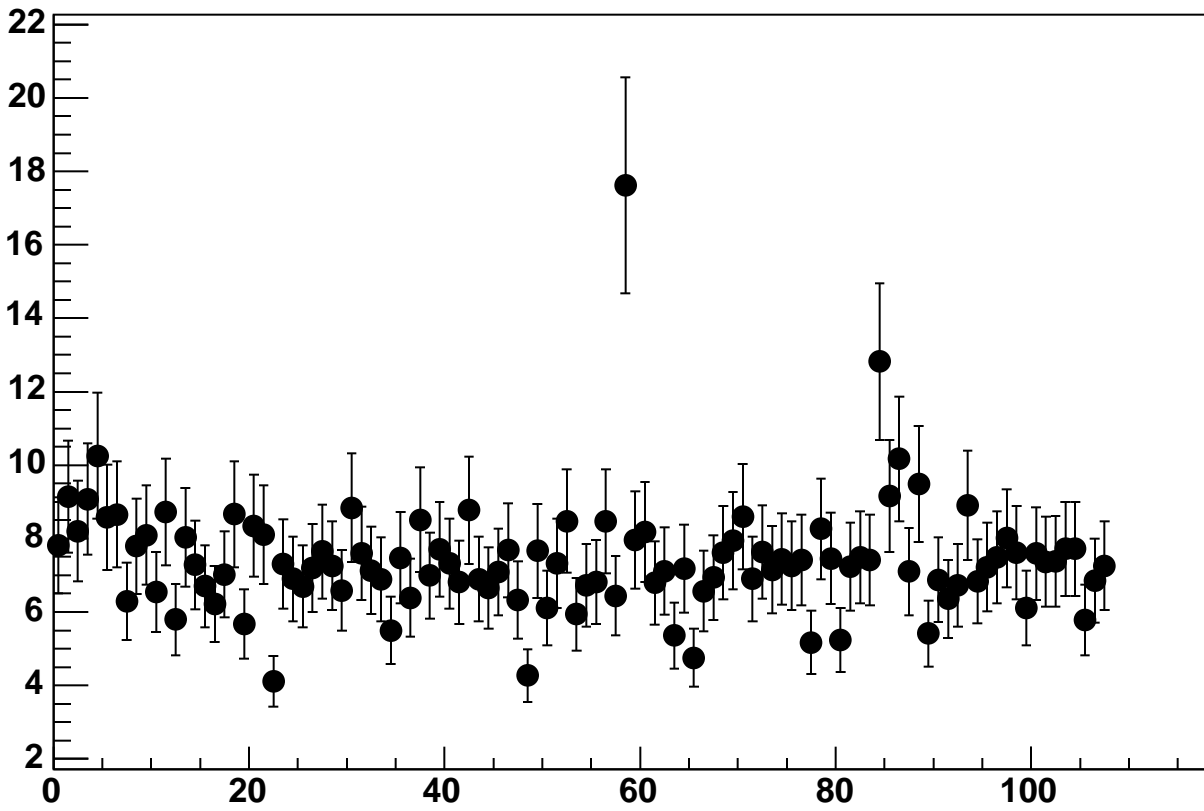
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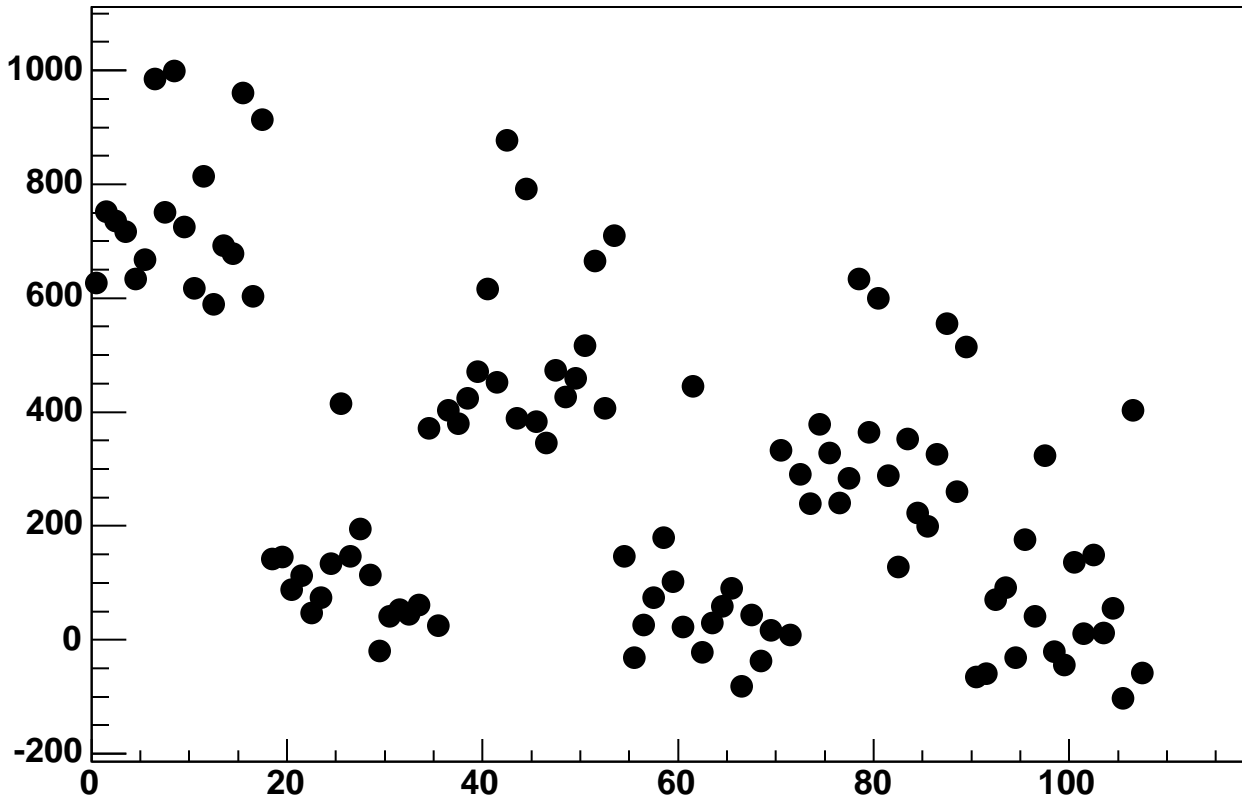
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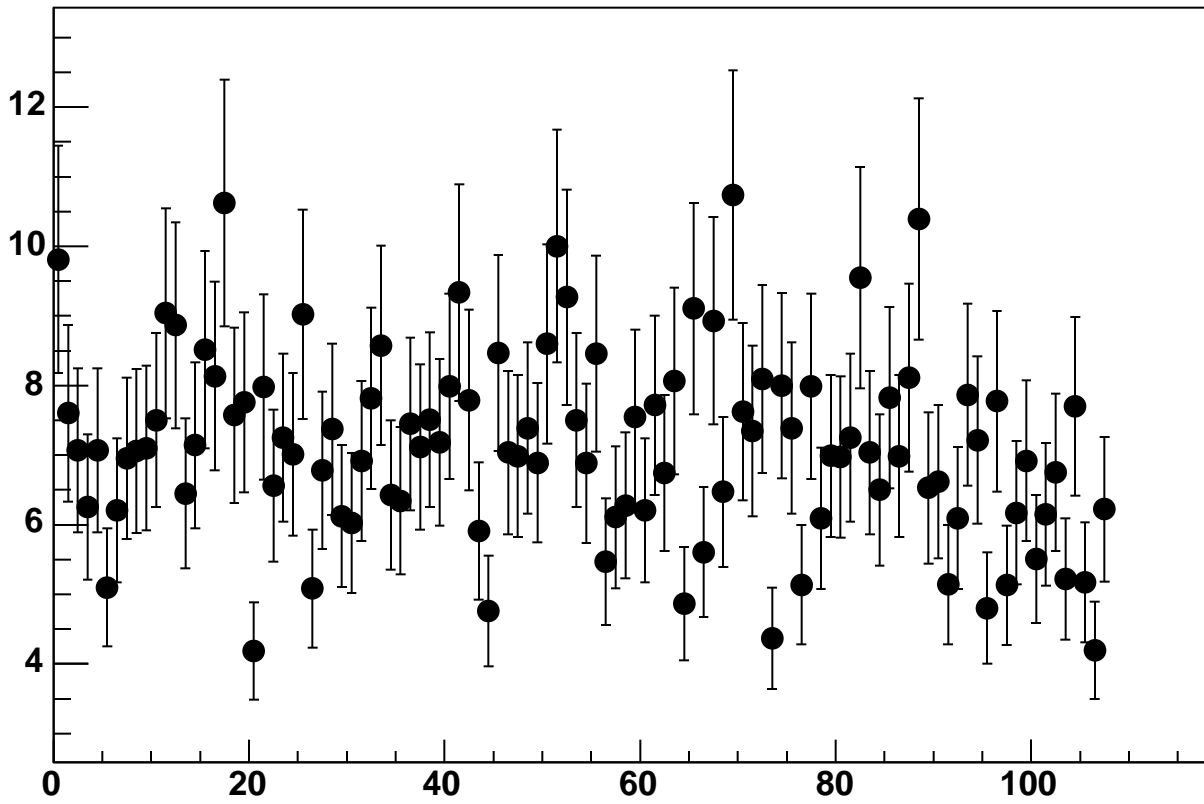
Enable 1, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



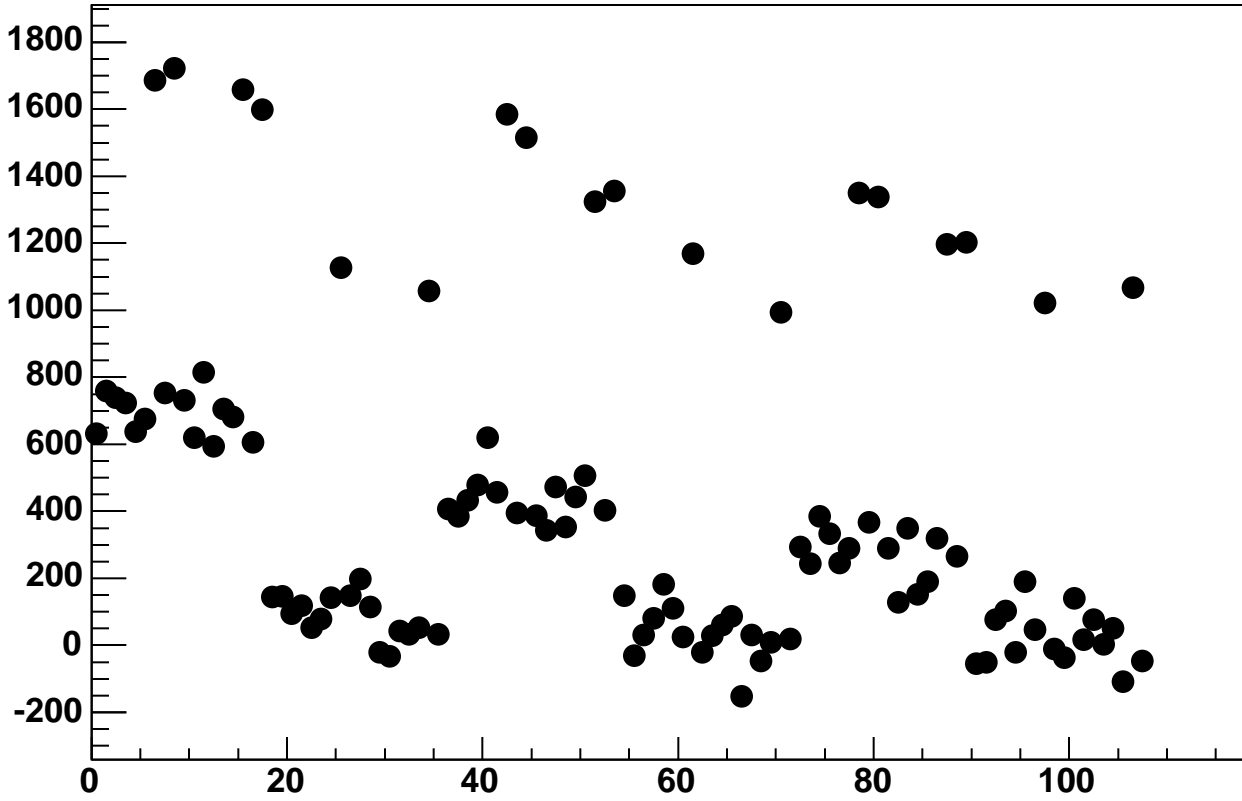
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



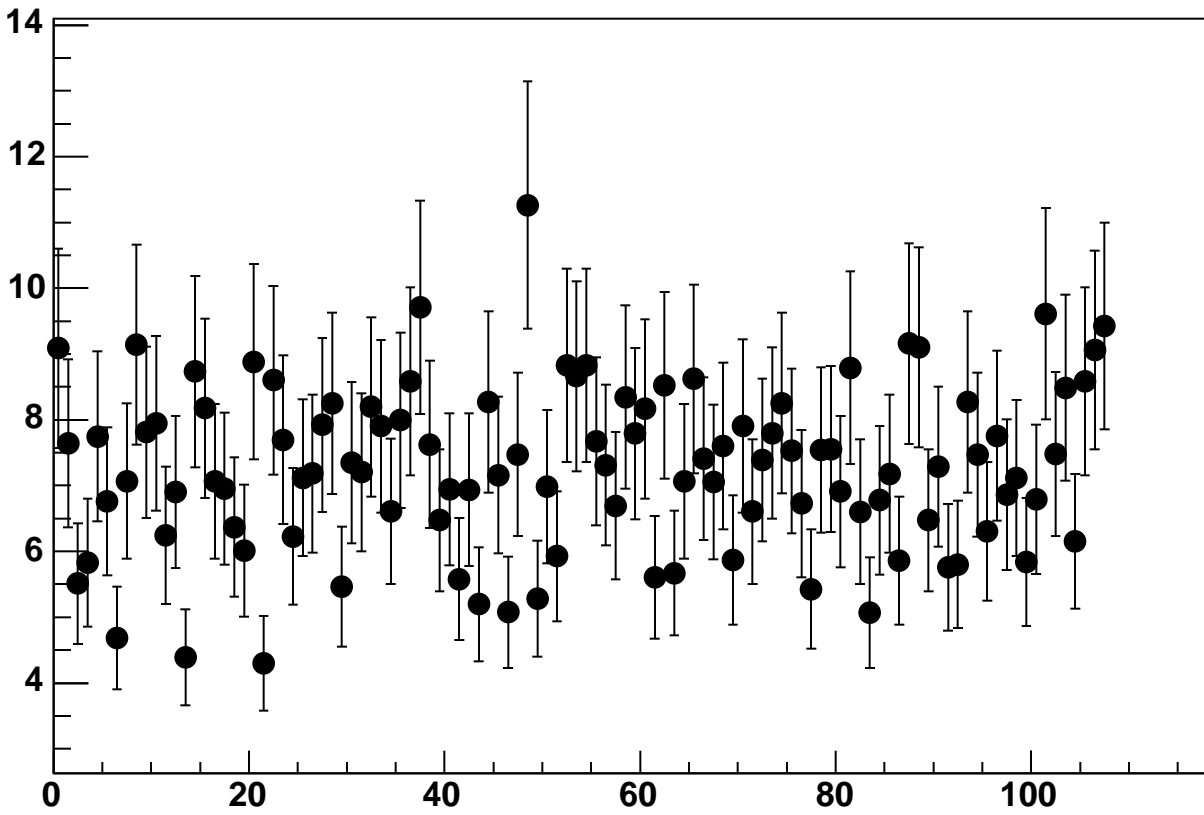
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



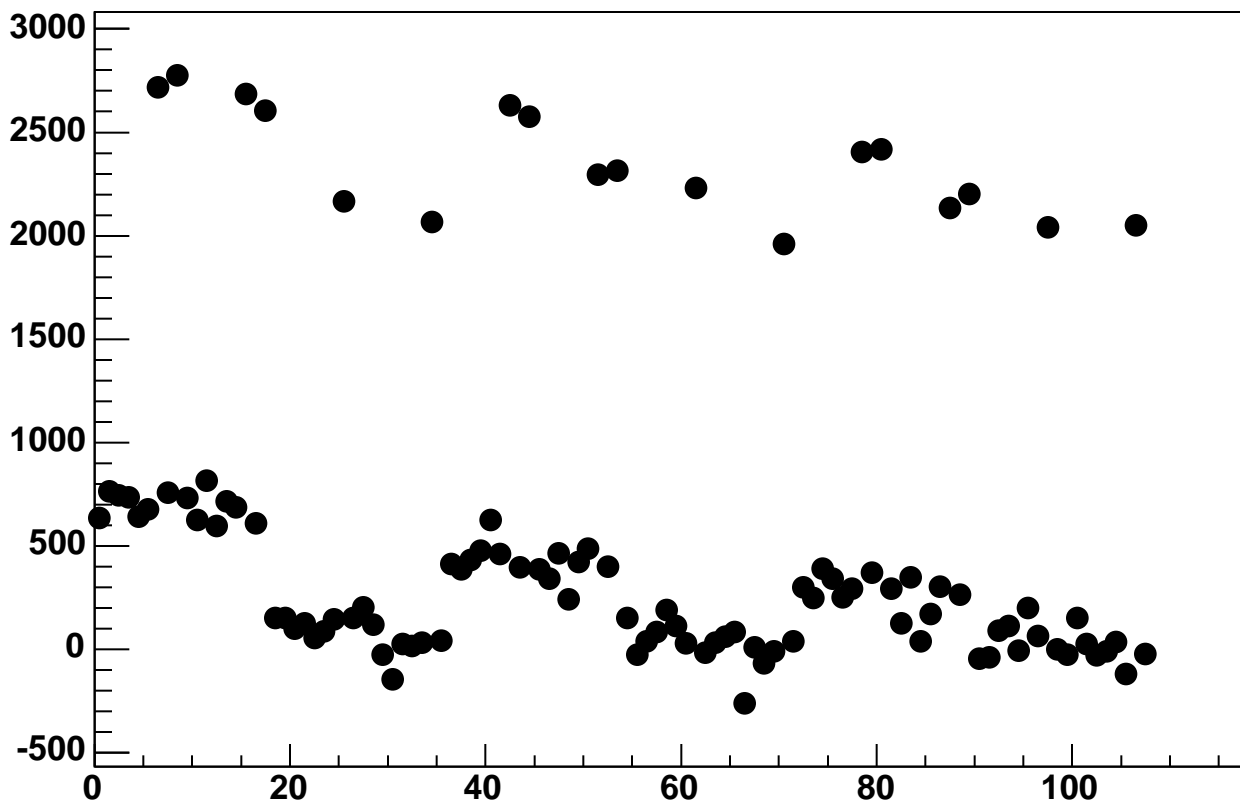
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



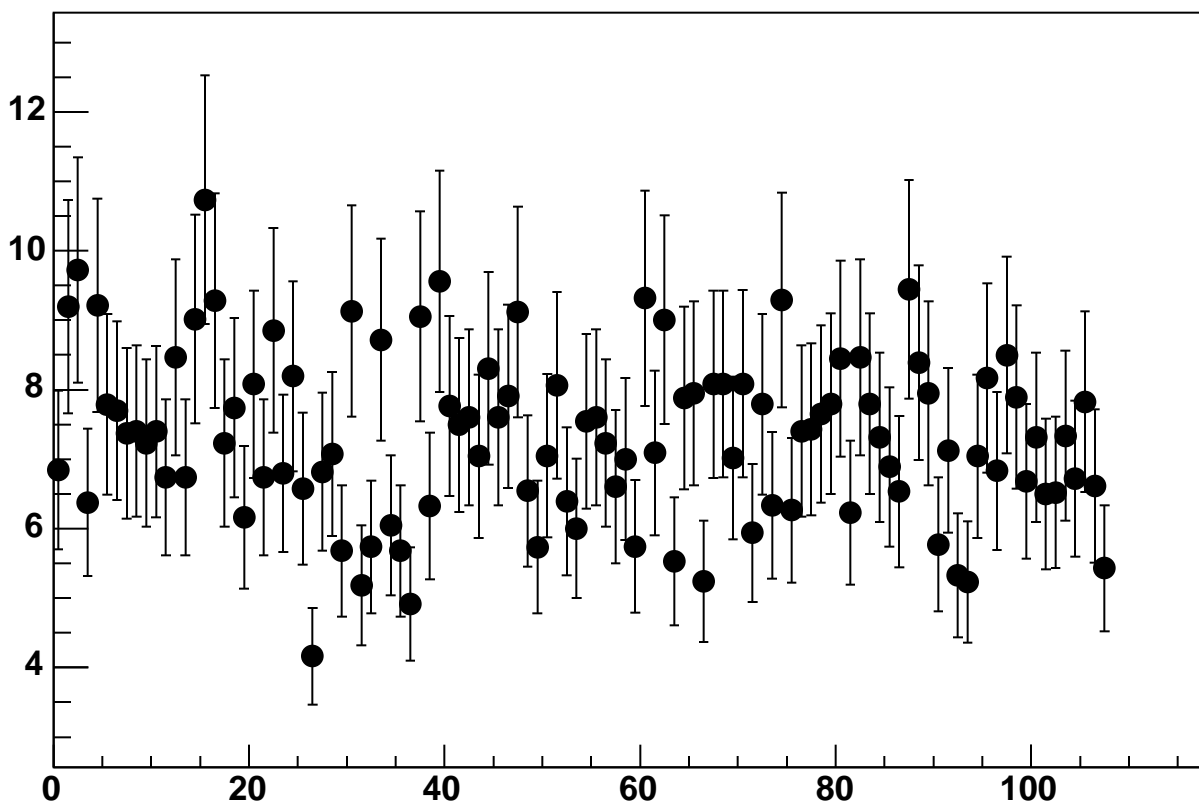
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



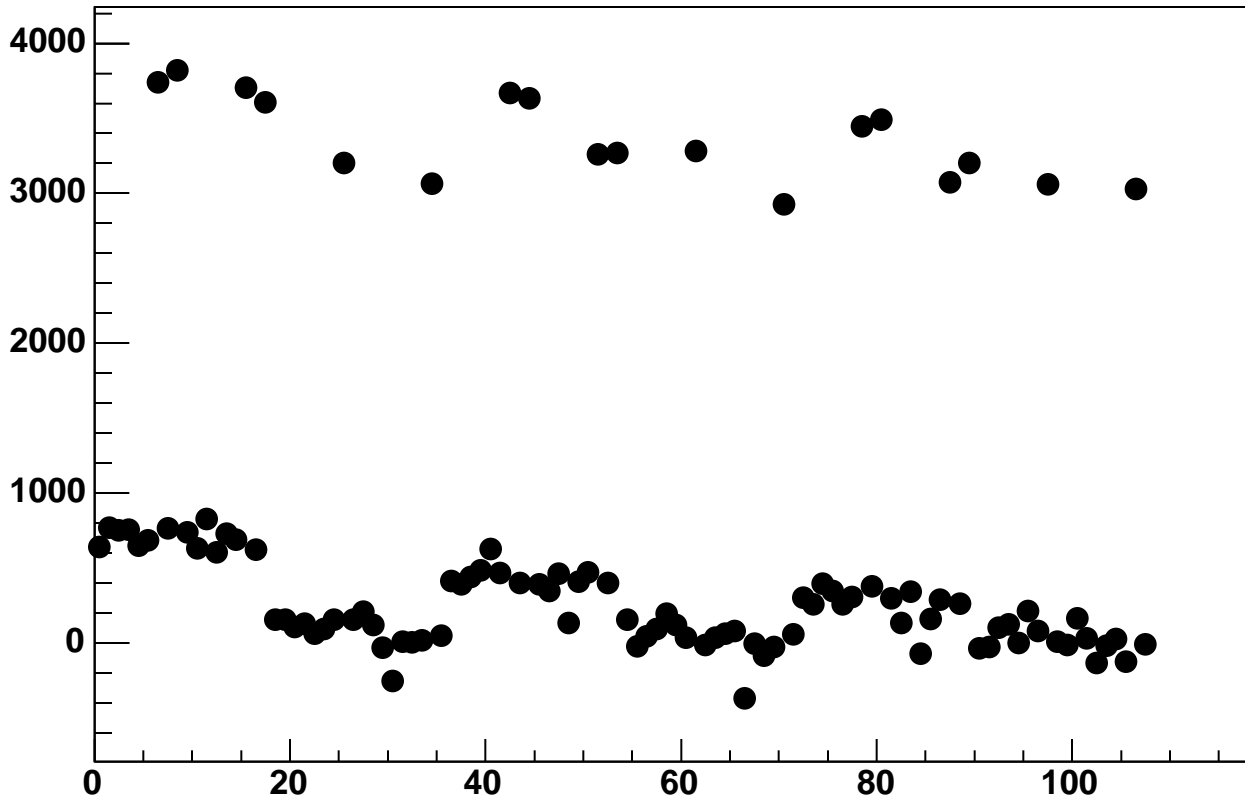
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



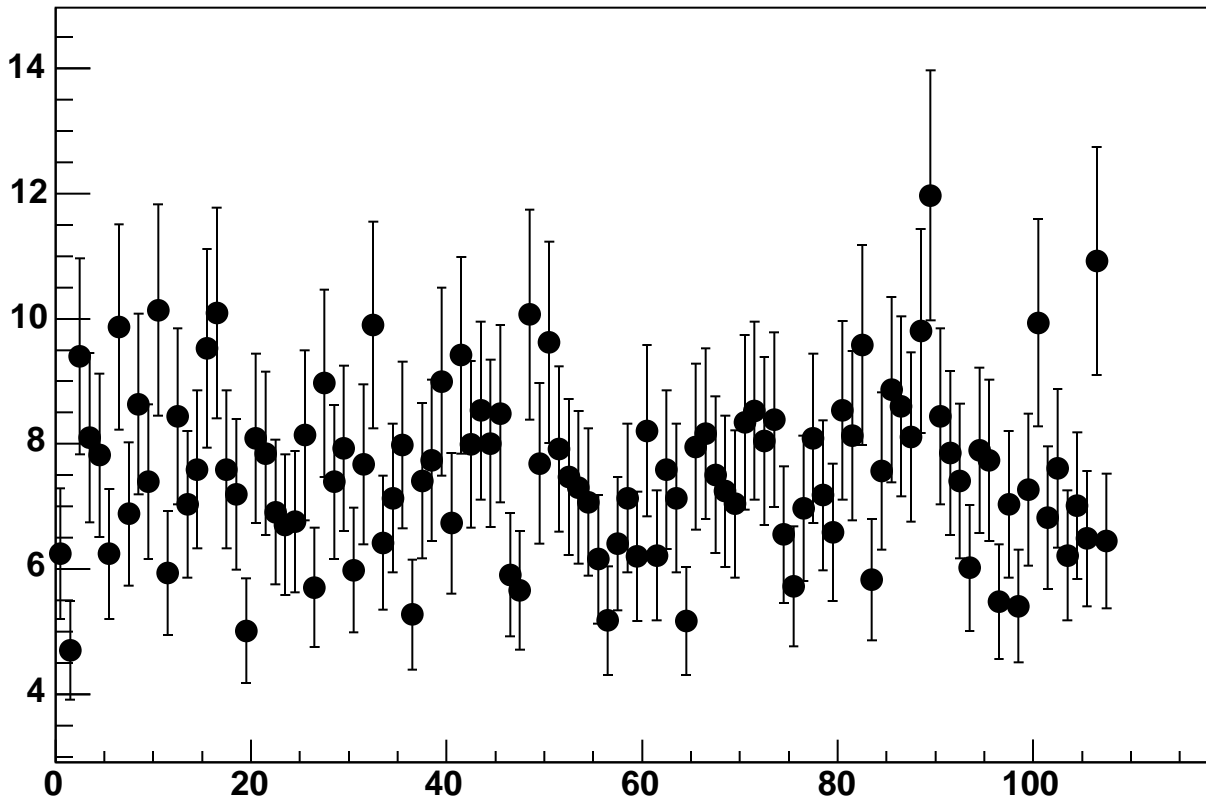
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



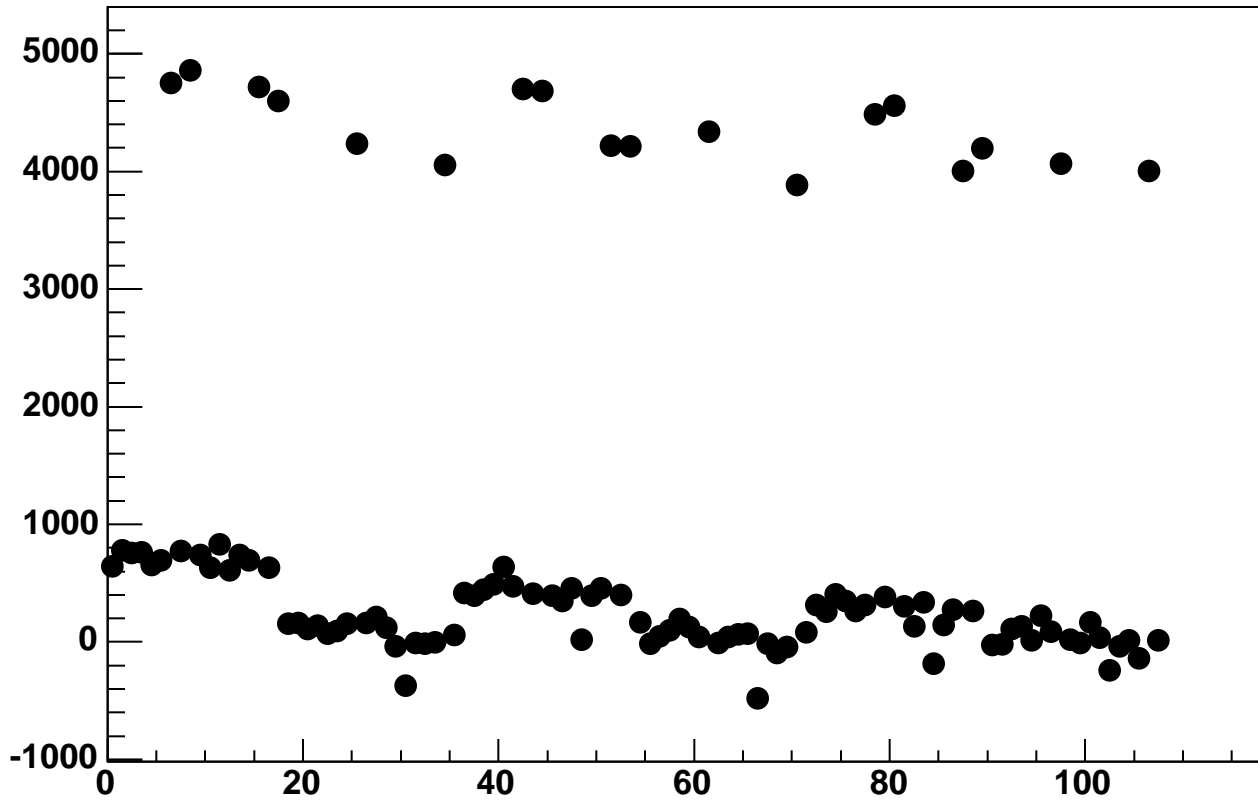
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



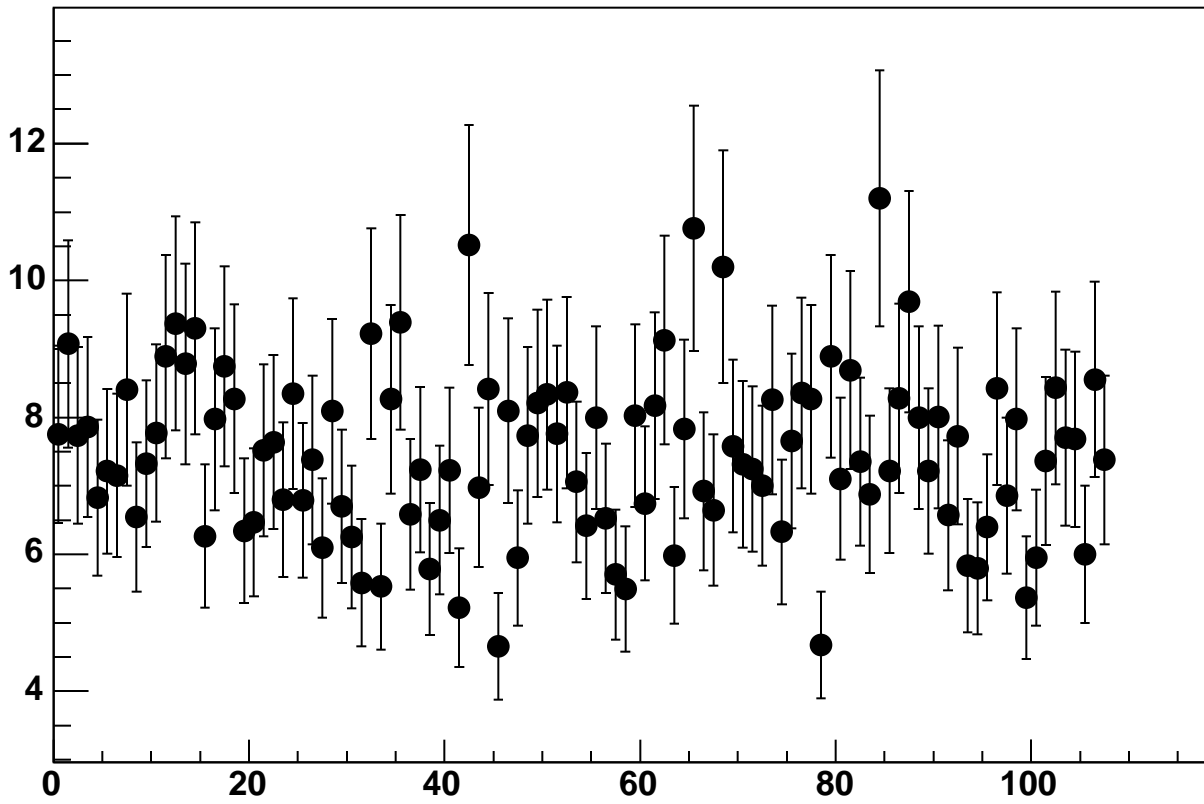
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



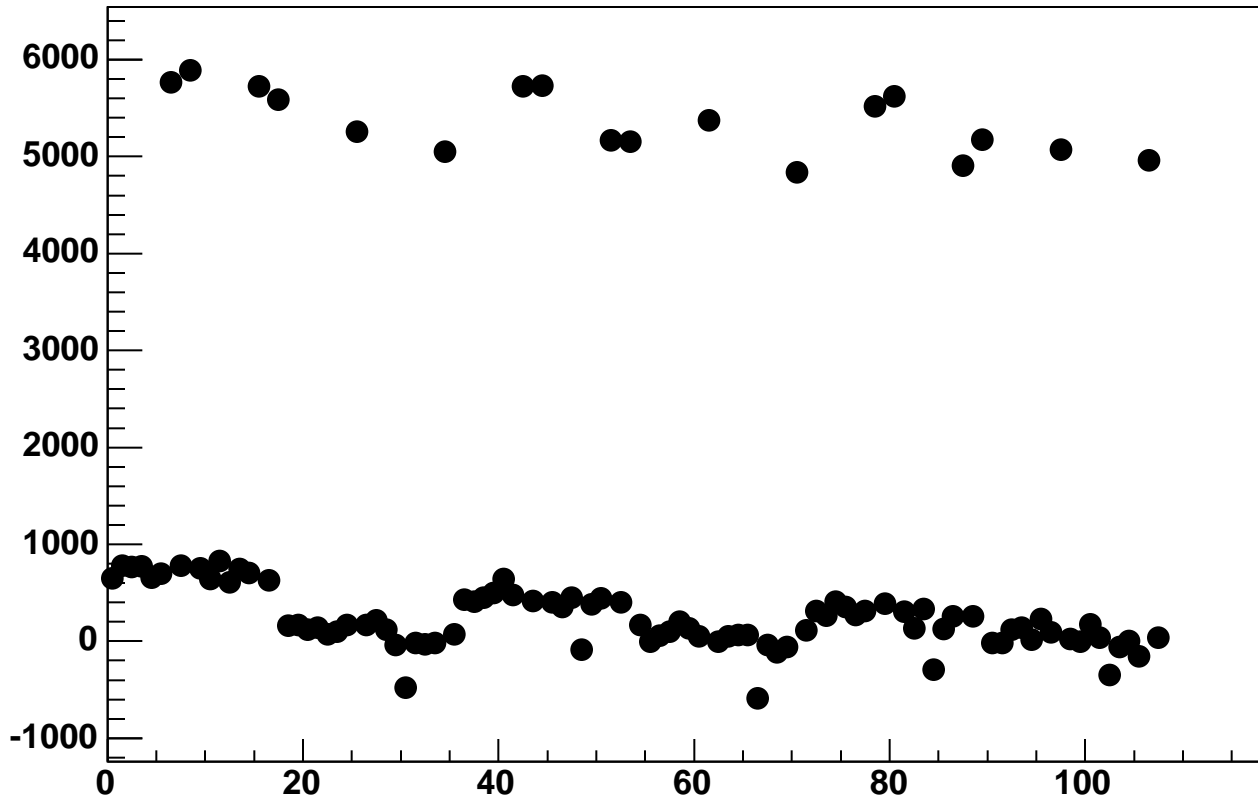
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



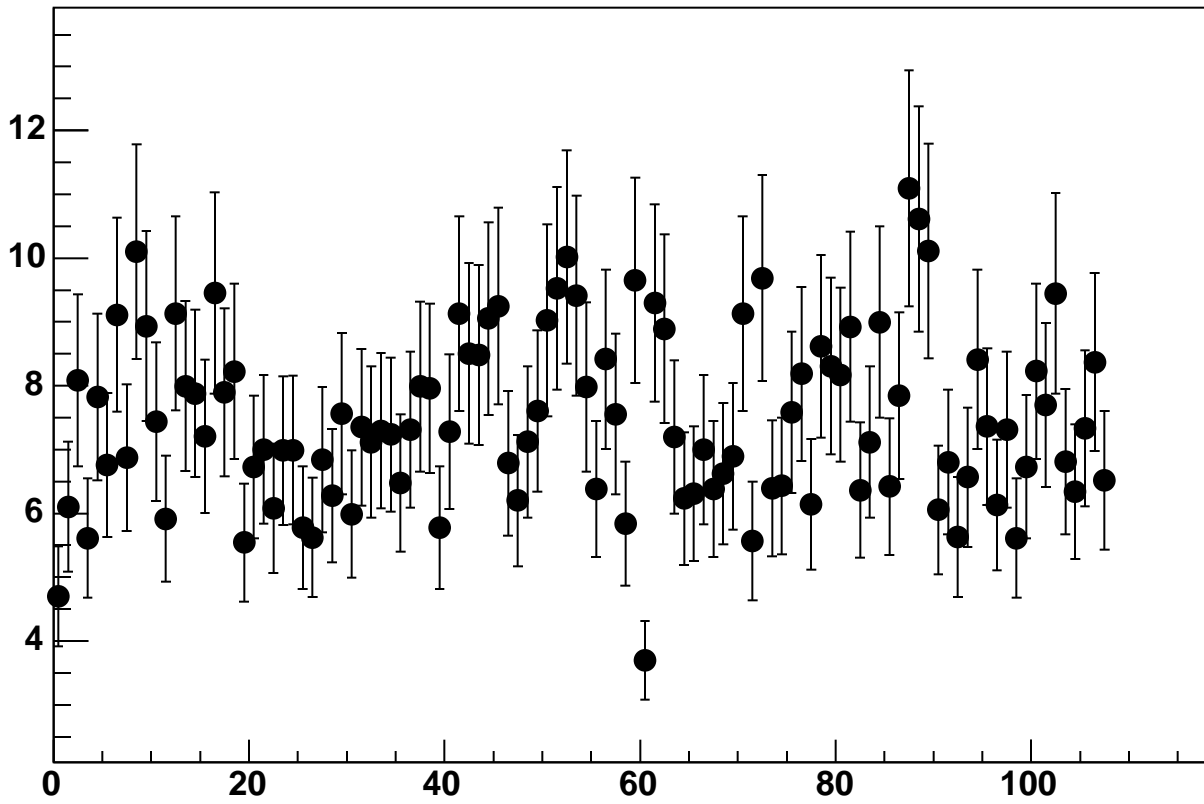
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



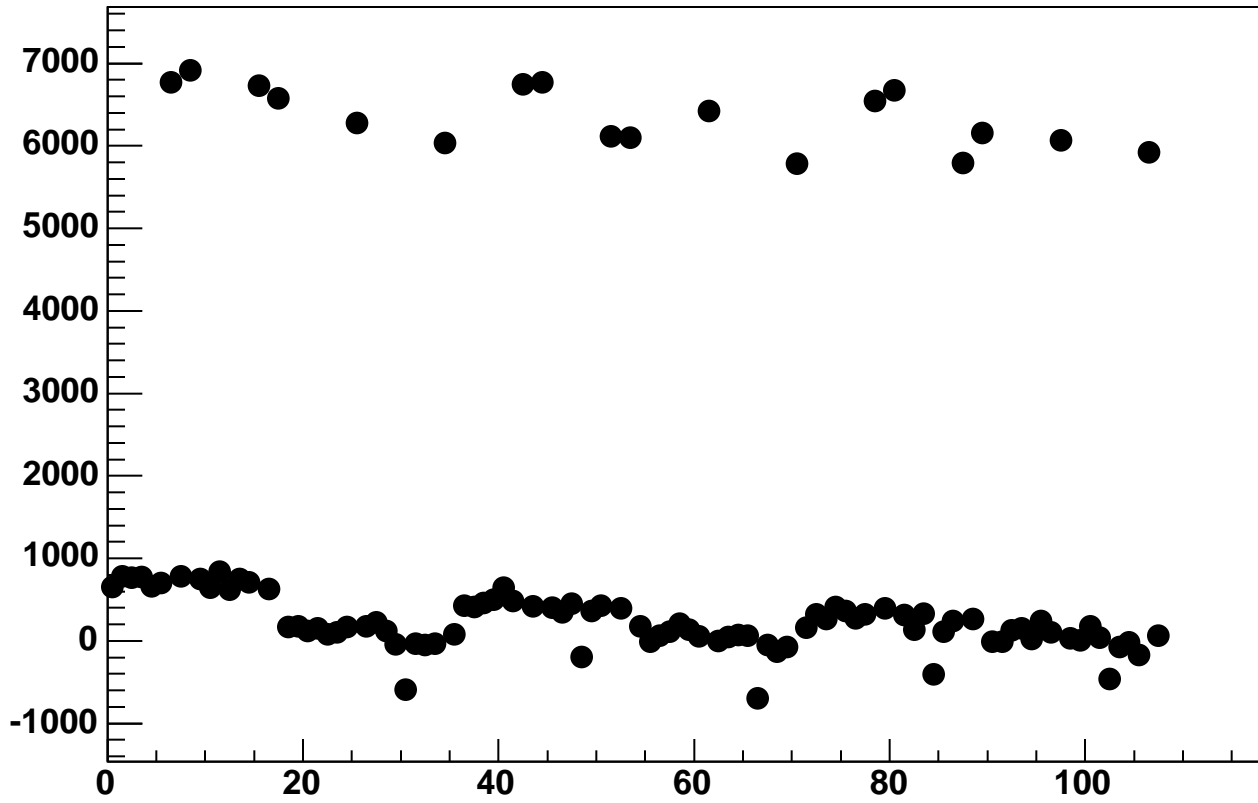
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



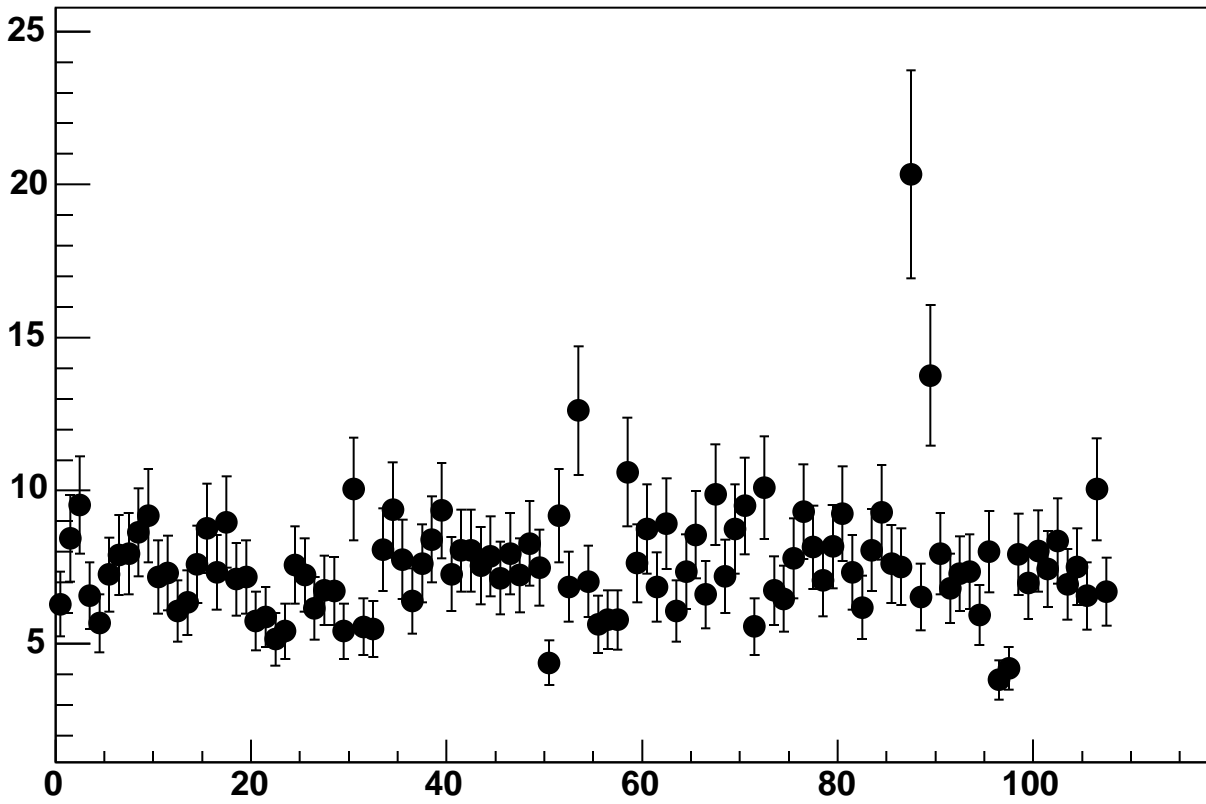
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



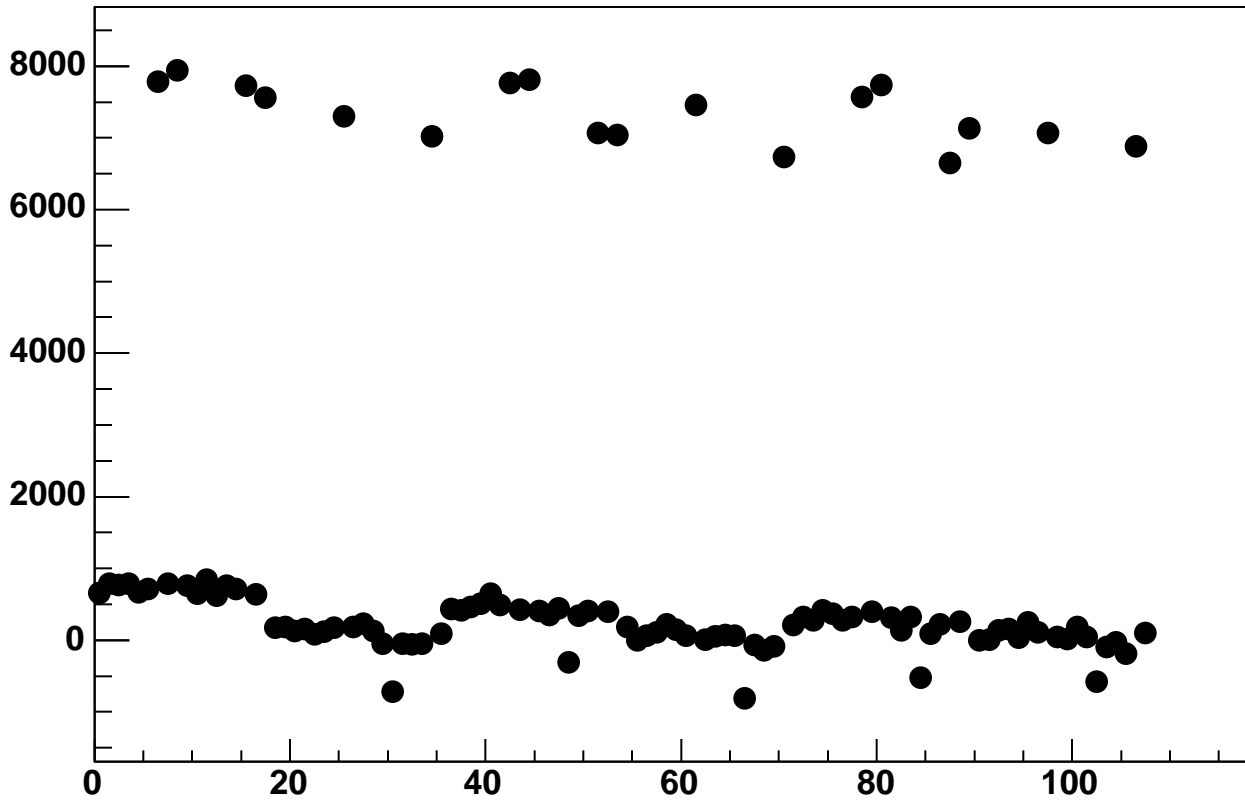
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



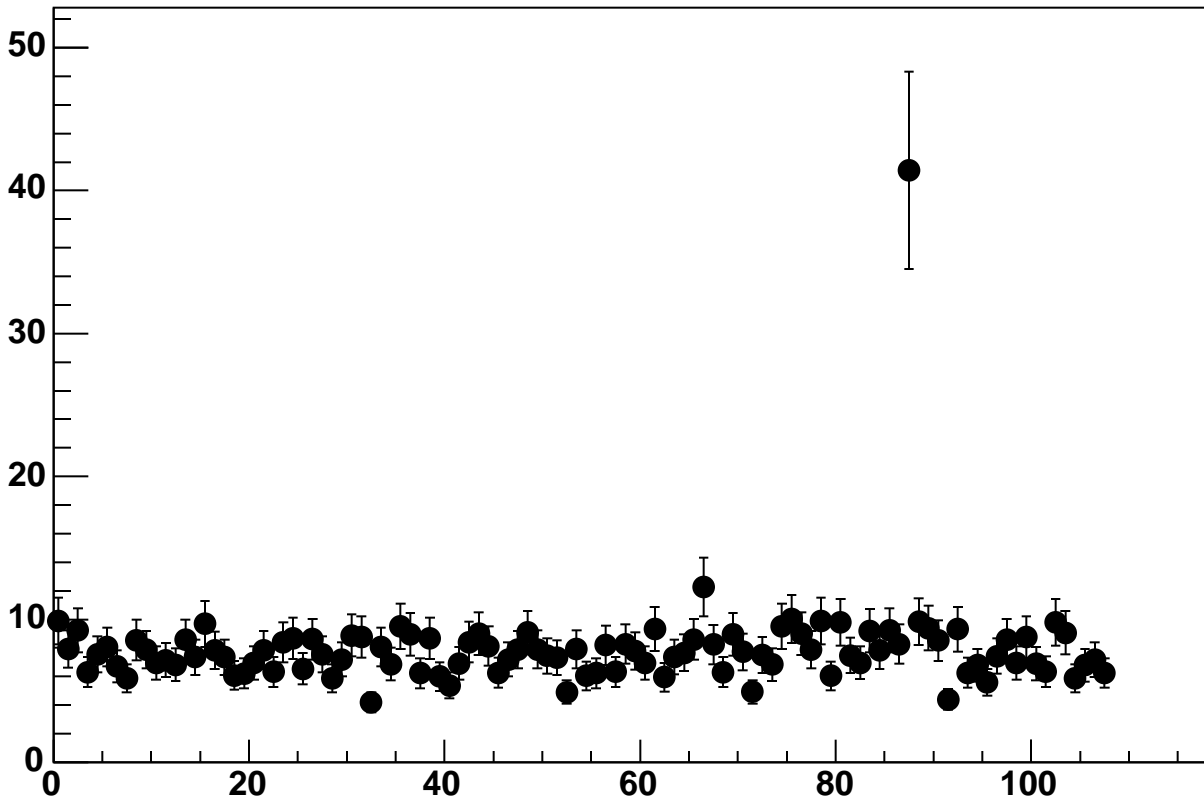
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



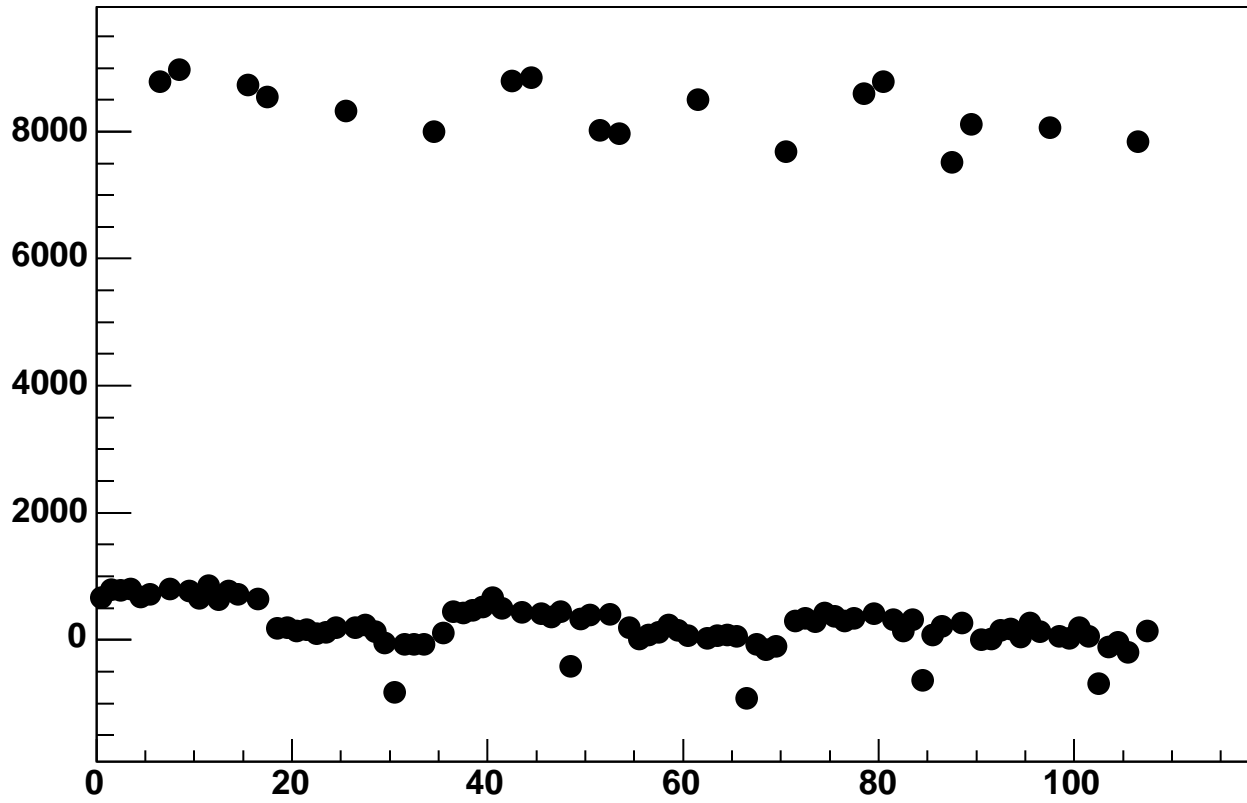
Enable 2, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



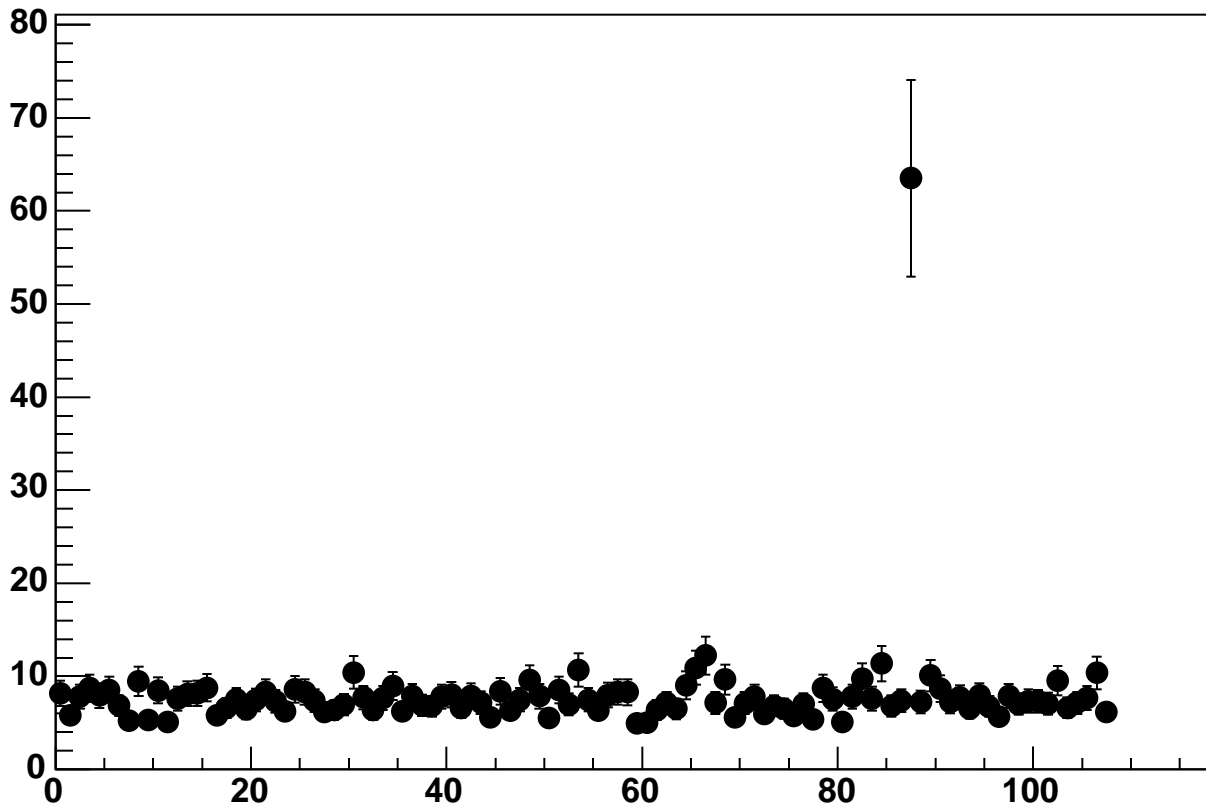
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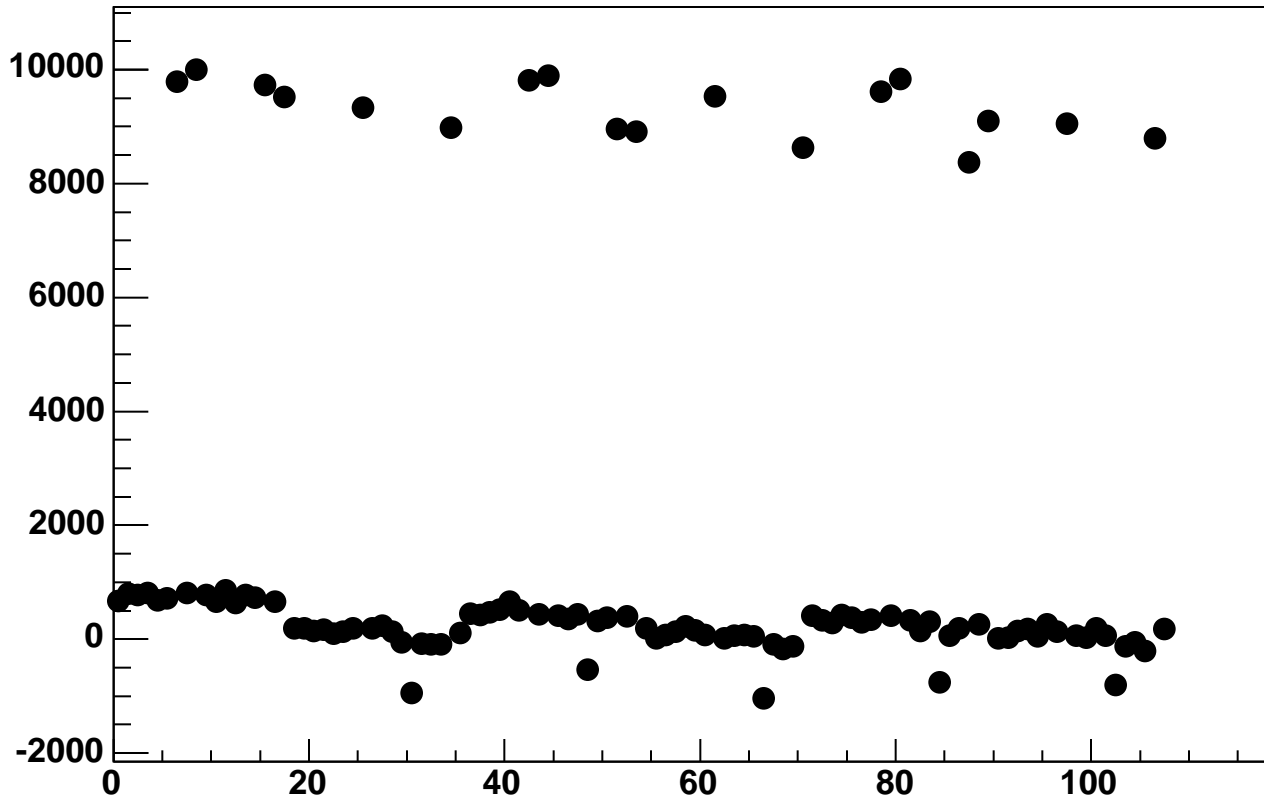
Enable 2, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



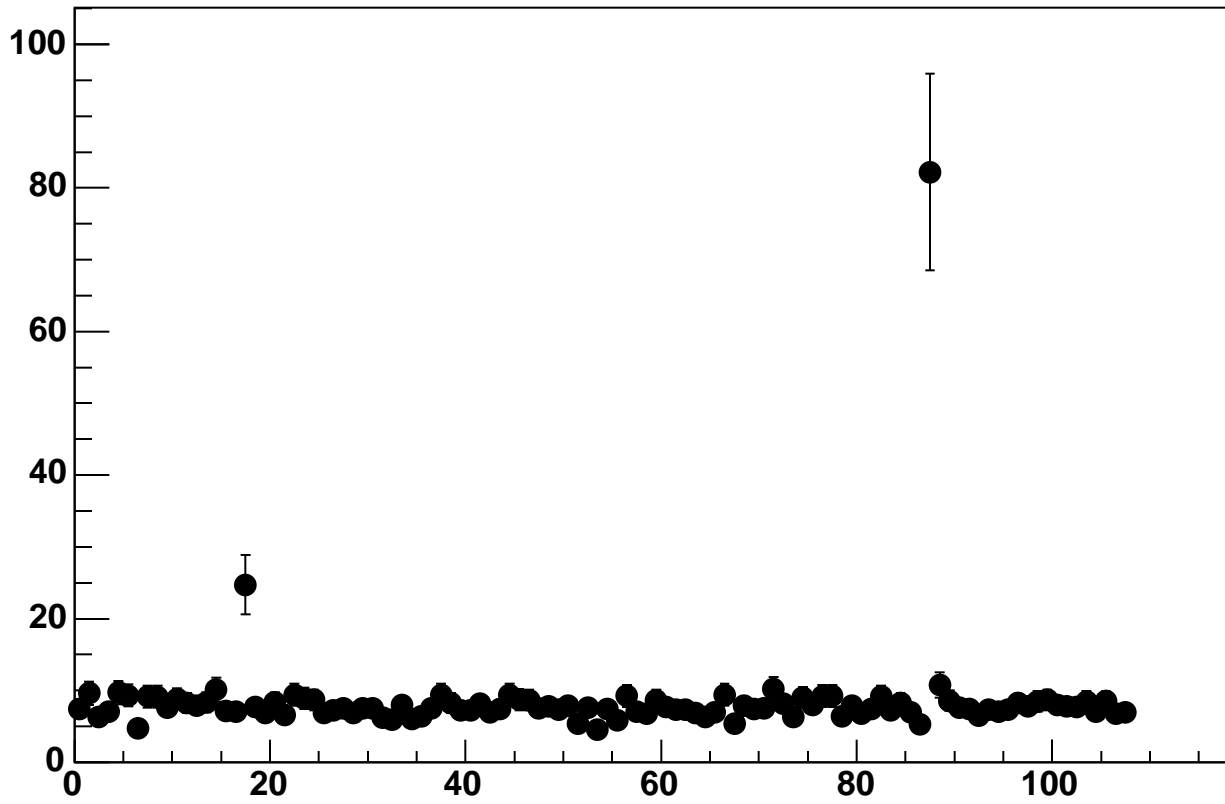
Enable 2, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



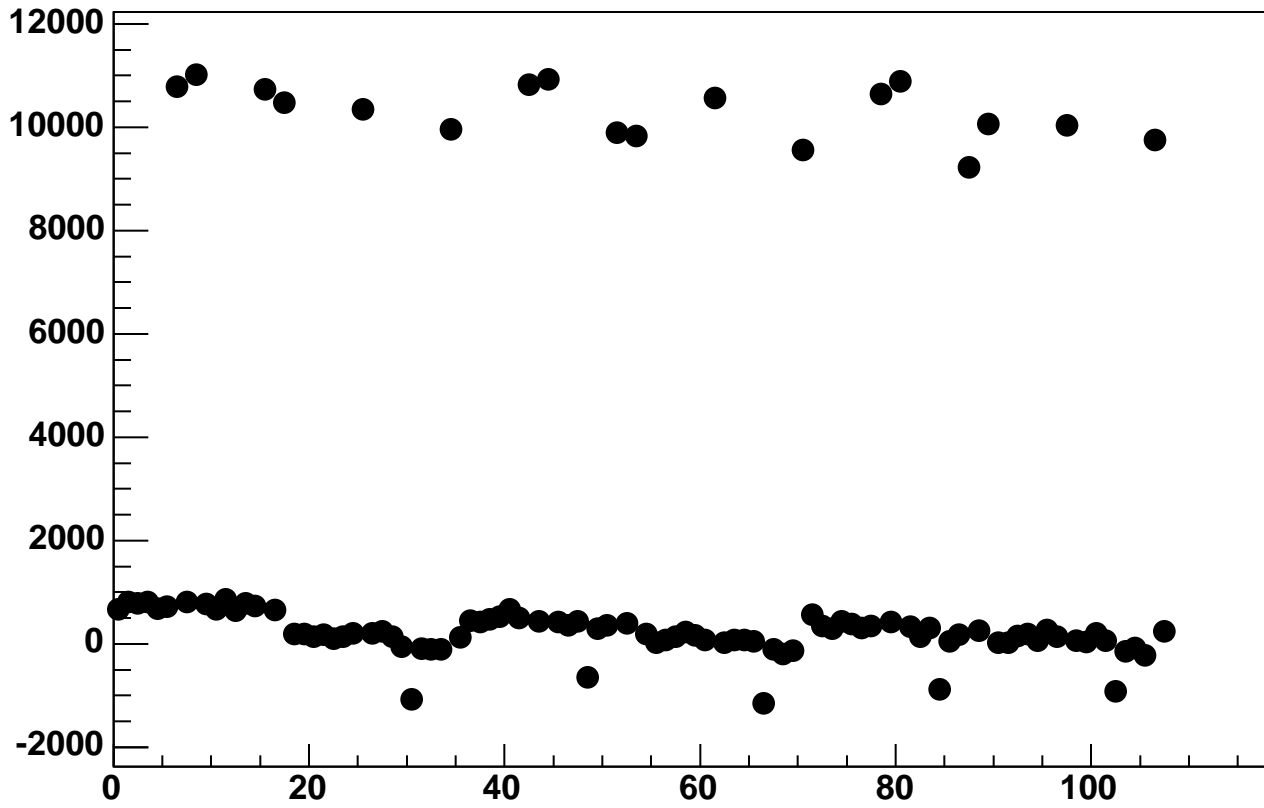
Enable 2, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



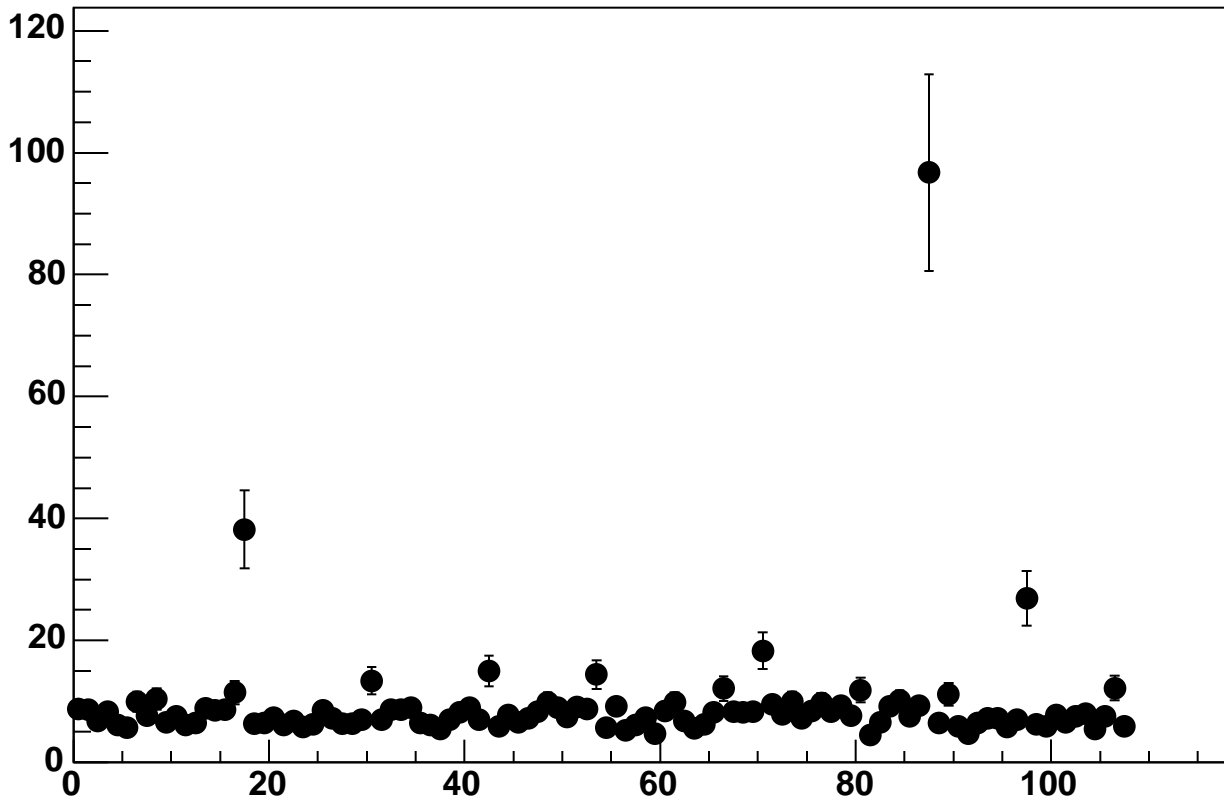
Enable 2, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



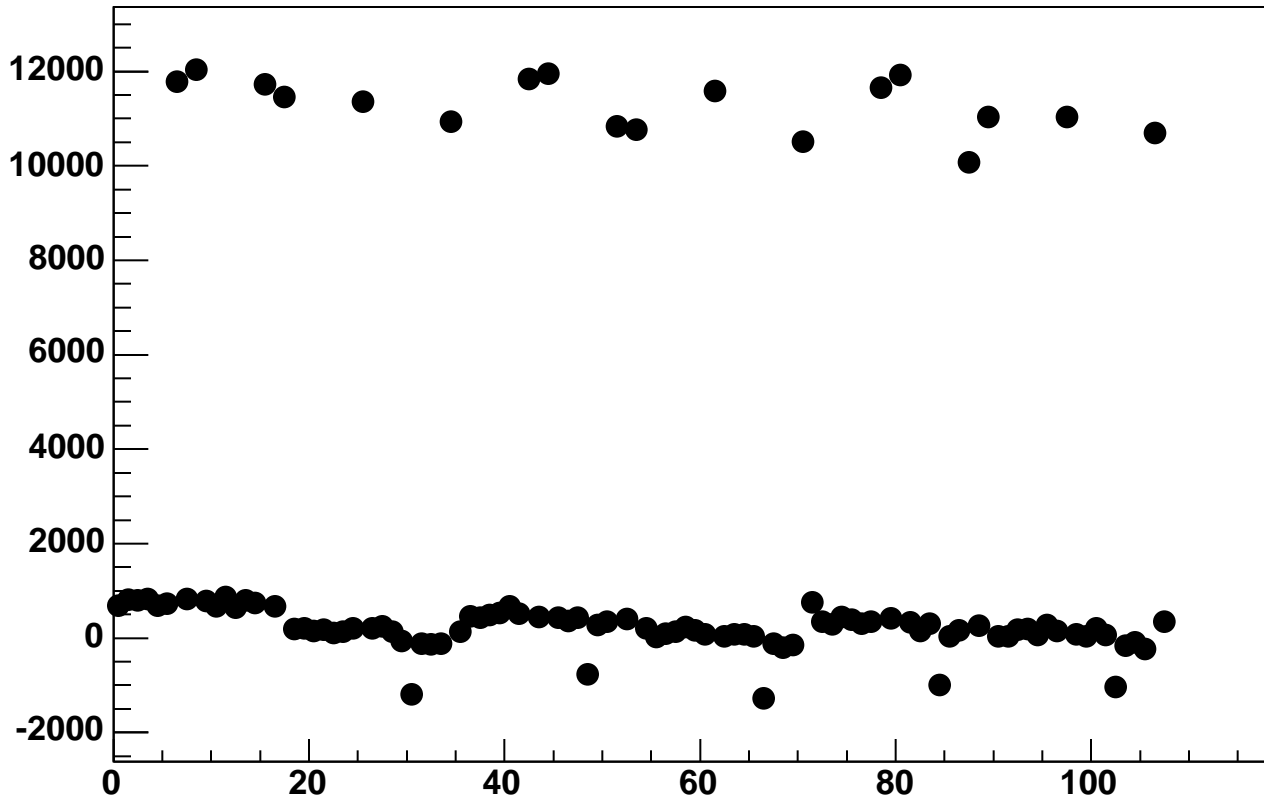
Enable 2, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



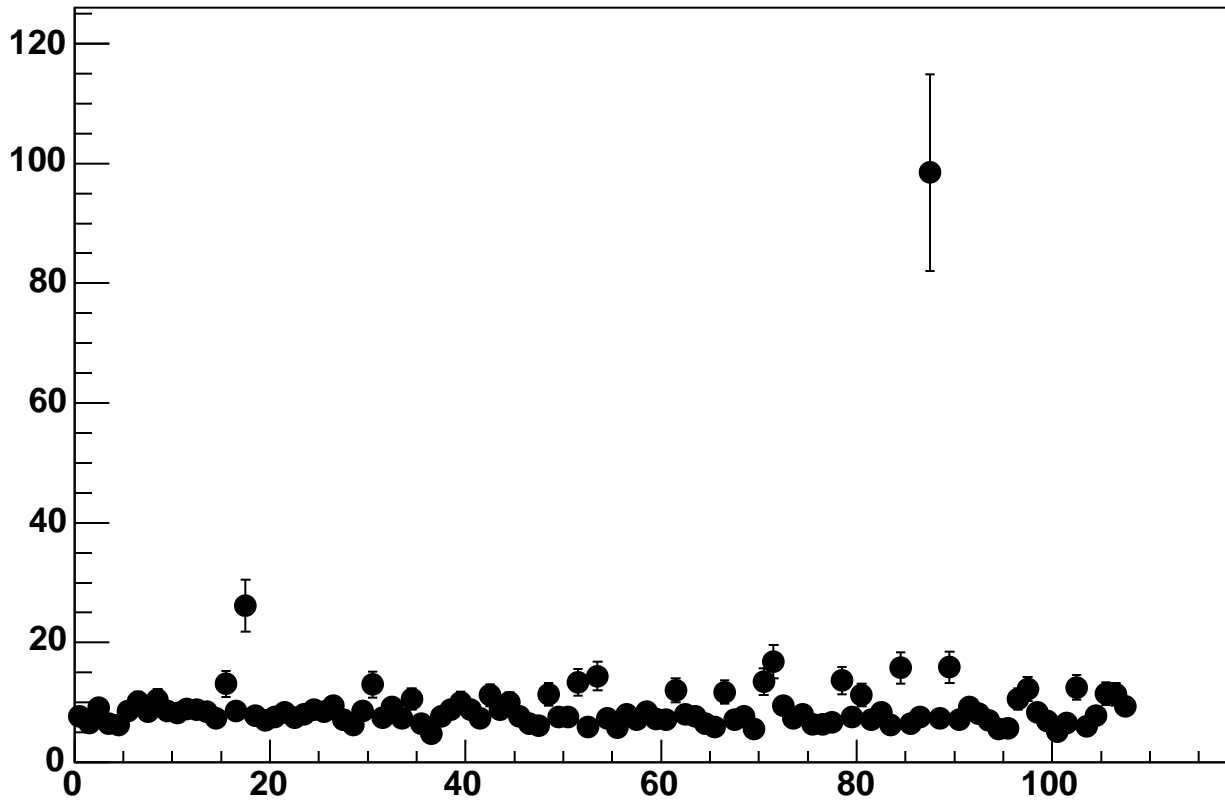
Enable 2, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



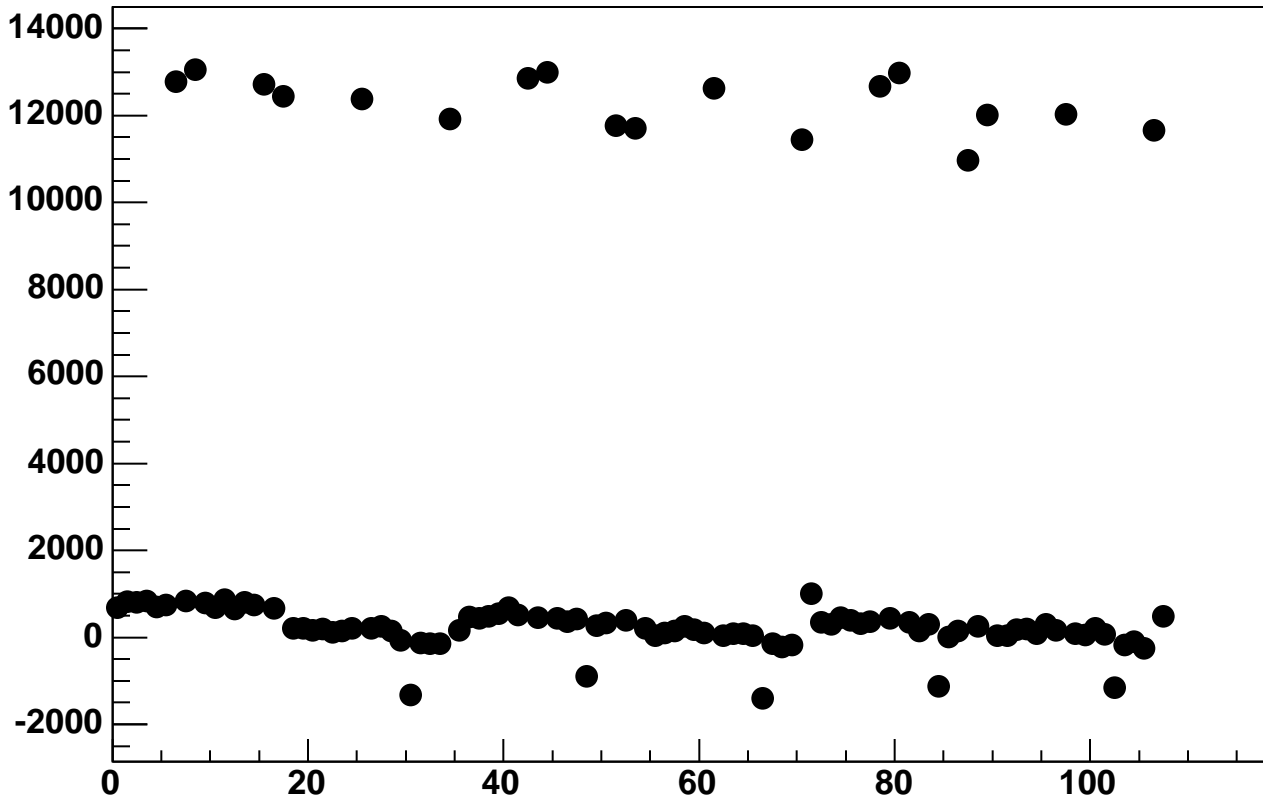
Enable 2, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



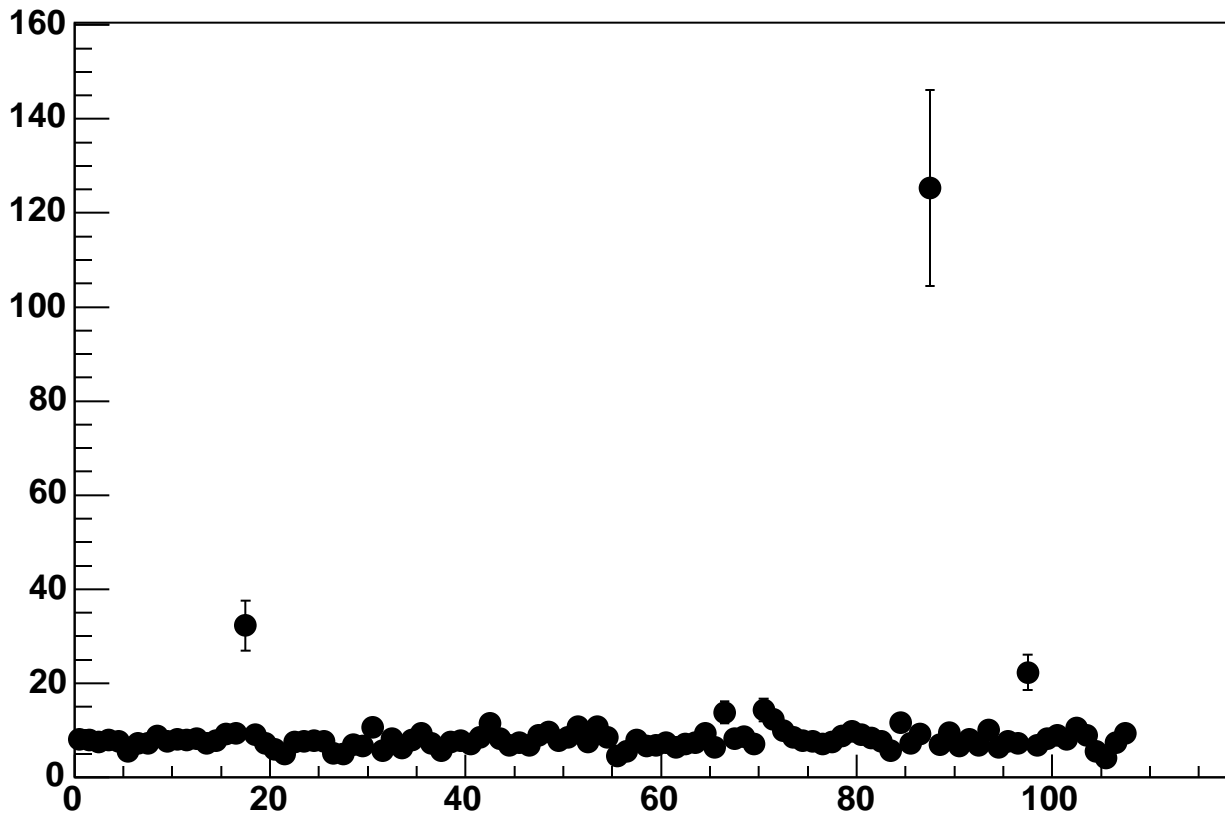
Enable 2, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



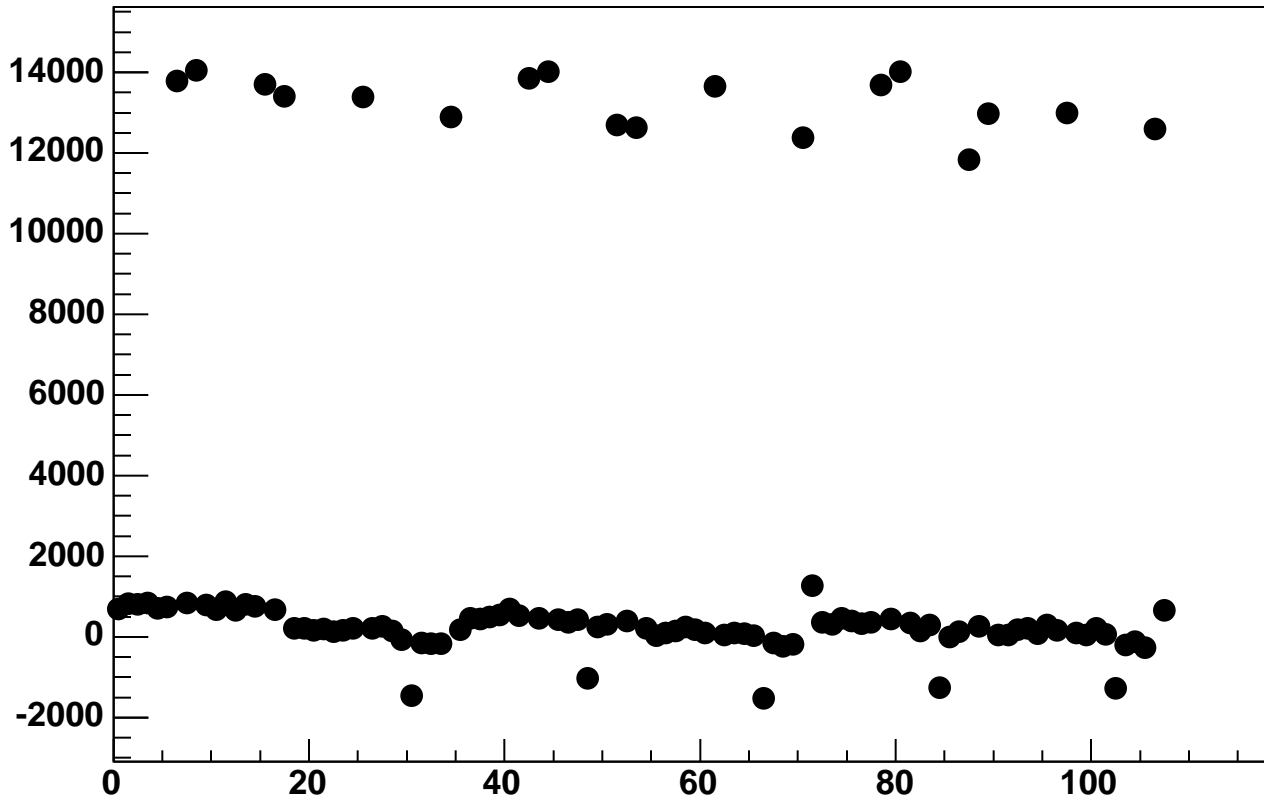
Enable 2, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



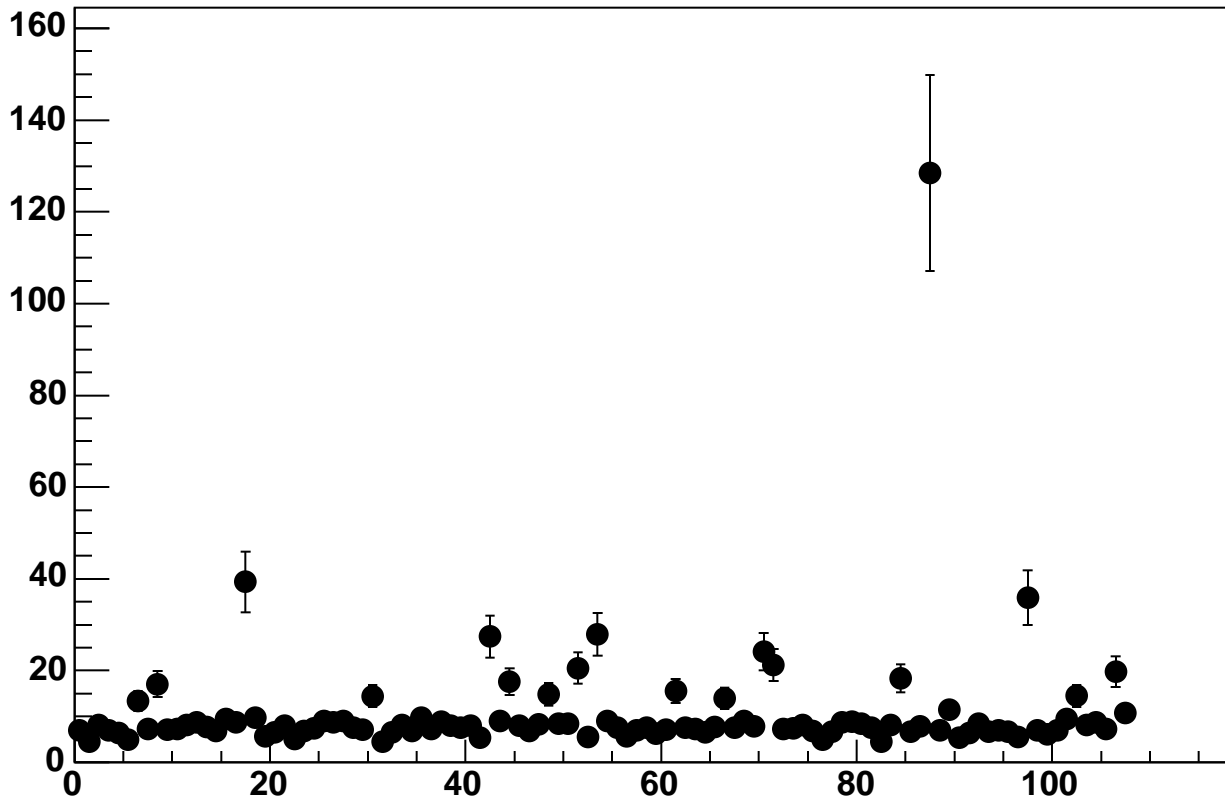
Enable 2, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



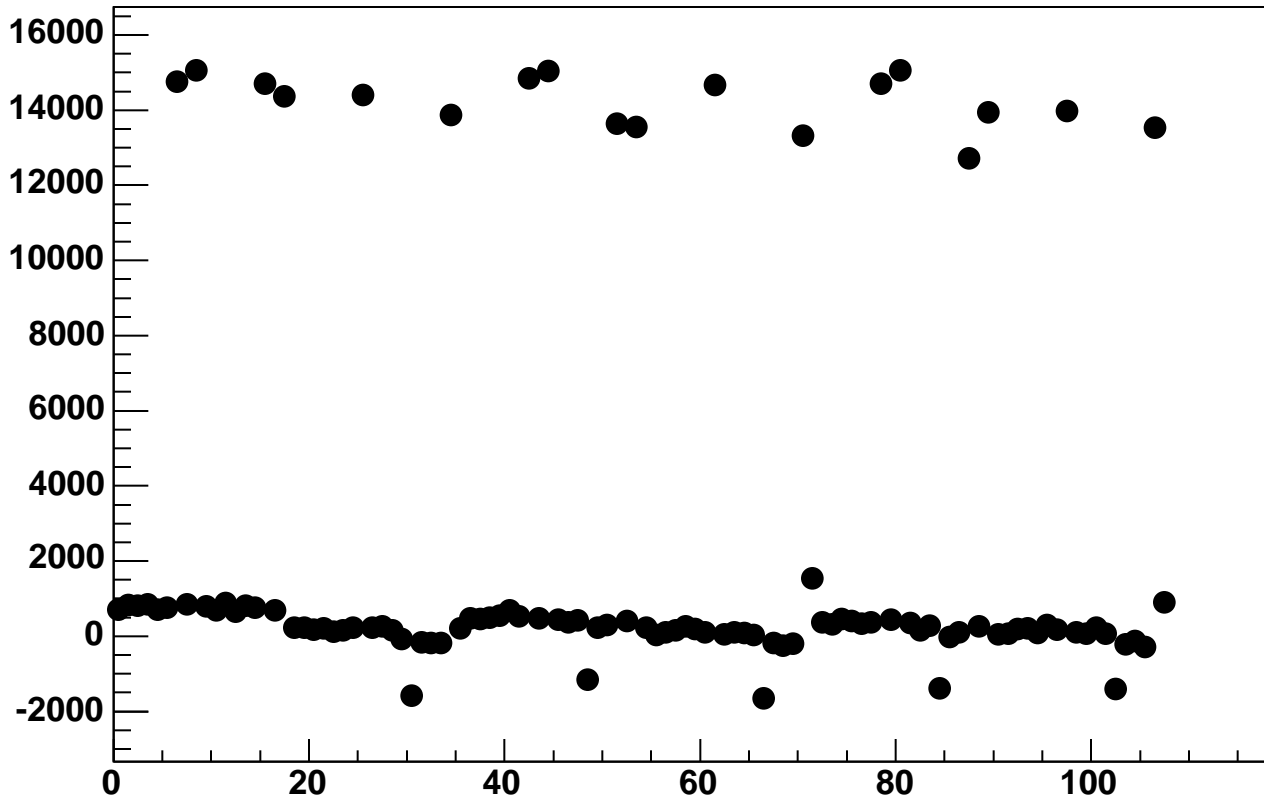
Enable 2, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



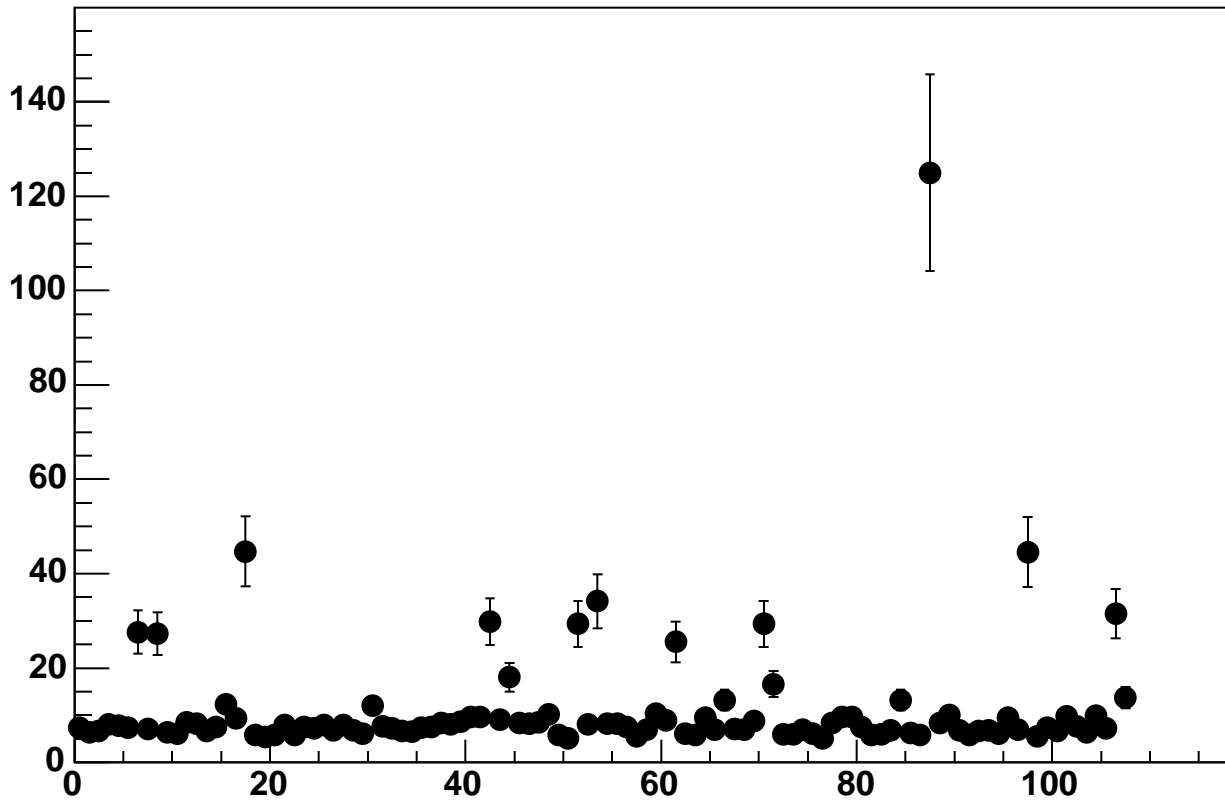
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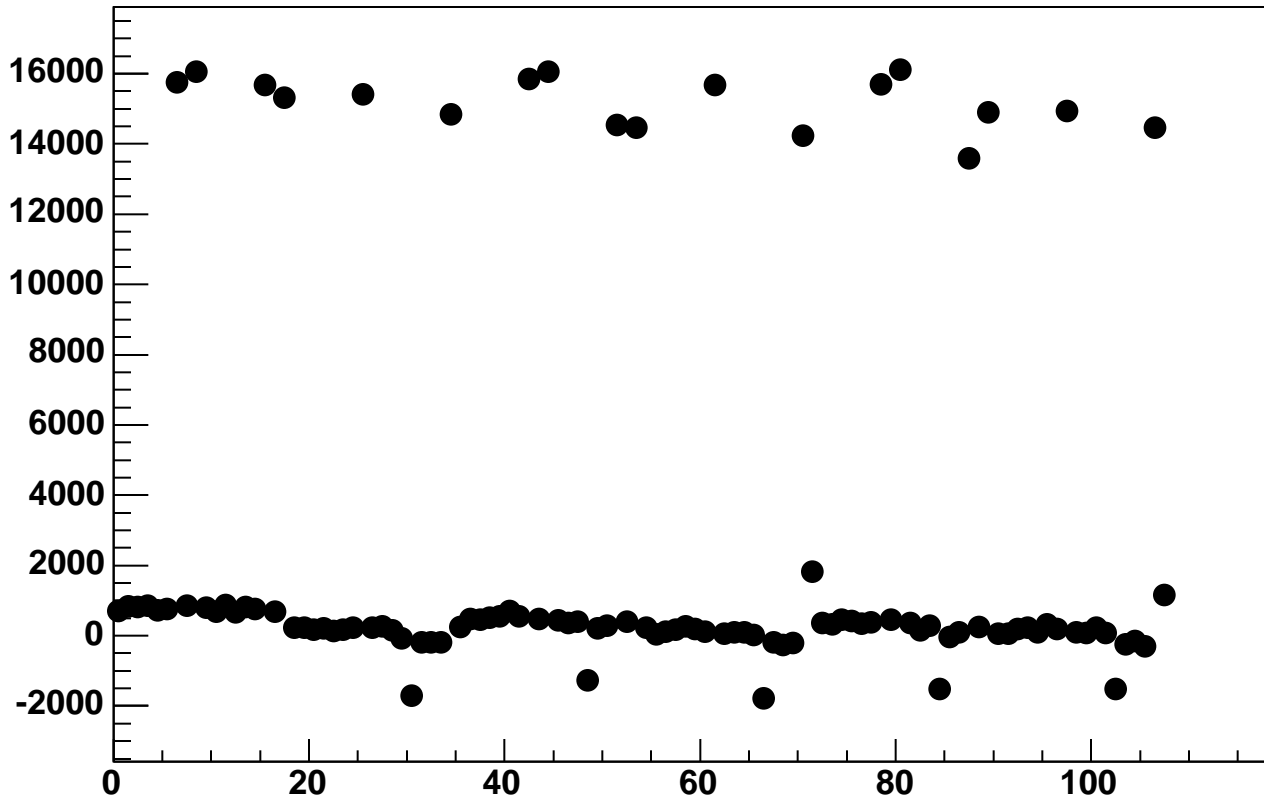
Enable 2, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



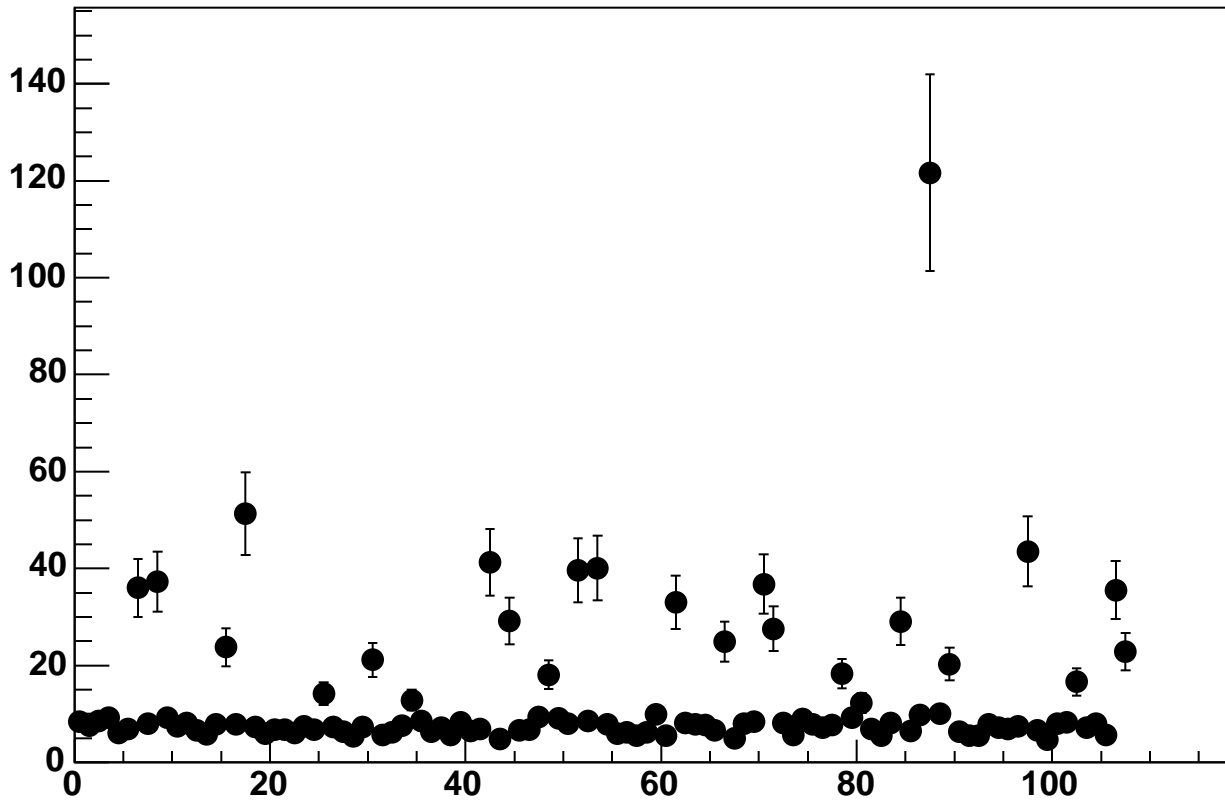
Enable 2, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



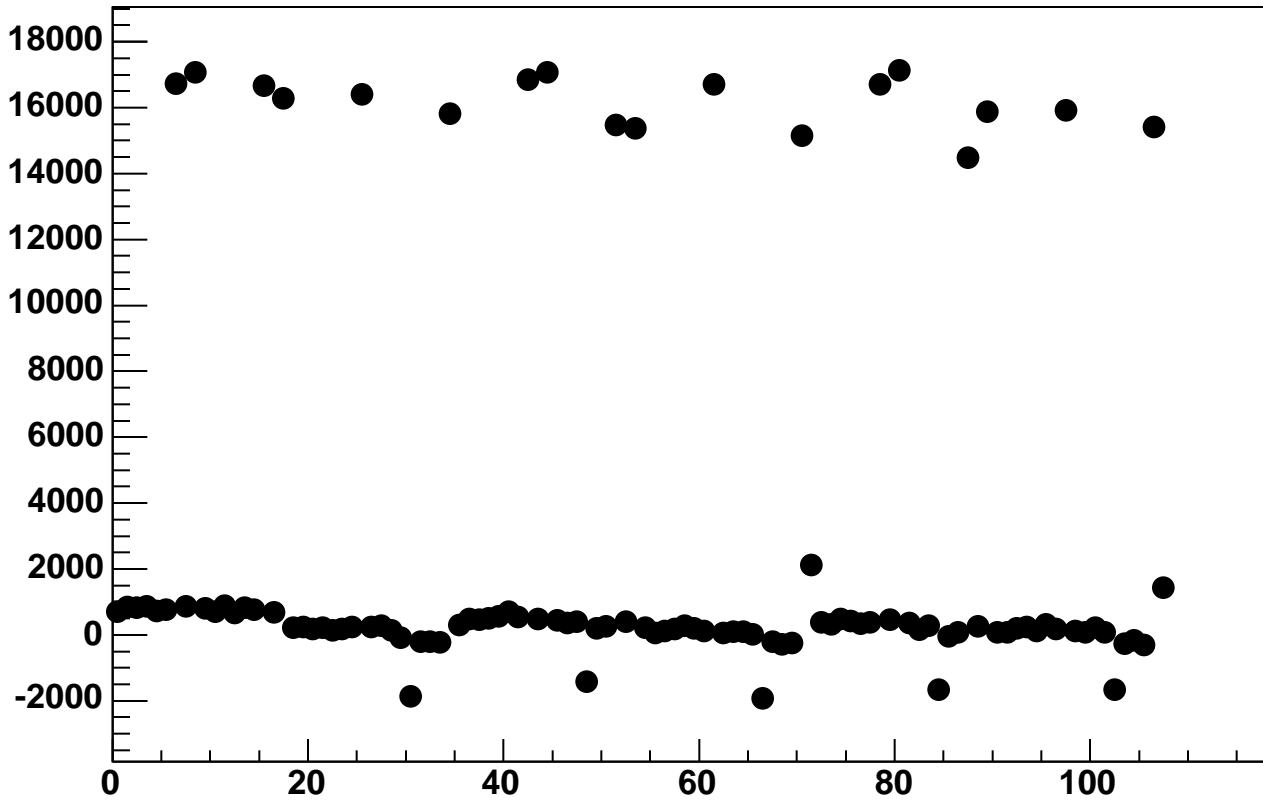
Enable 2, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



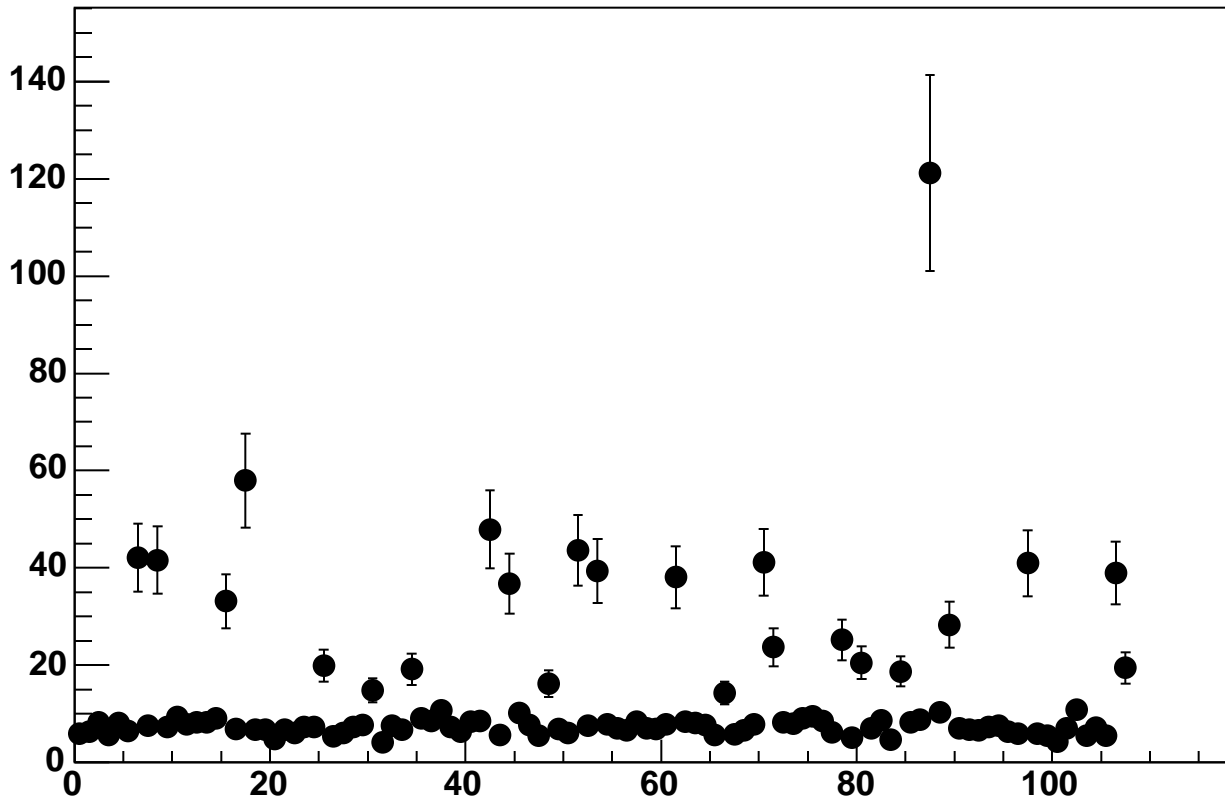
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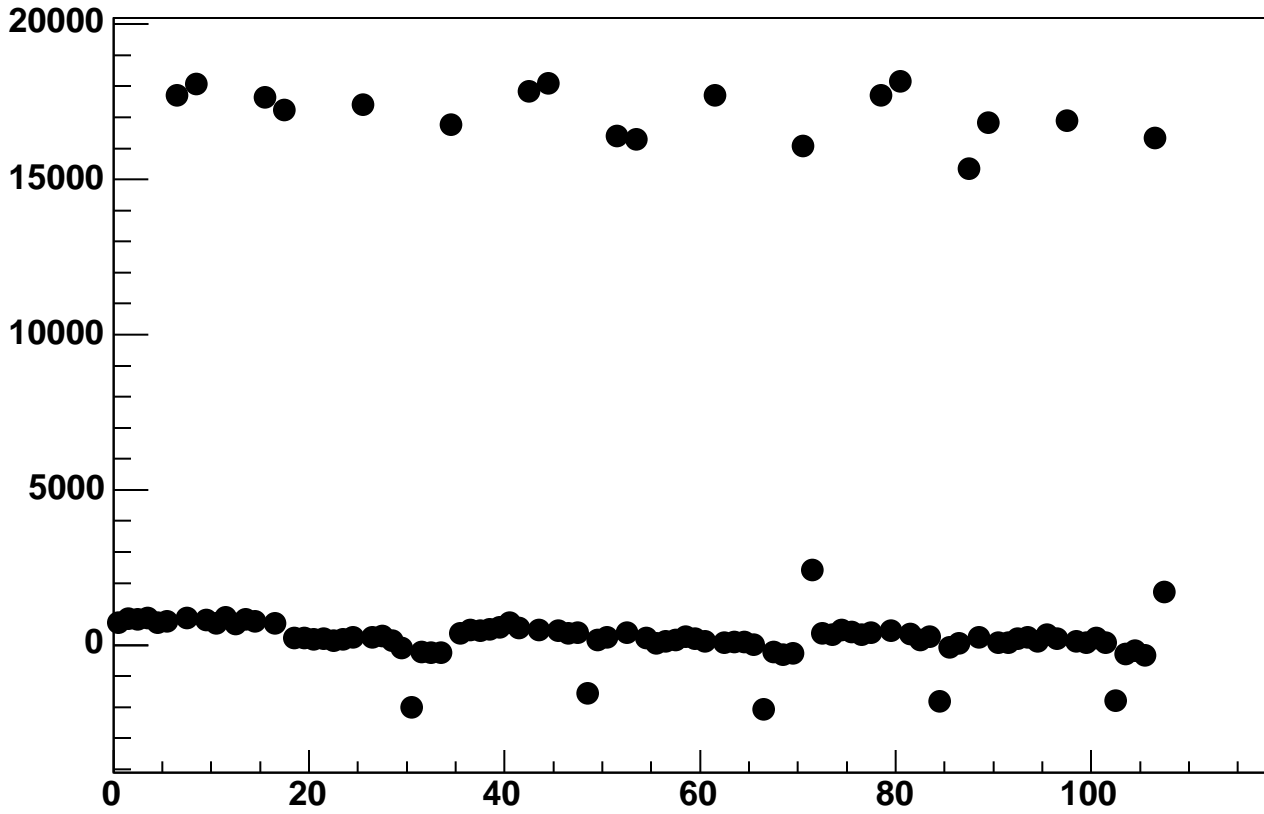
Enable 2, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



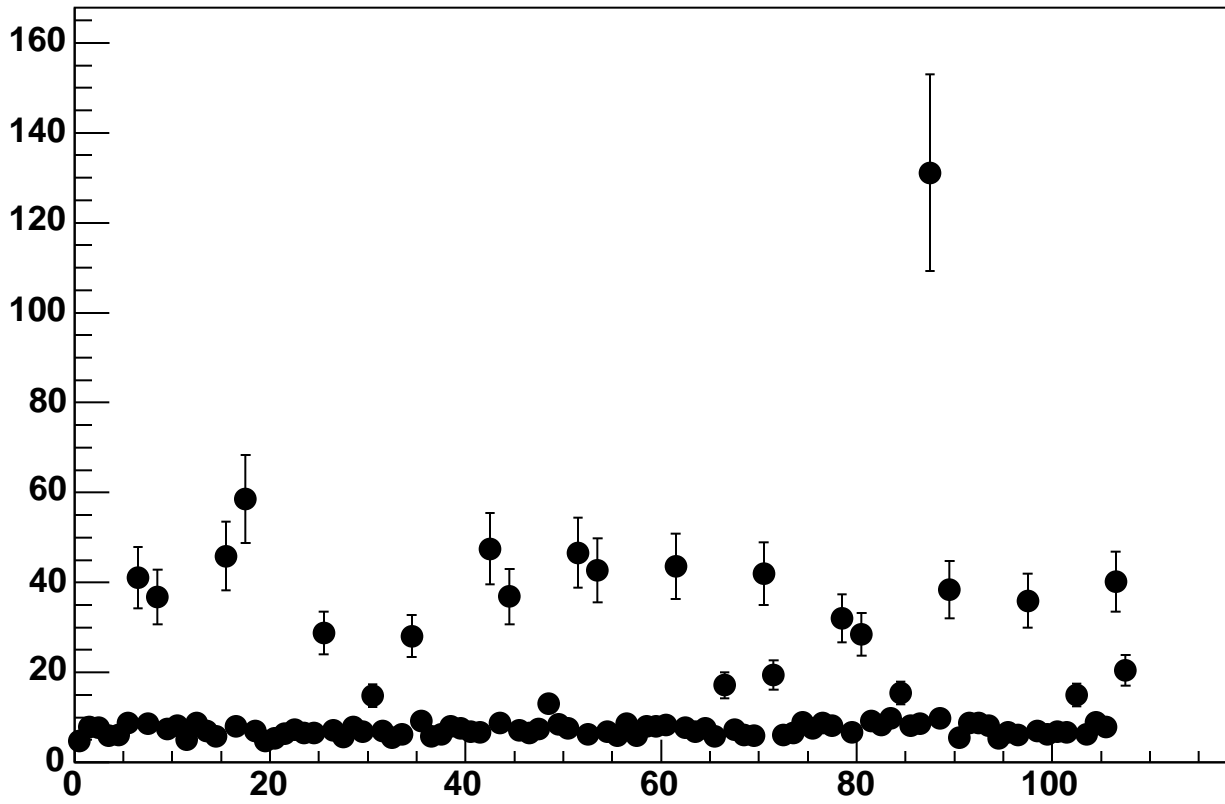
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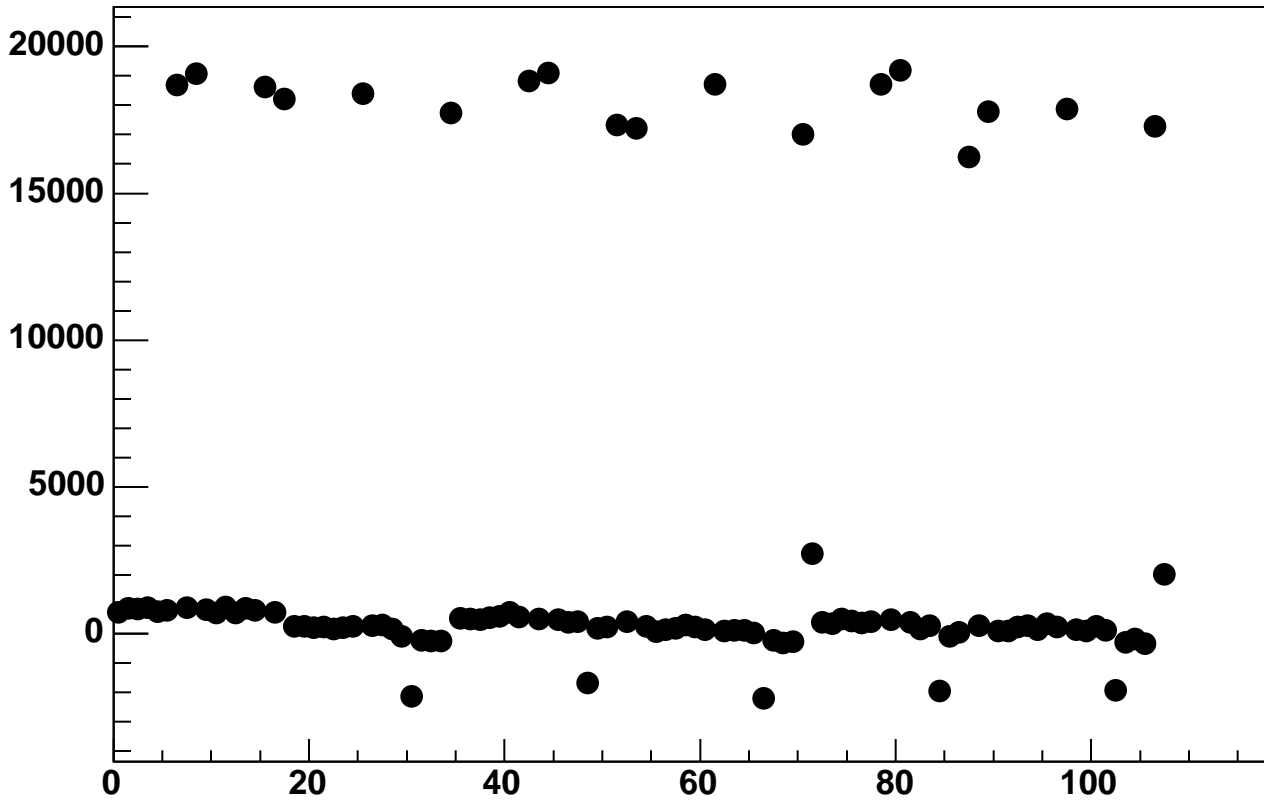
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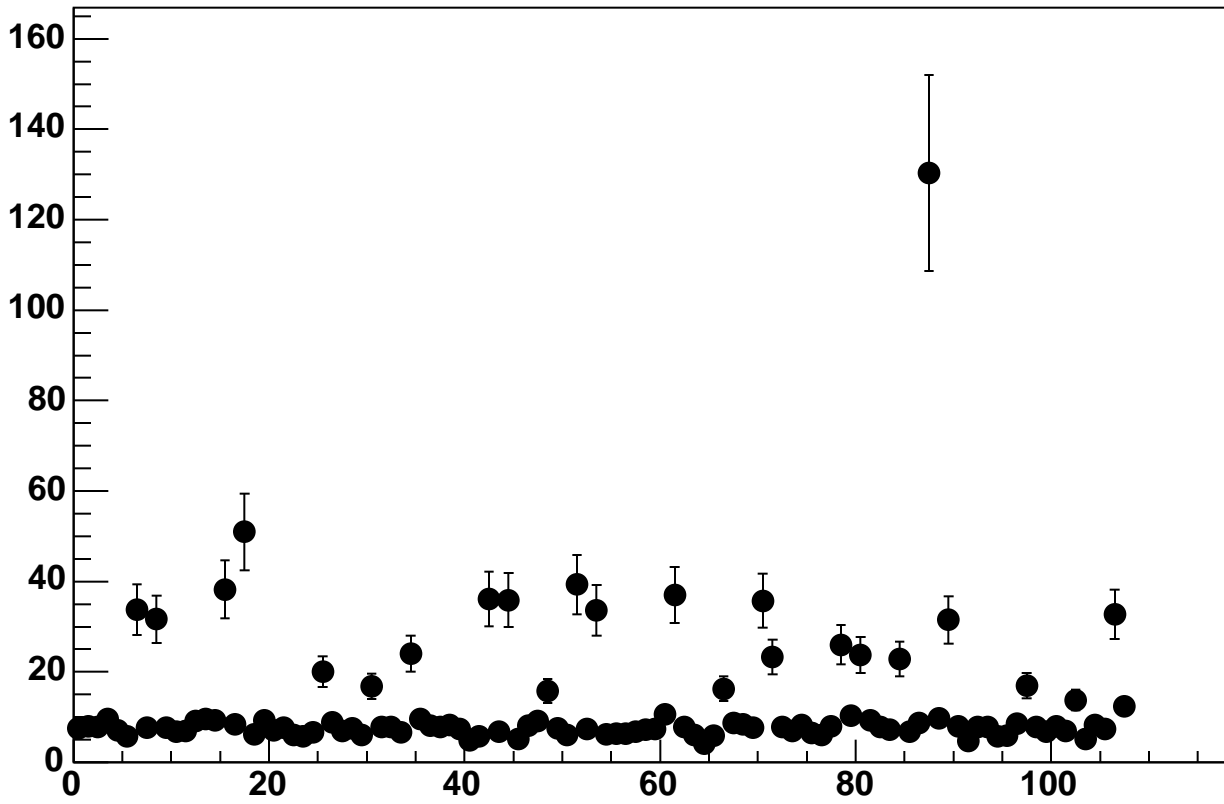
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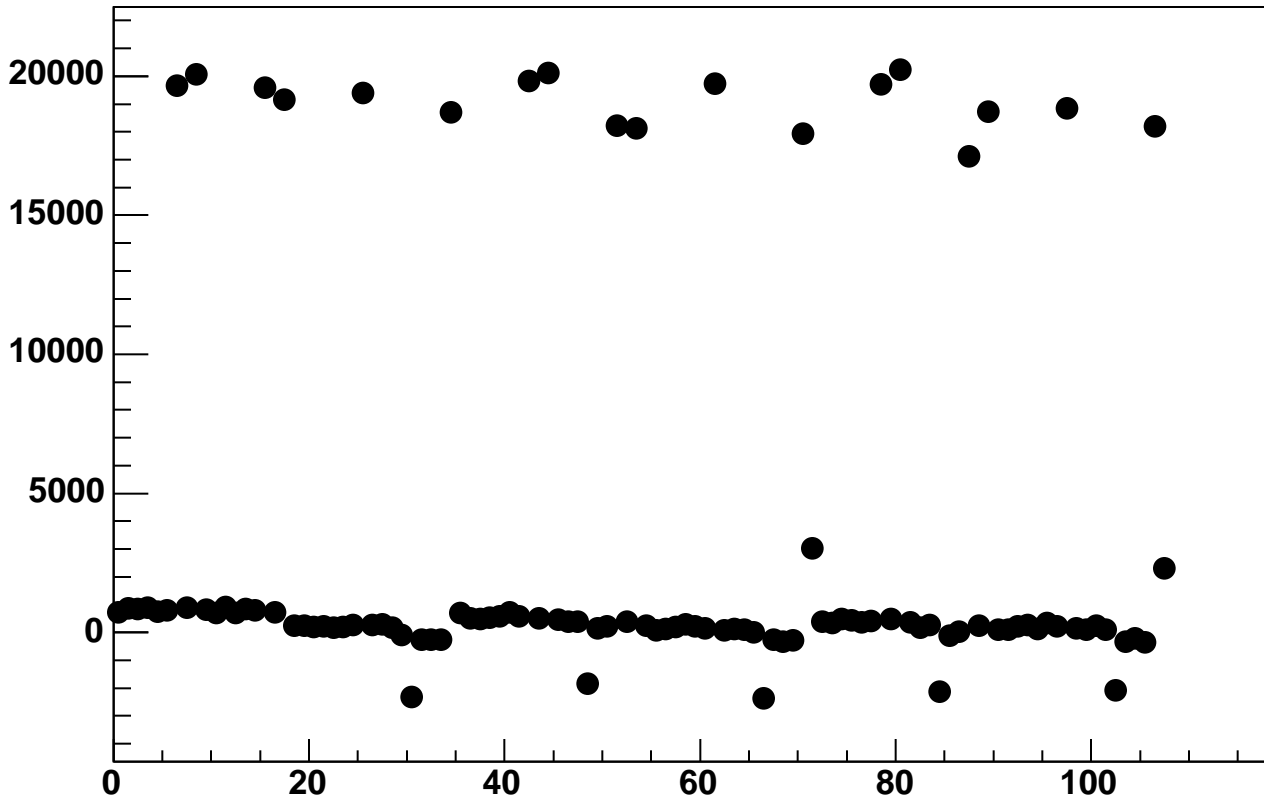
Enable 2, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



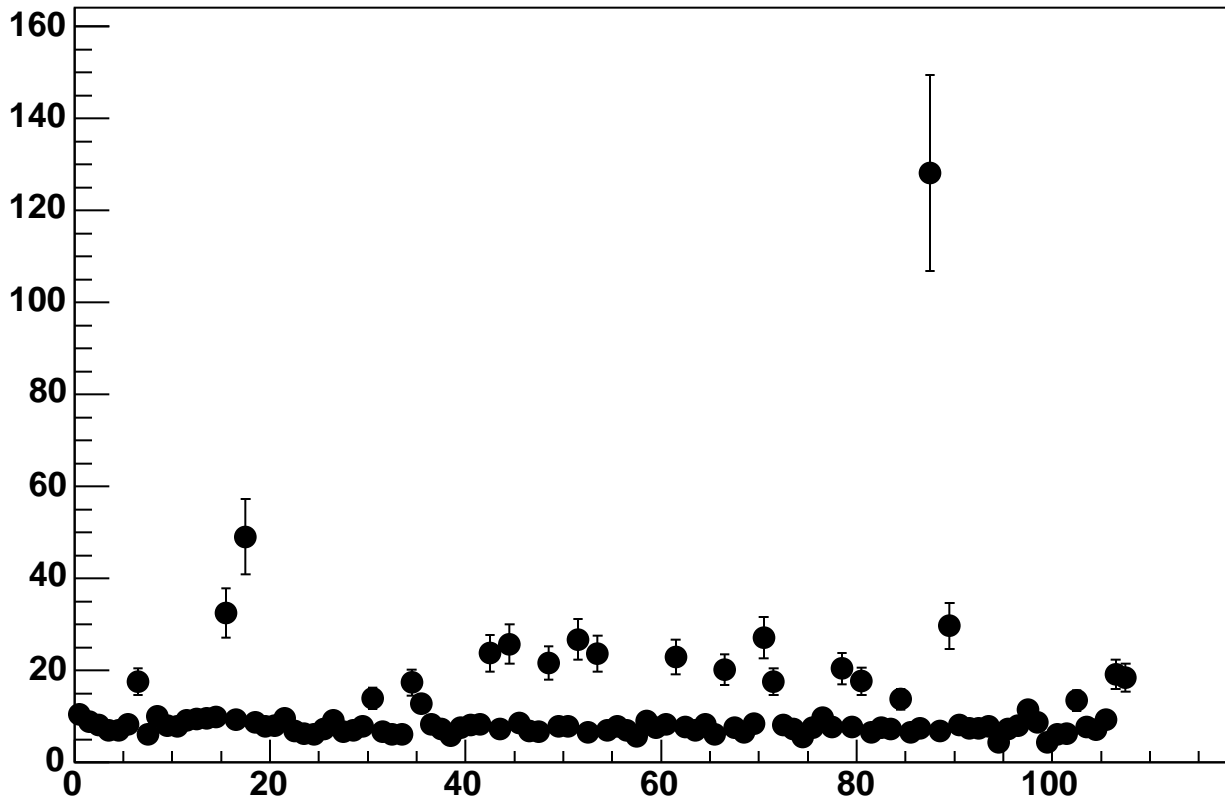
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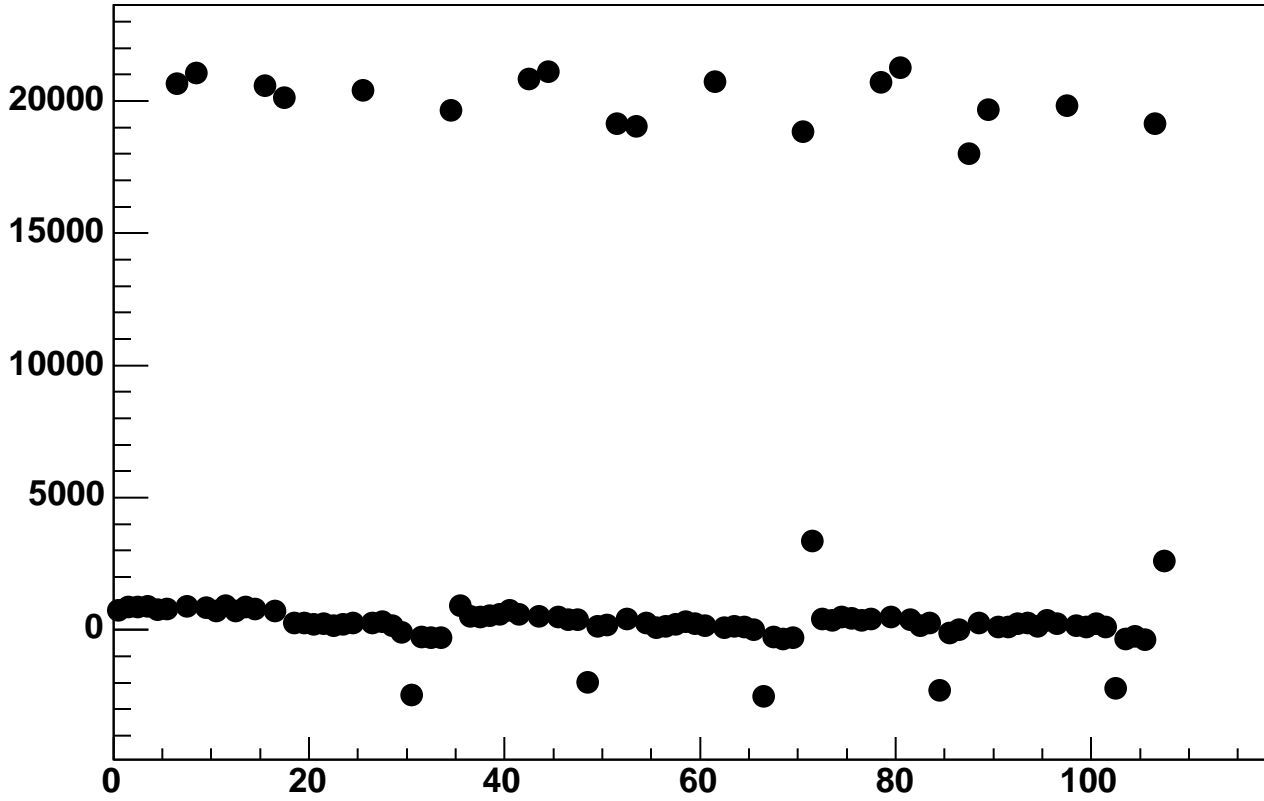
Enable 2, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



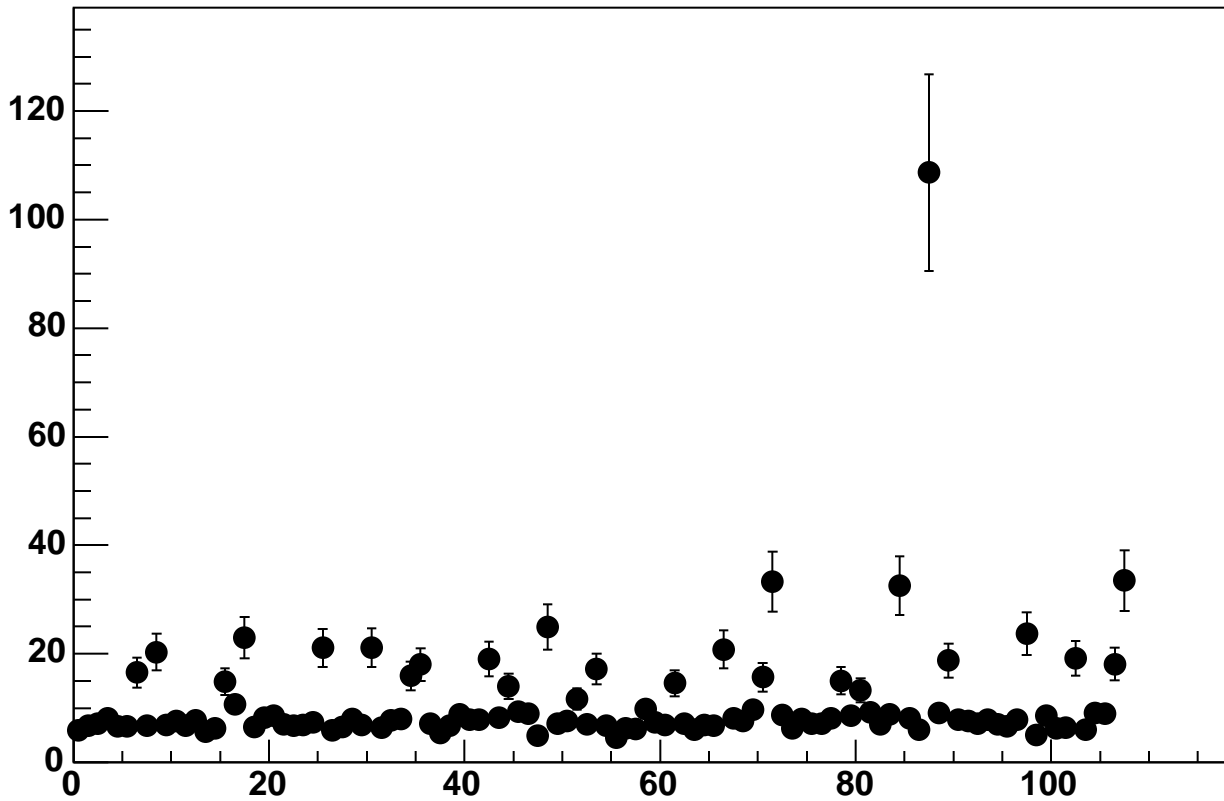
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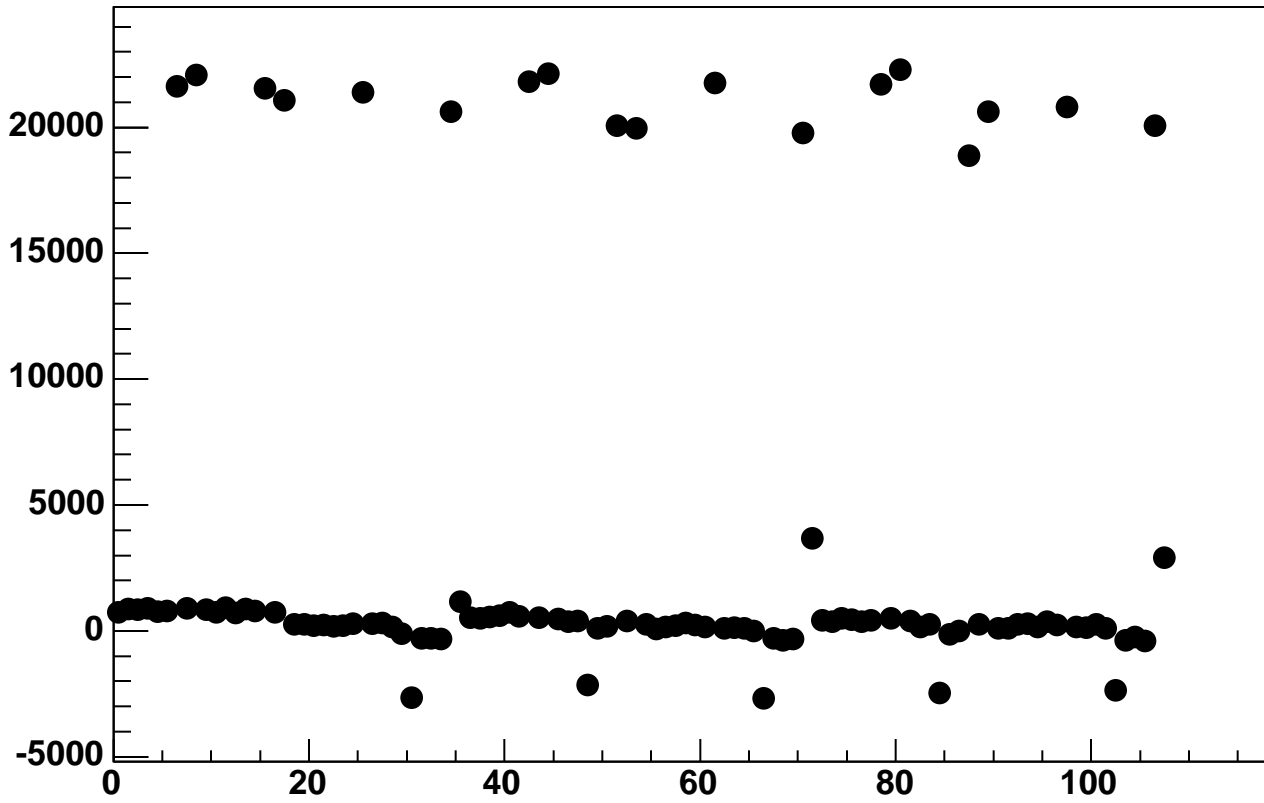
Enable 2, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



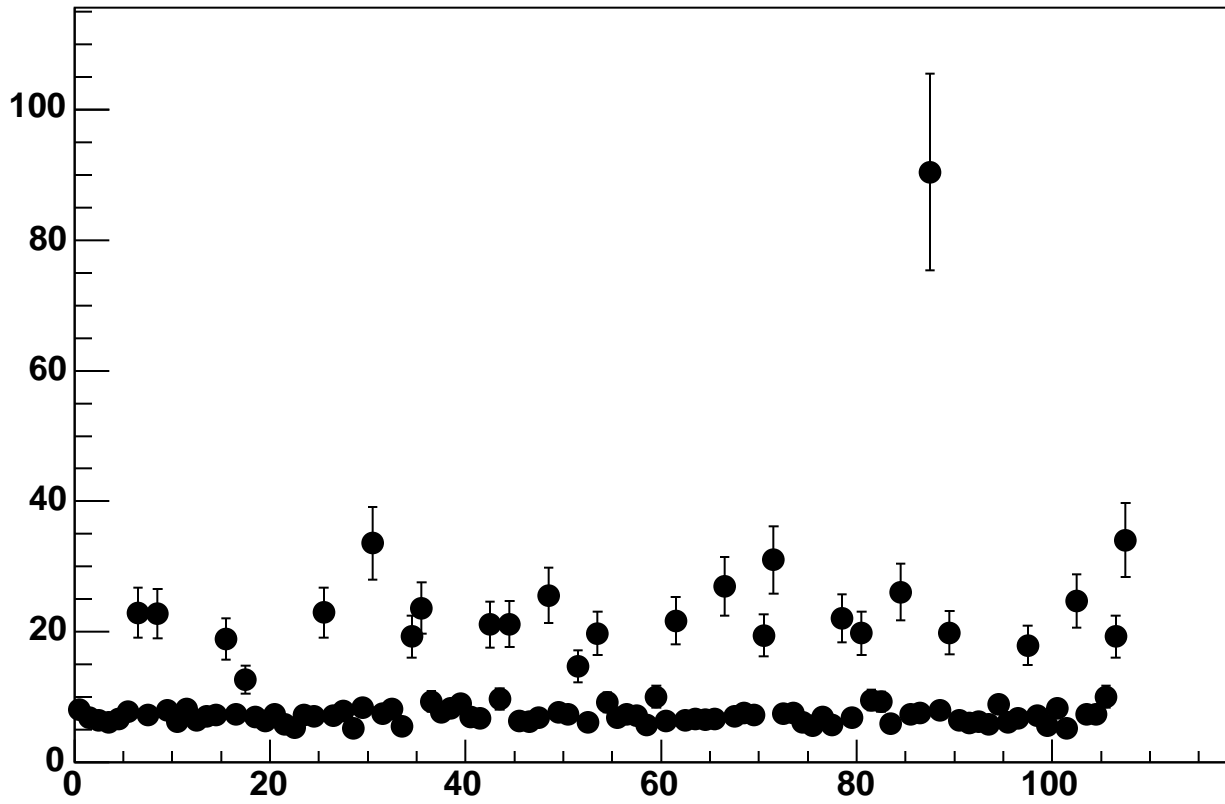
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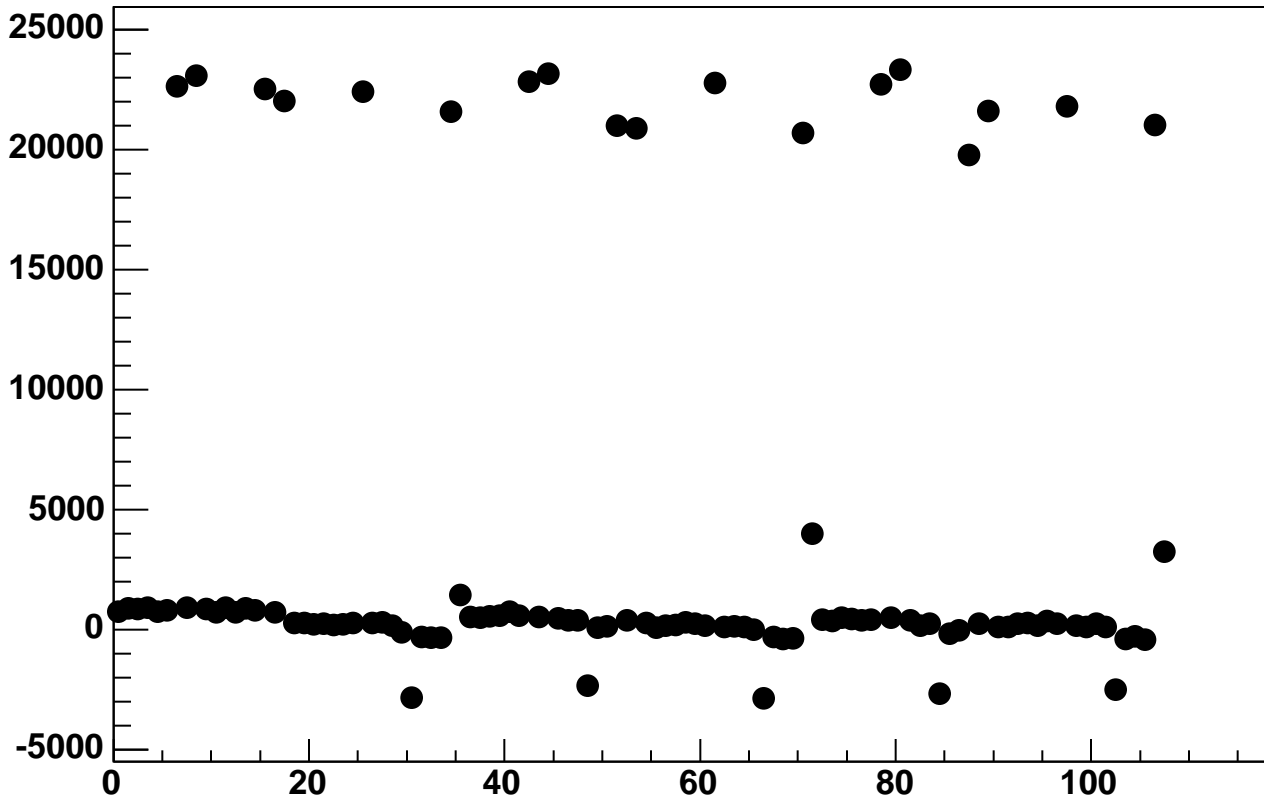
Enable 2, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



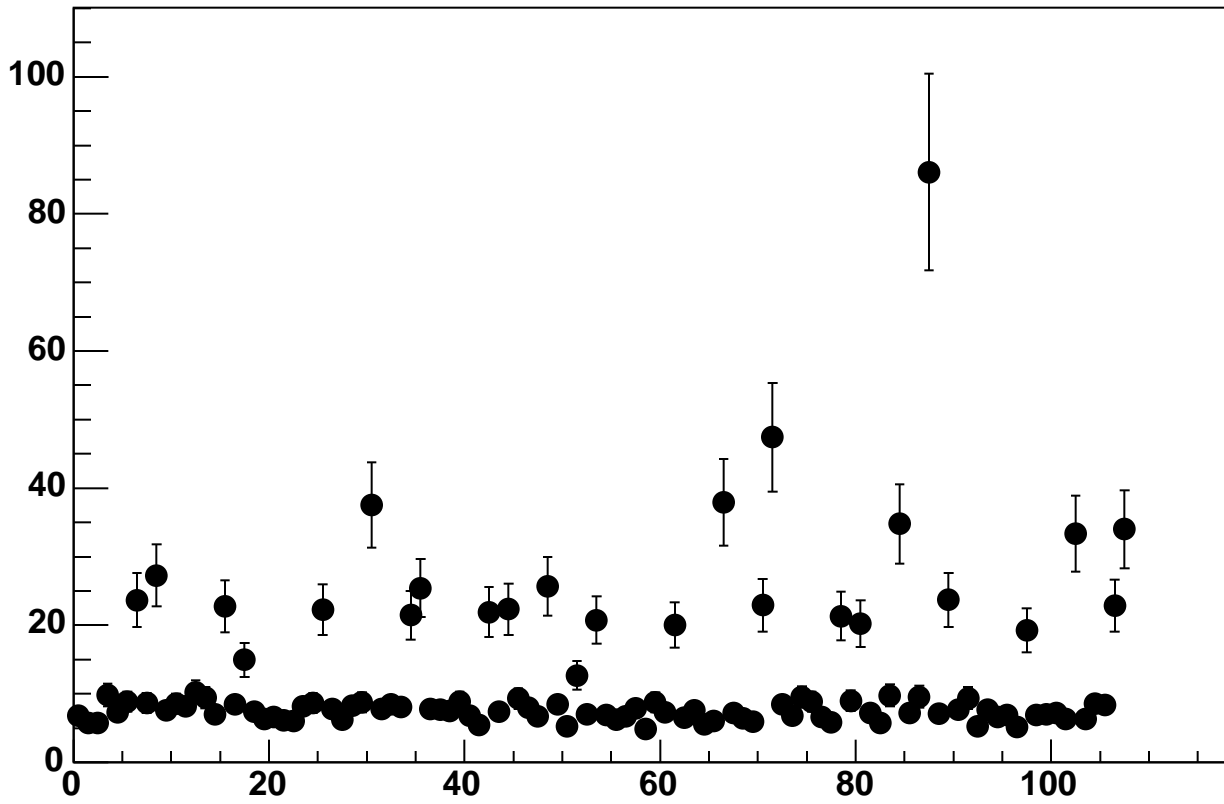
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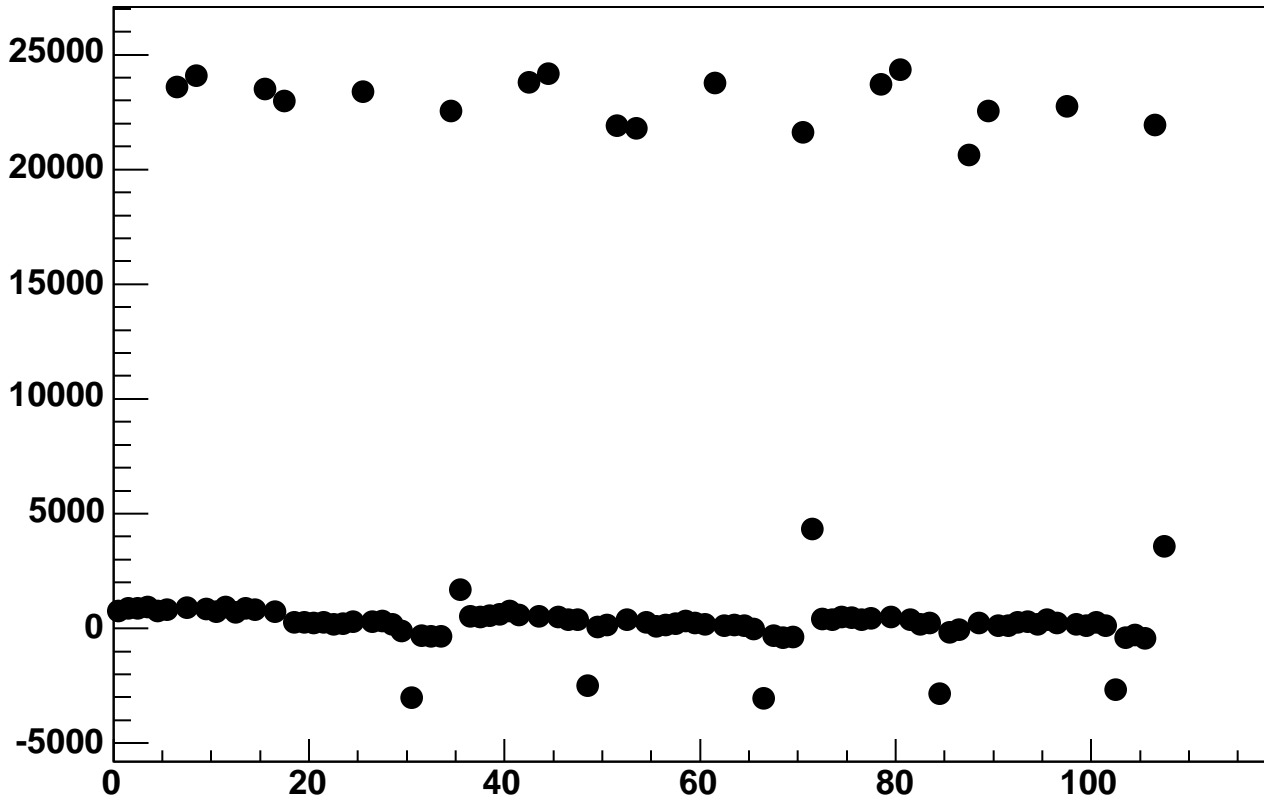
Enable 2, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



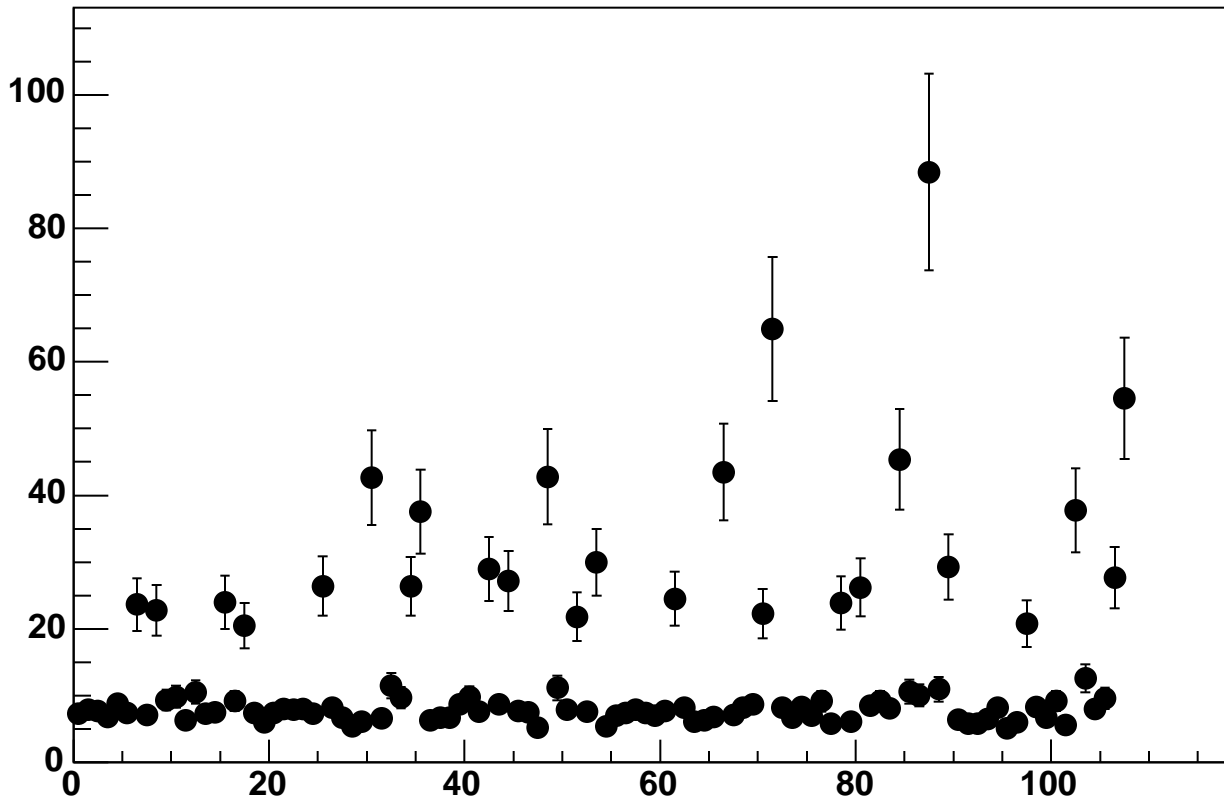
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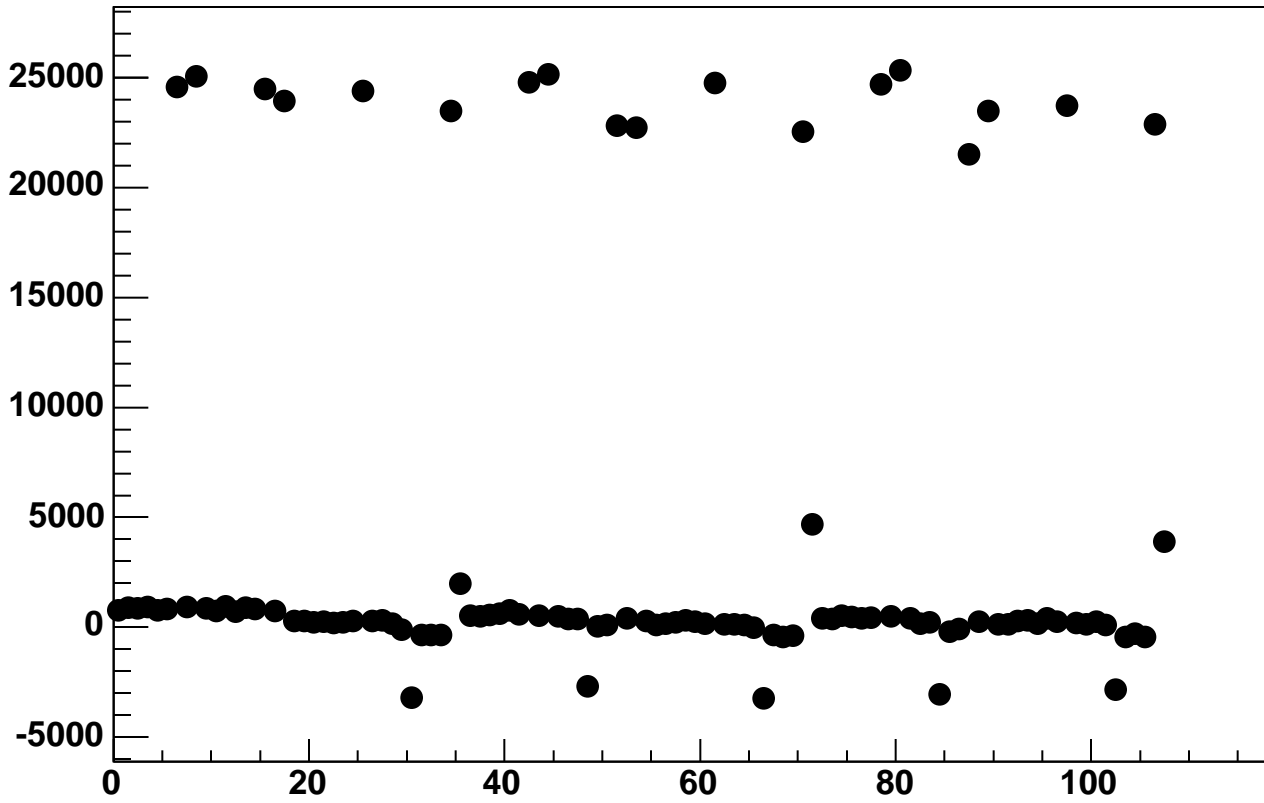
Enable 2, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



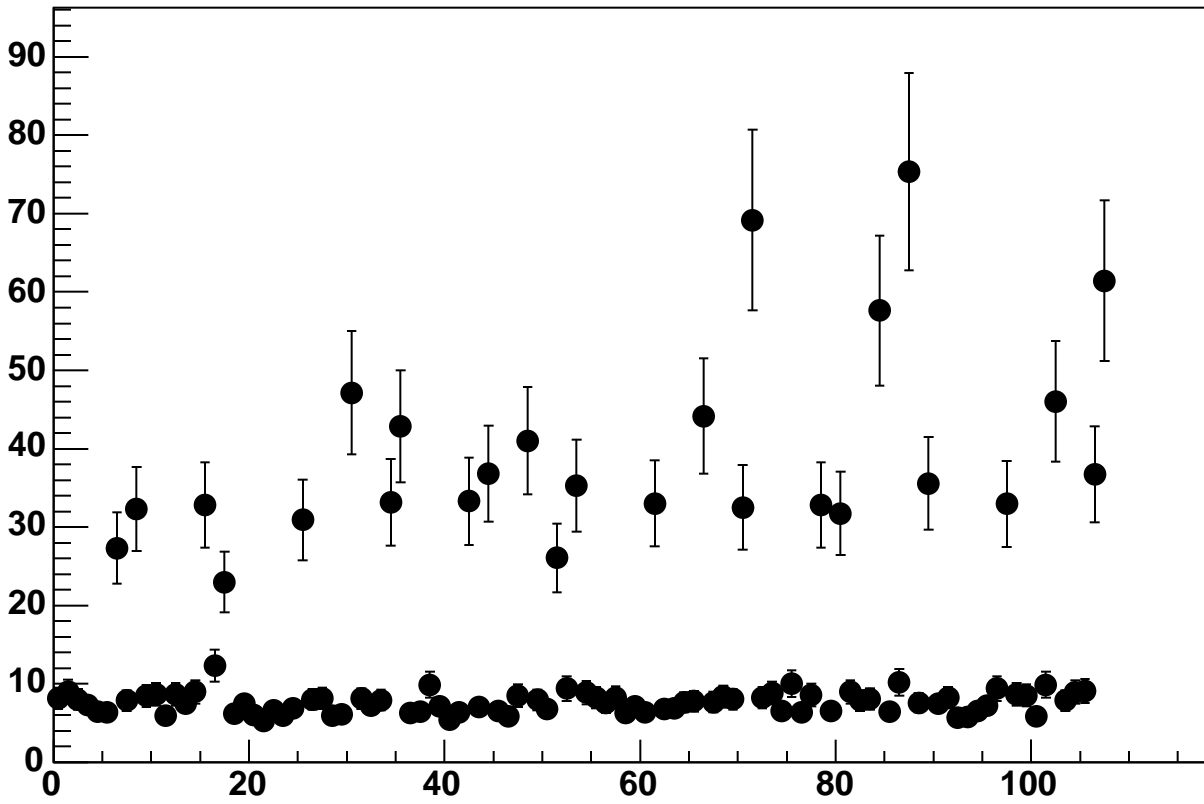
Enable 2, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



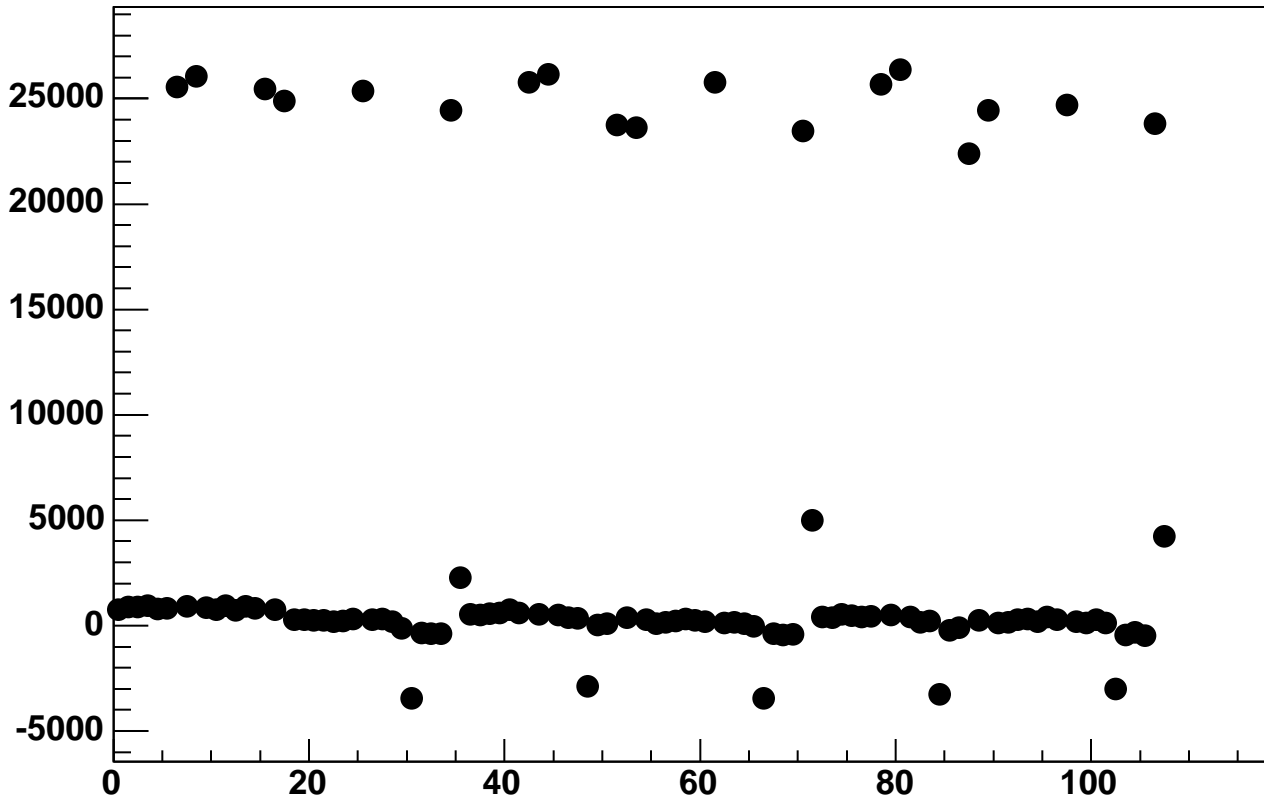
Enable 2, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



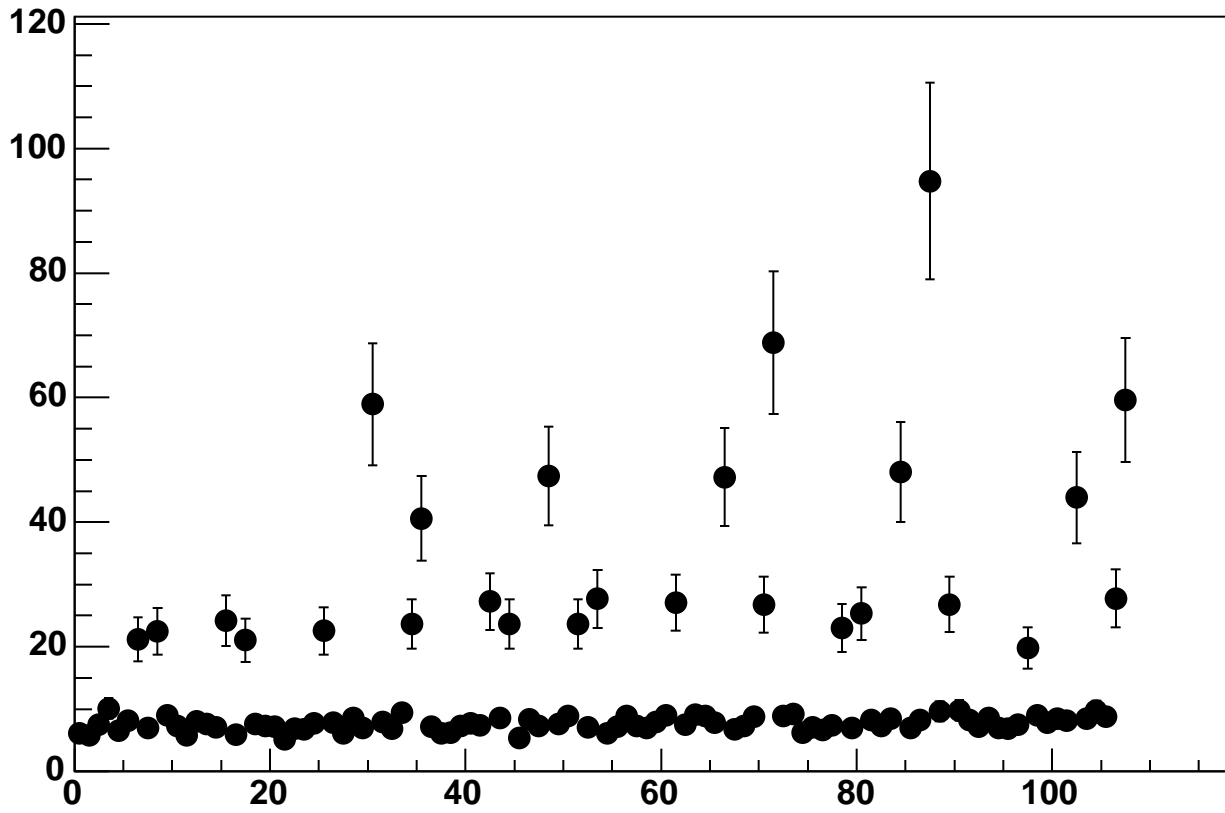
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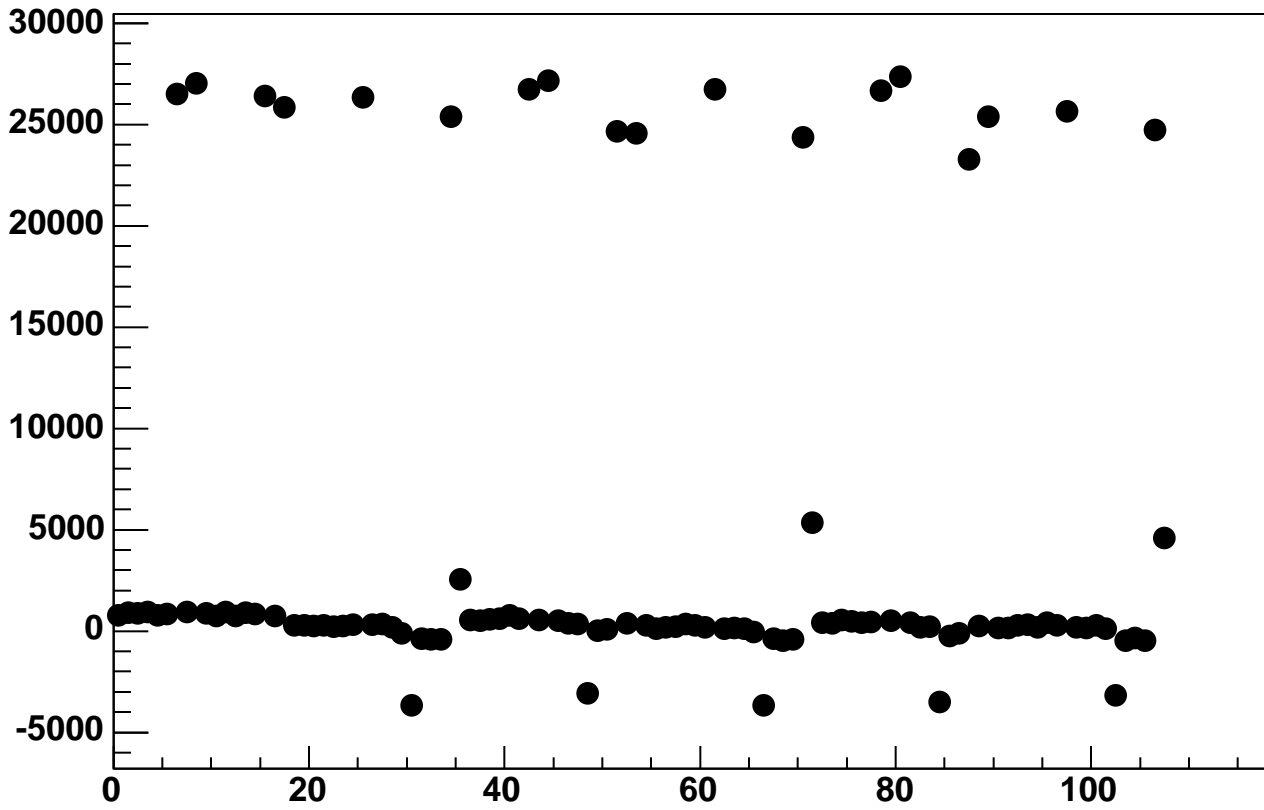
Enable 2, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



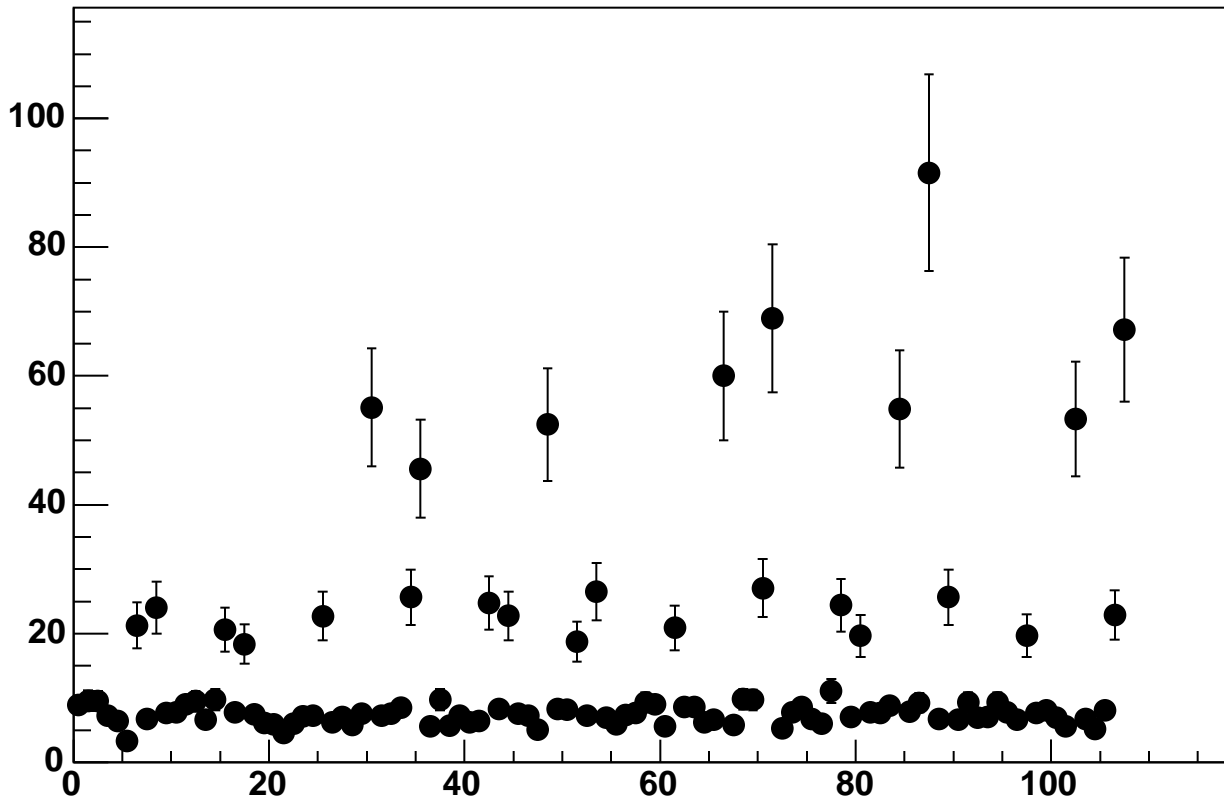
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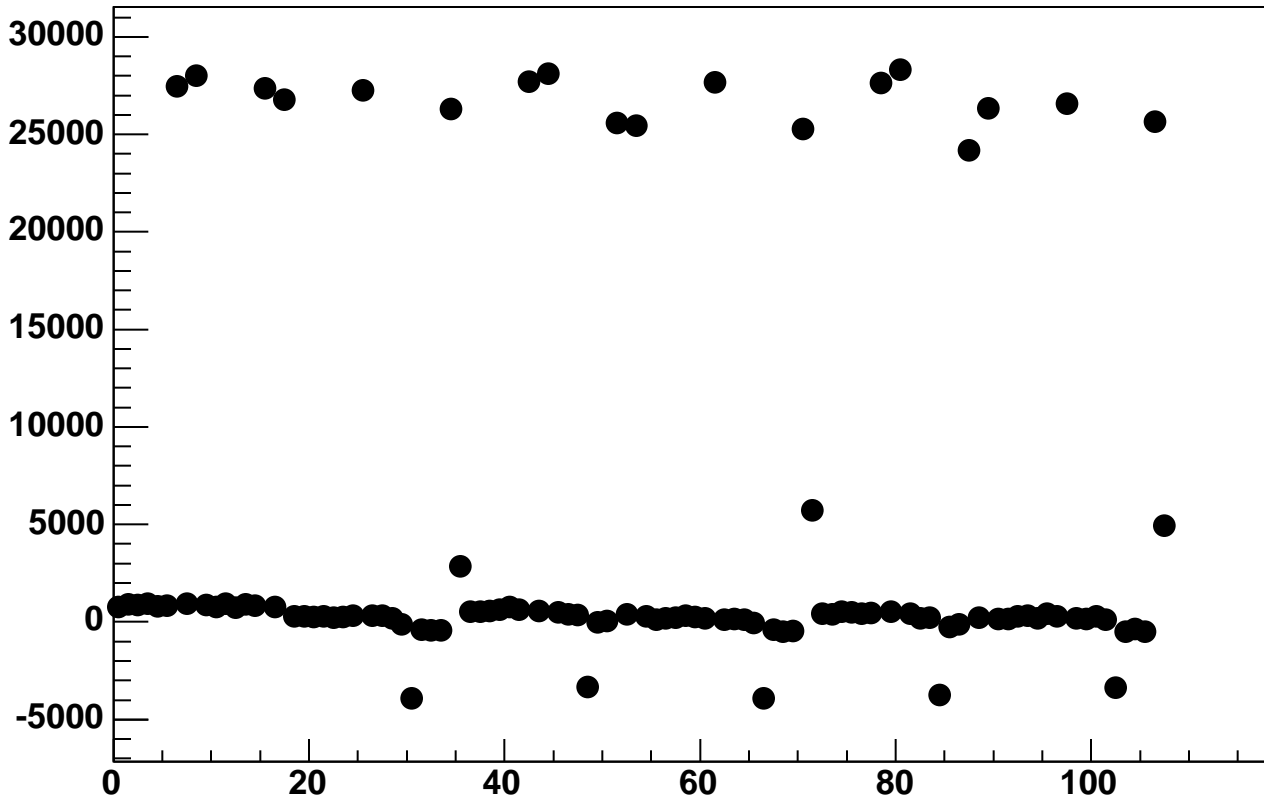
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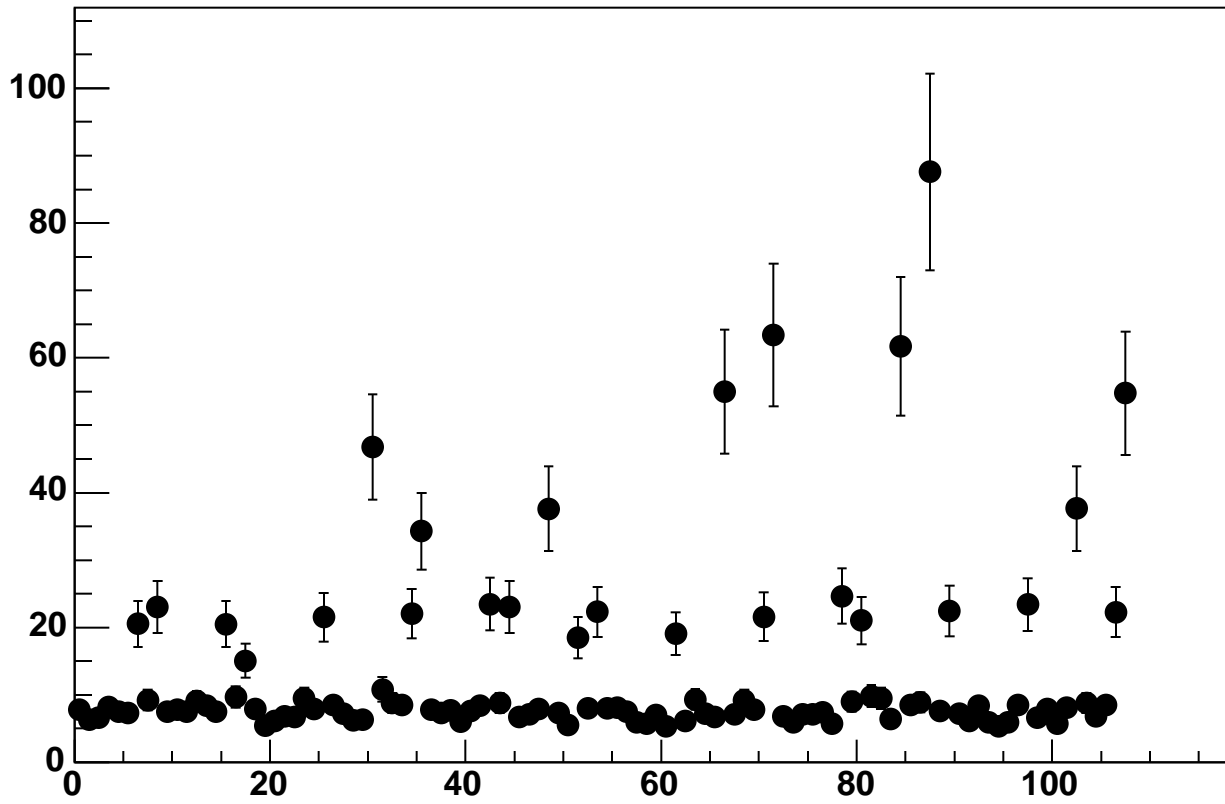
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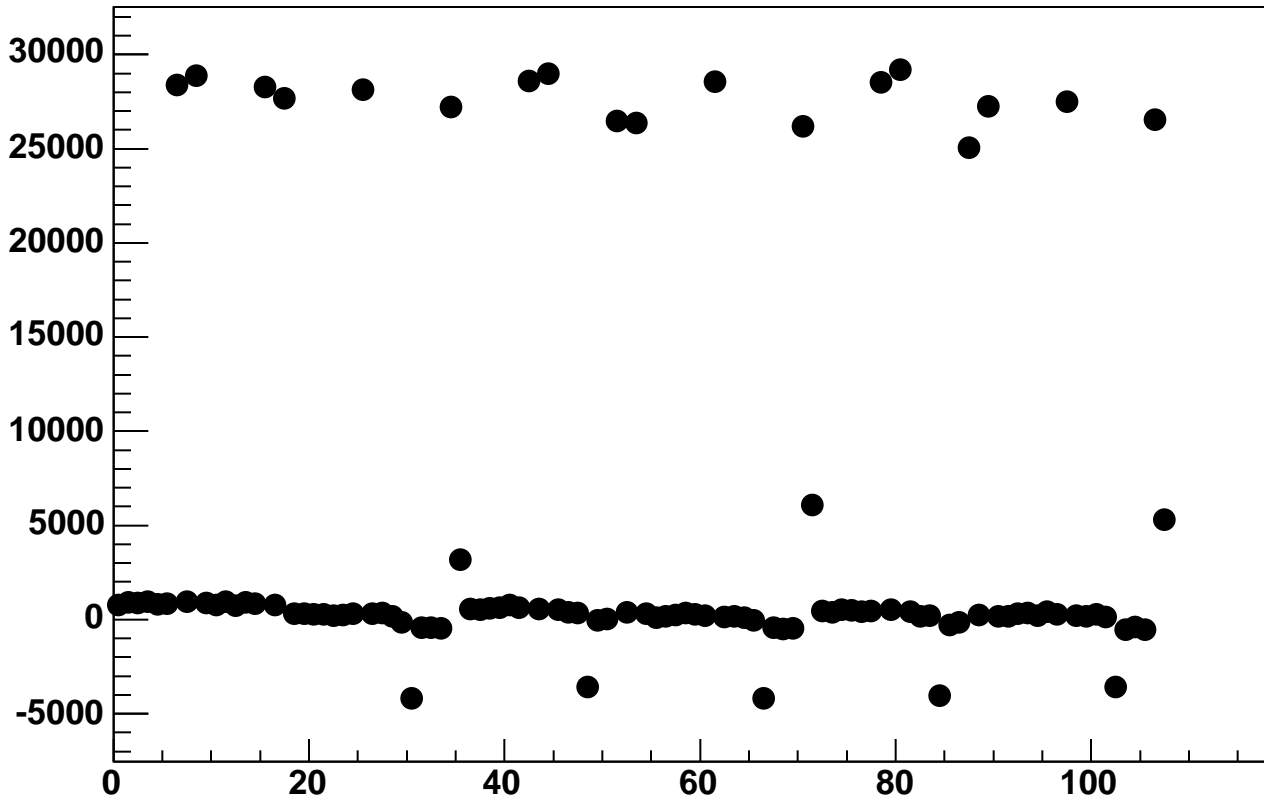
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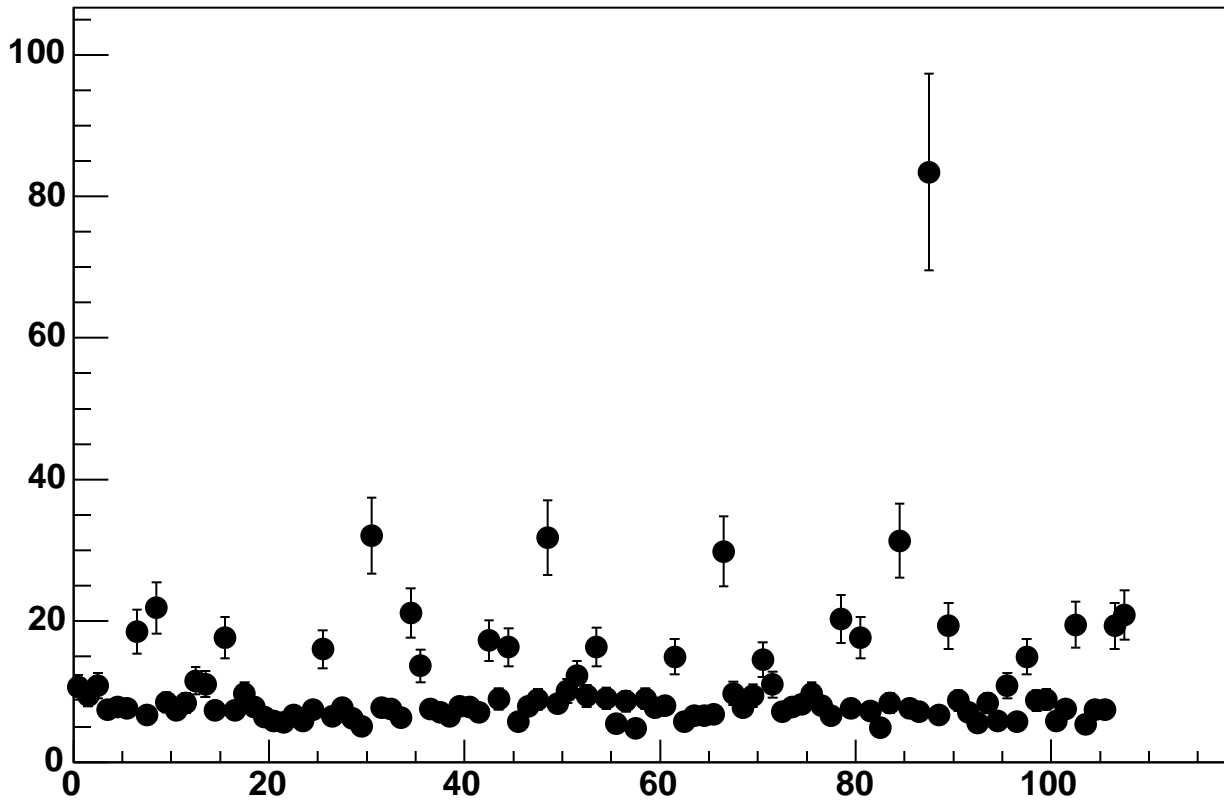
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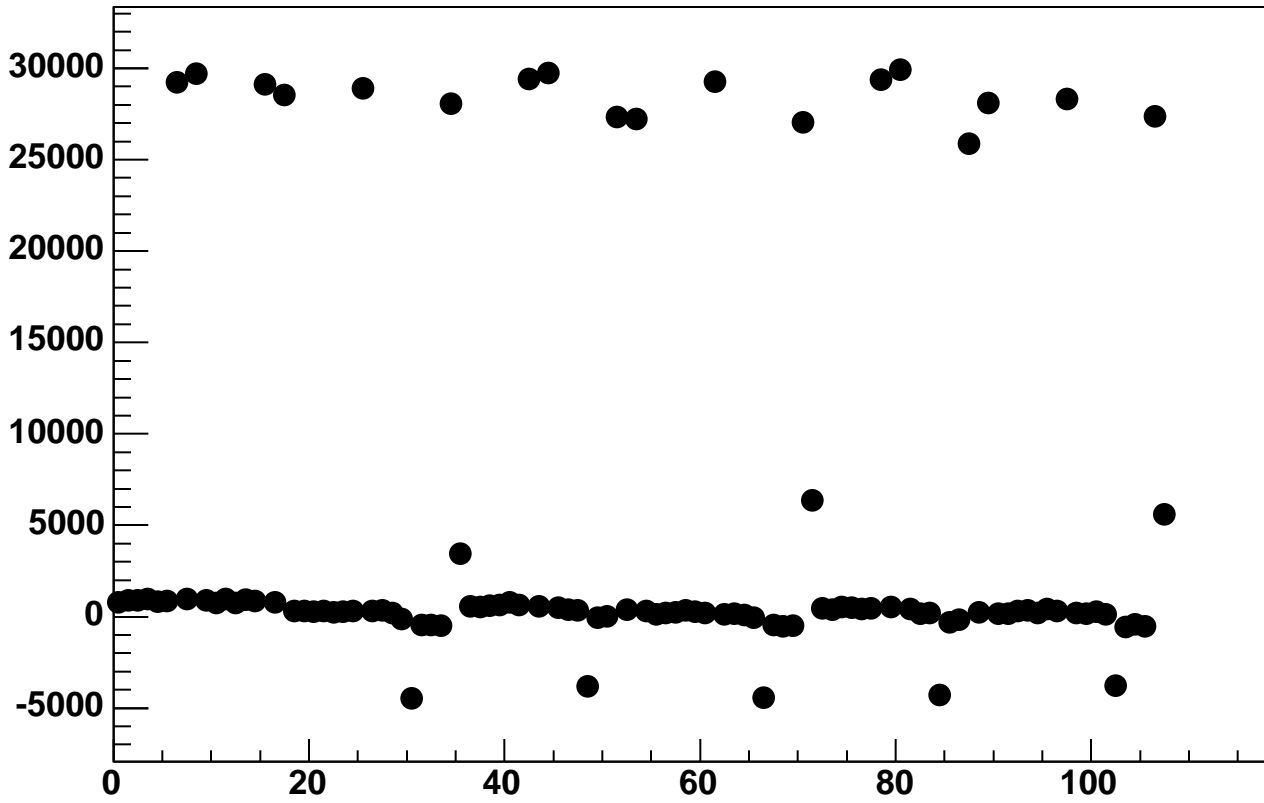
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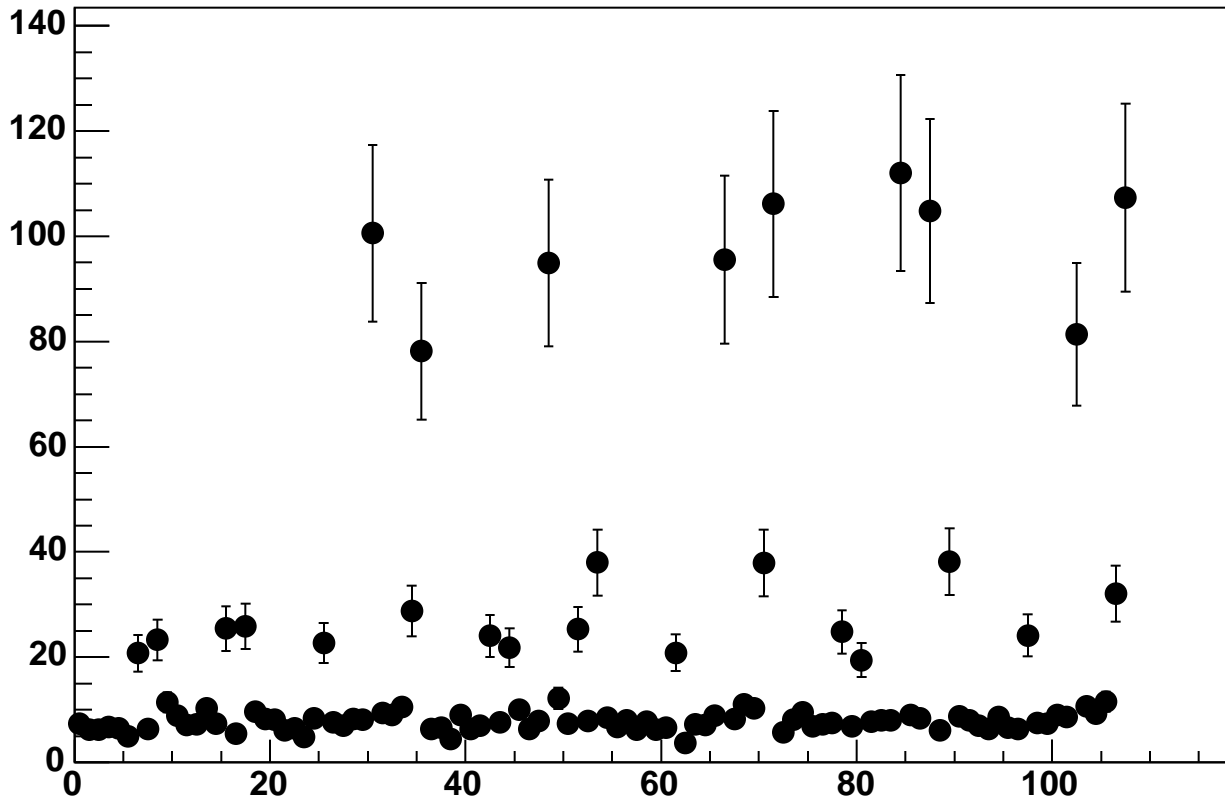
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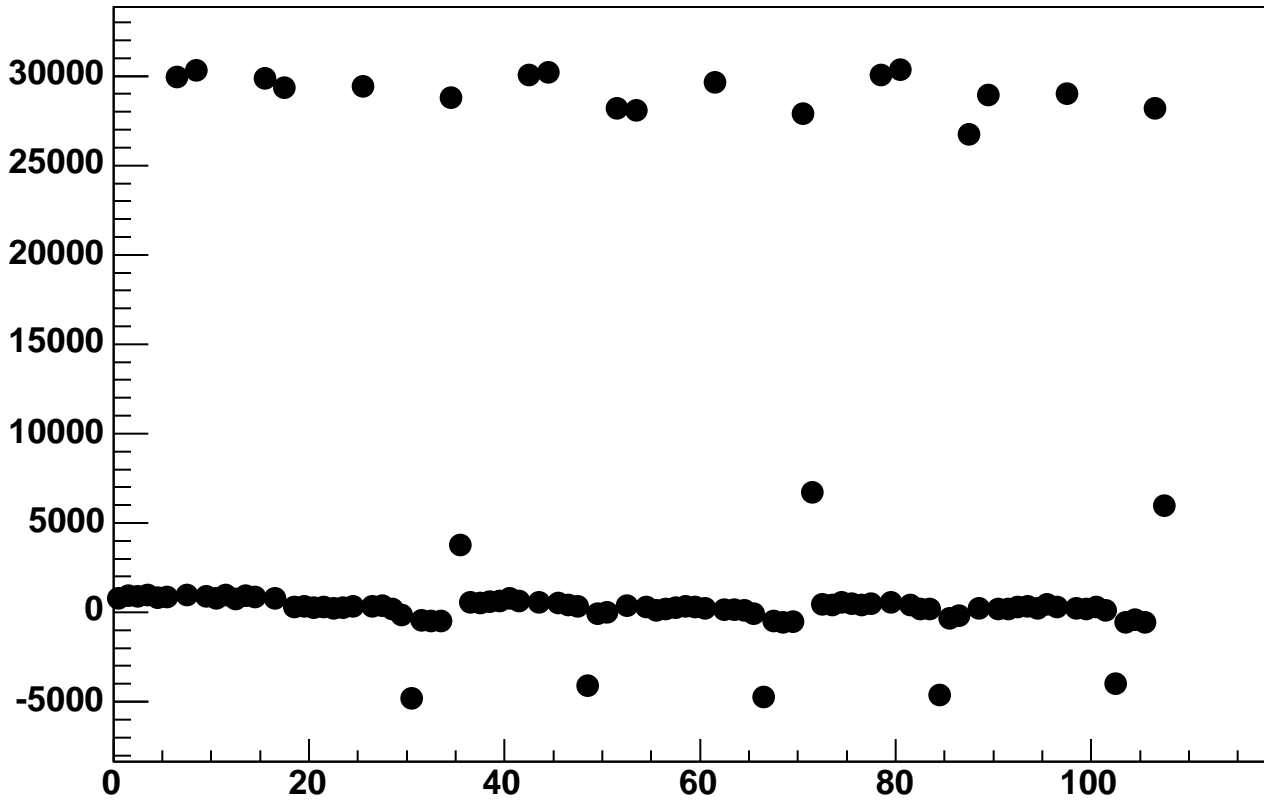
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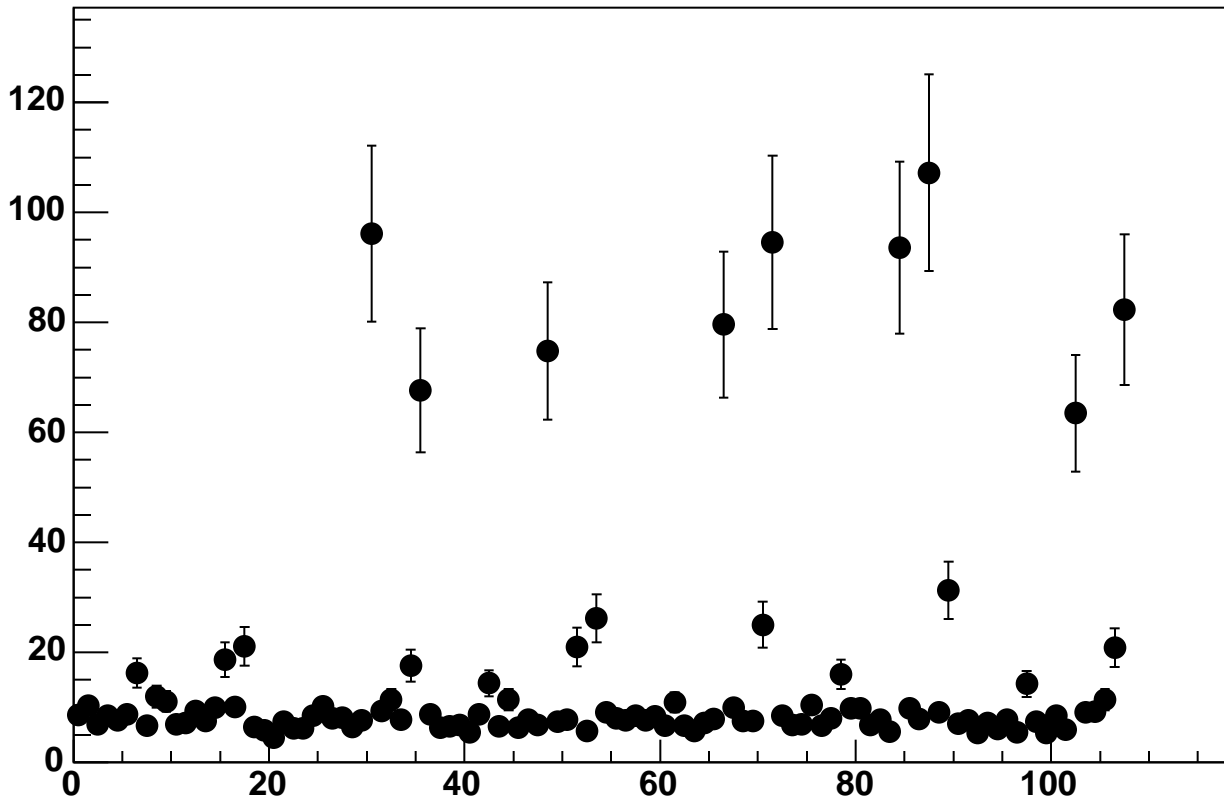
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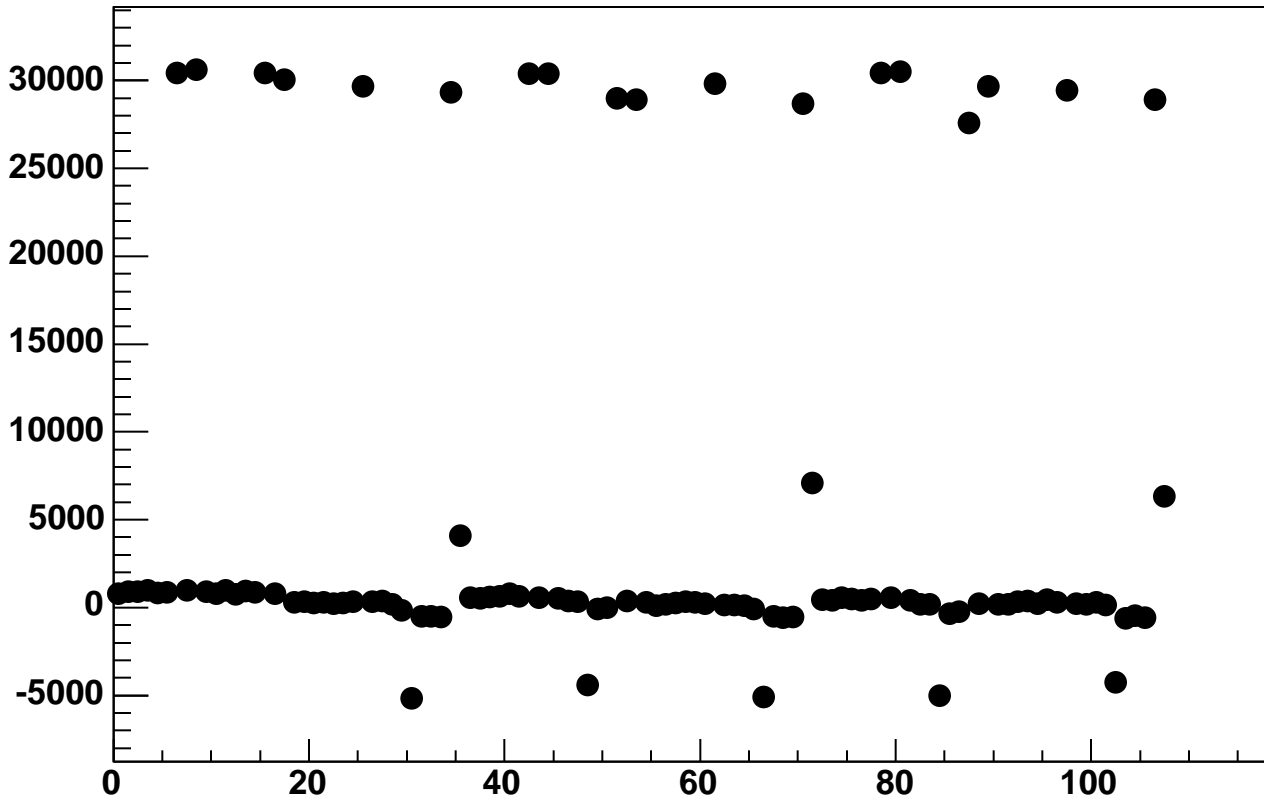
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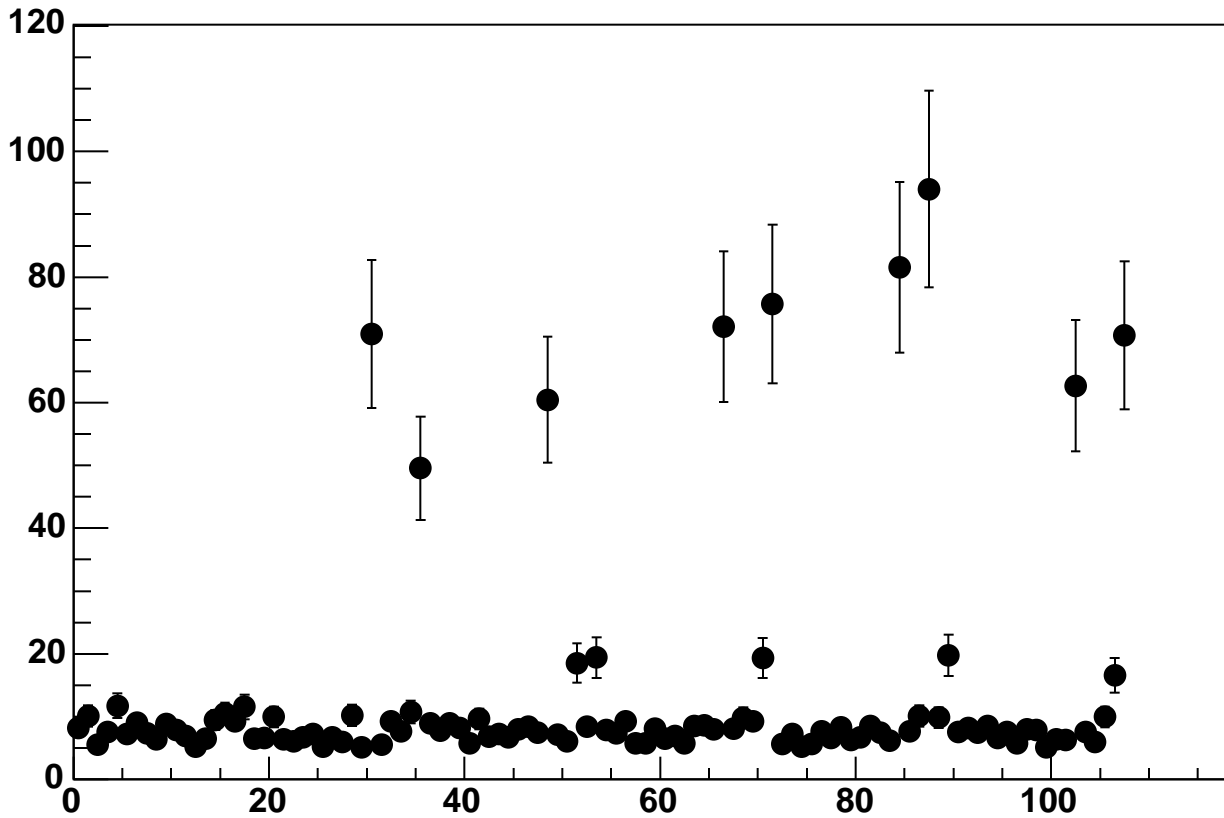
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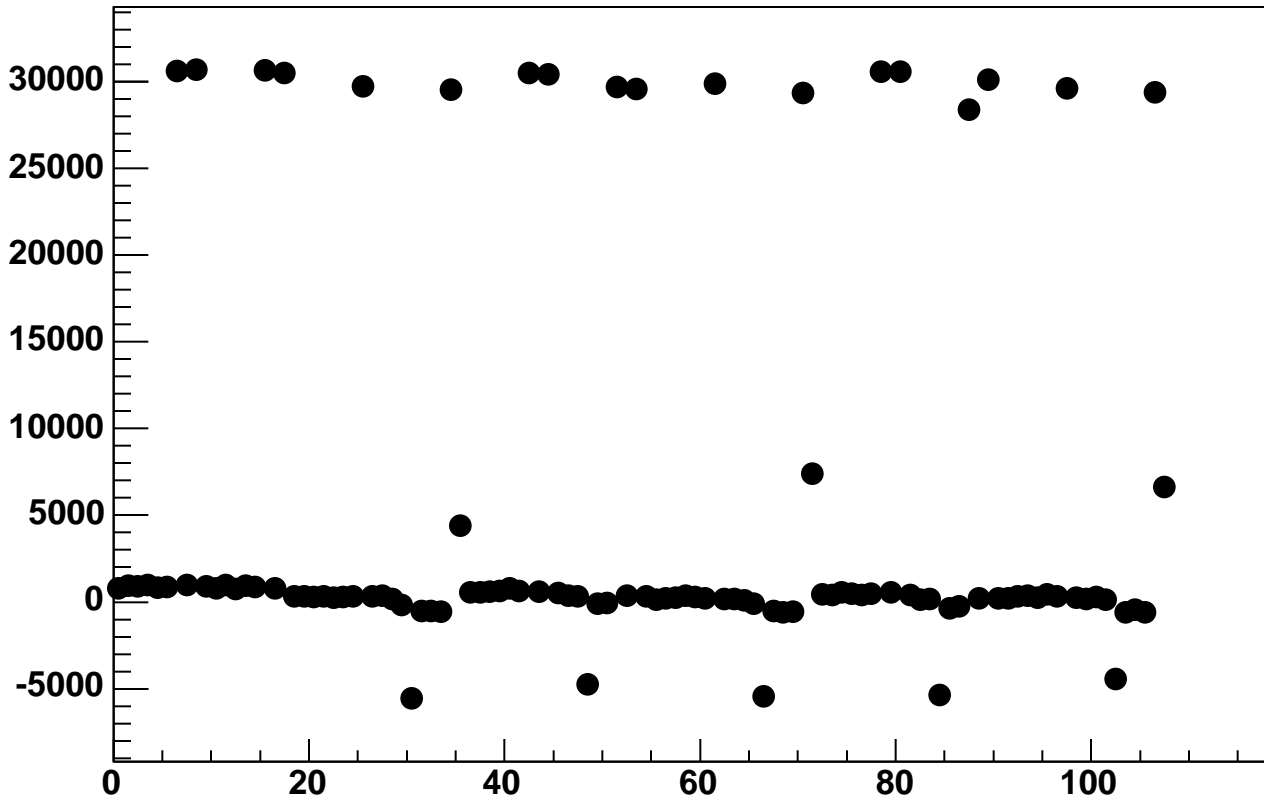
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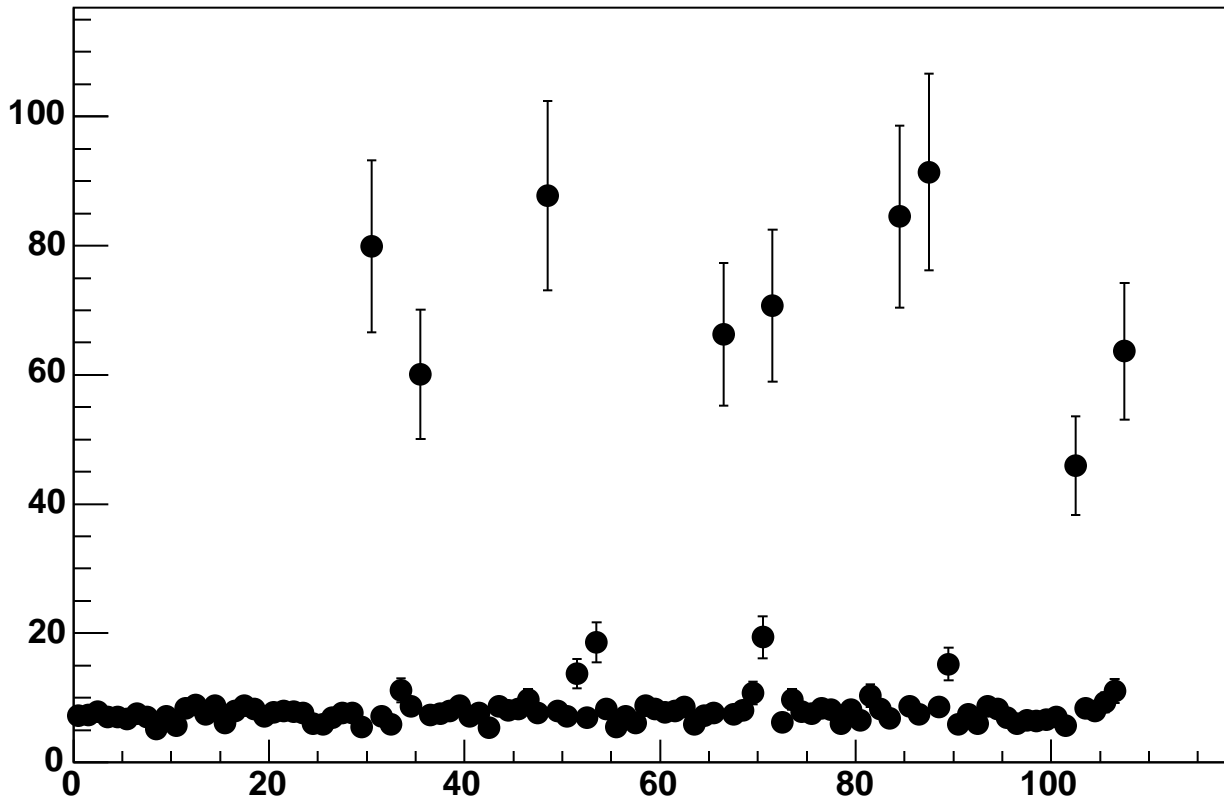
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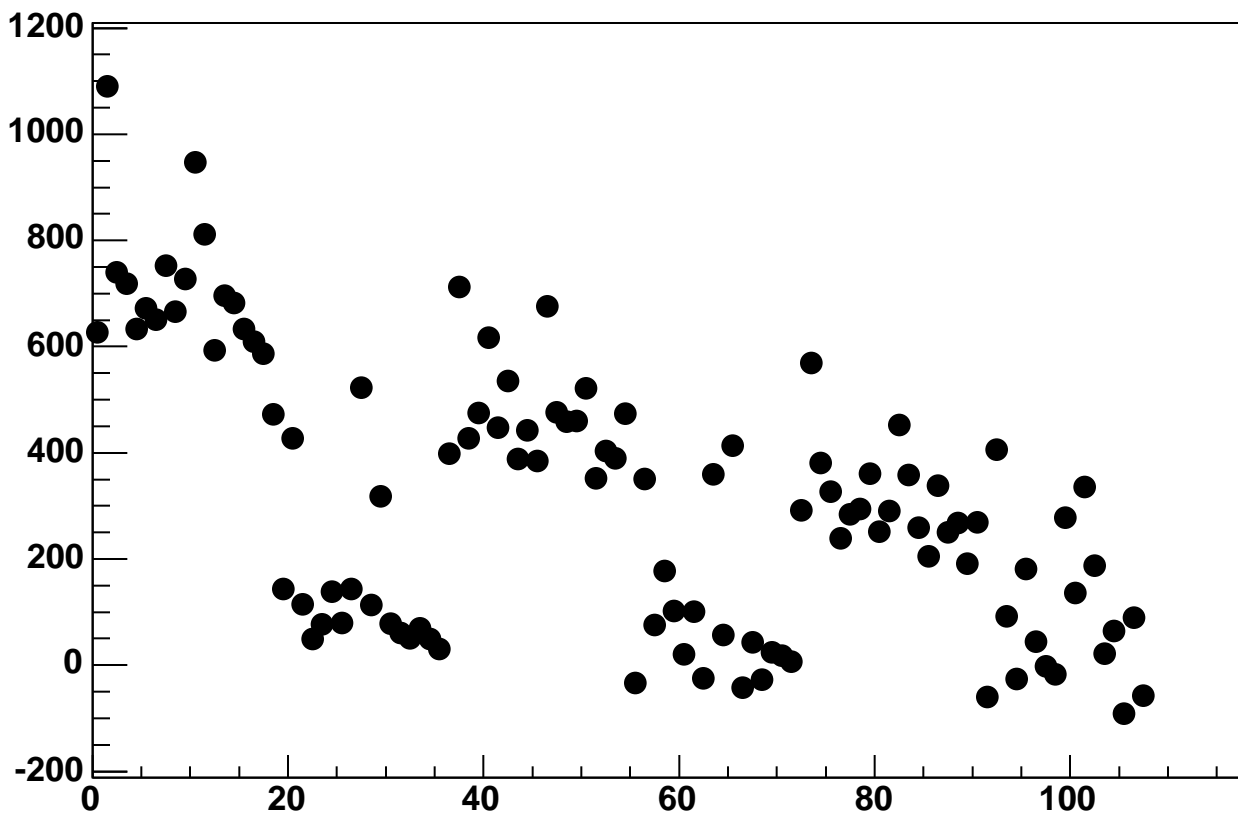
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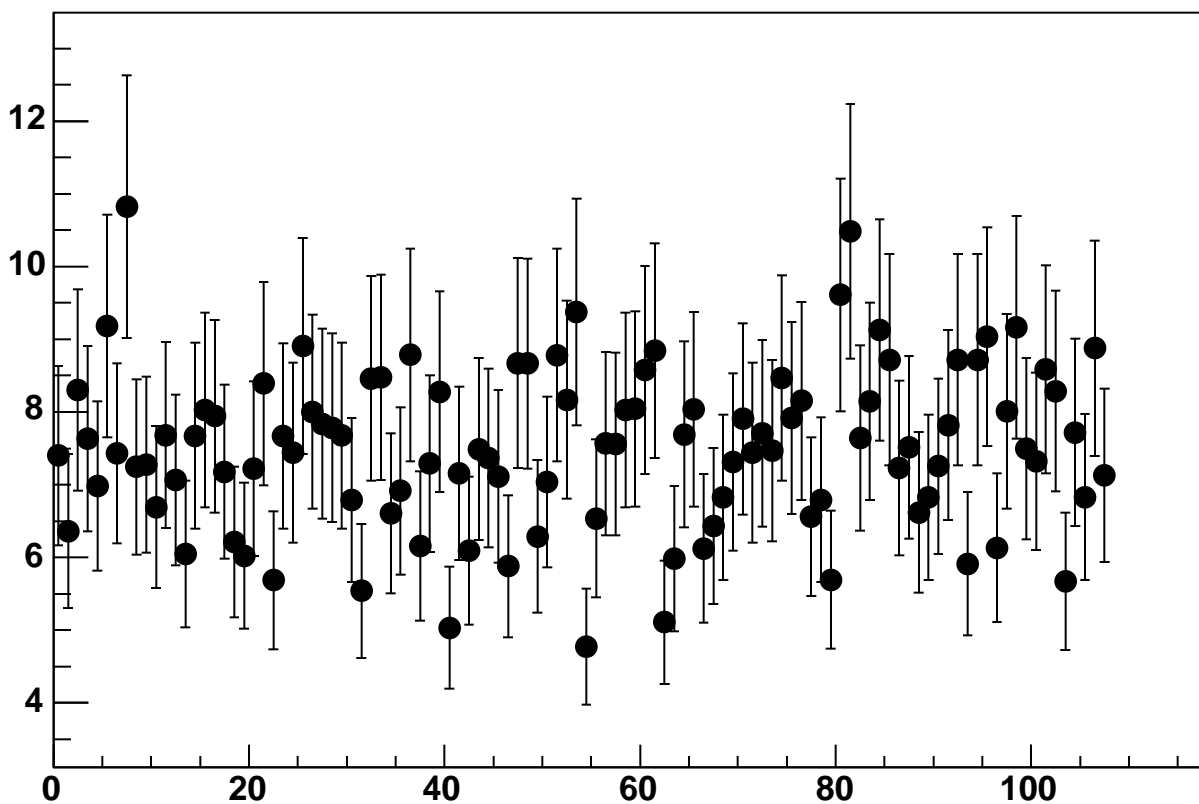
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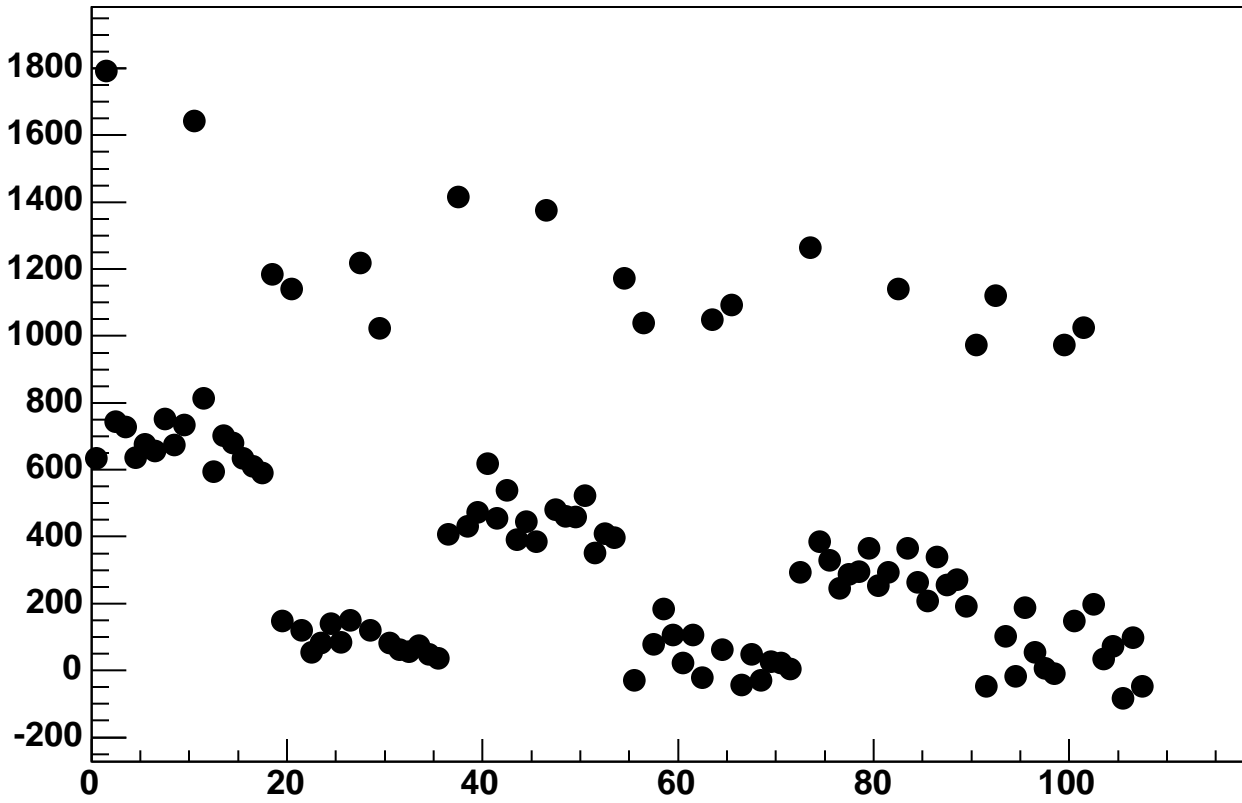
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



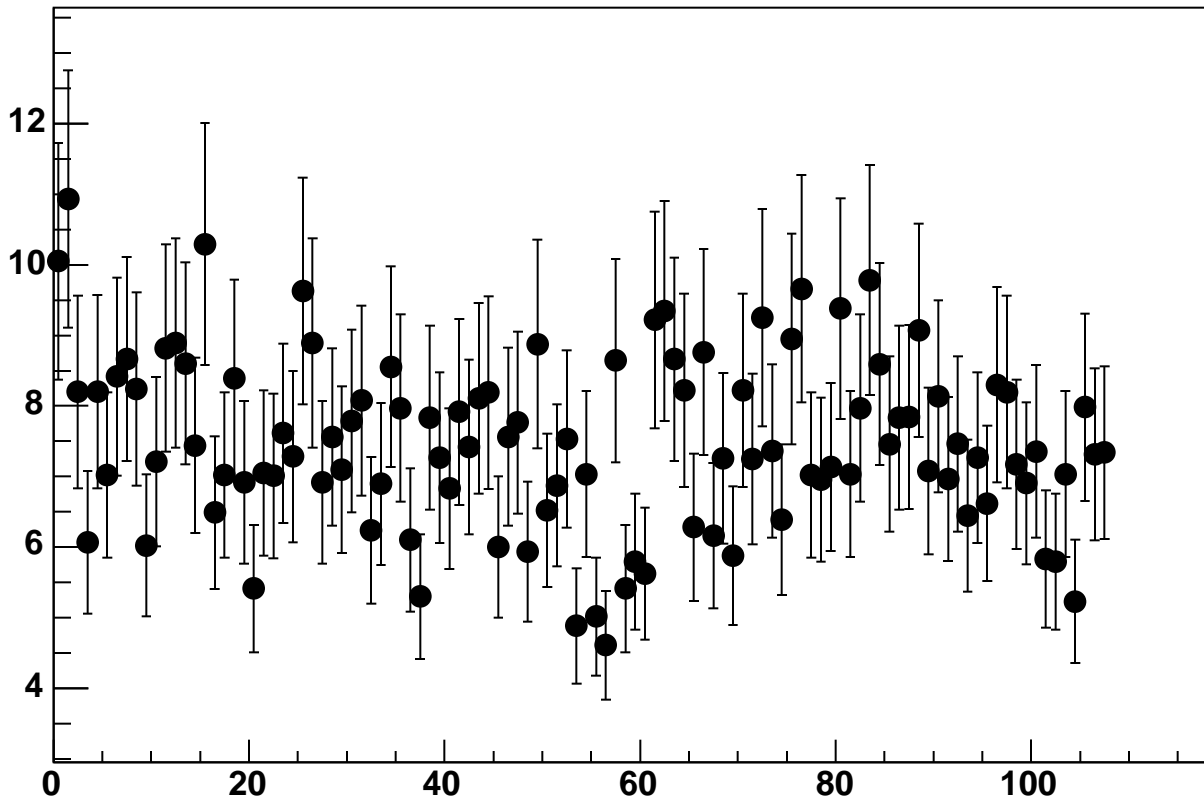
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



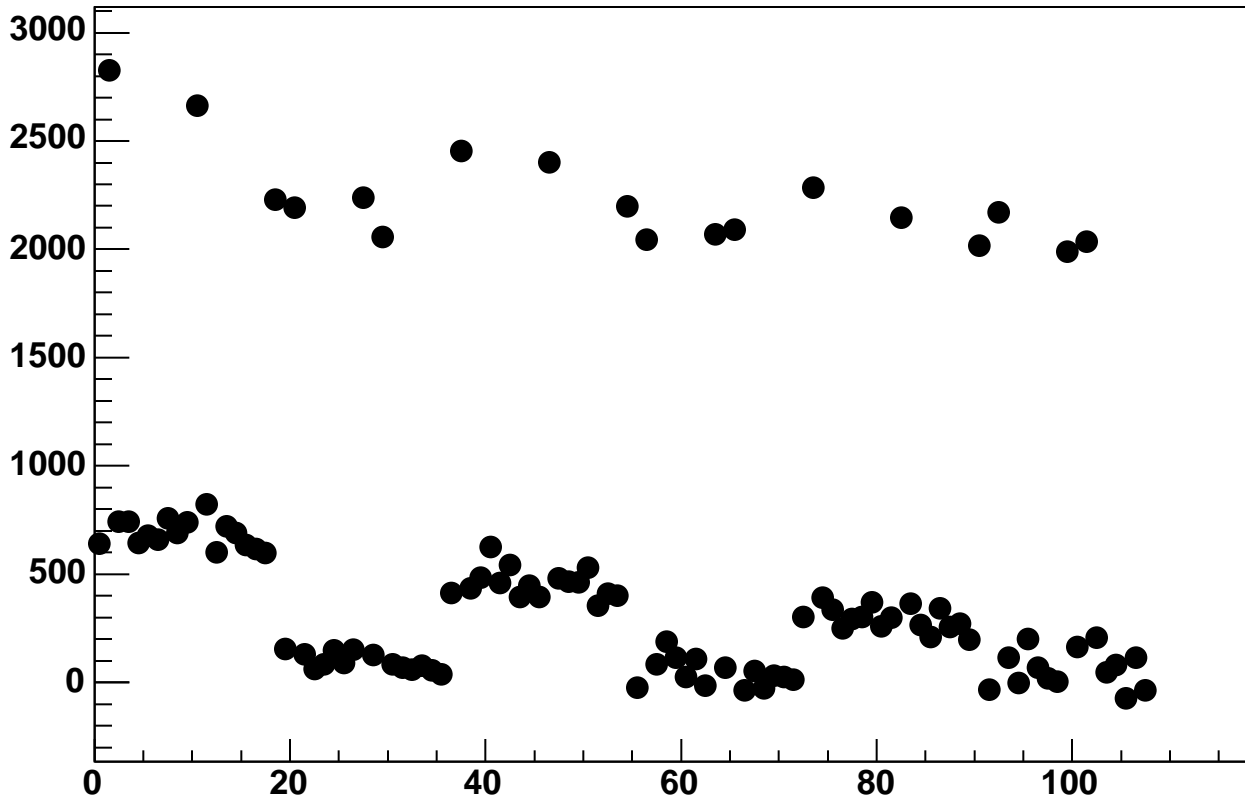
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



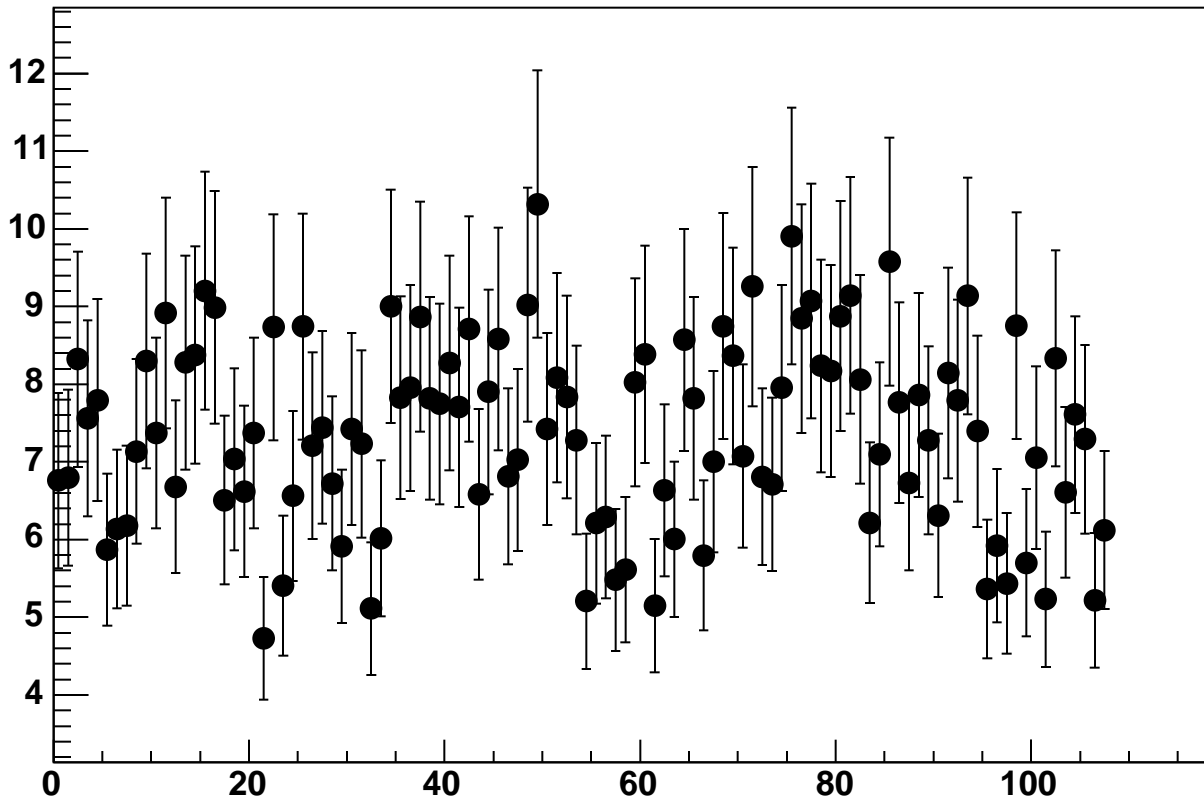
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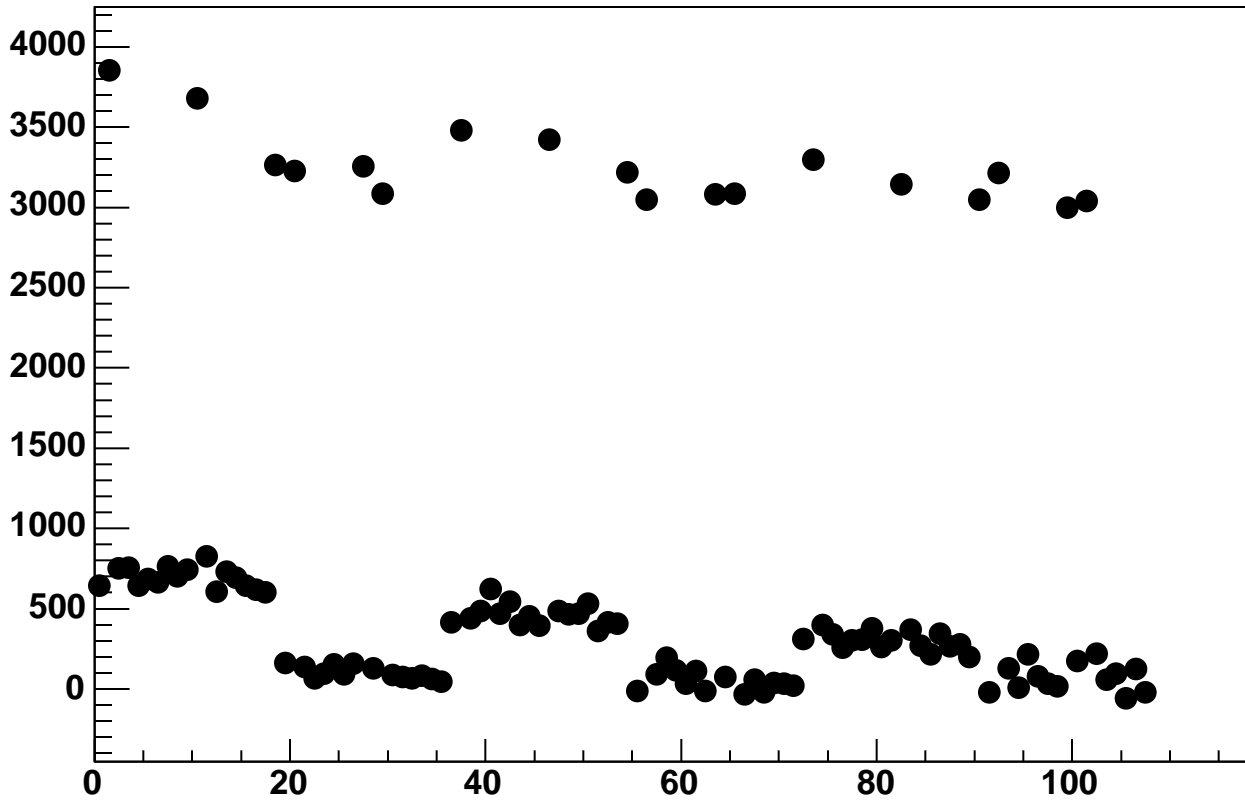
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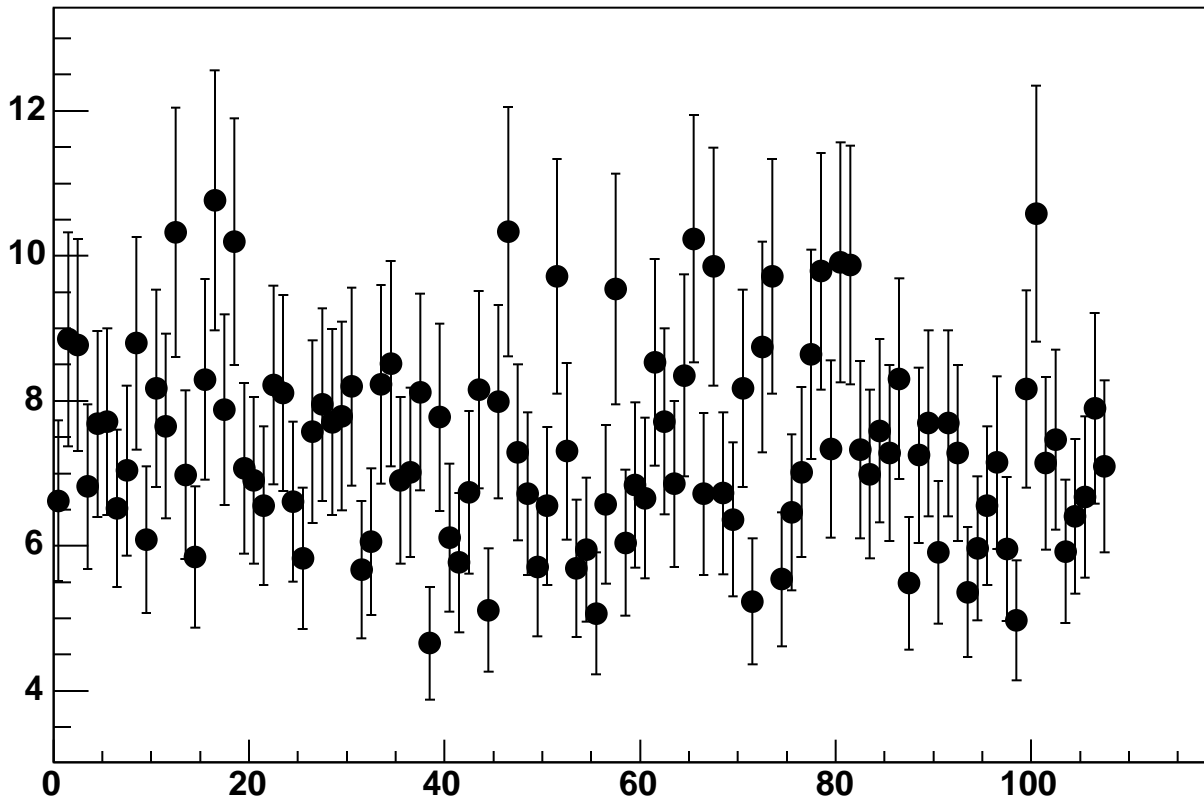
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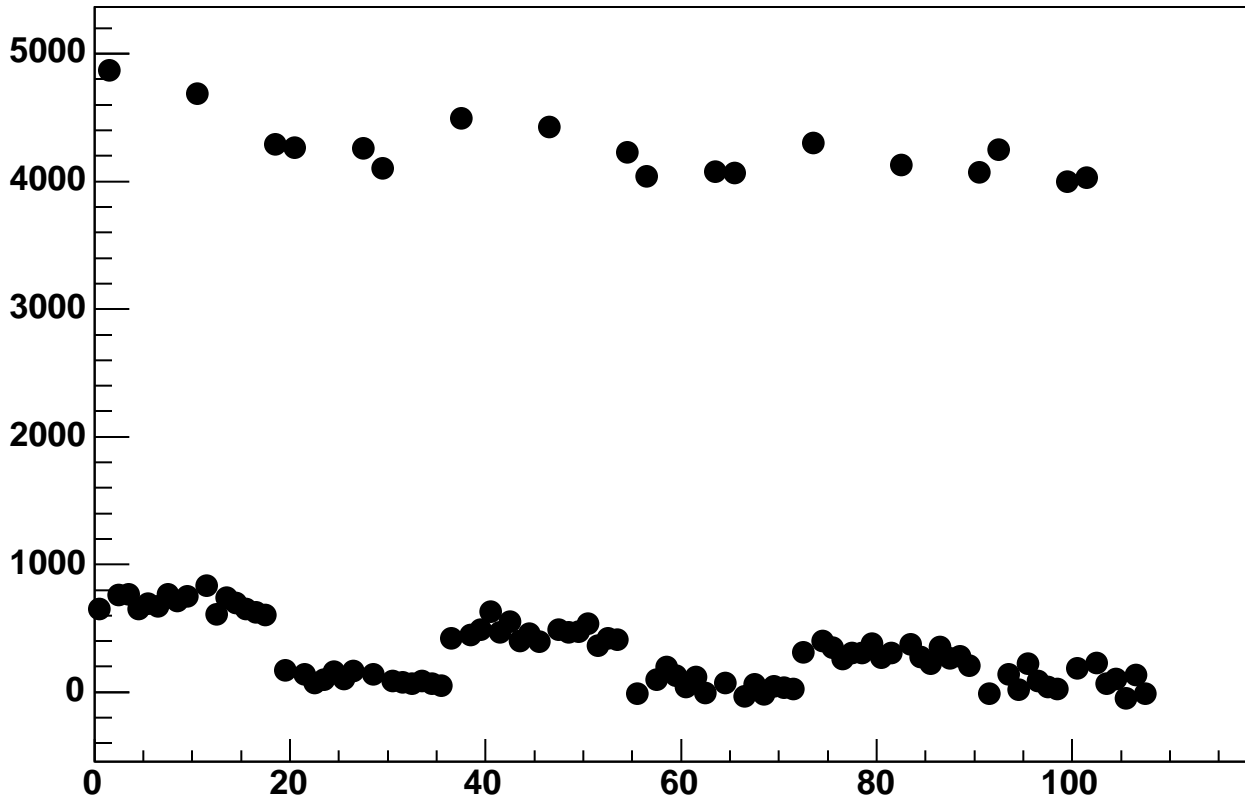
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



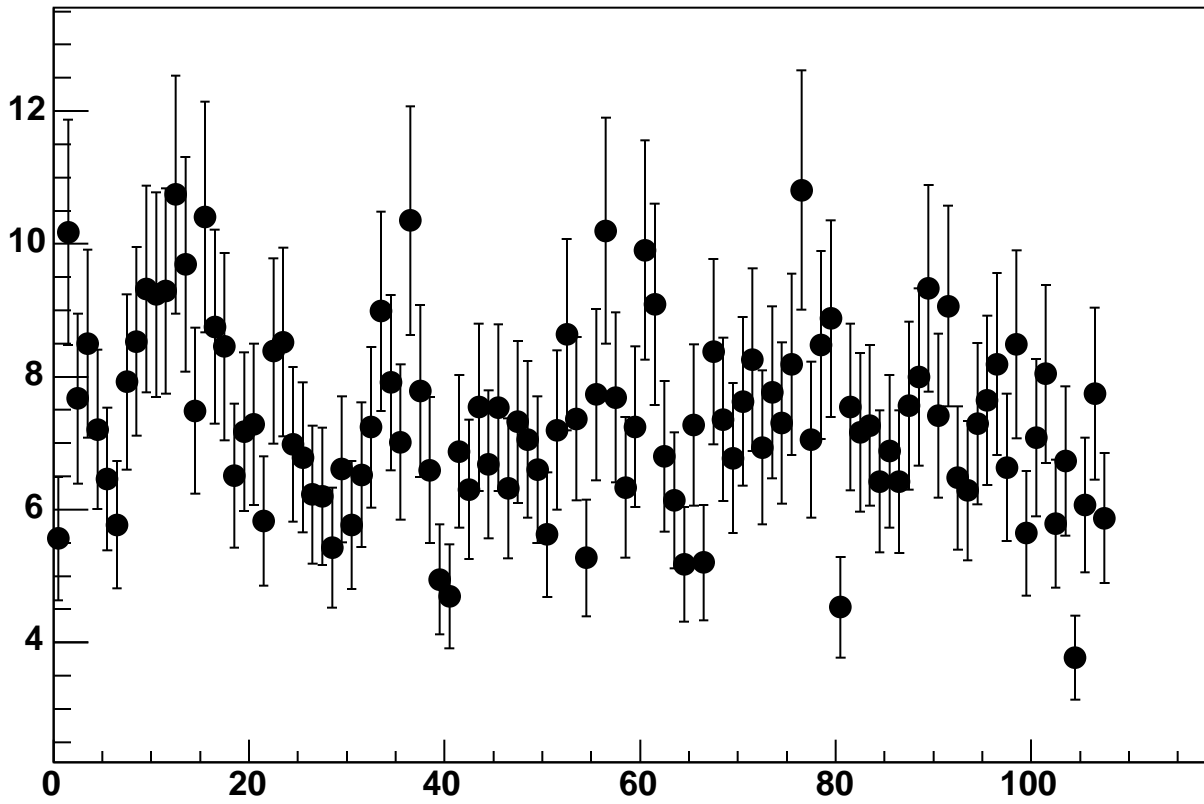
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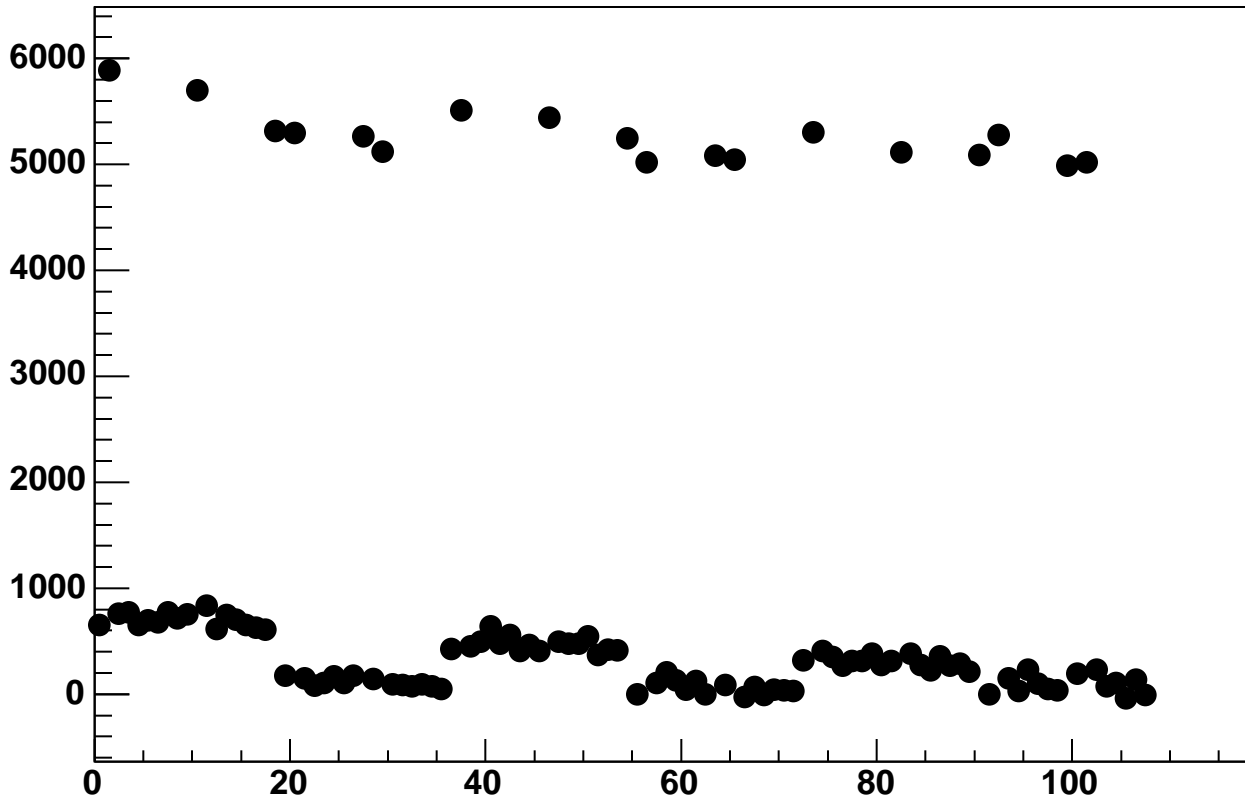
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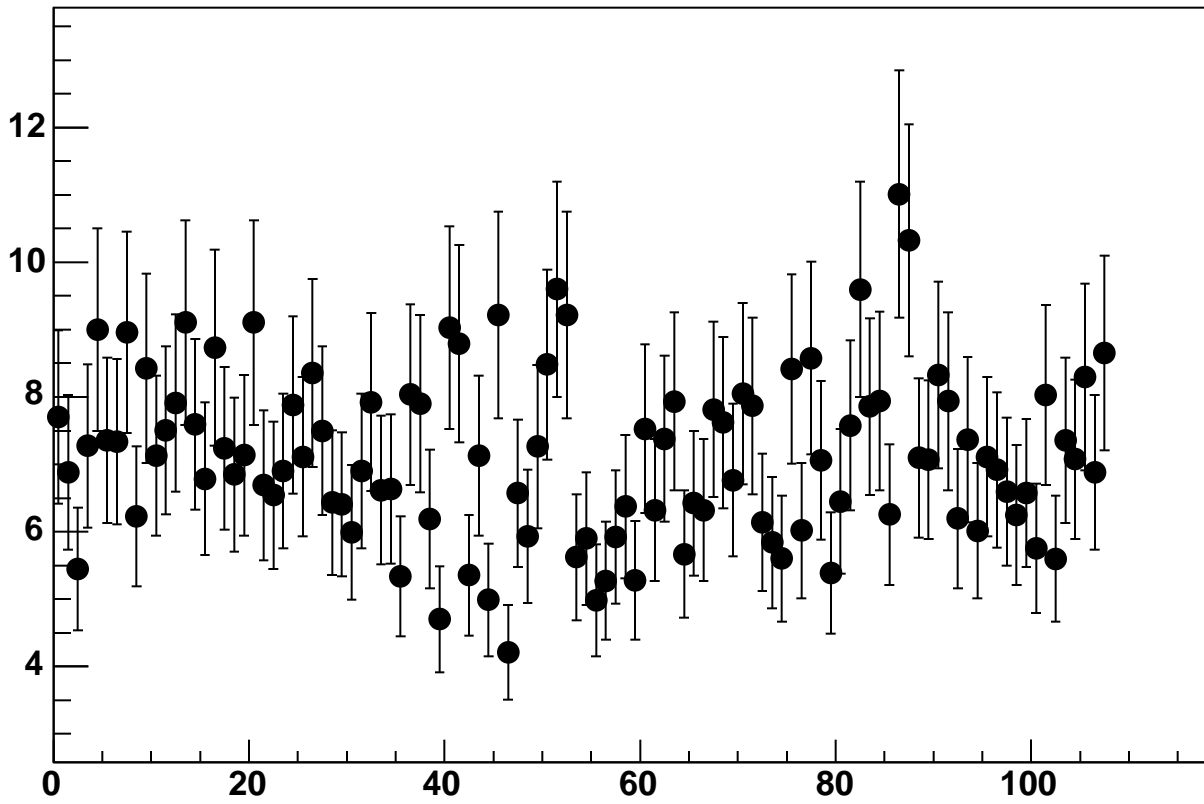
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



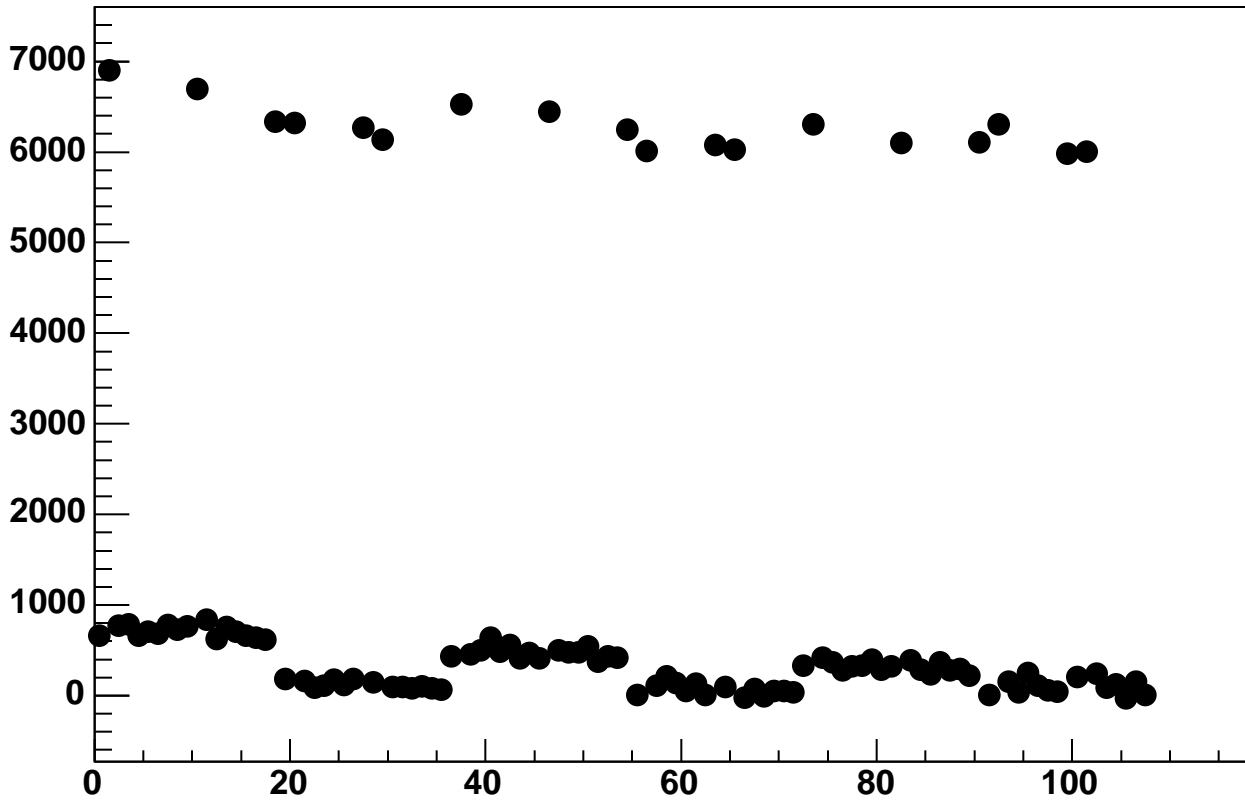
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



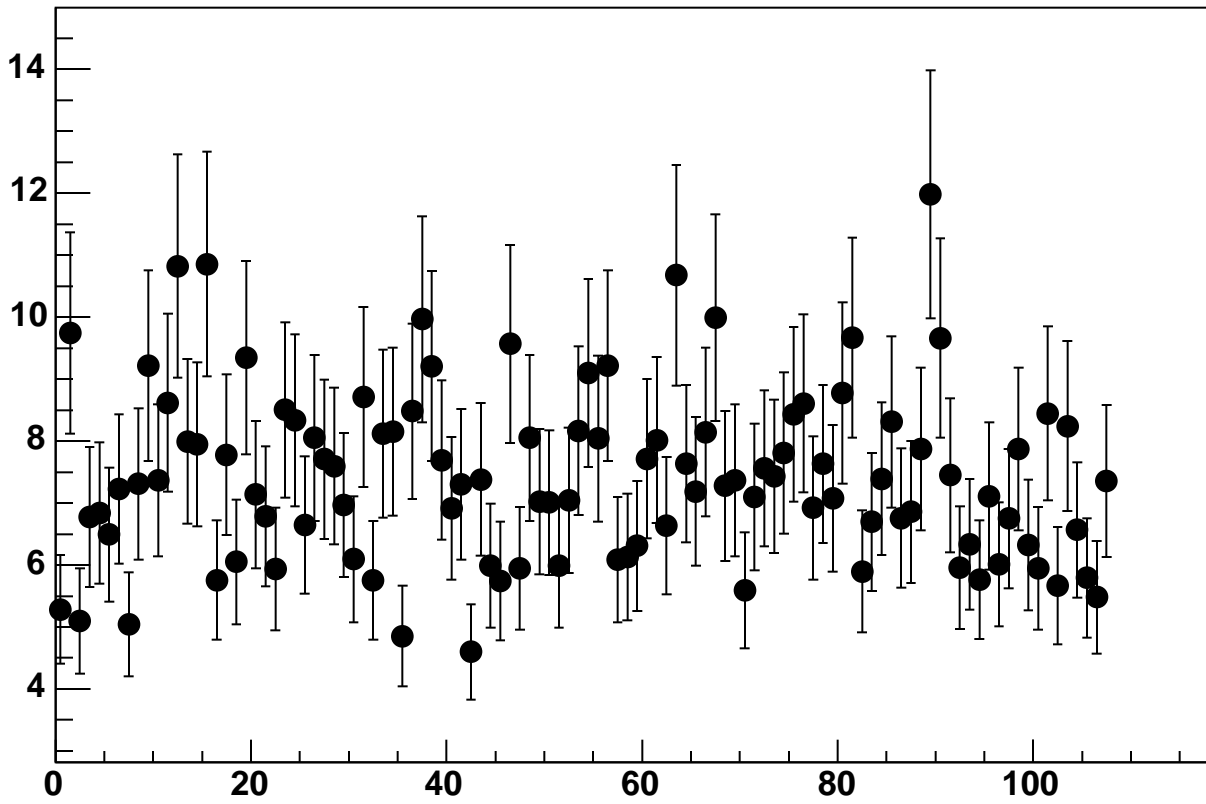
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



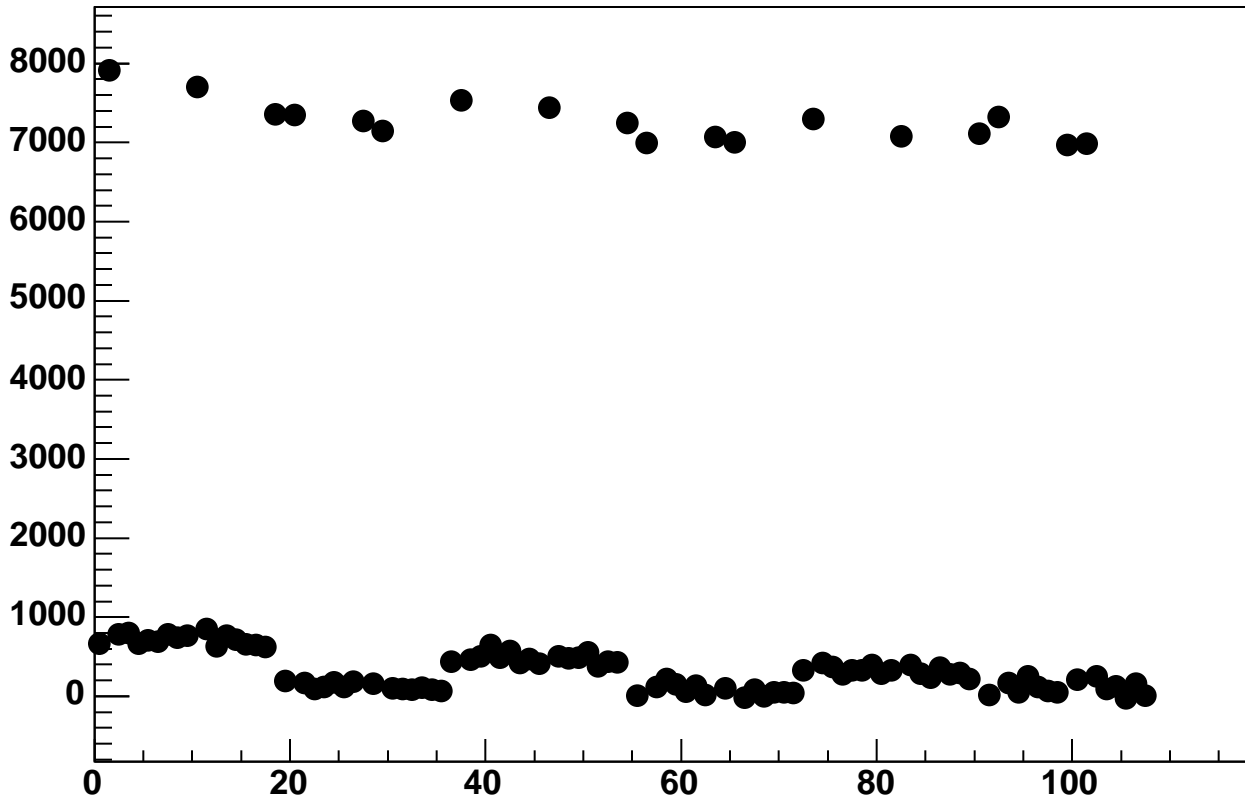
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



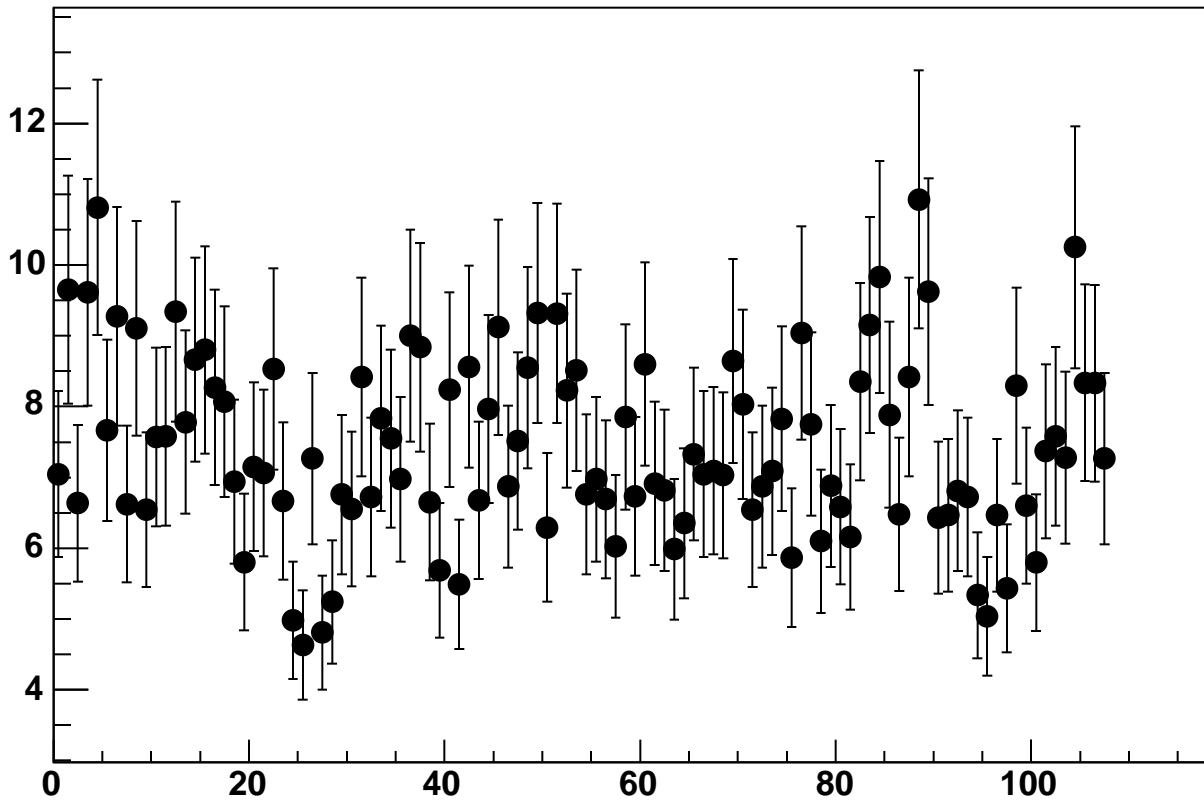
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



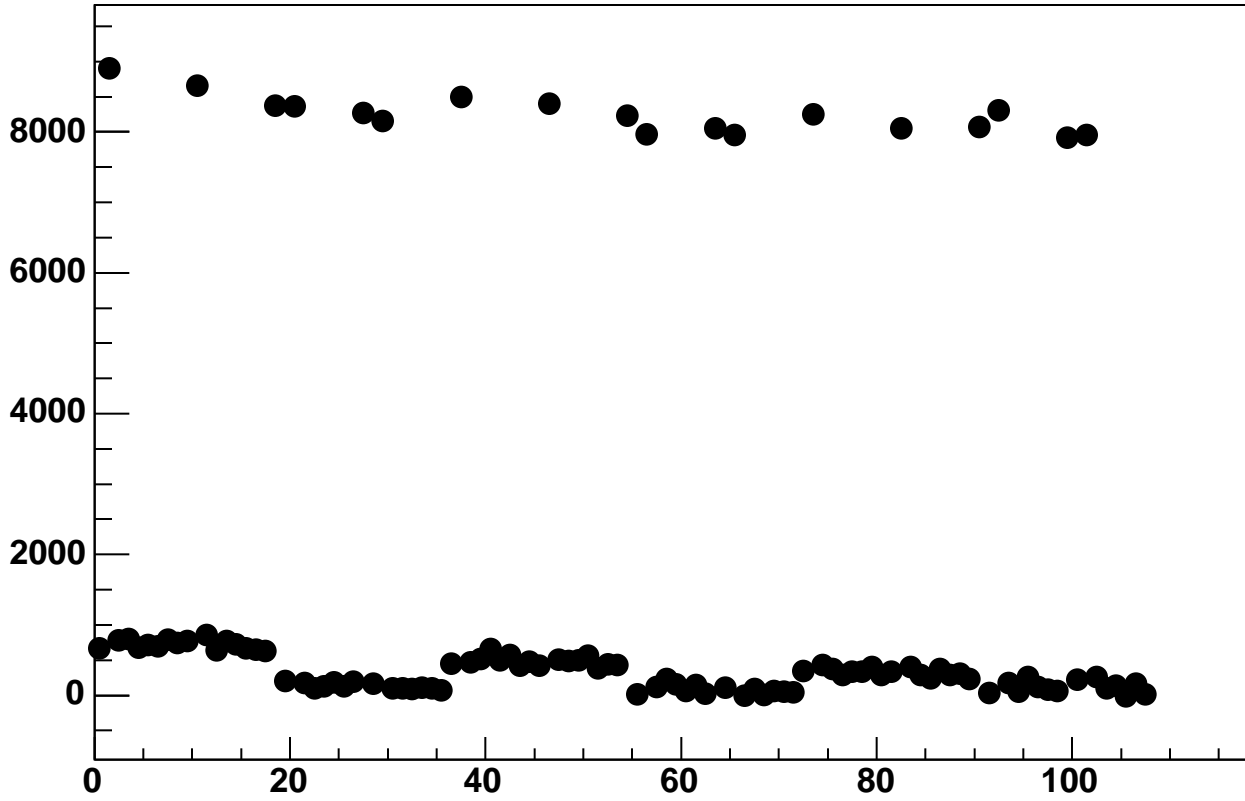
Enable 3, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



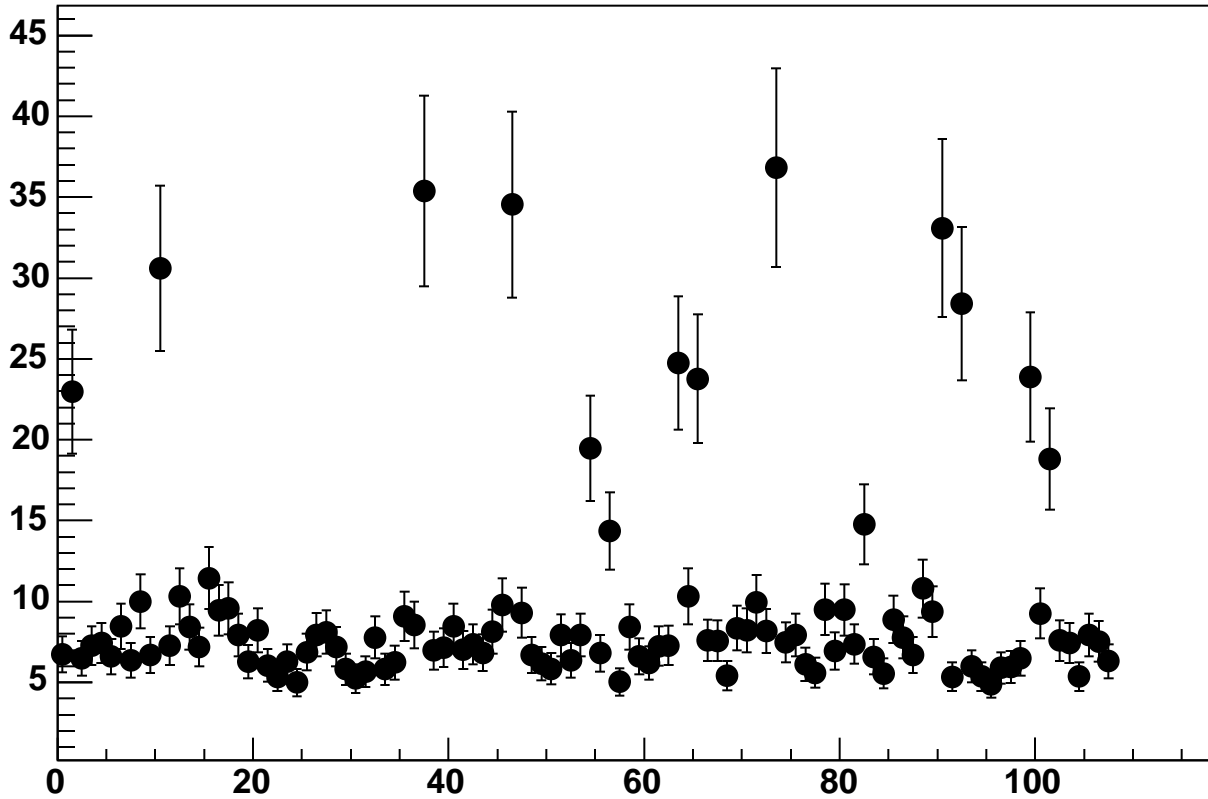
Enable 3, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



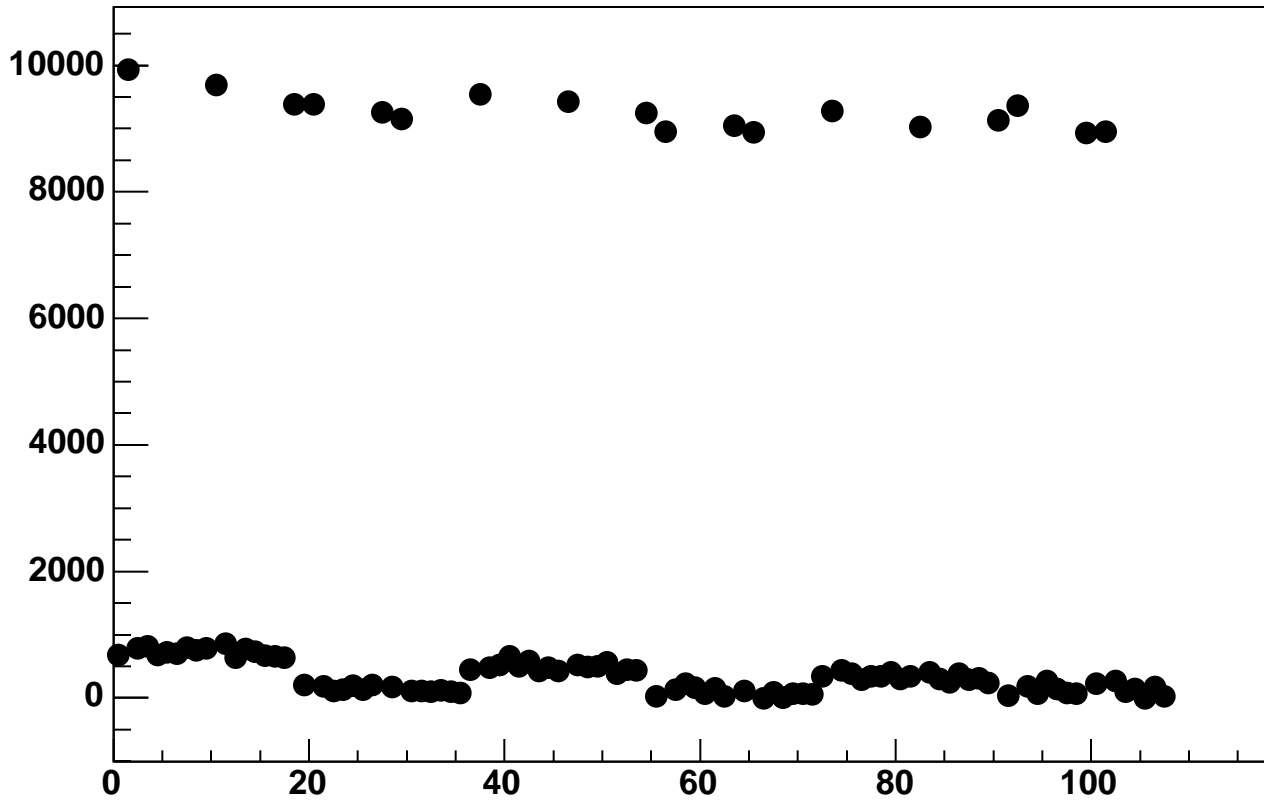
Enable 3, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



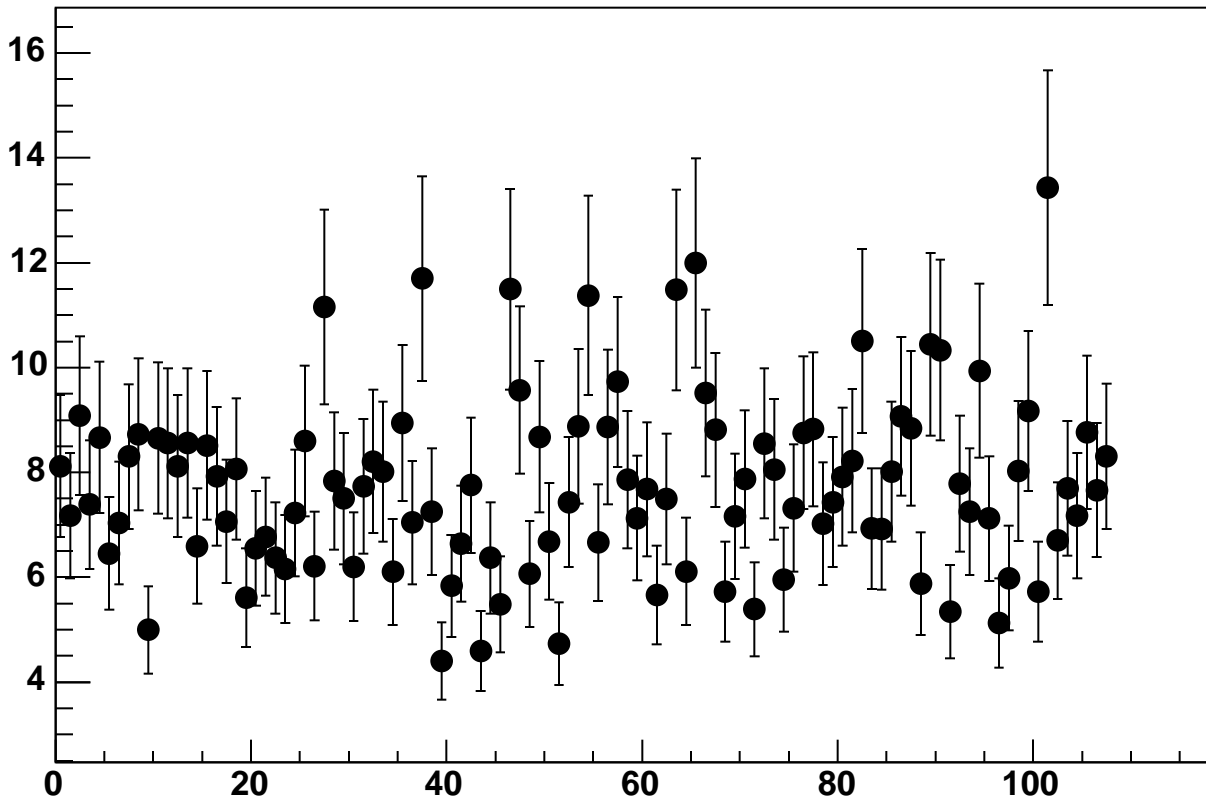
Enable 3, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



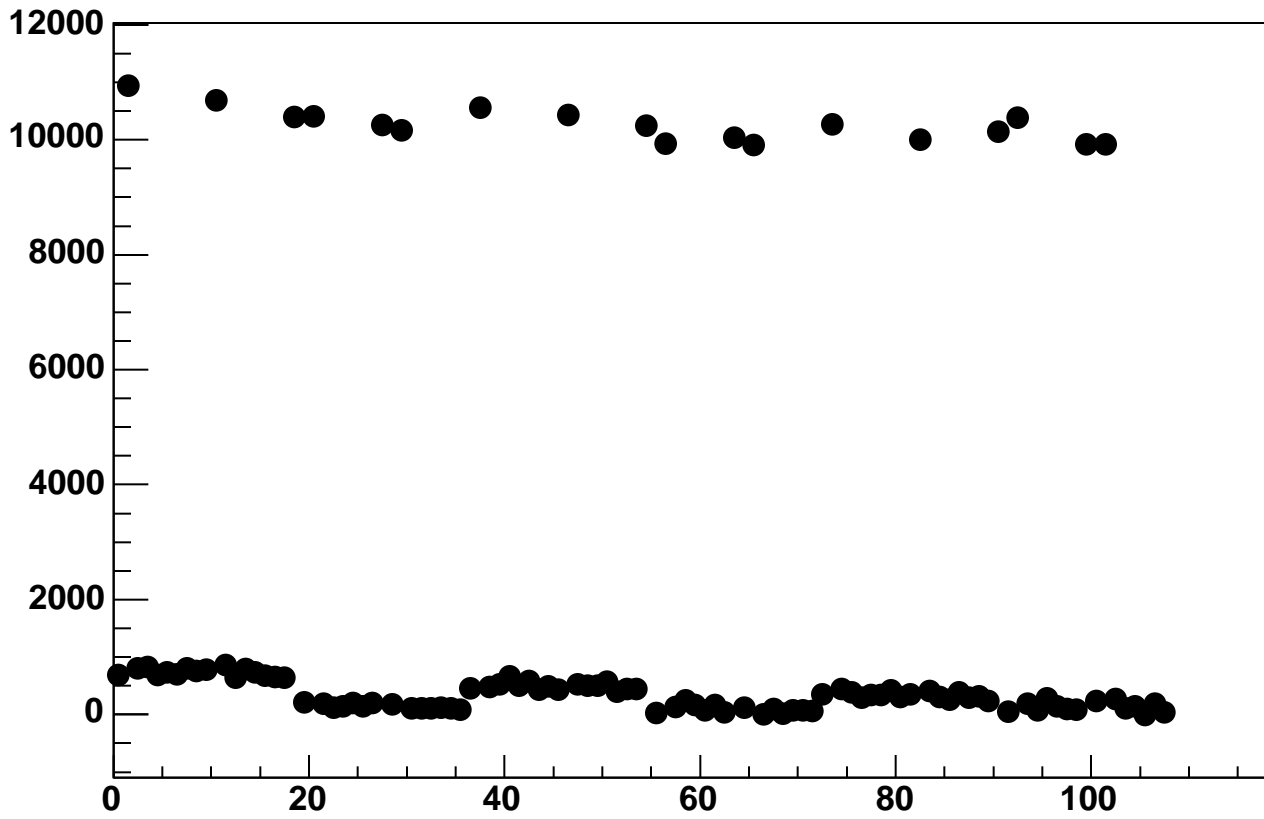
Enable 3, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



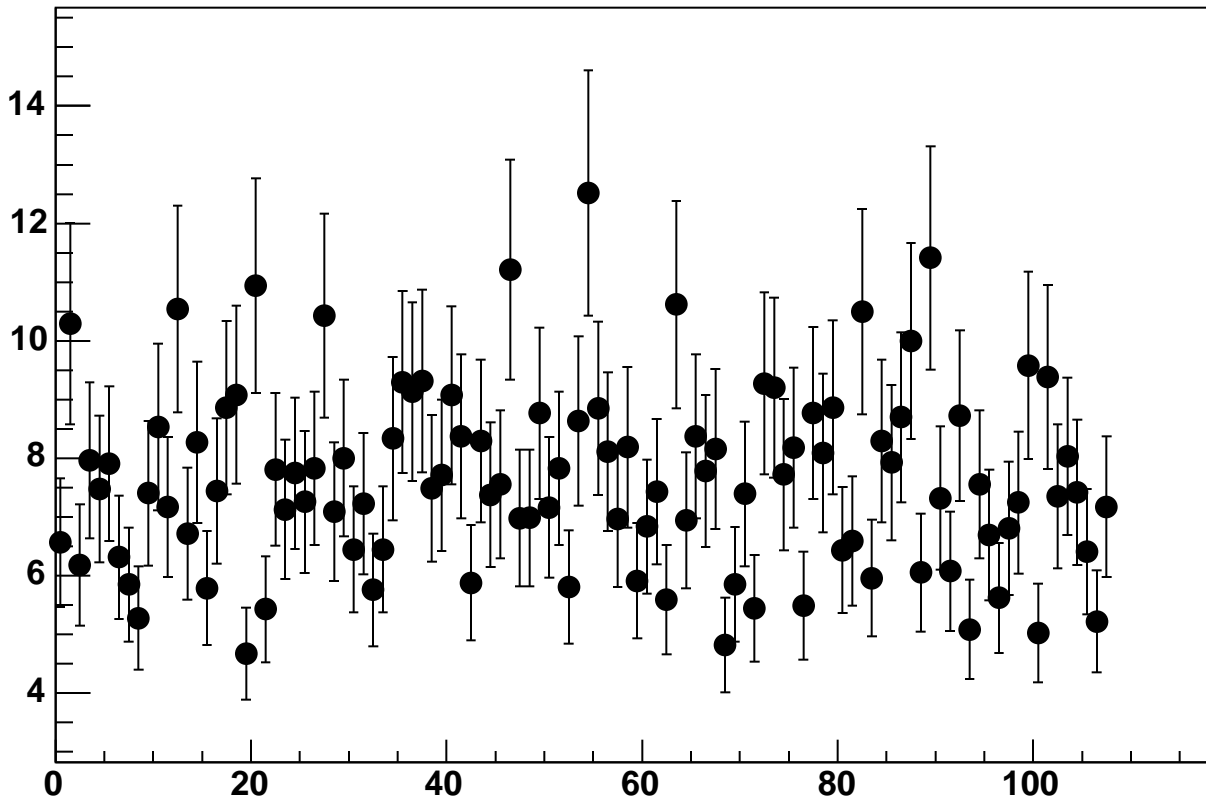
Enable 3, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



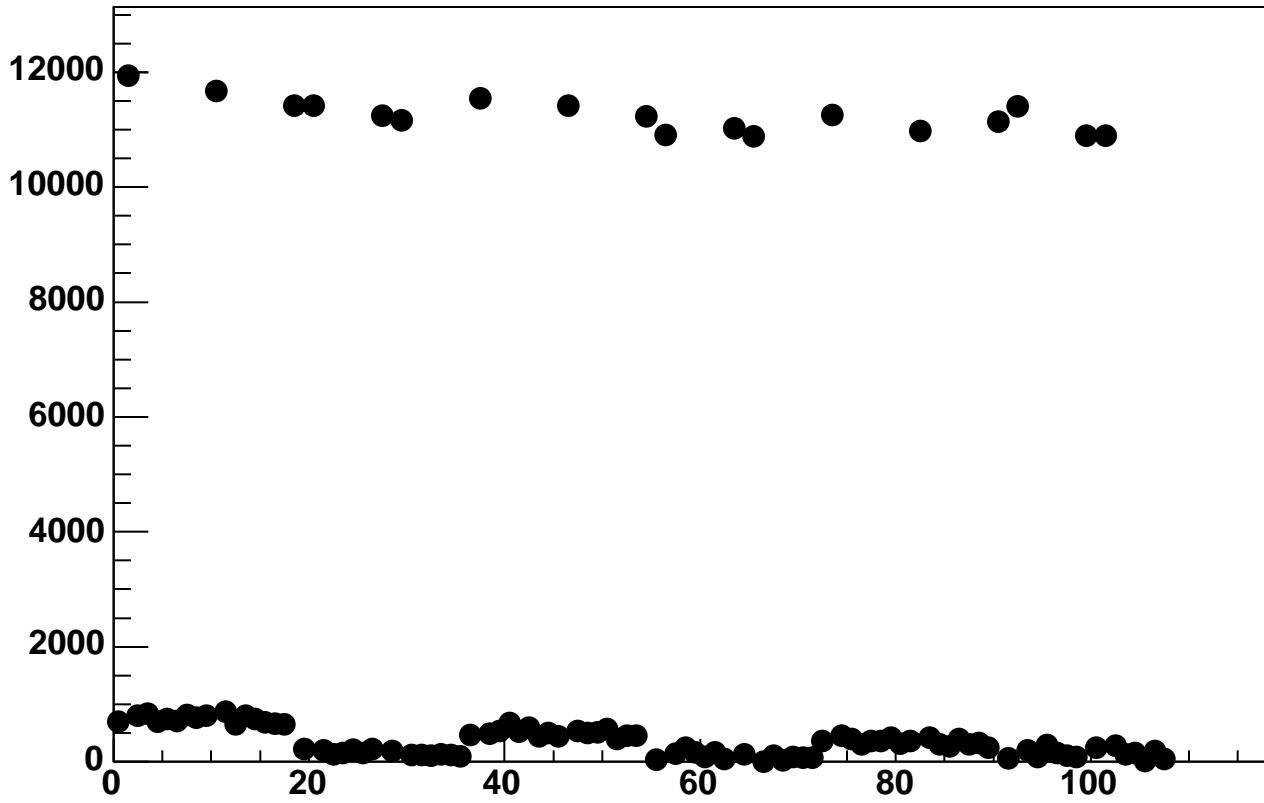
Enable 3, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



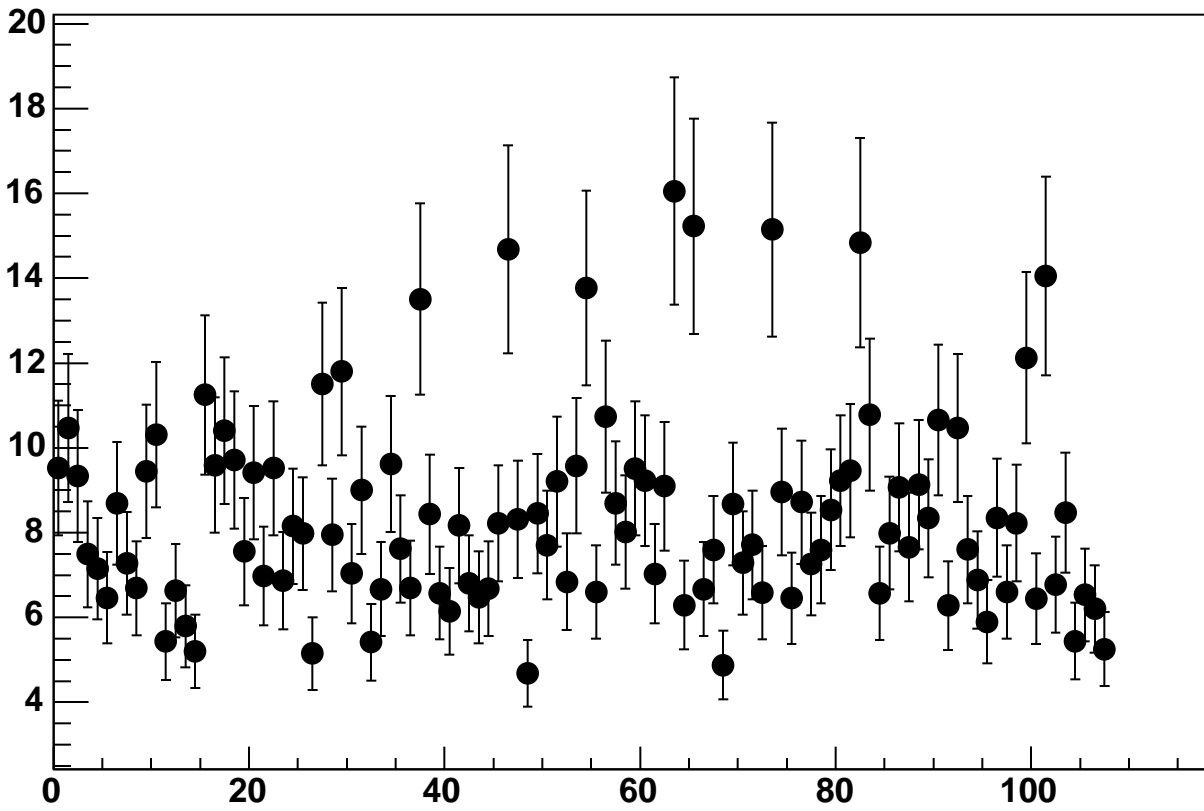
Enable 3, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



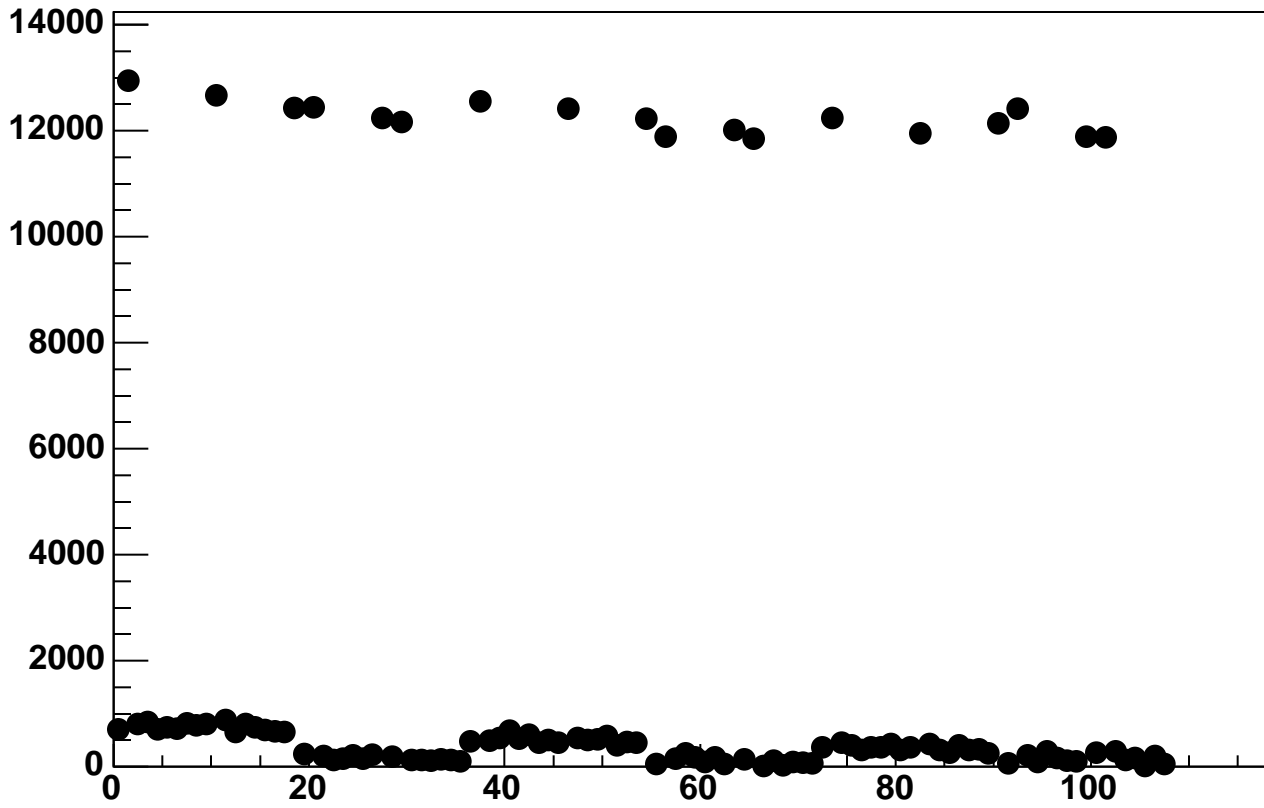
Enable 3, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



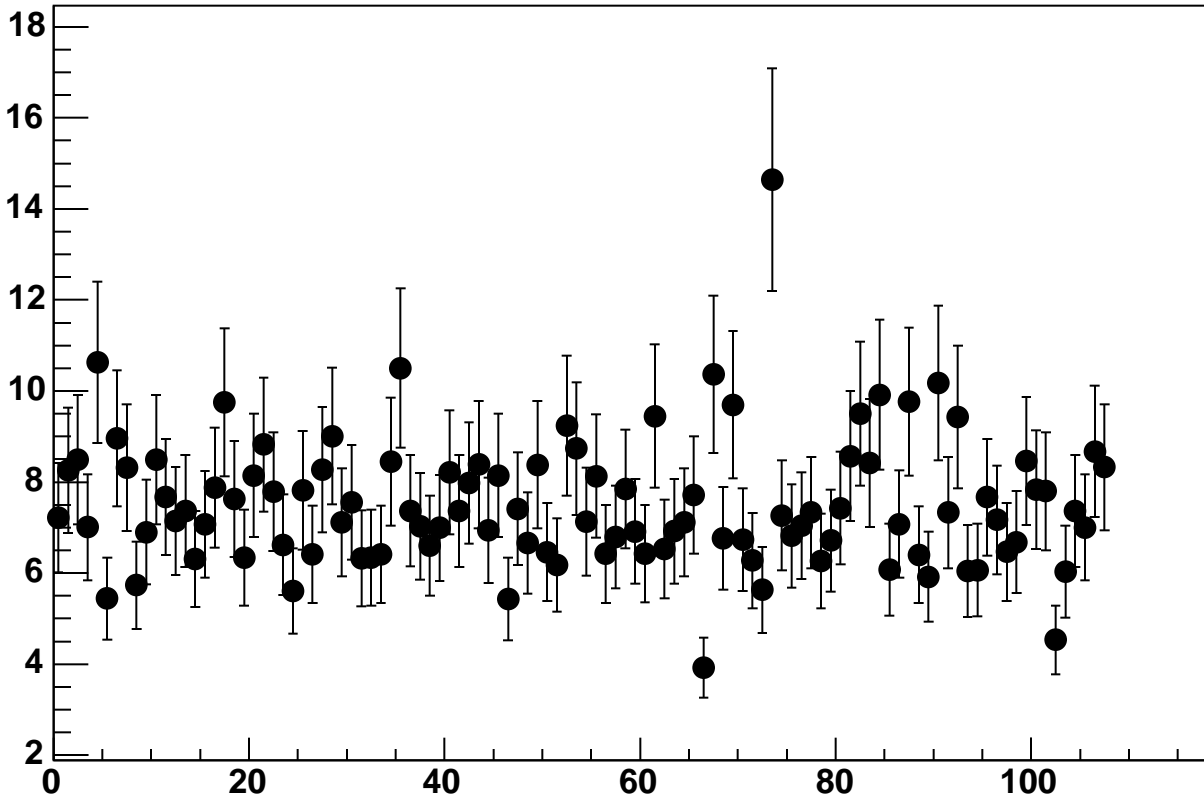
Enable 3, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



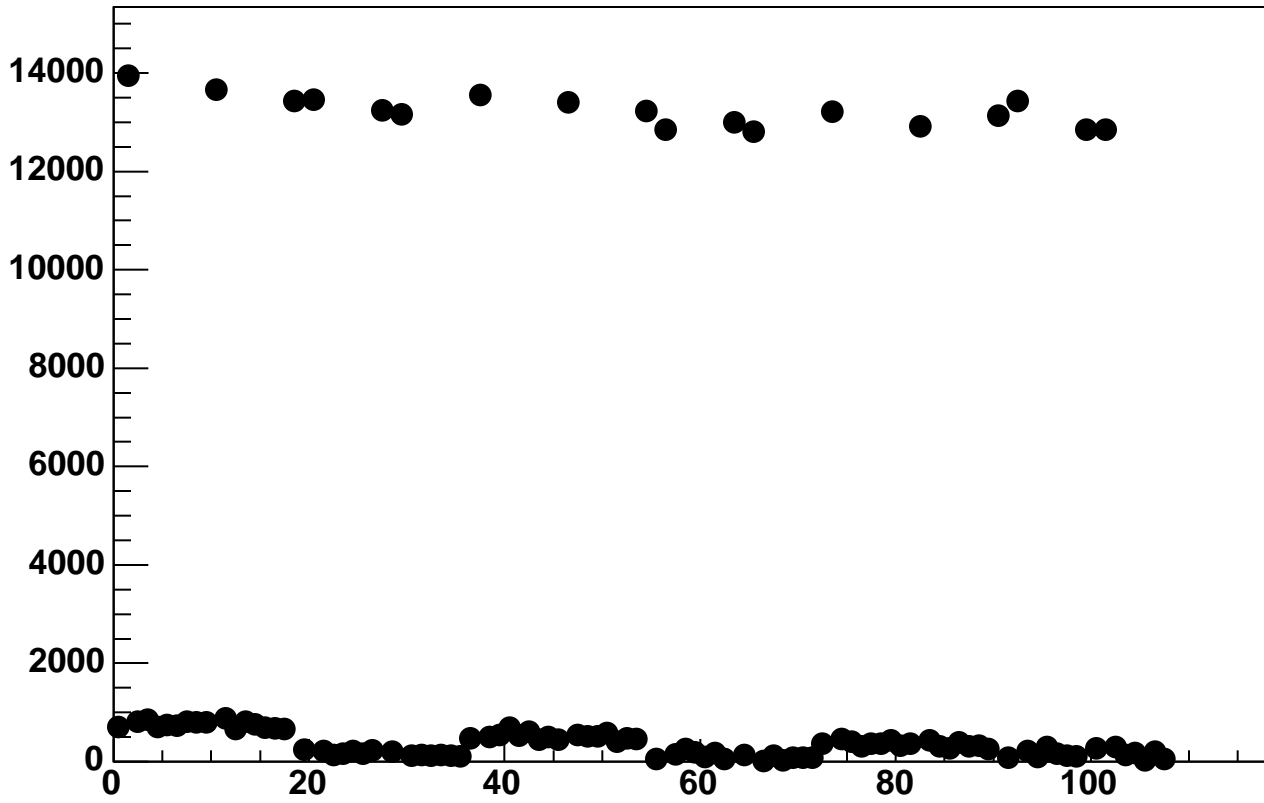
Enable 3, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



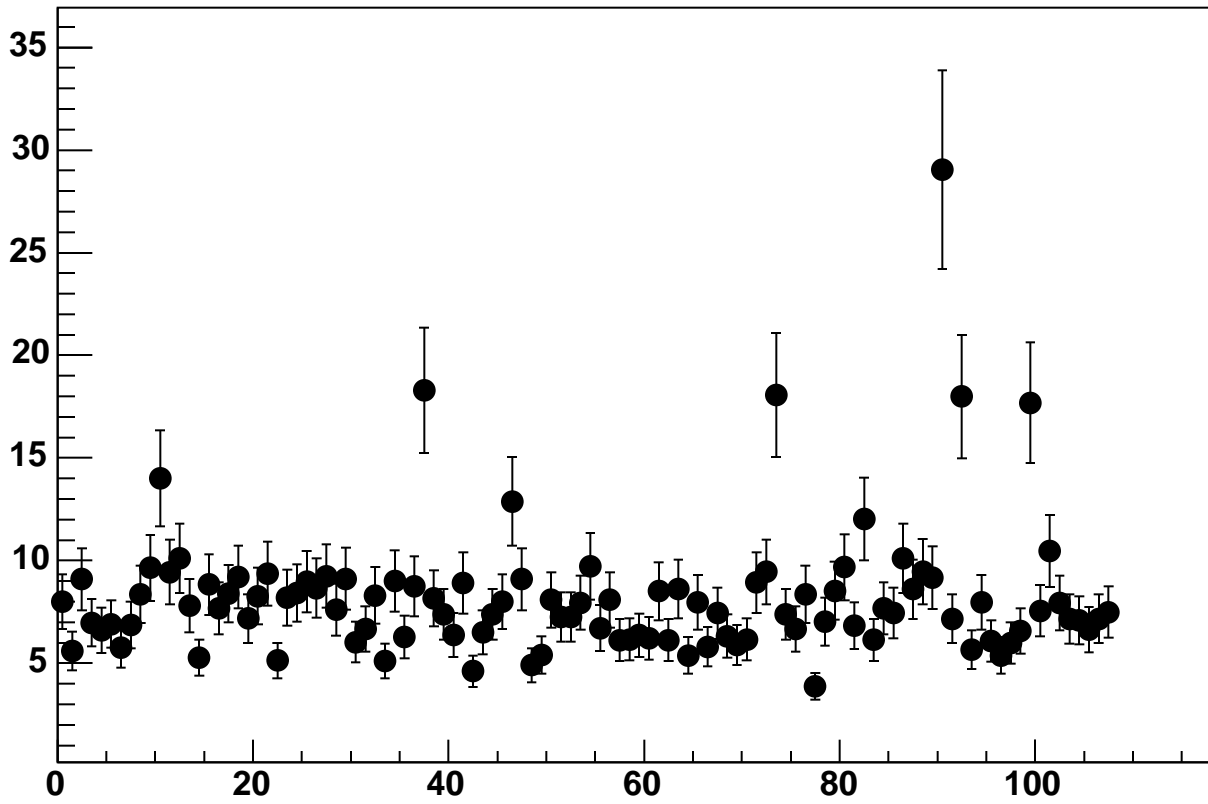
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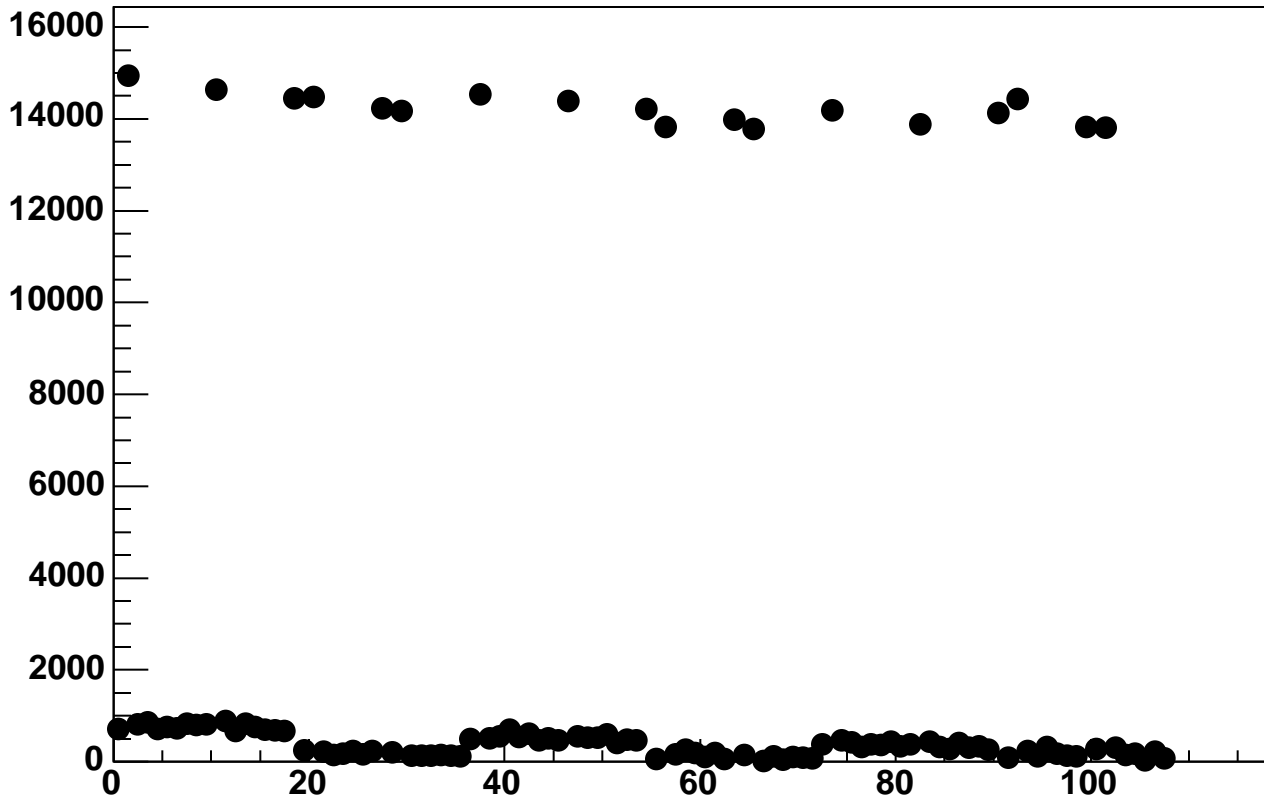
Enable 3, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



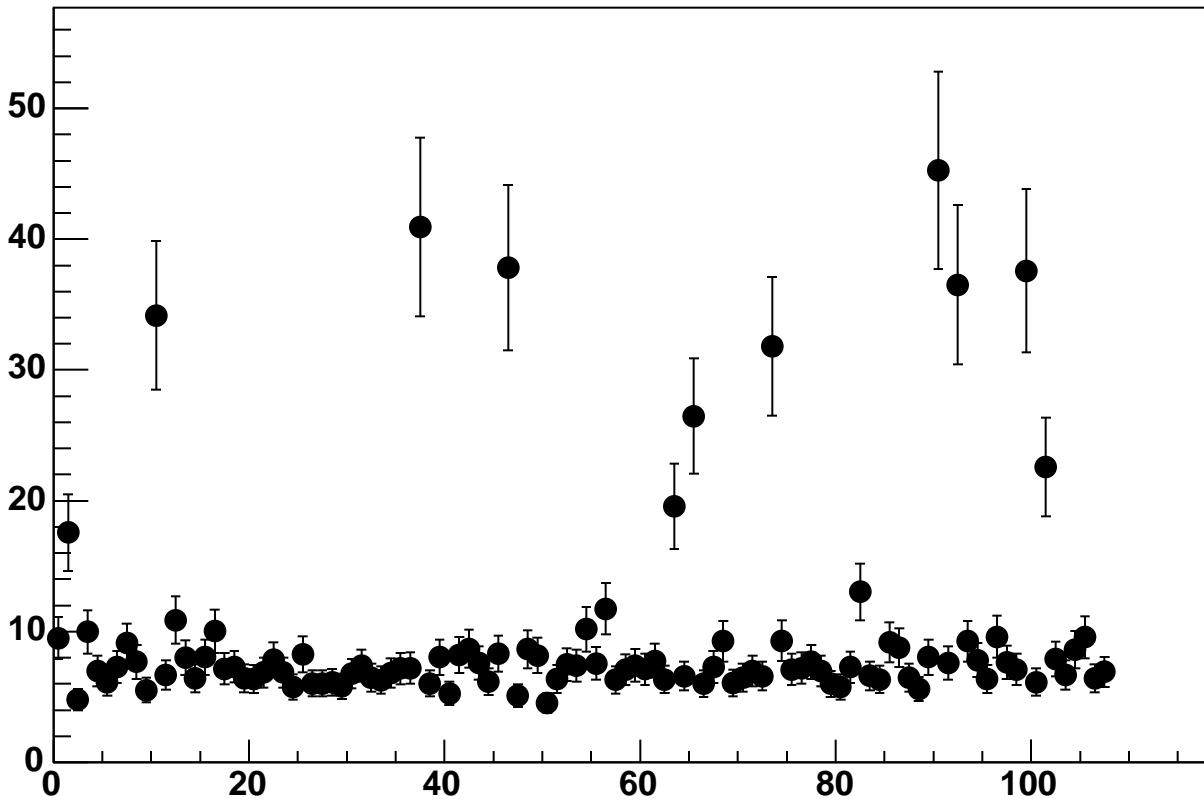
Enable 3, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



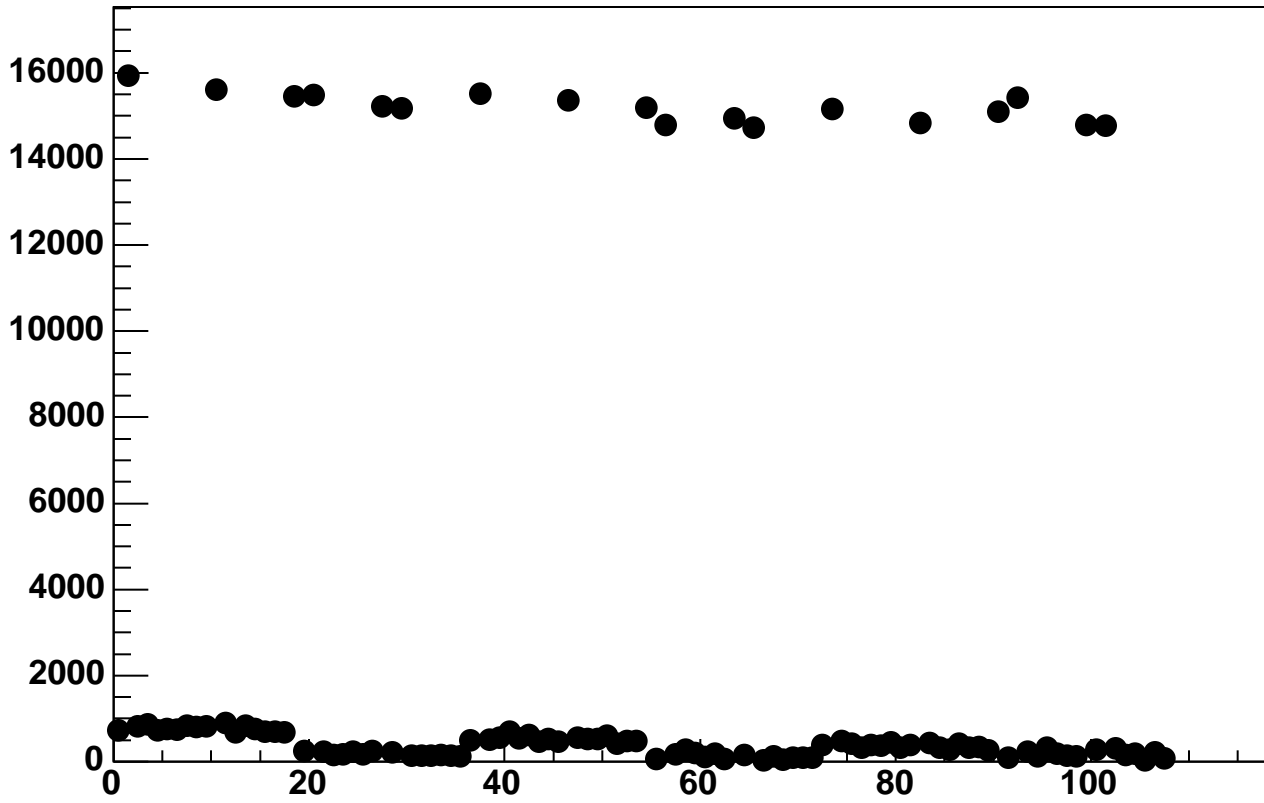
Enable 3, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



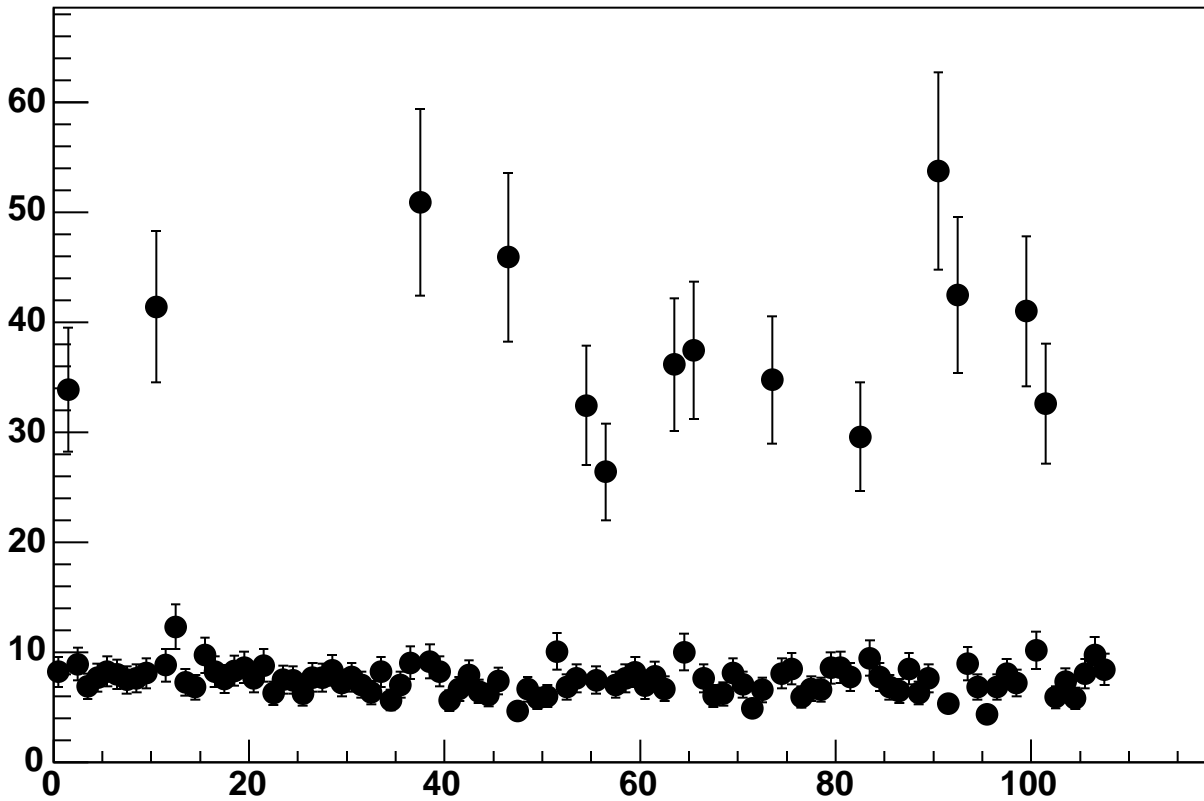
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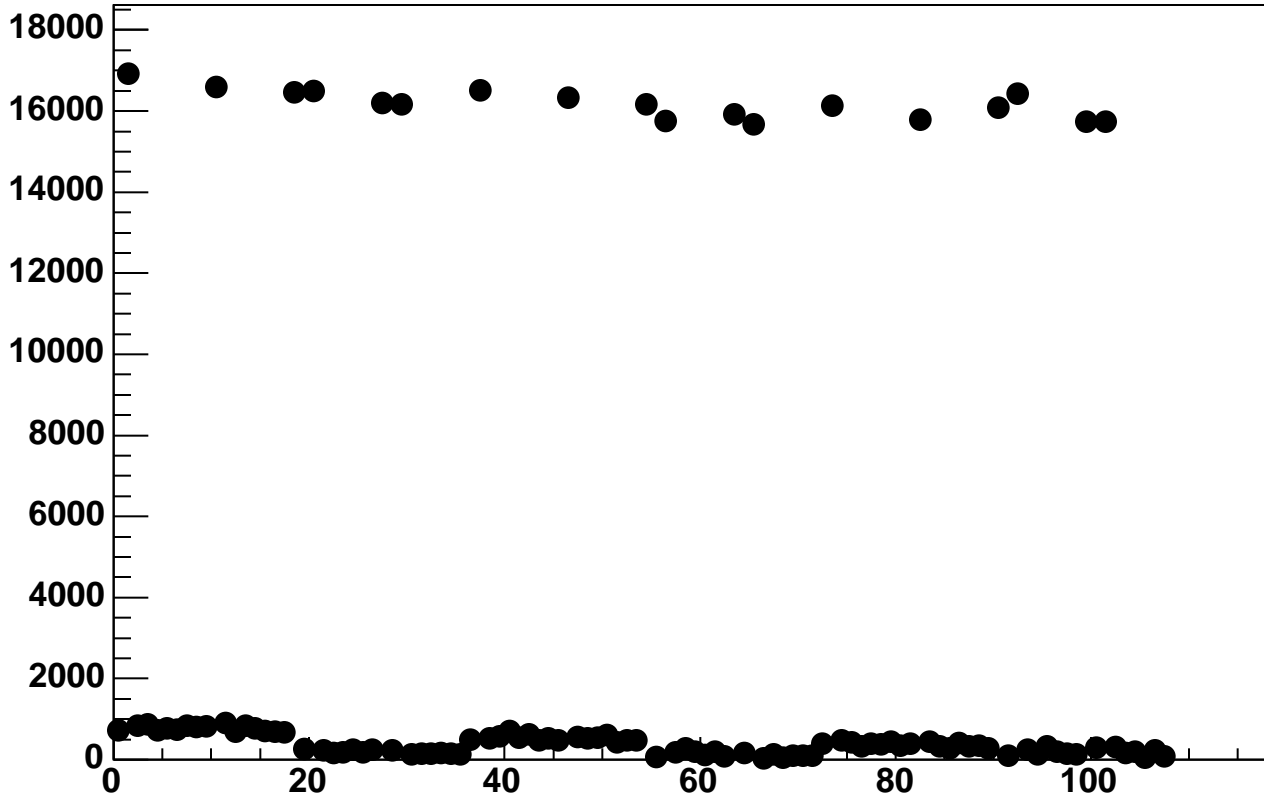
Enable 3, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



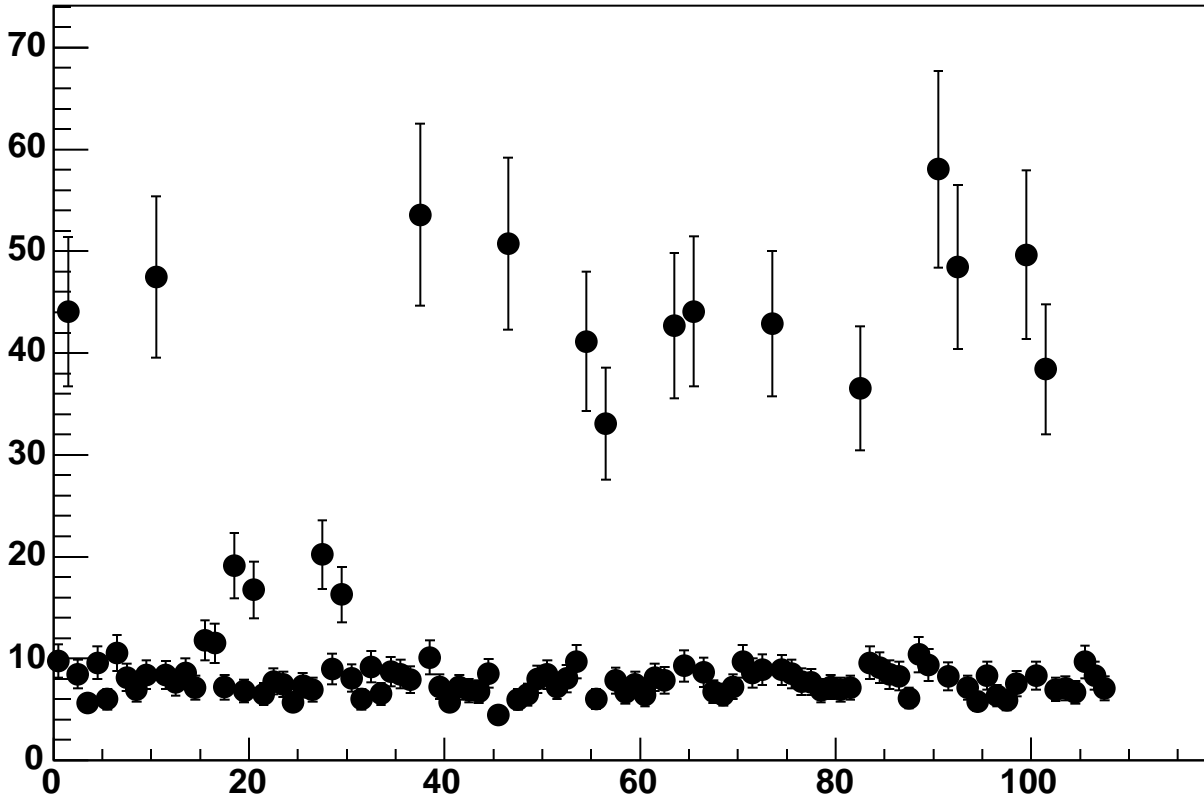
Enable 3, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



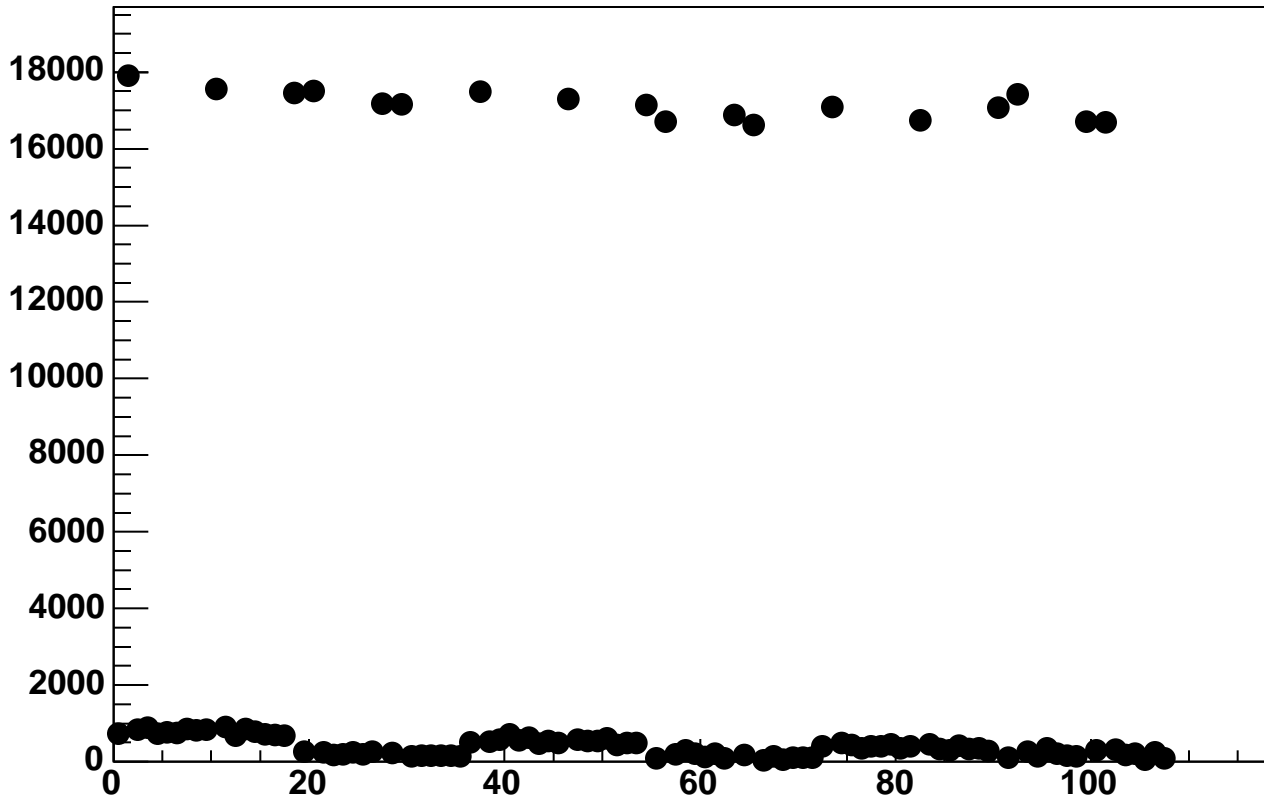
Enable 3, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



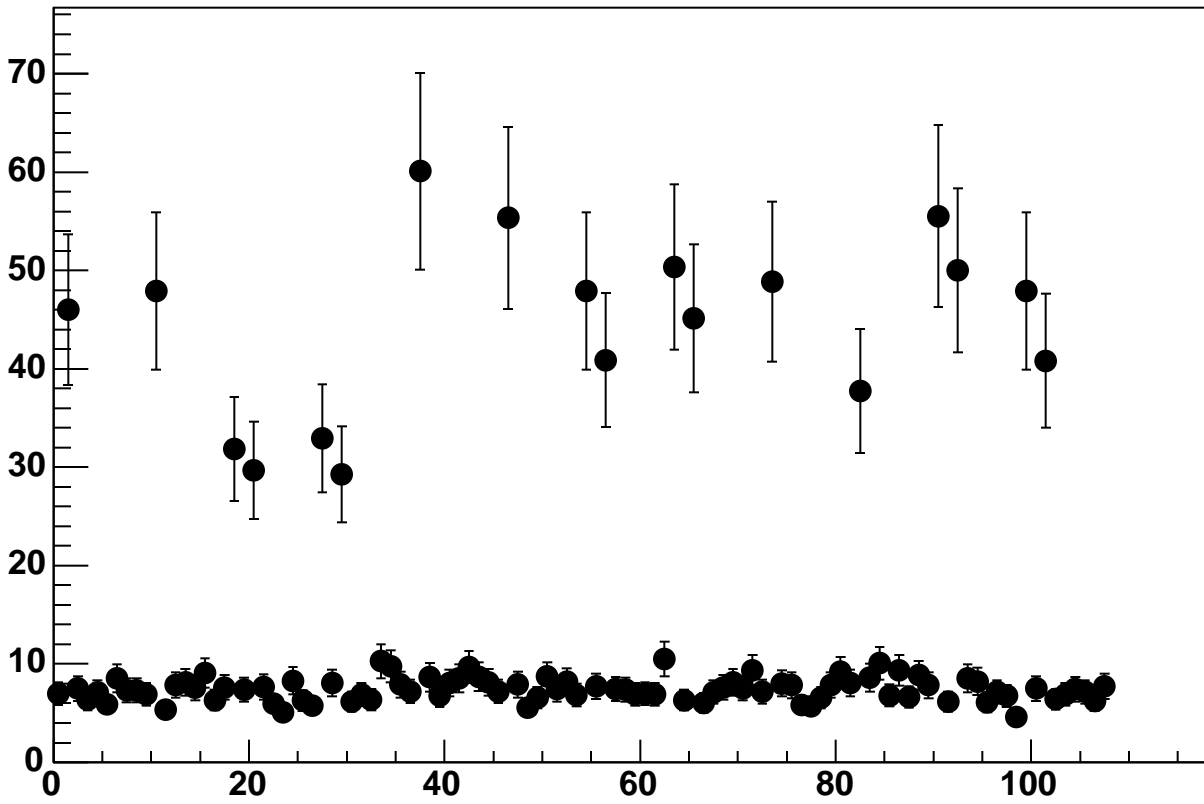
Enable 3, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



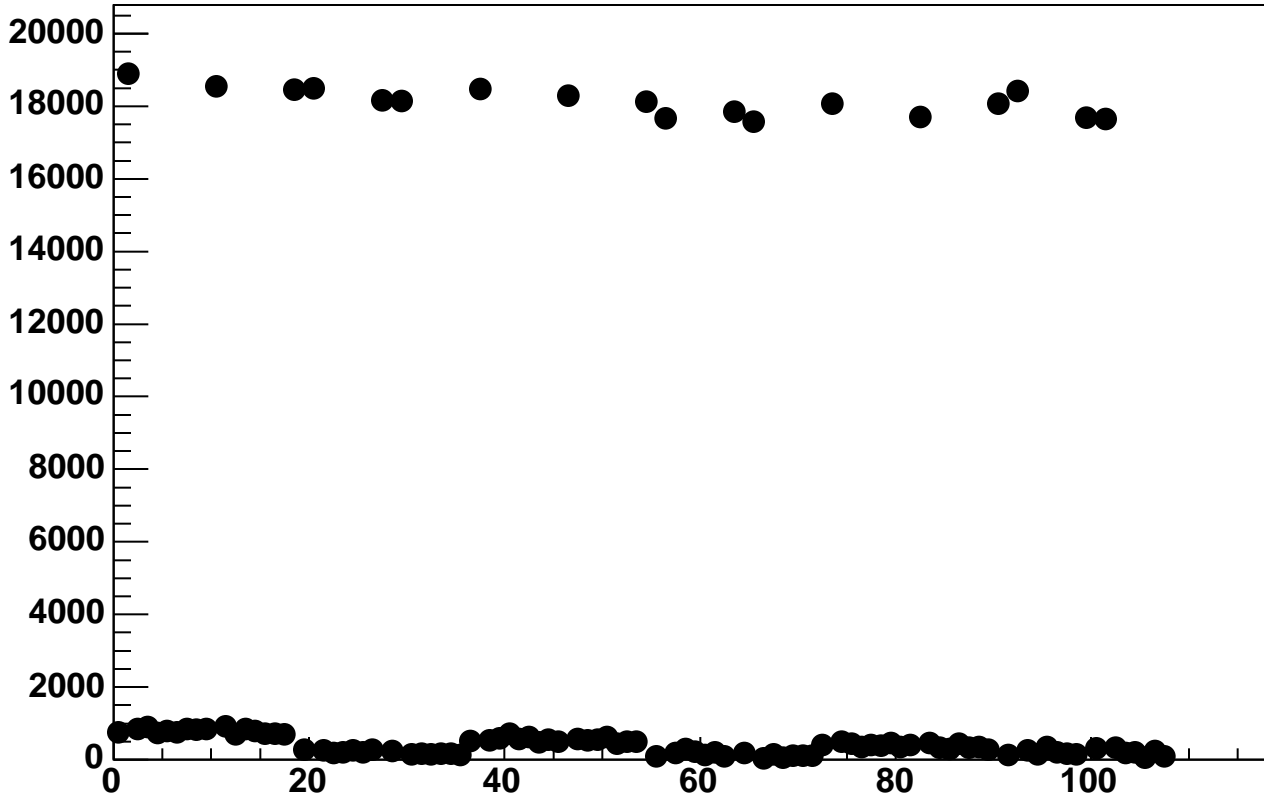
Enable 3, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



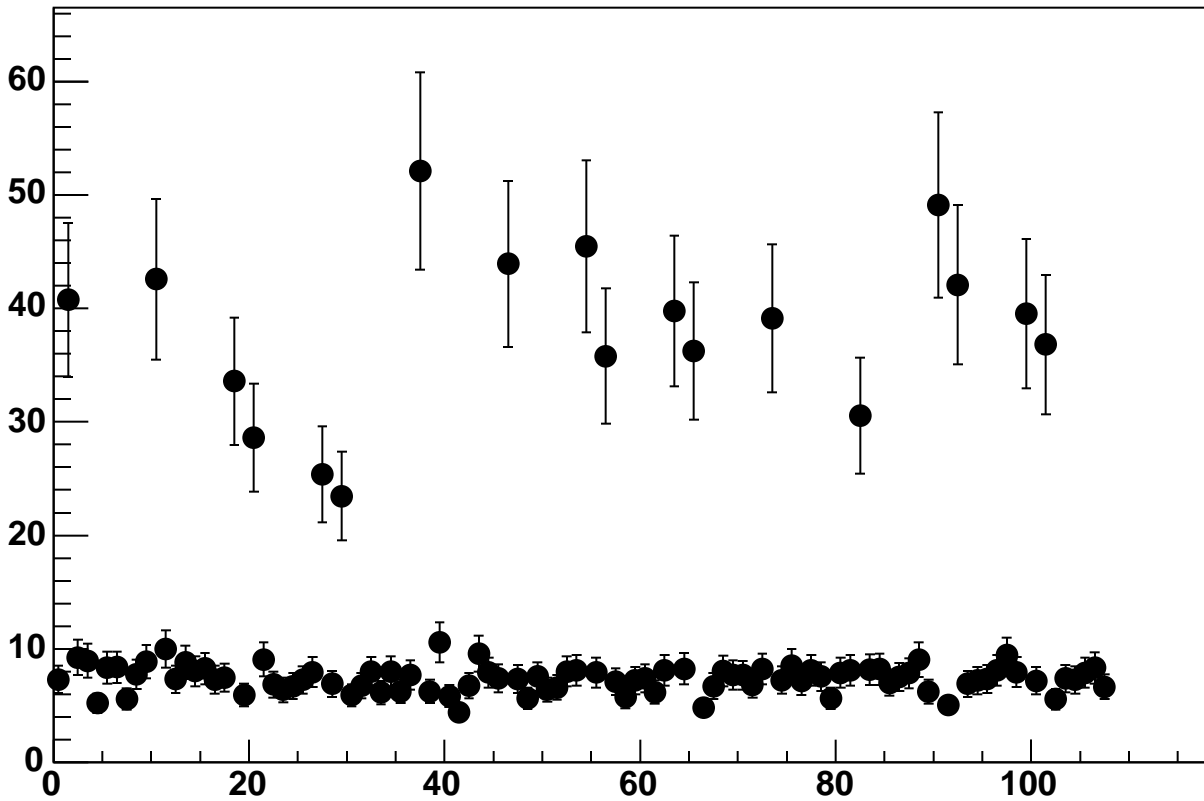
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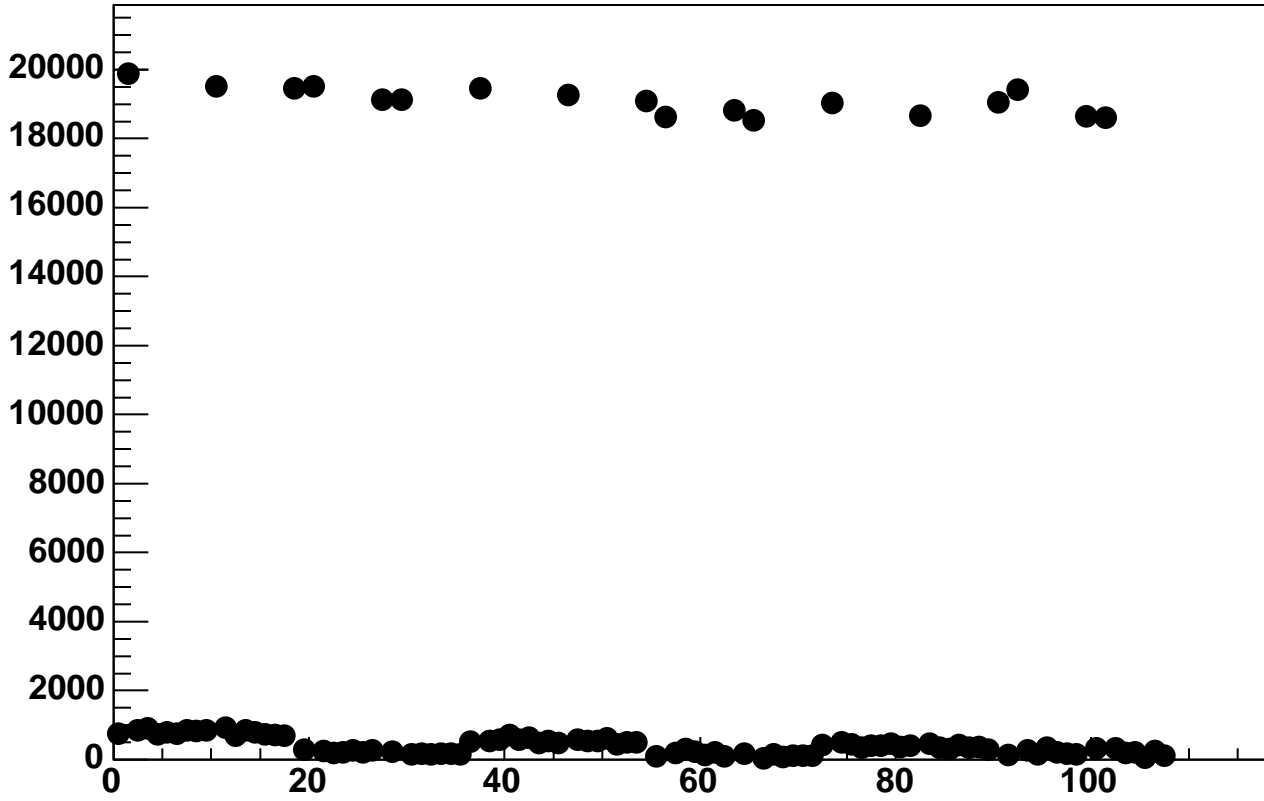
Enable 3, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



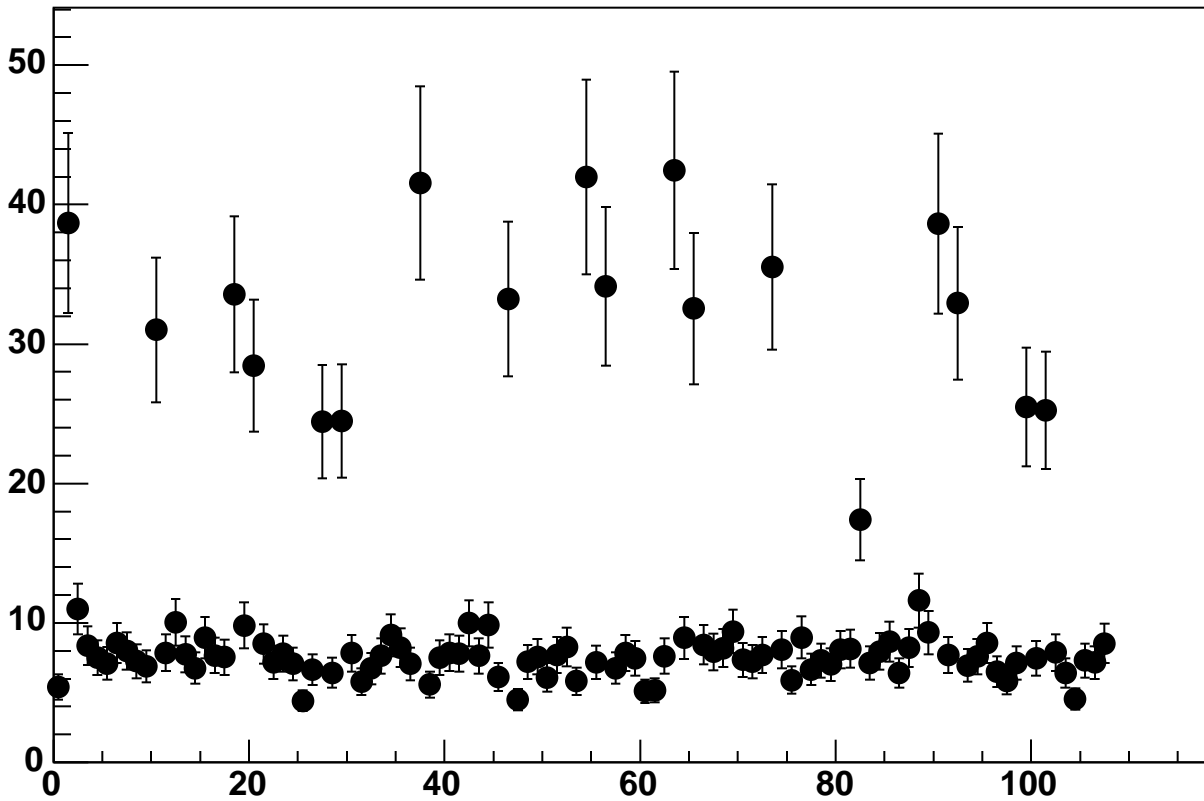
Enable 3, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



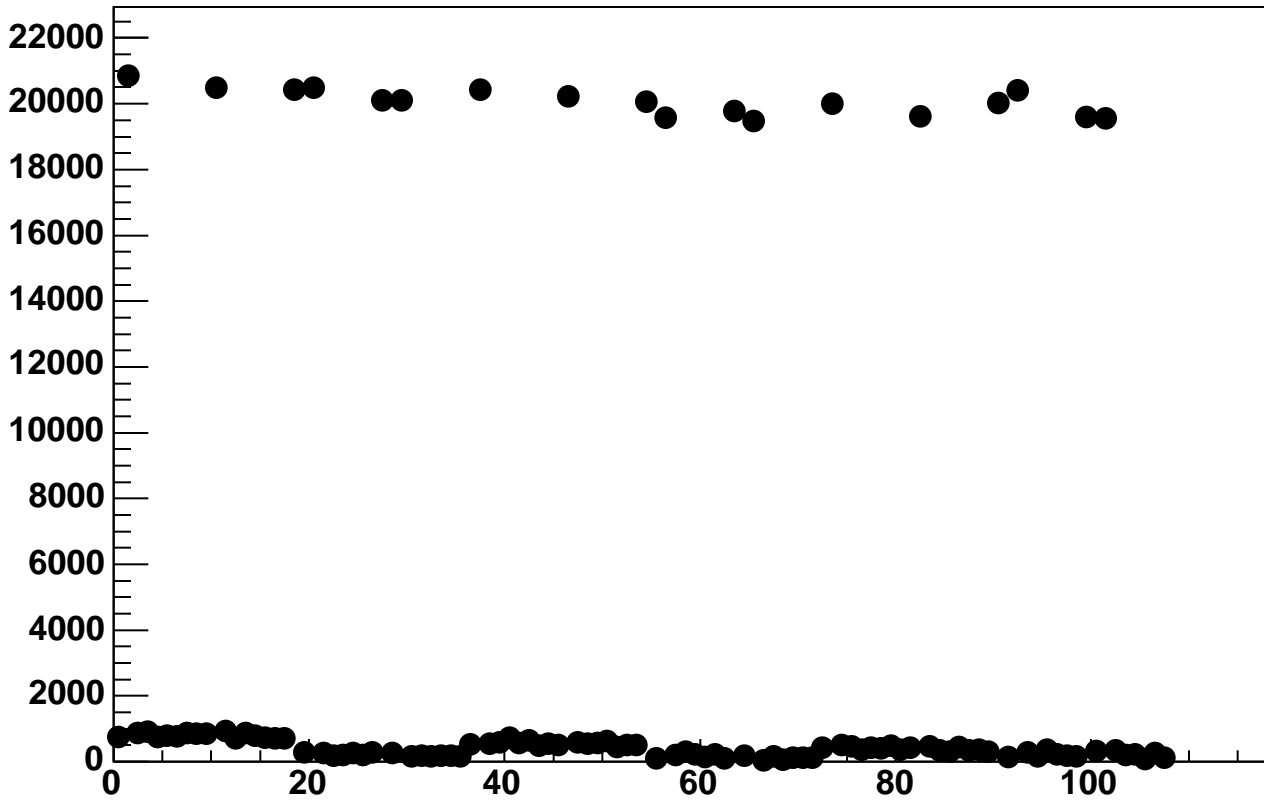
Enable 3, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



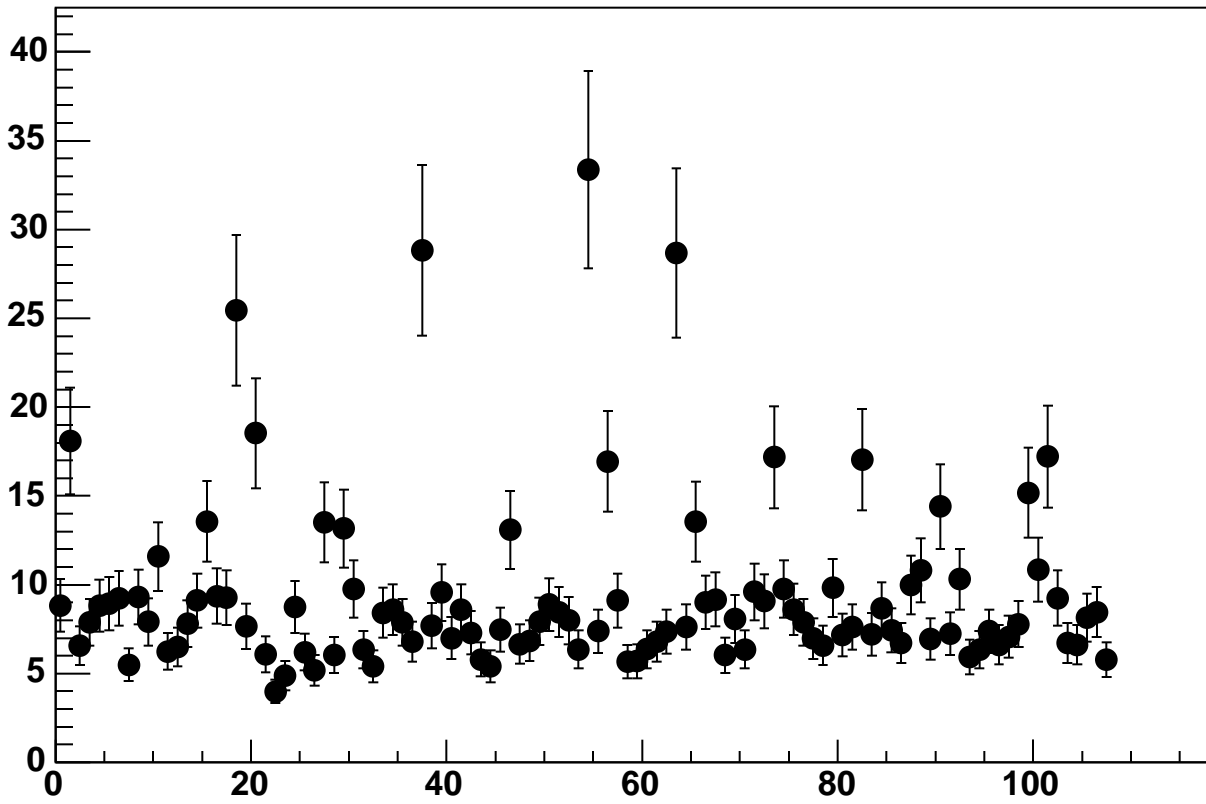
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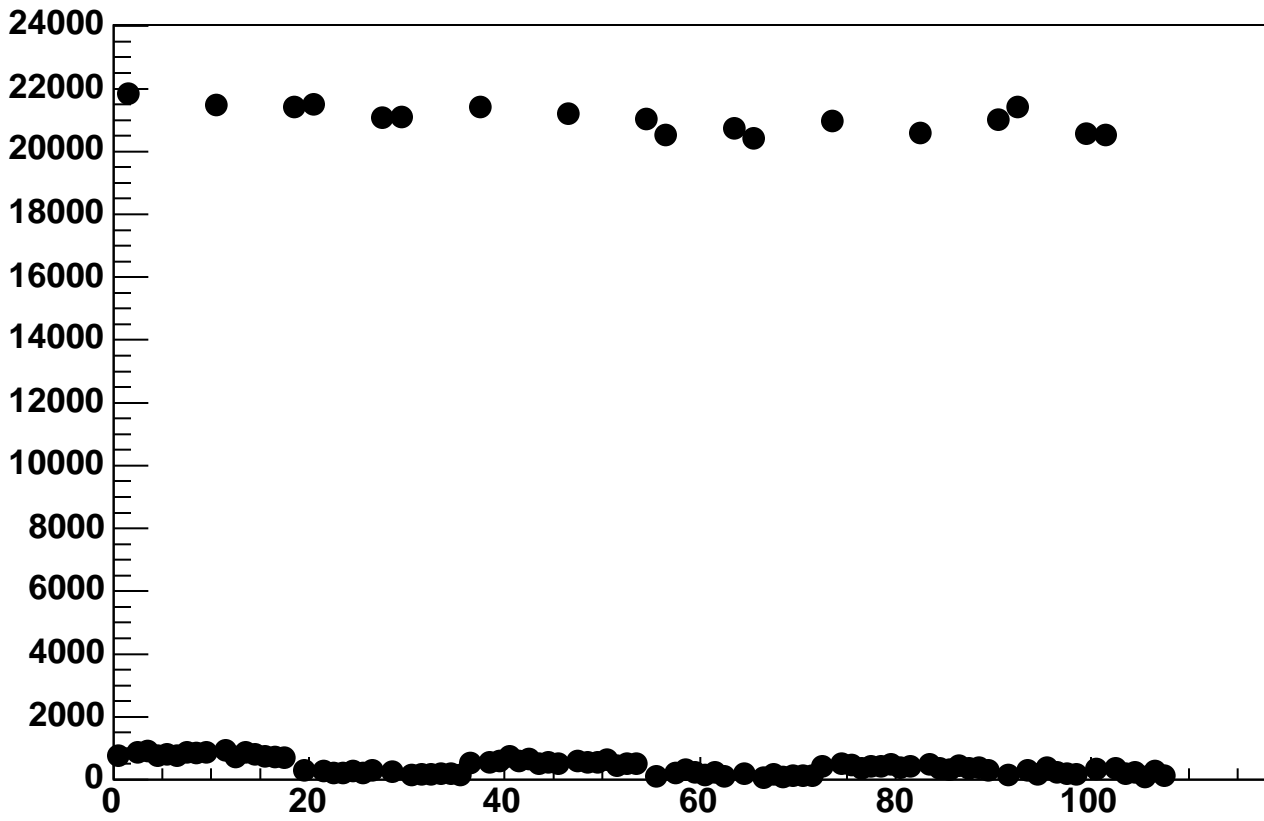
Enable 3, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



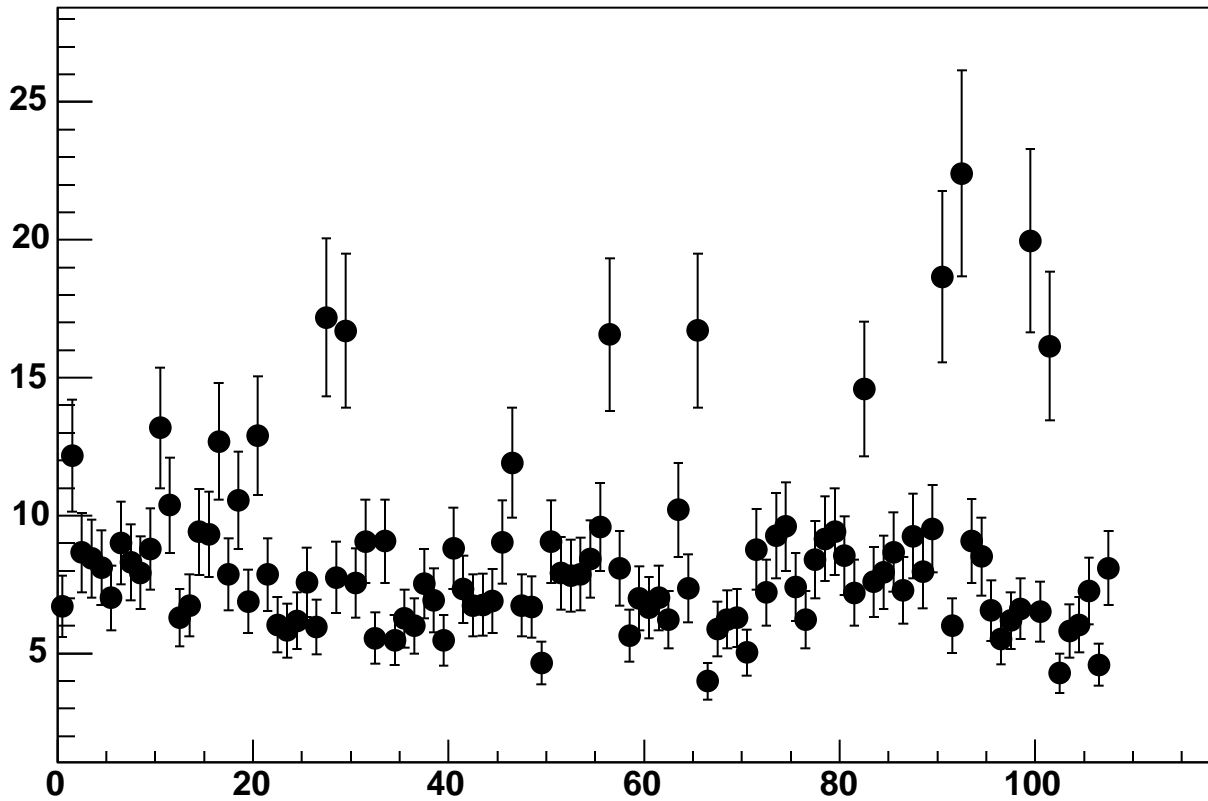
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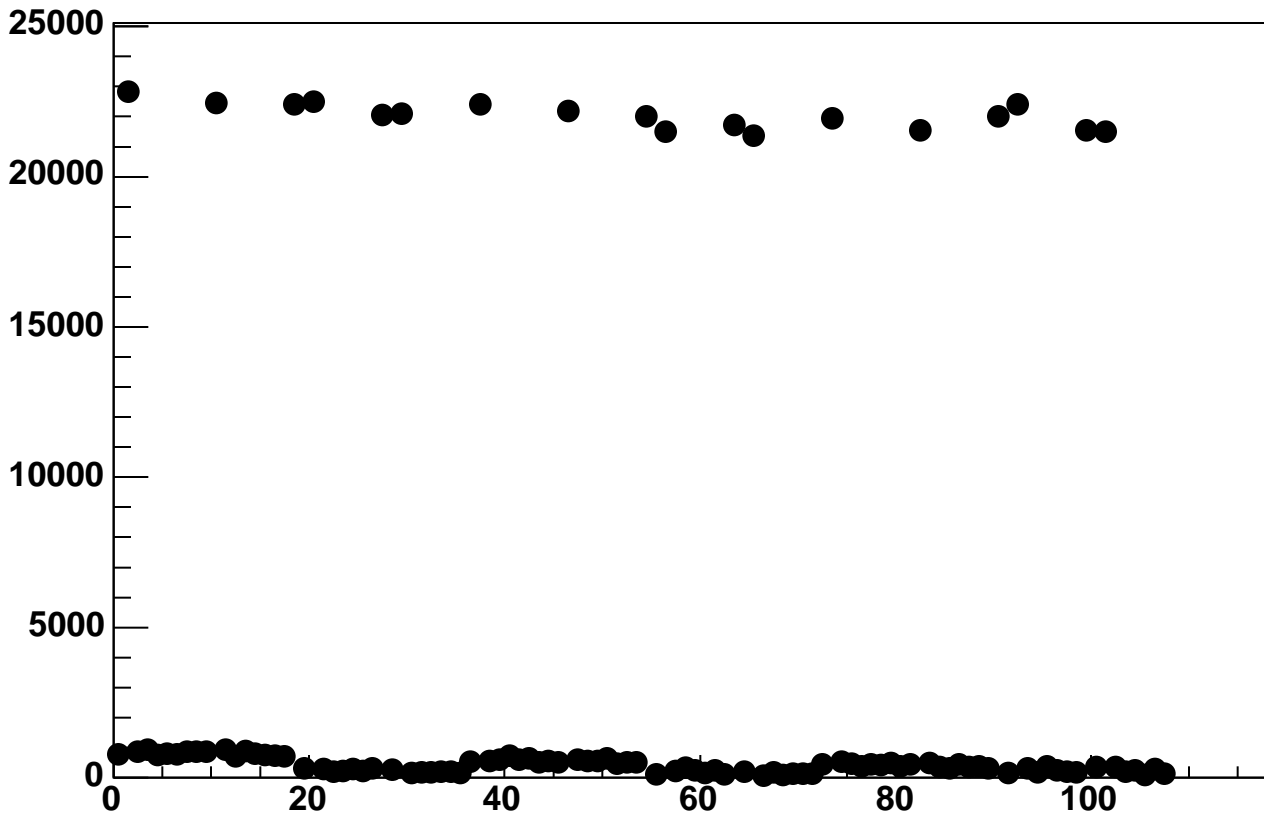
Enable 3, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



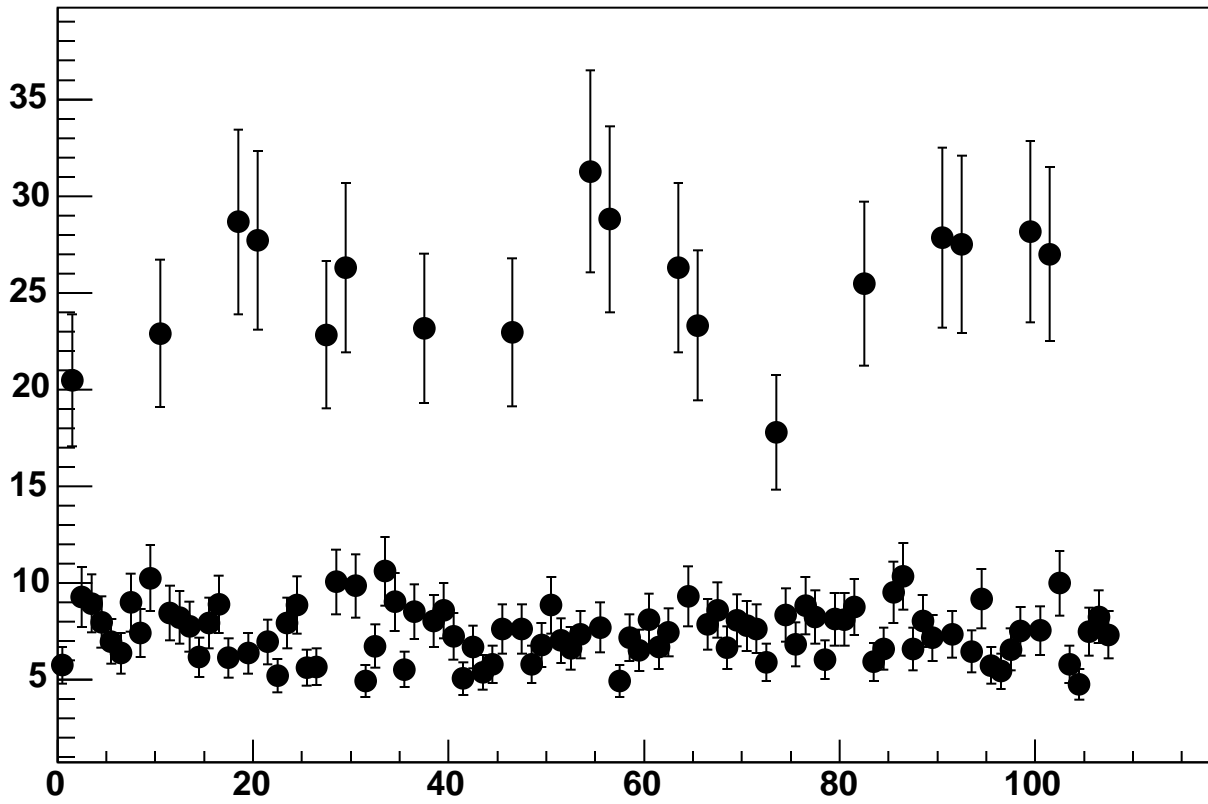
Enable 3, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



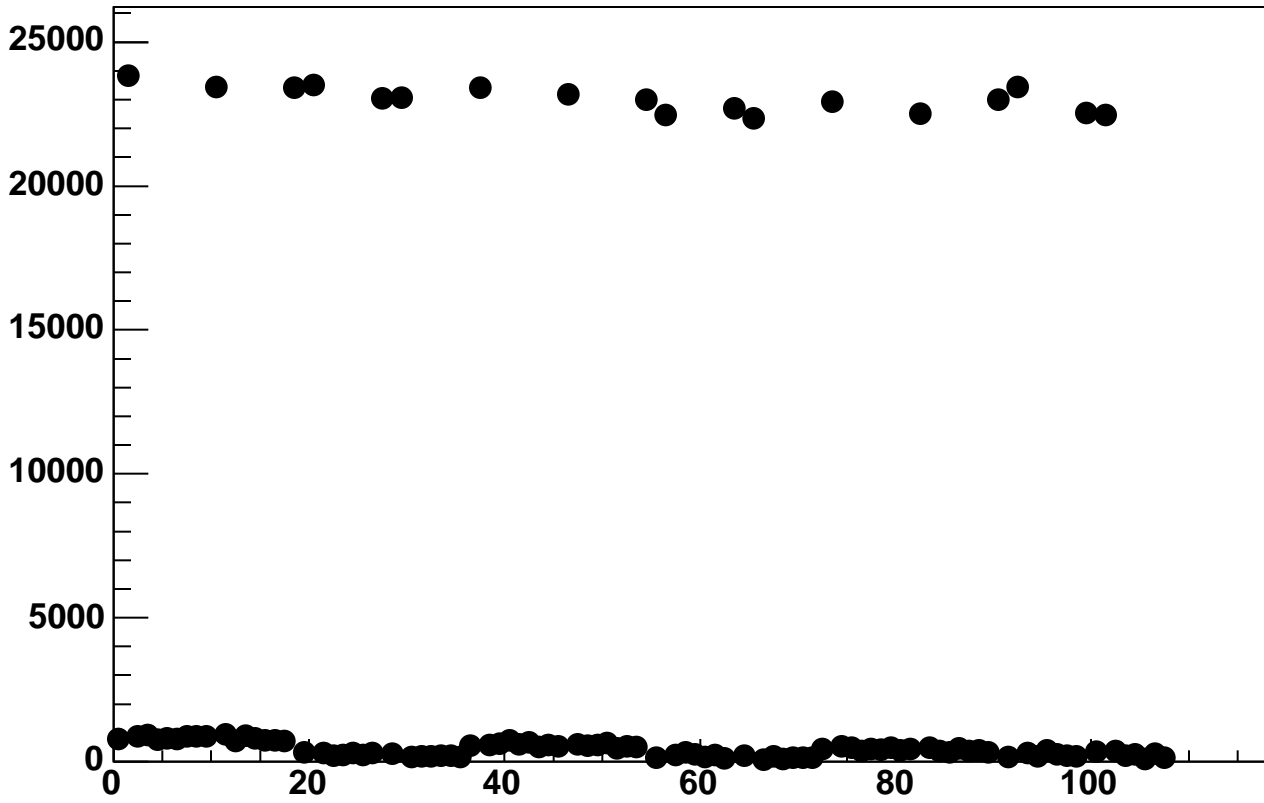
Enable 3, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



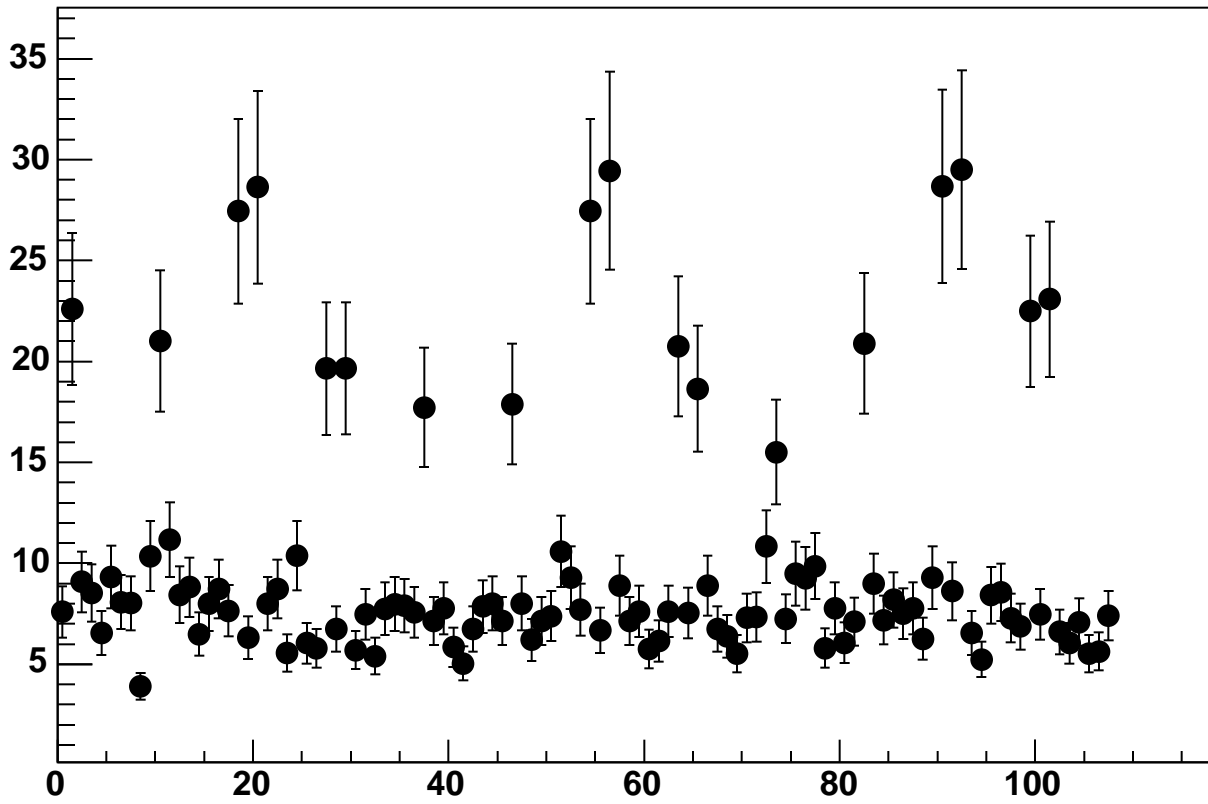
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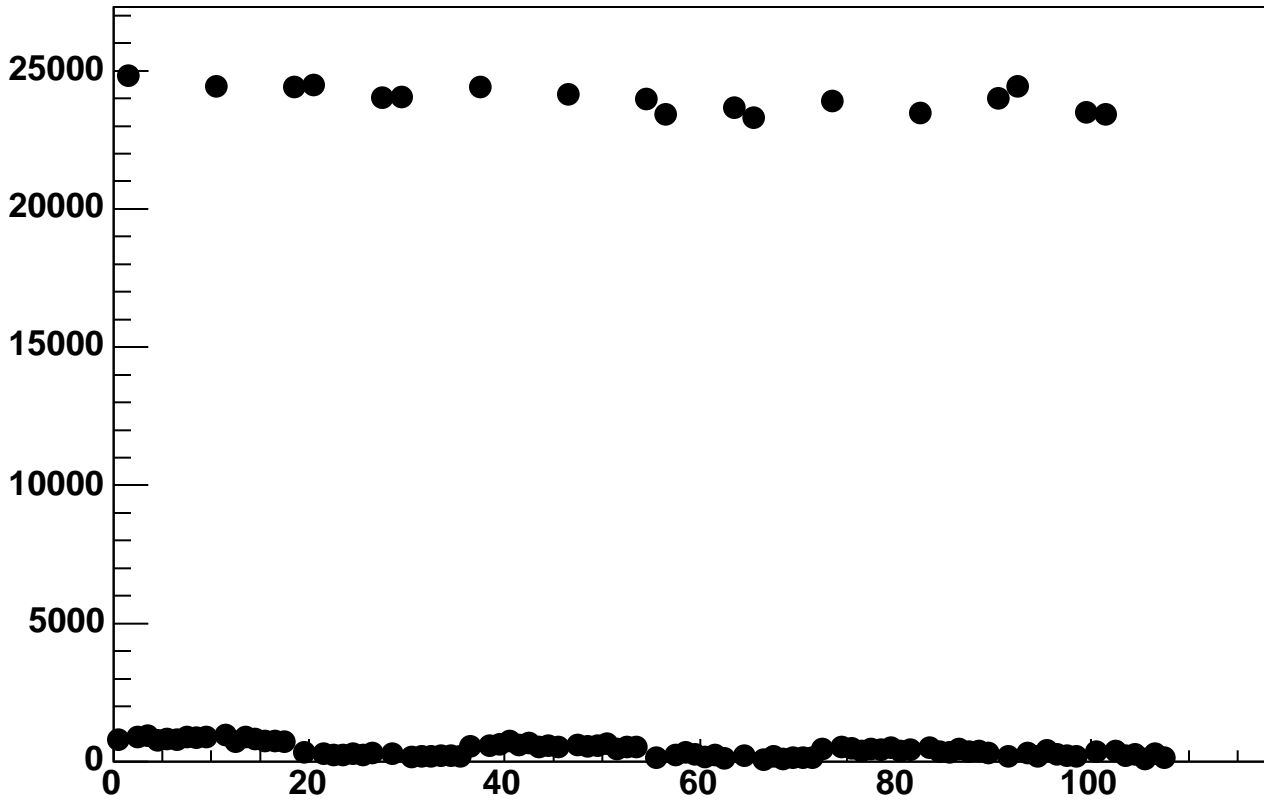
Enable 3, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



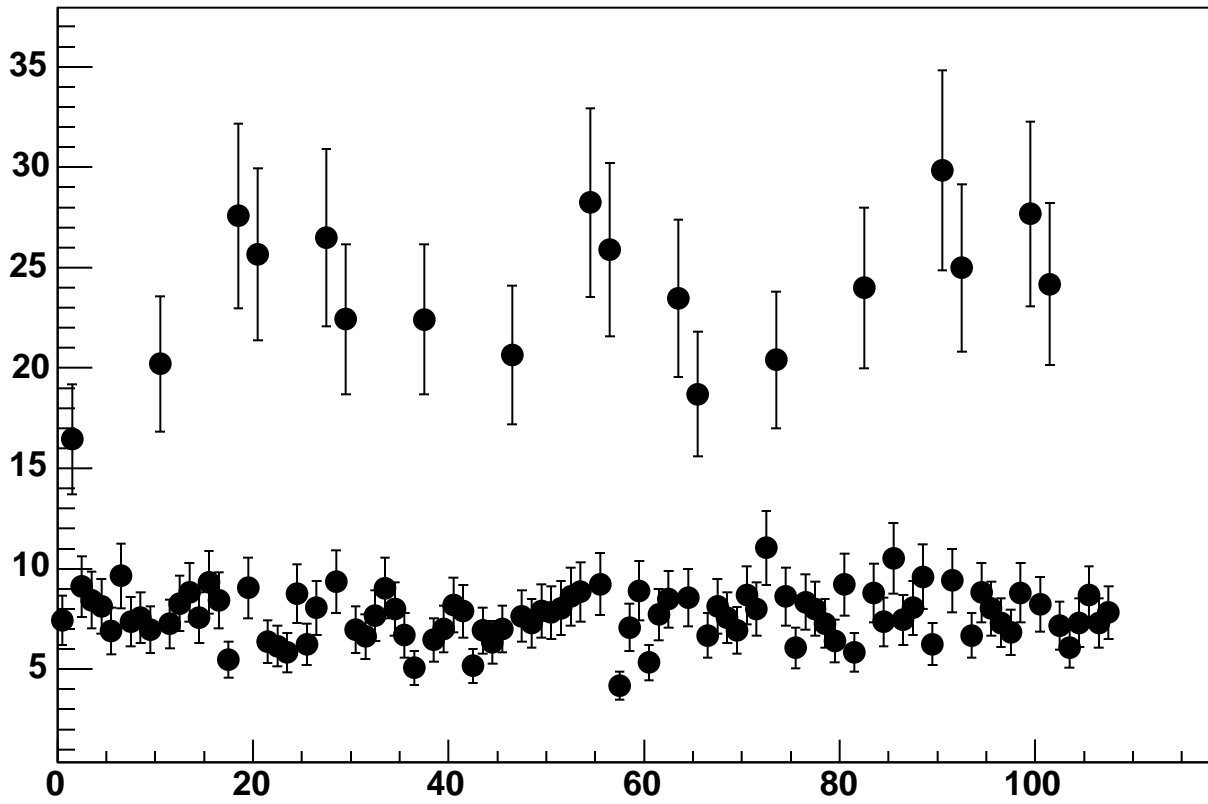
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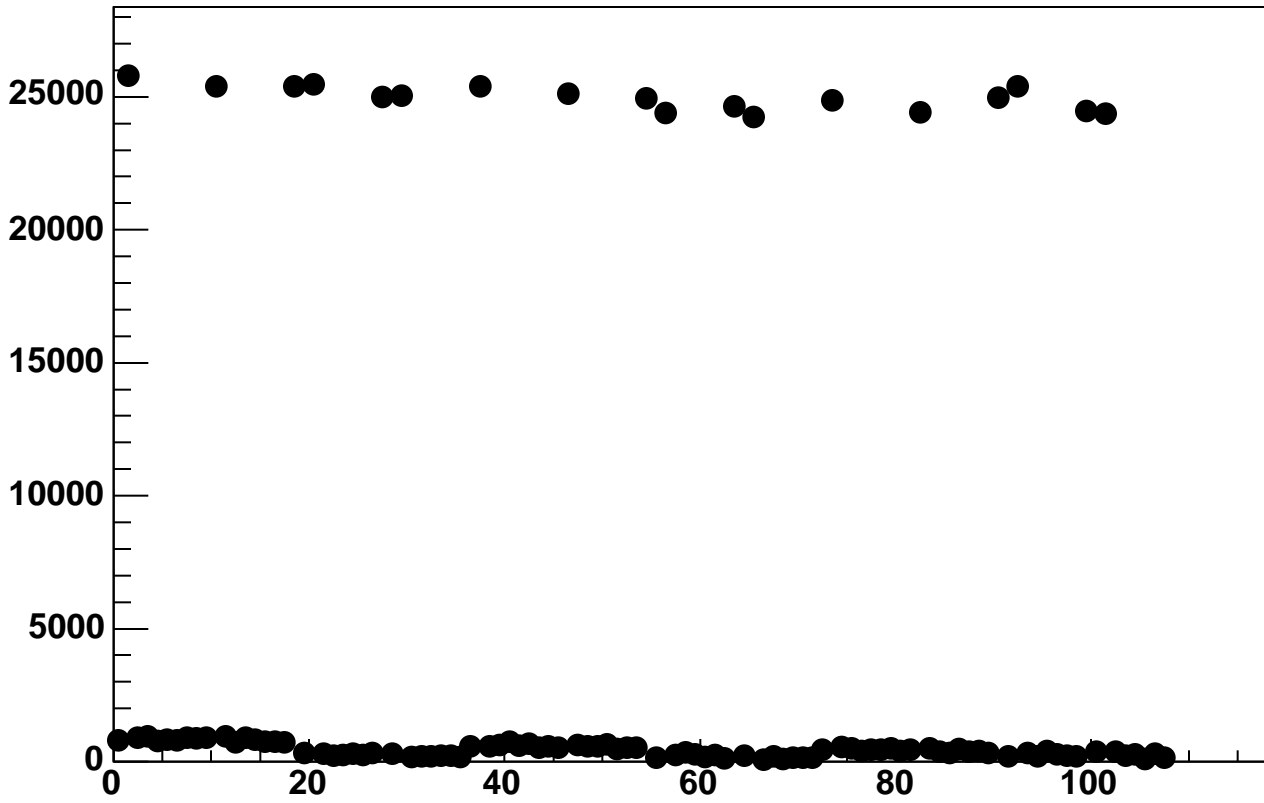
Enable 3, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



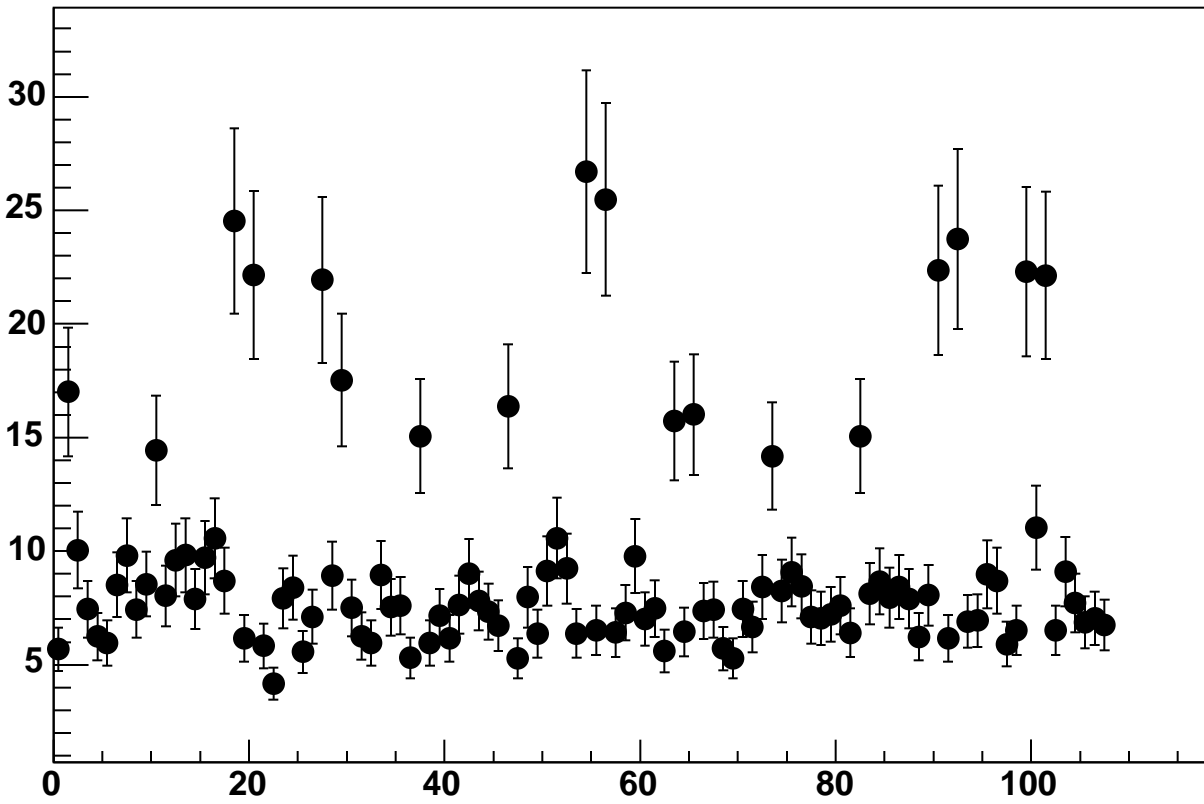
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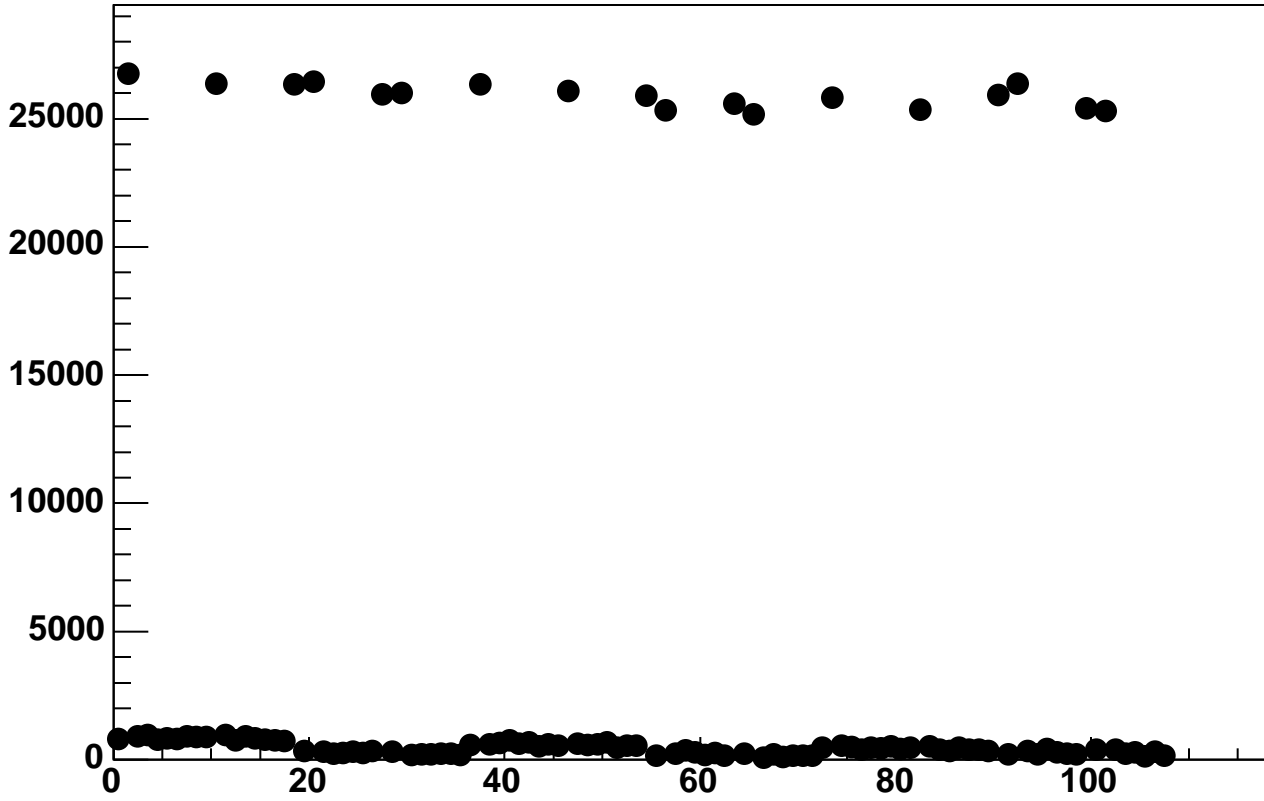
Enable 3, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



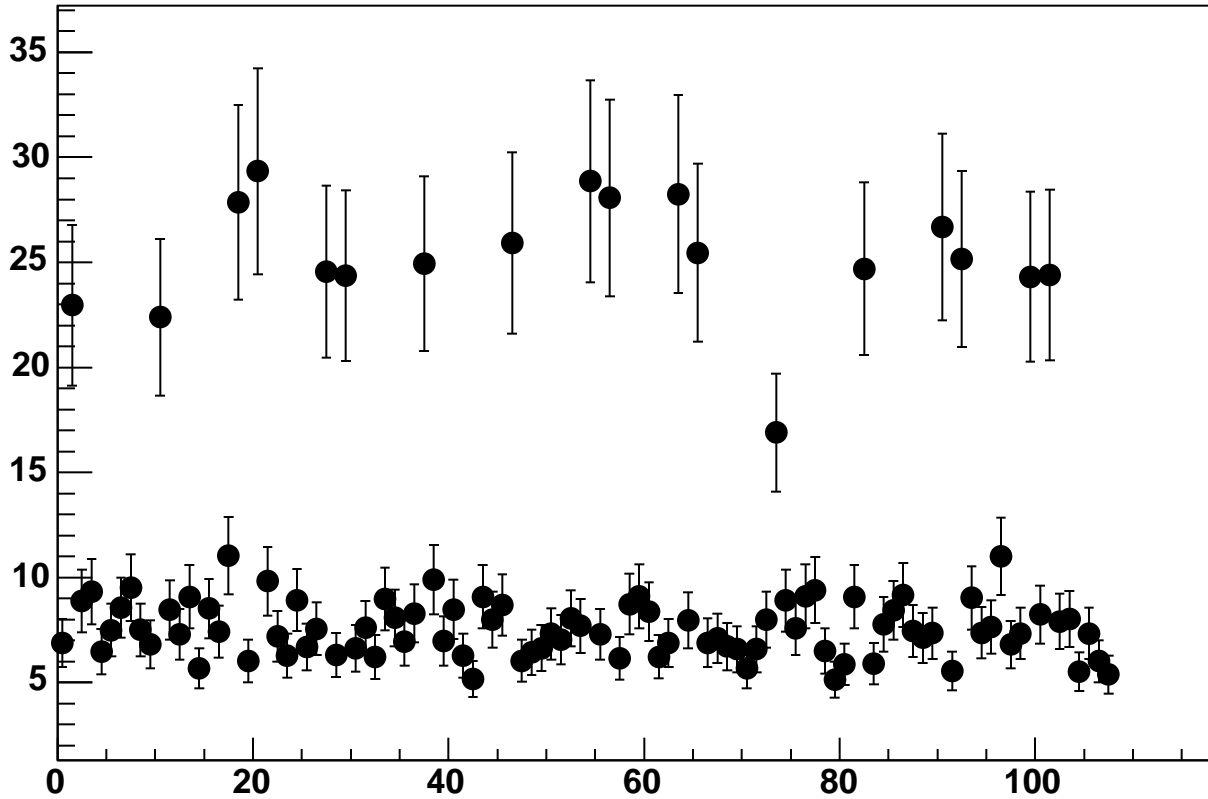
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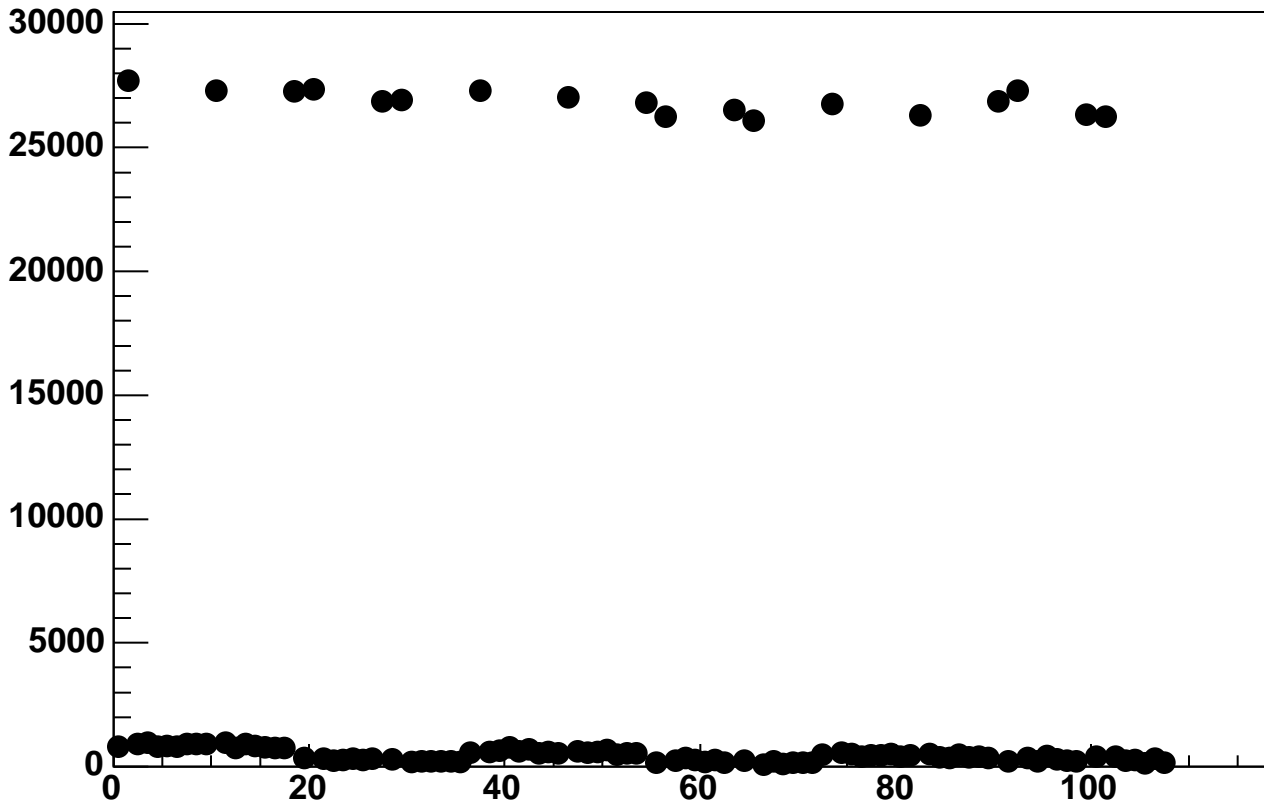
Enable 3, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



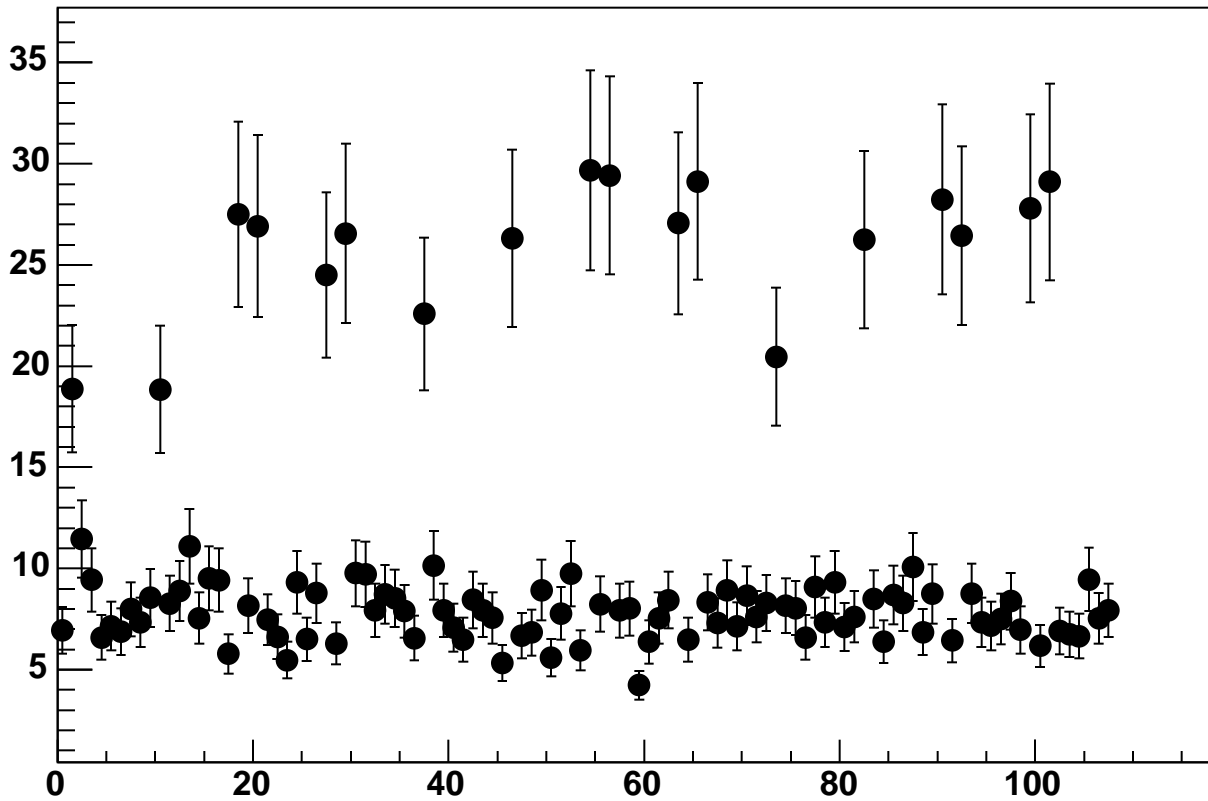
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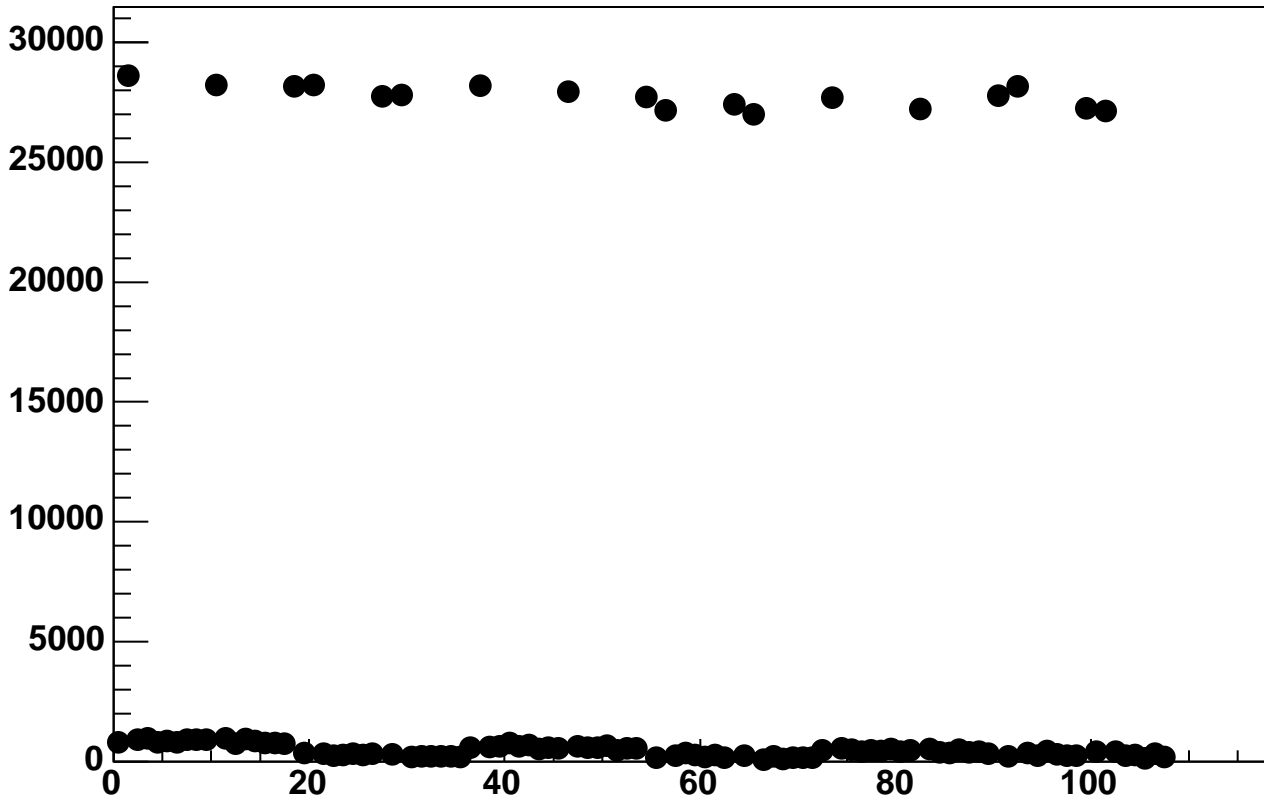
Enable 3, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



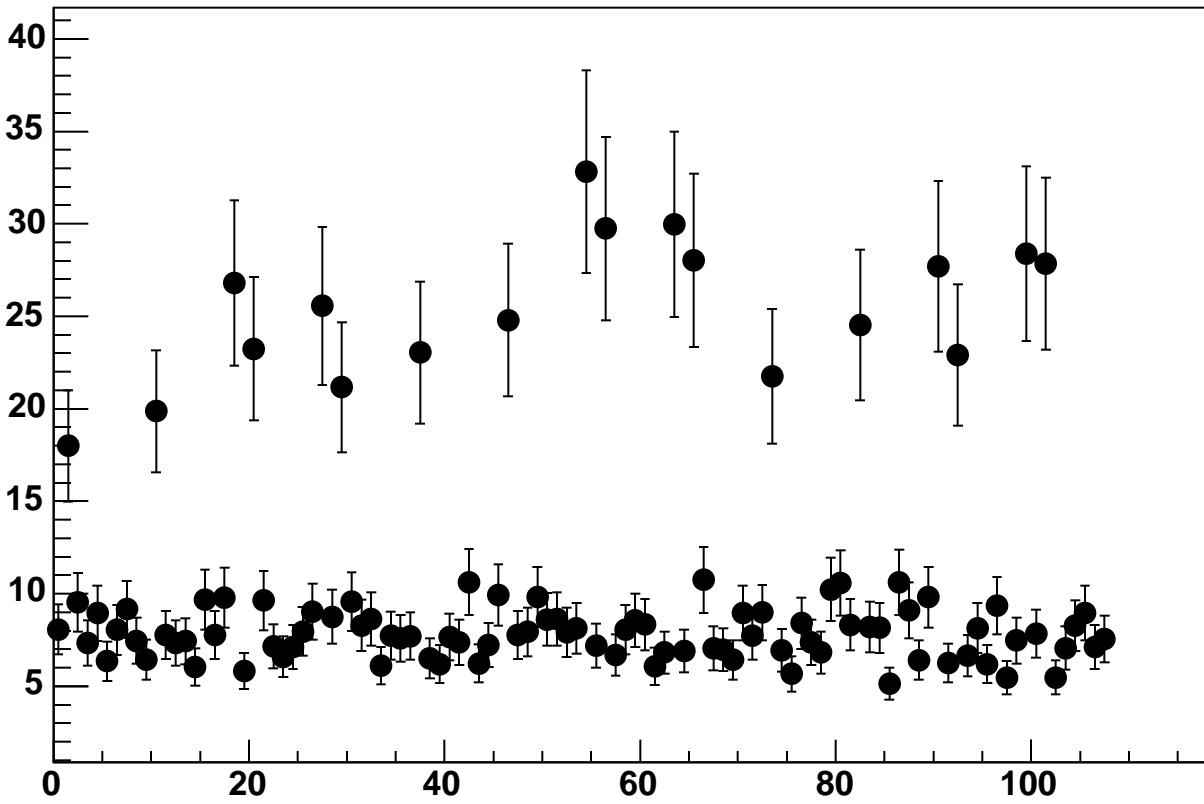
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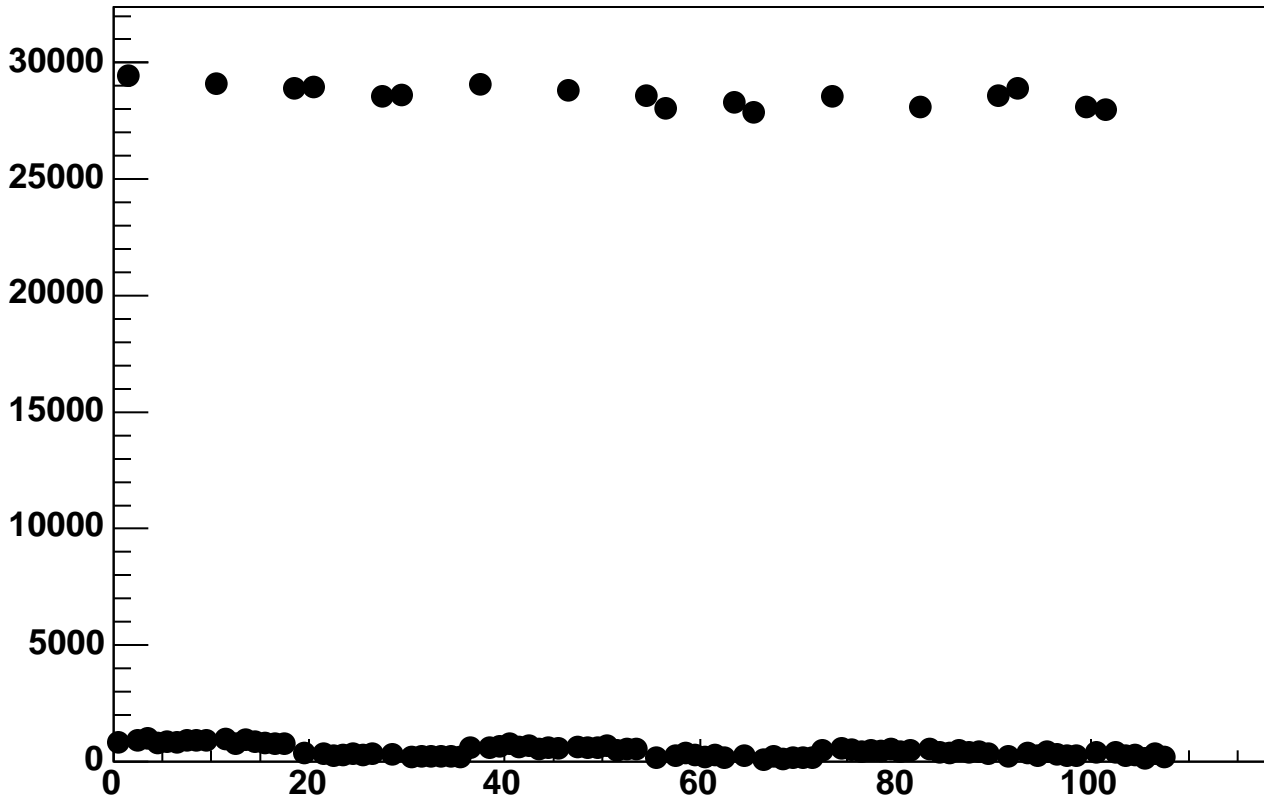
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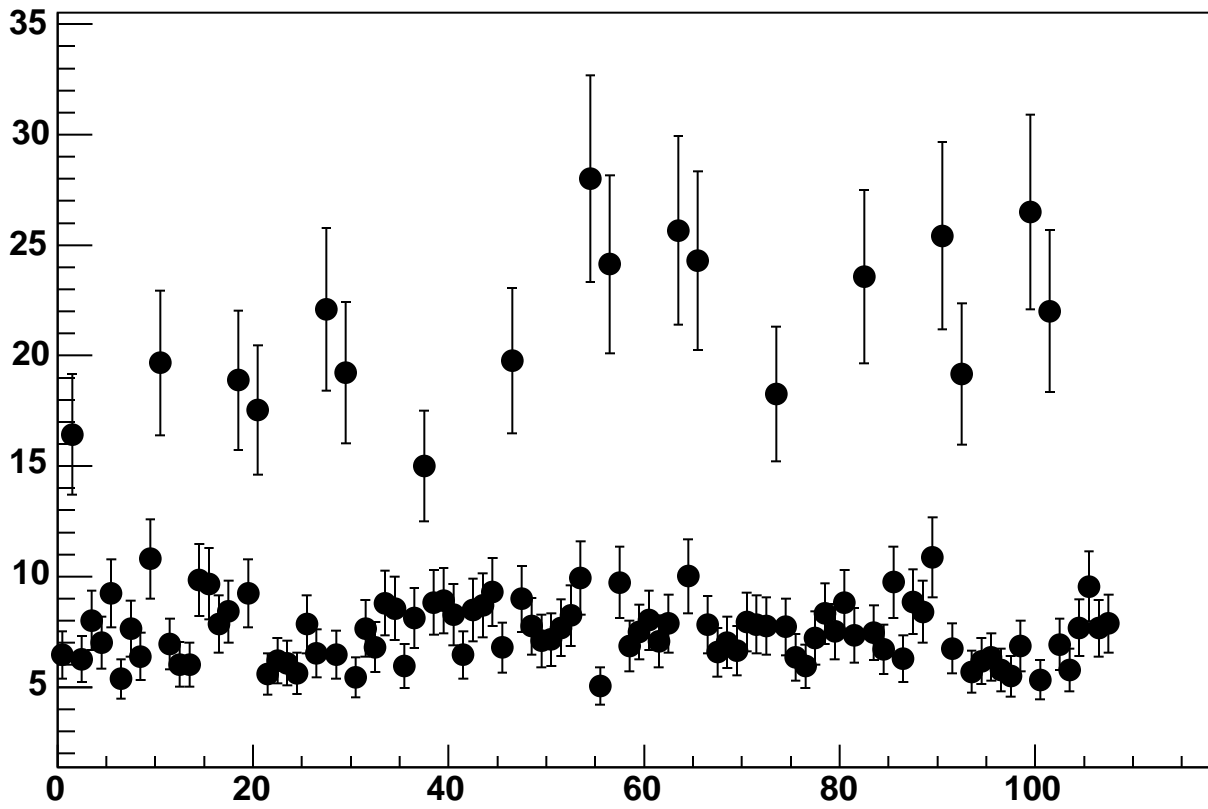
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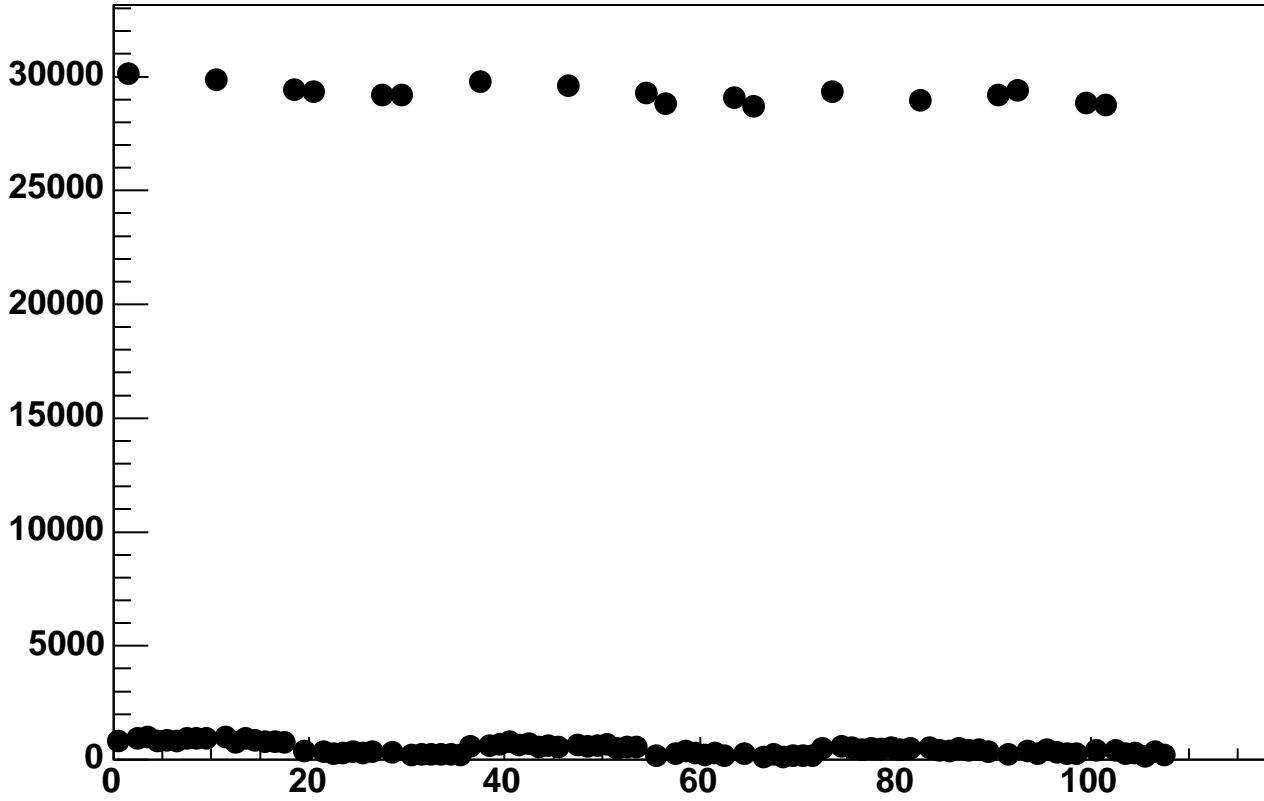
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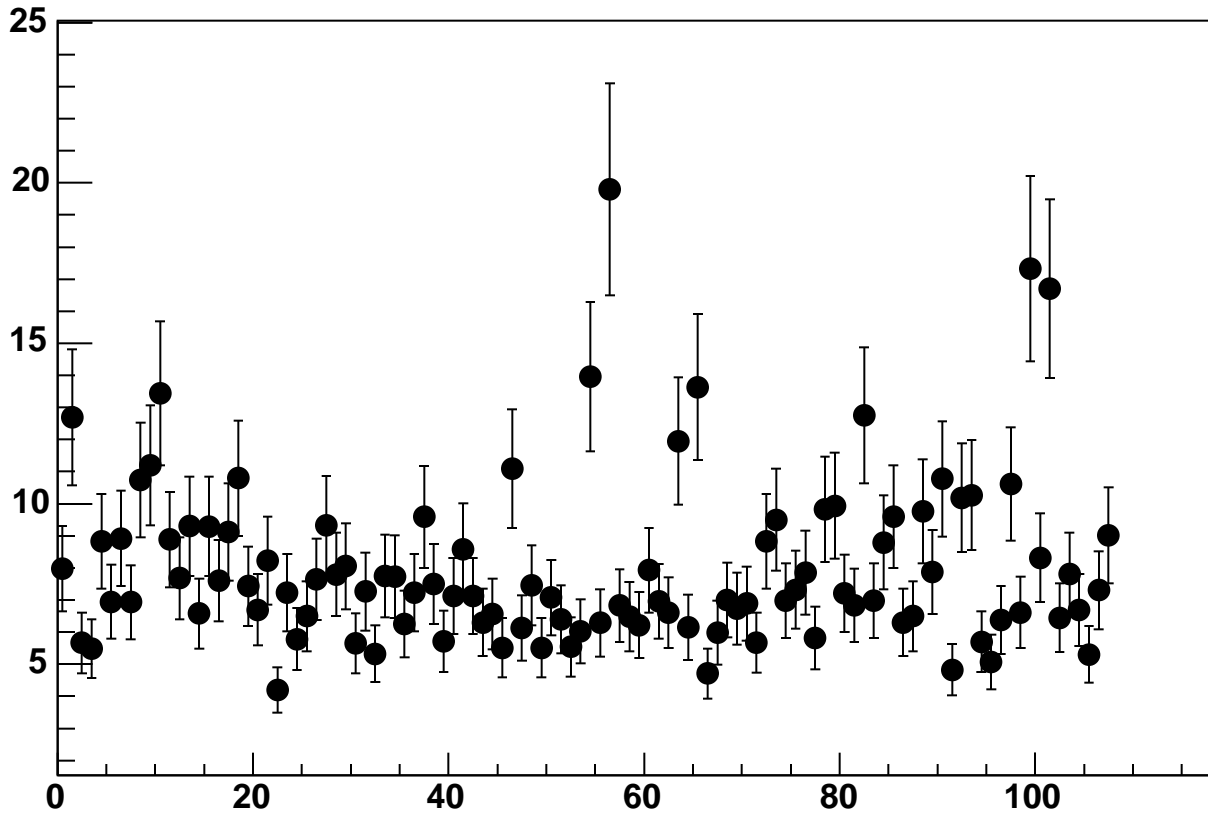
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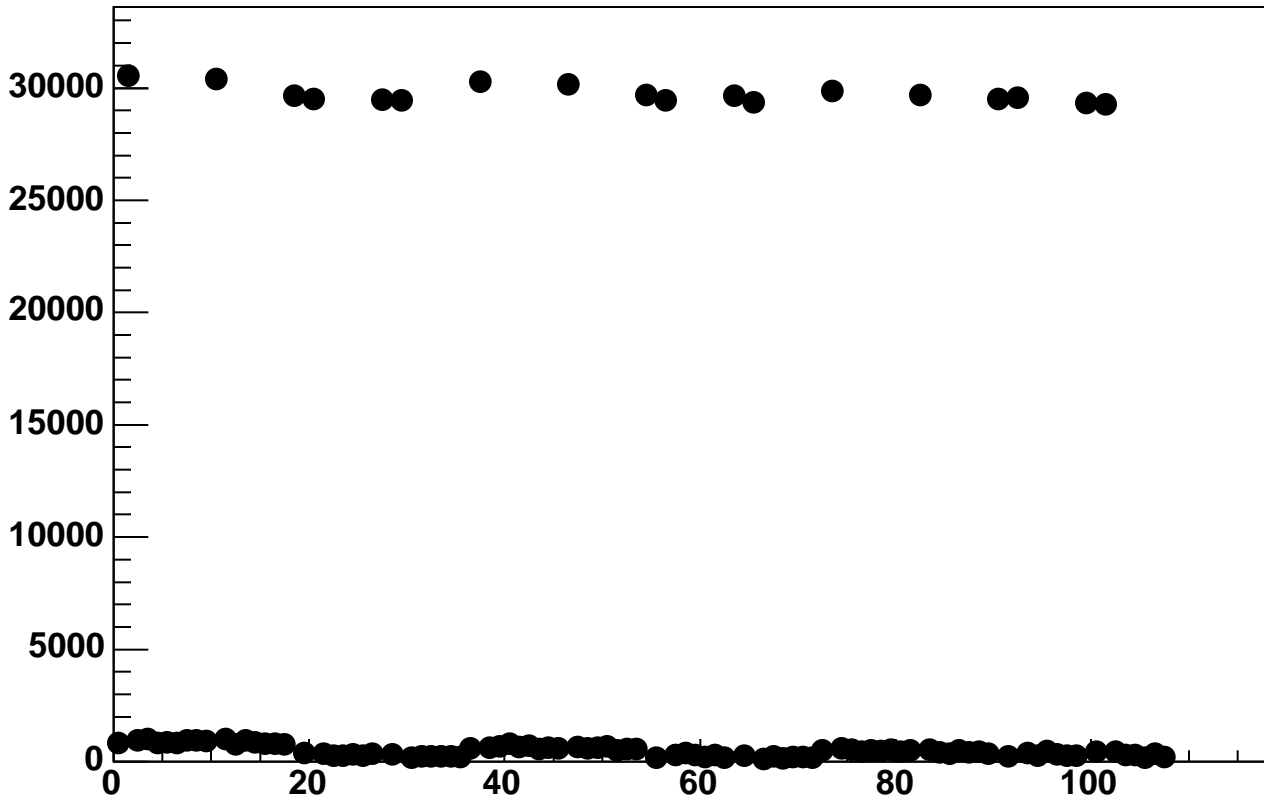
Enable 3, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



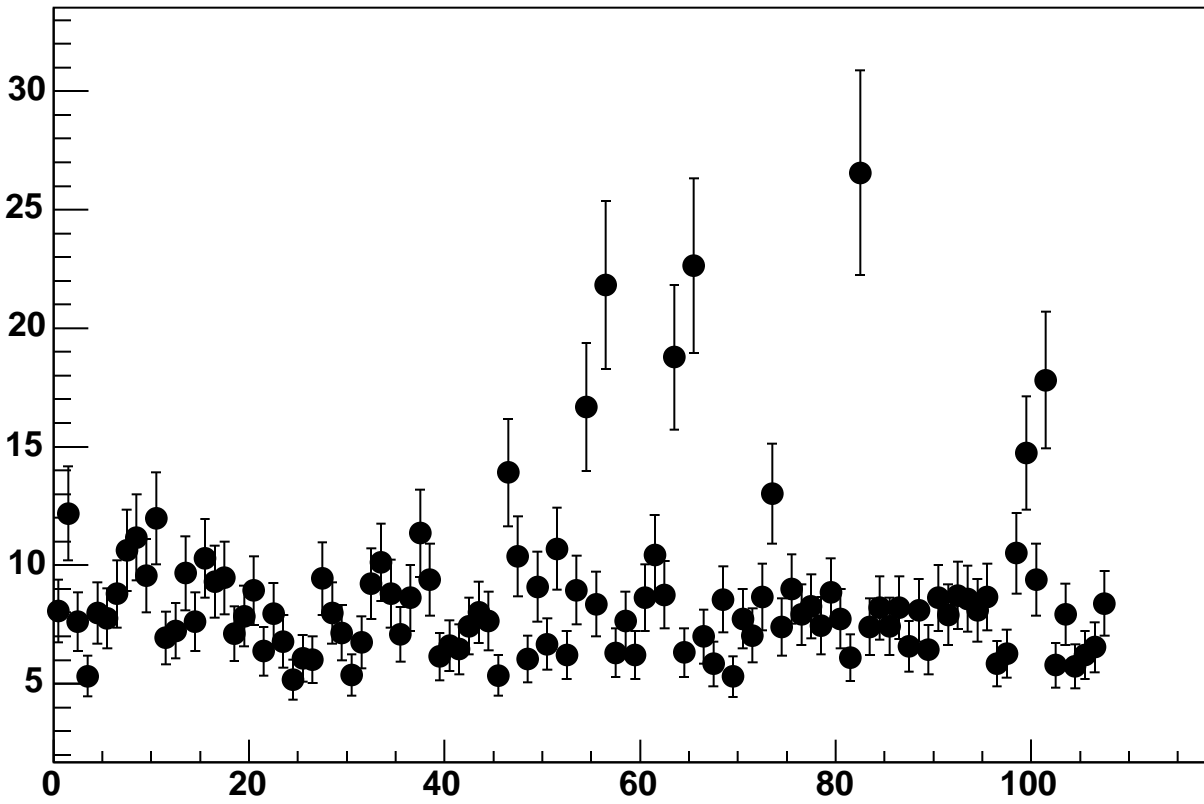
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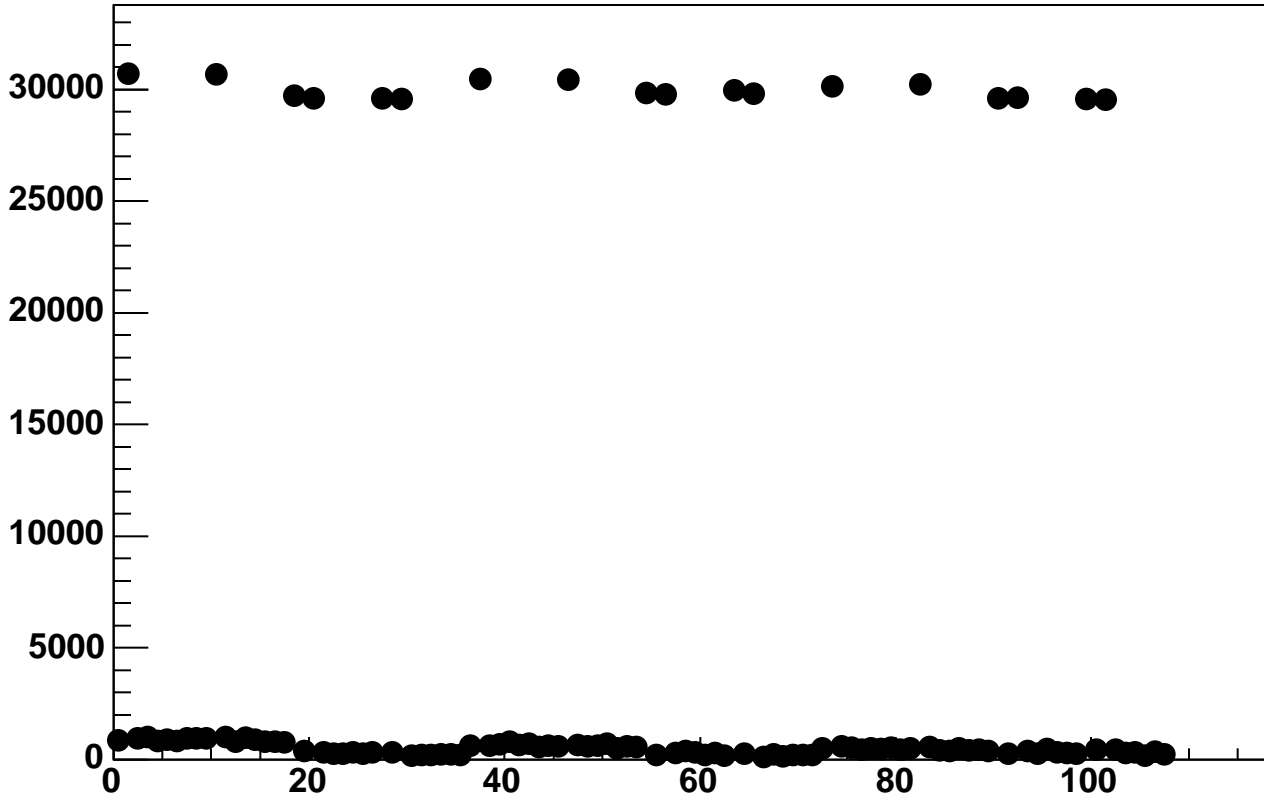
Enable 3, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



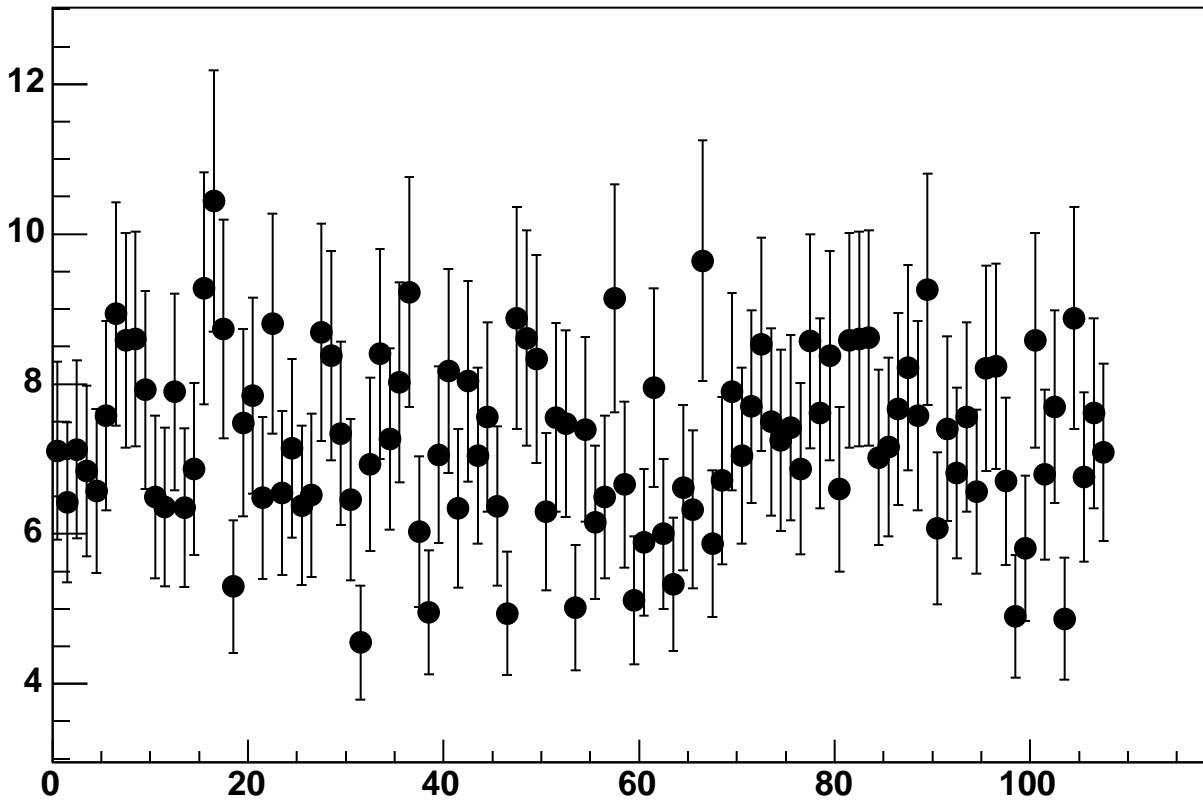
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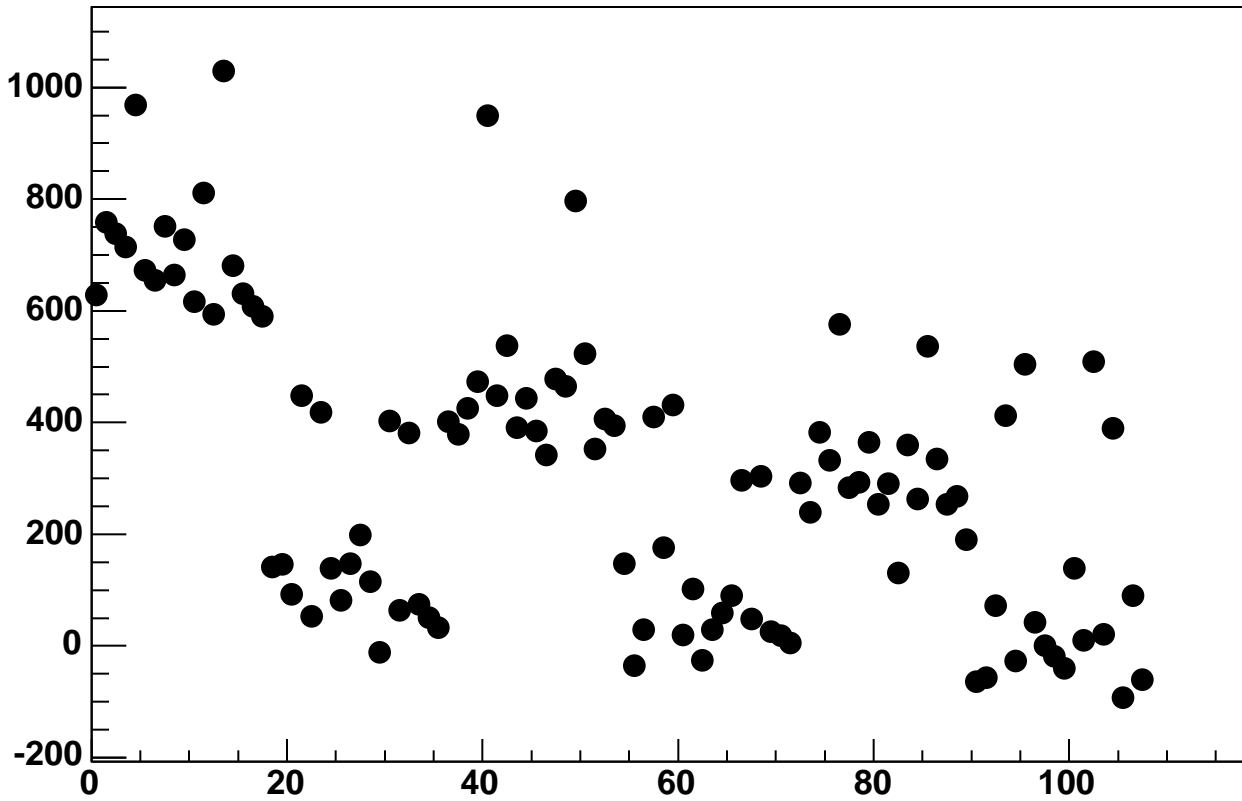
Enable 3, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



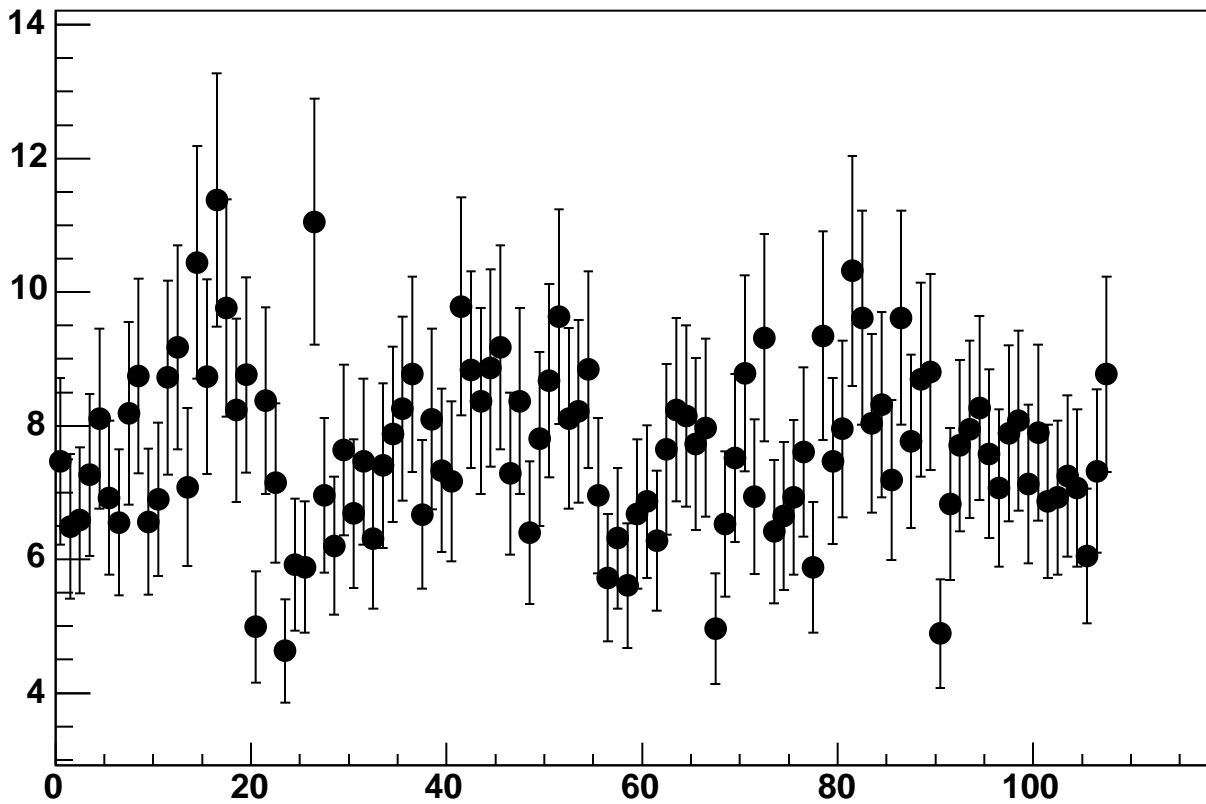
Enable 3, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



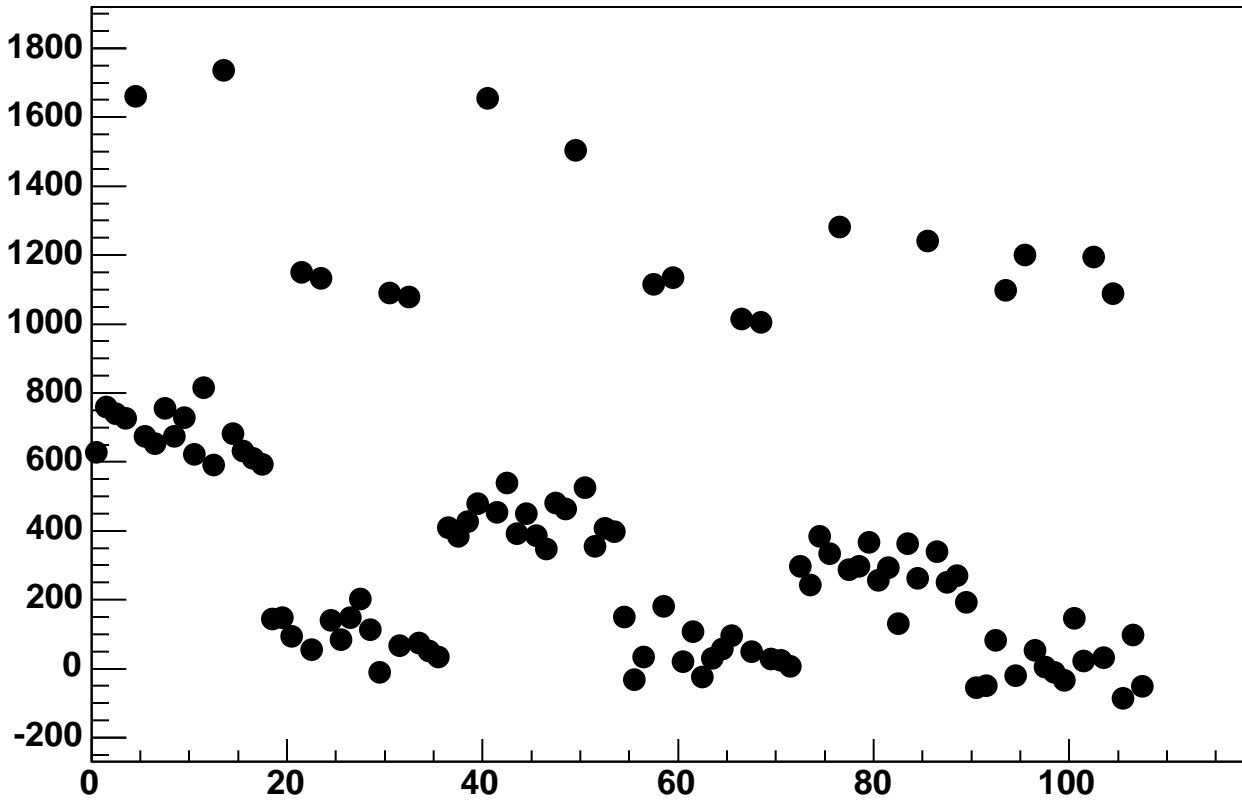
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



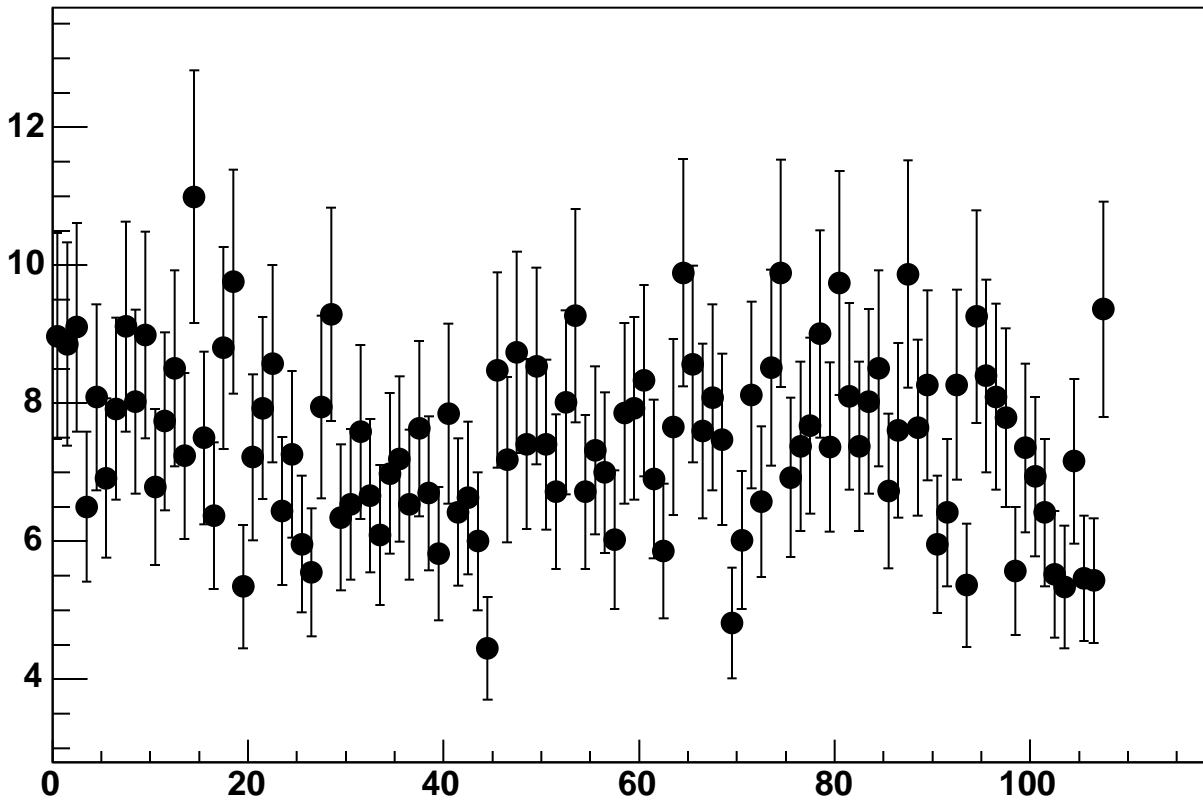
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



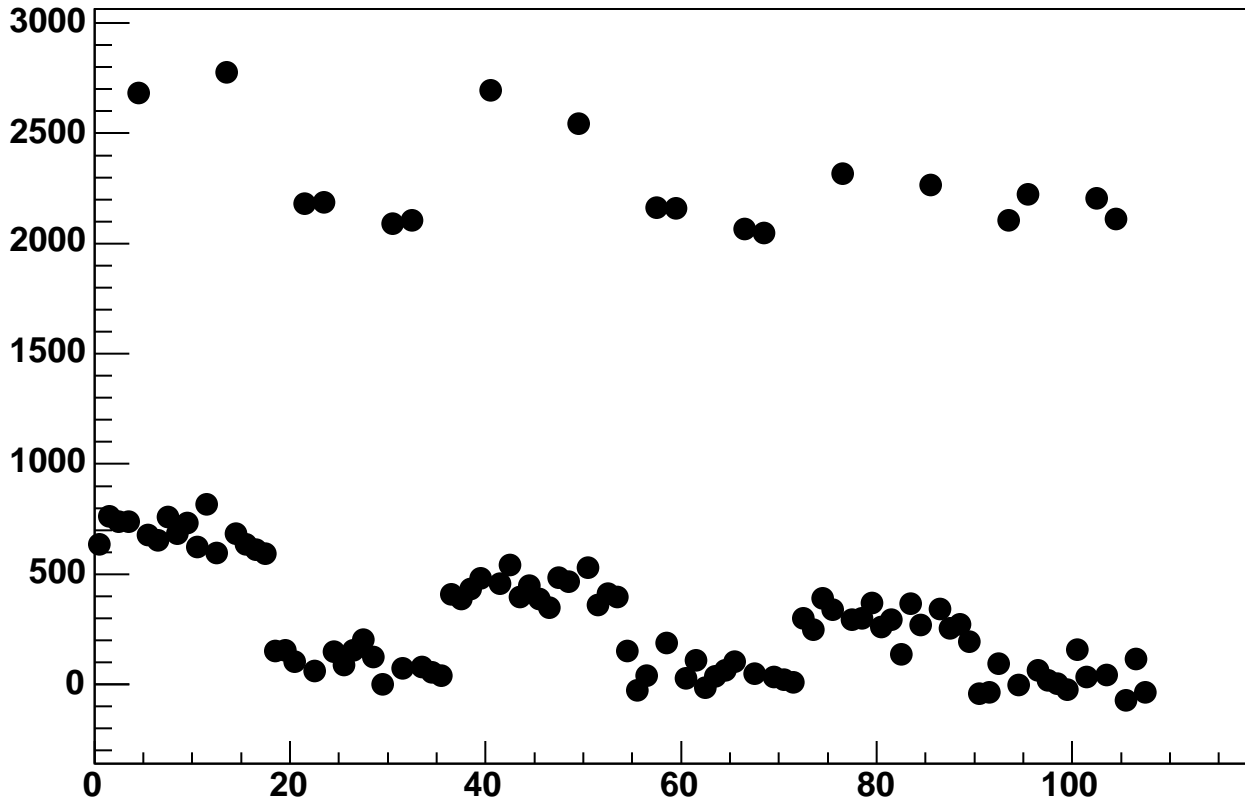
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



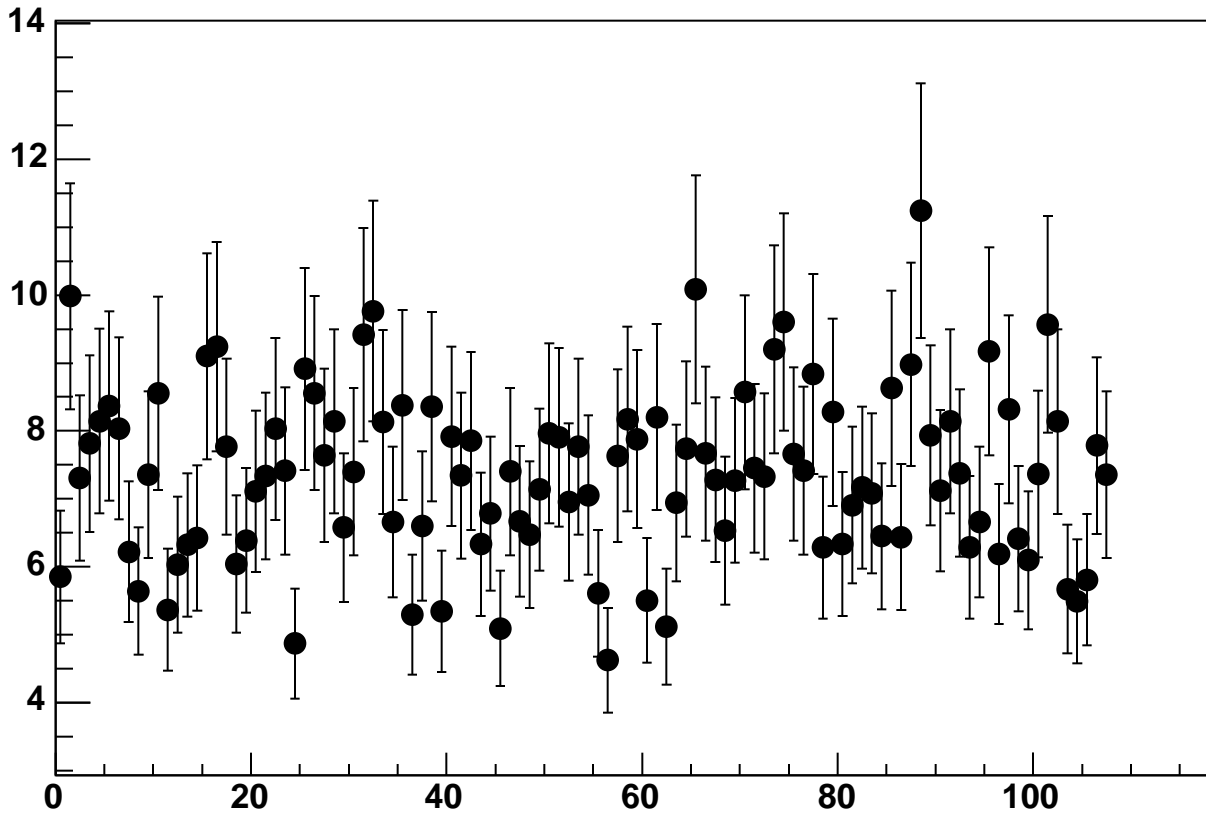
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



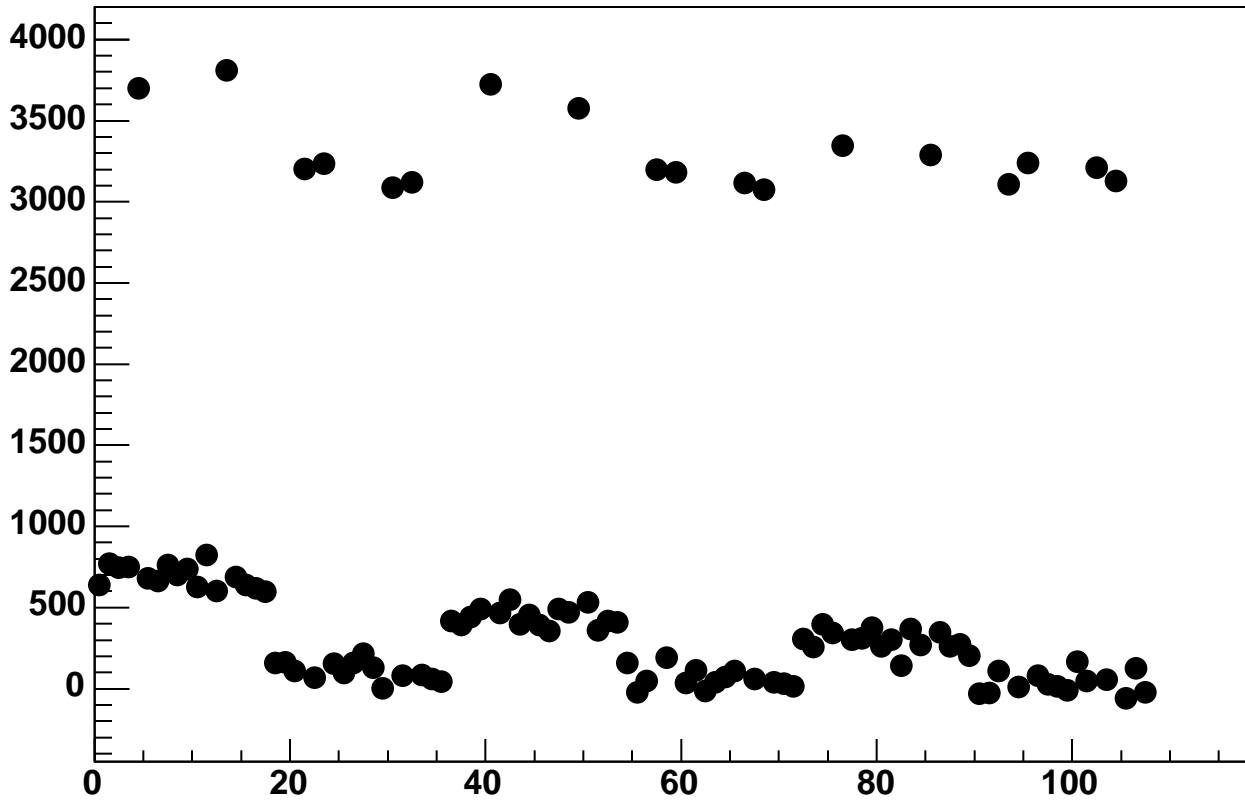
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



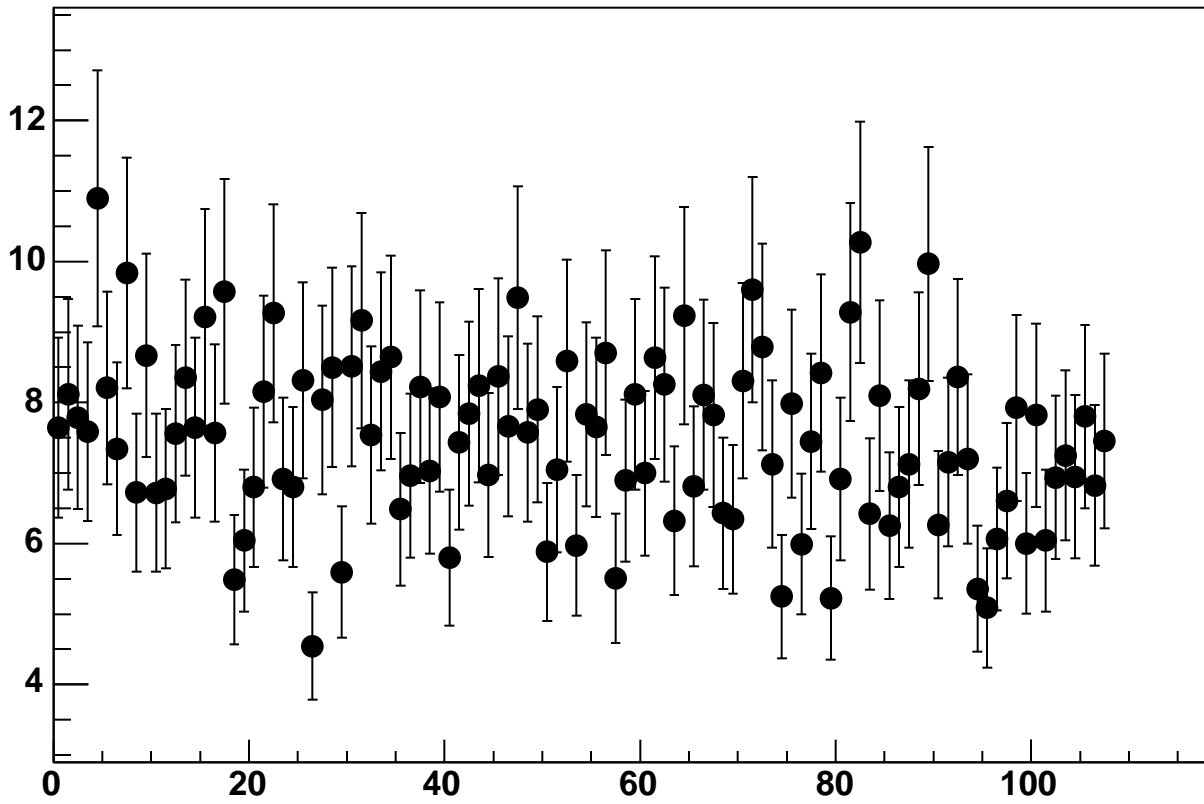
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



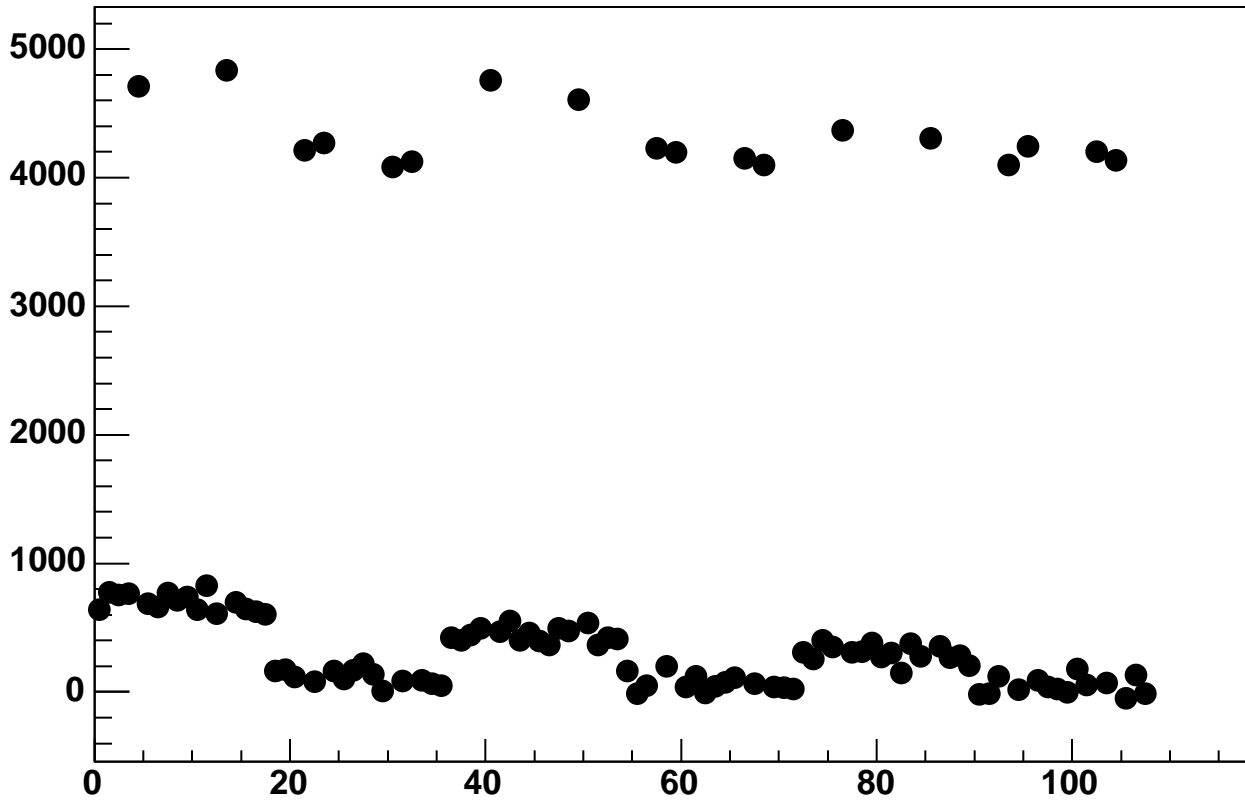
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



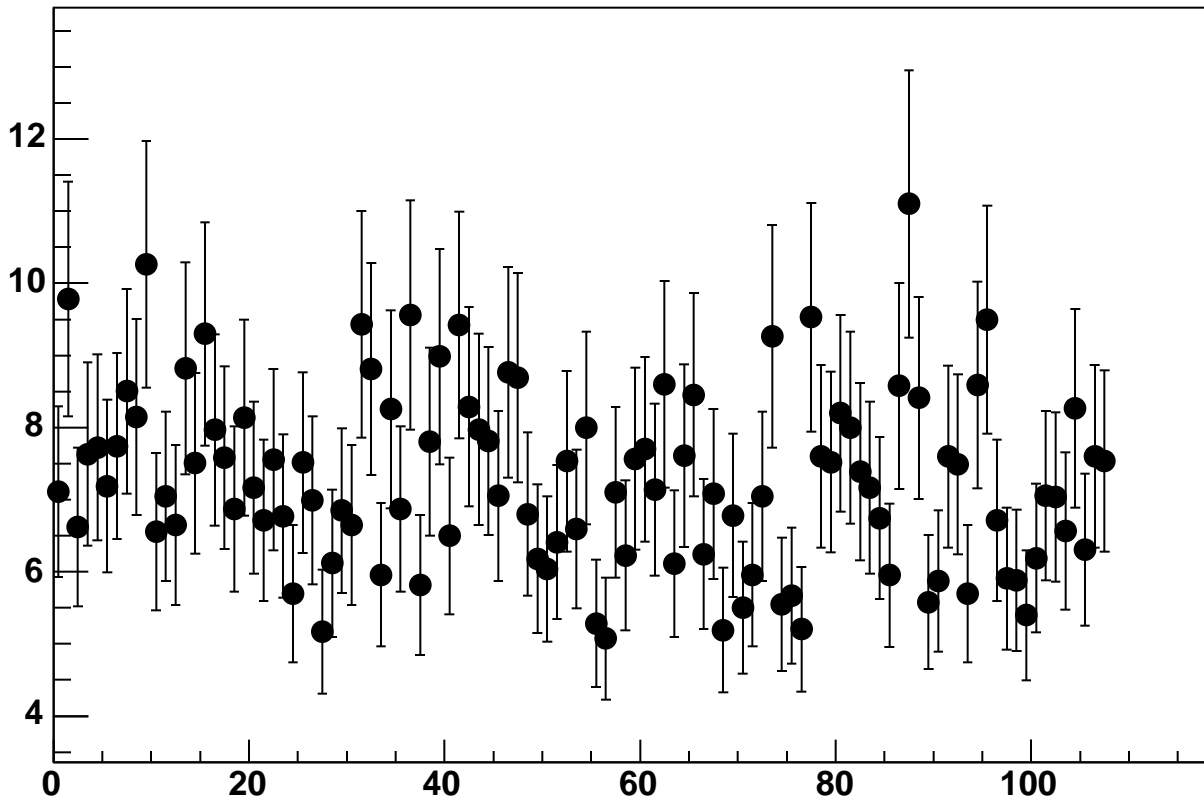
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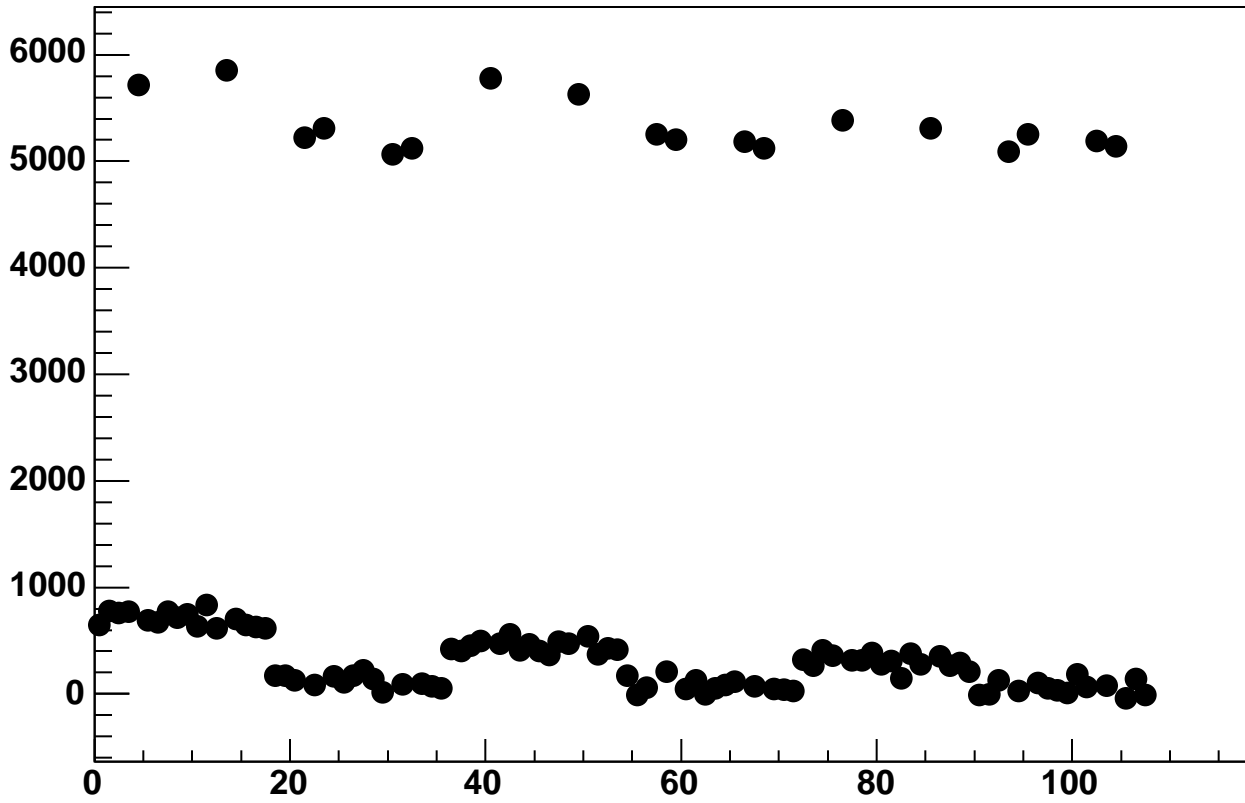
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



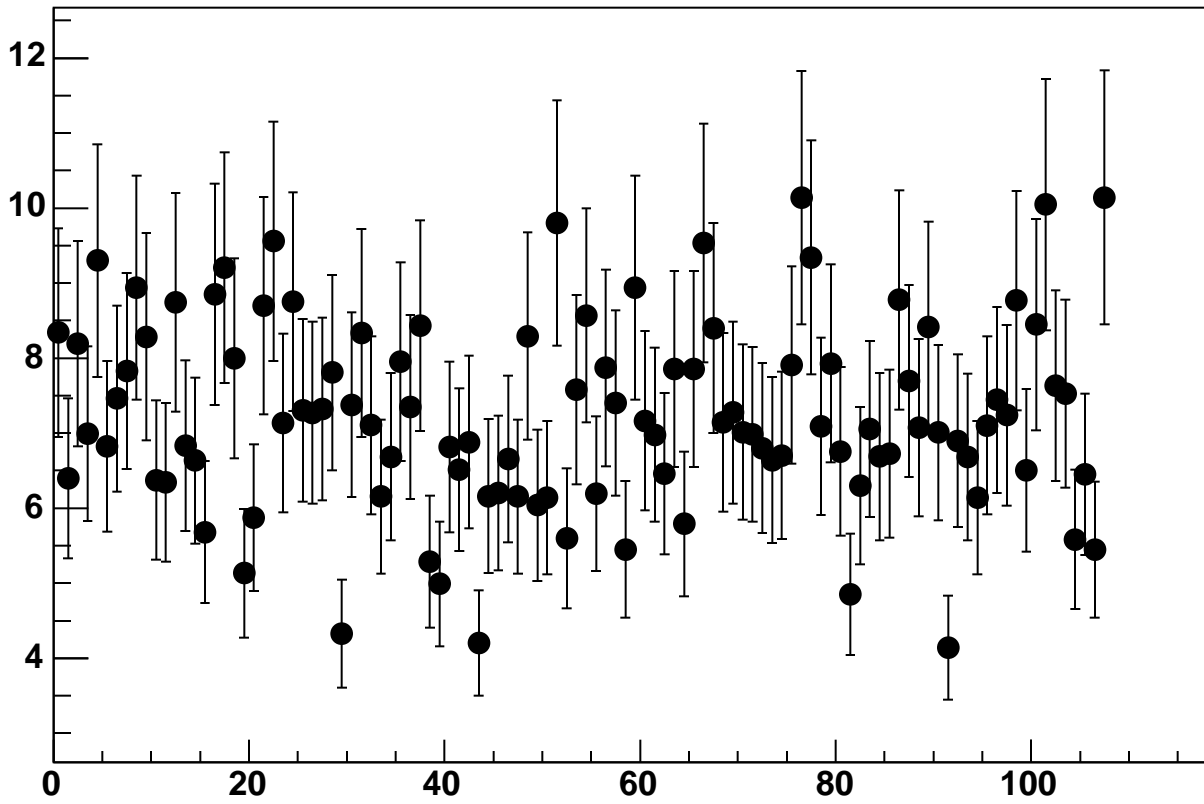
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



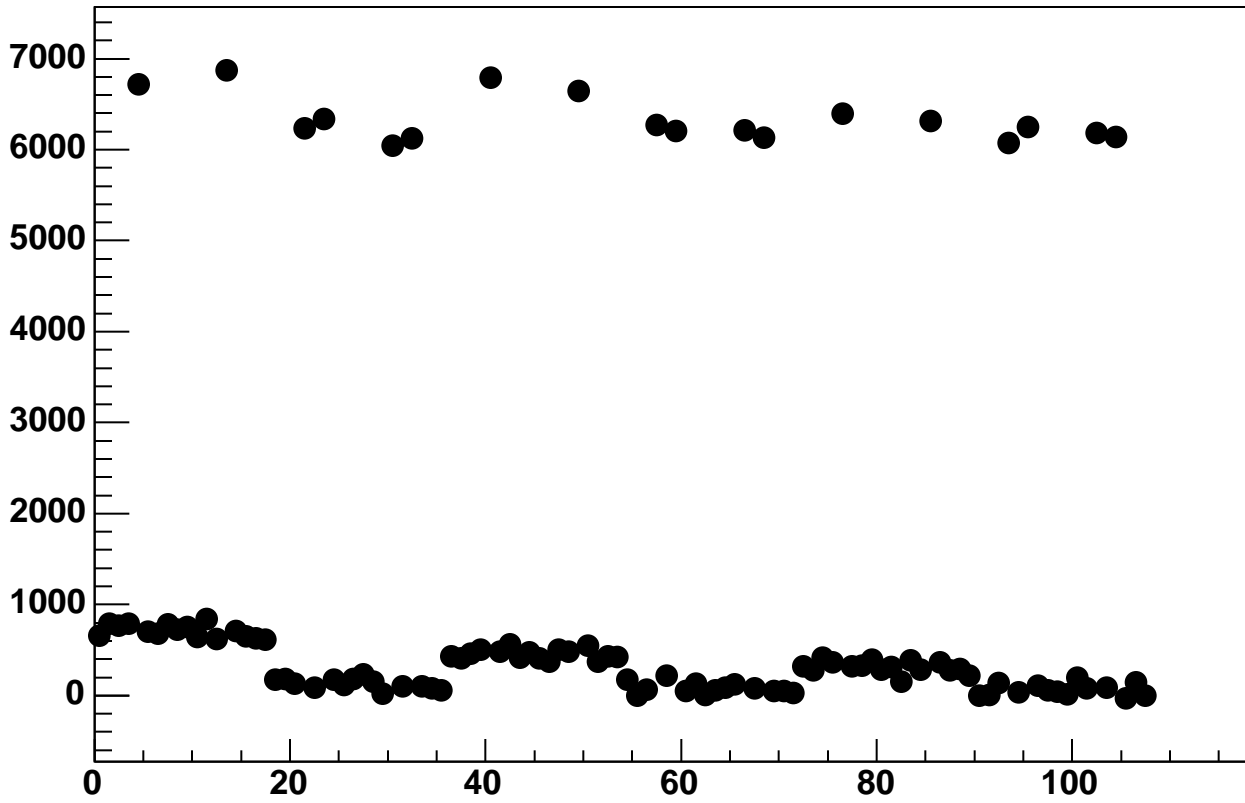
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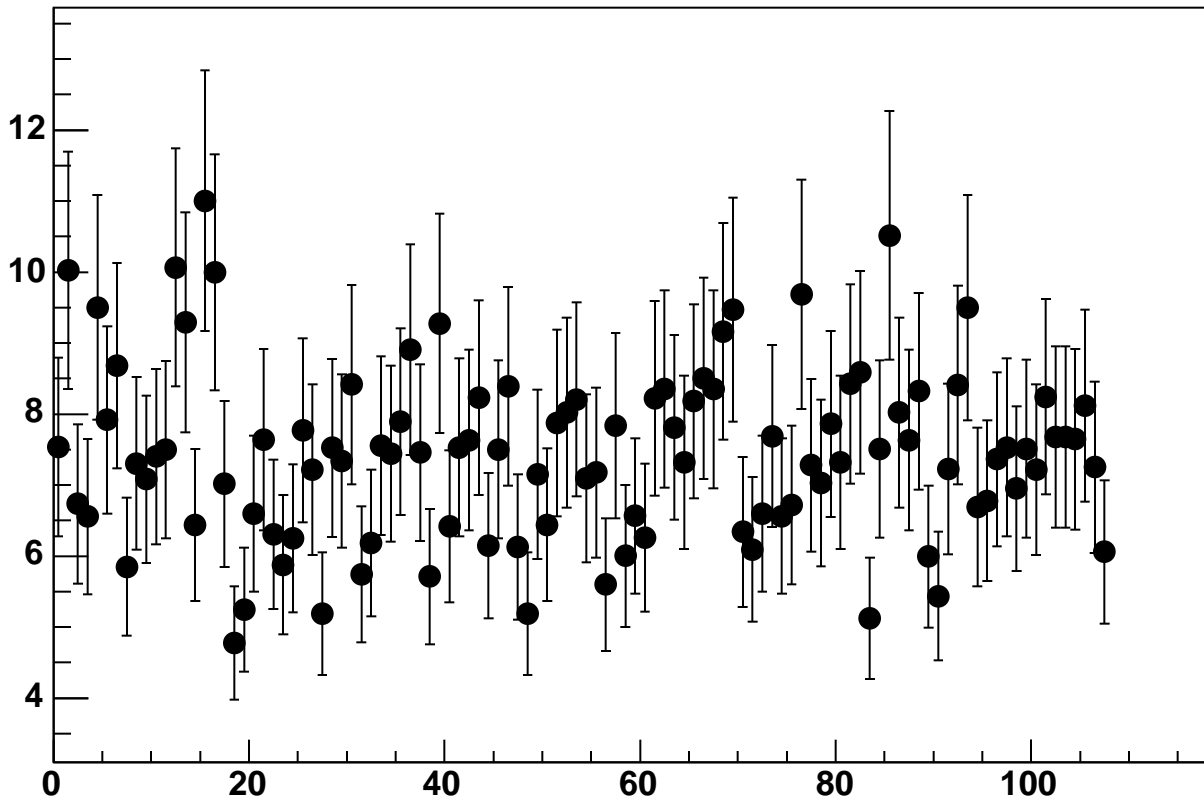
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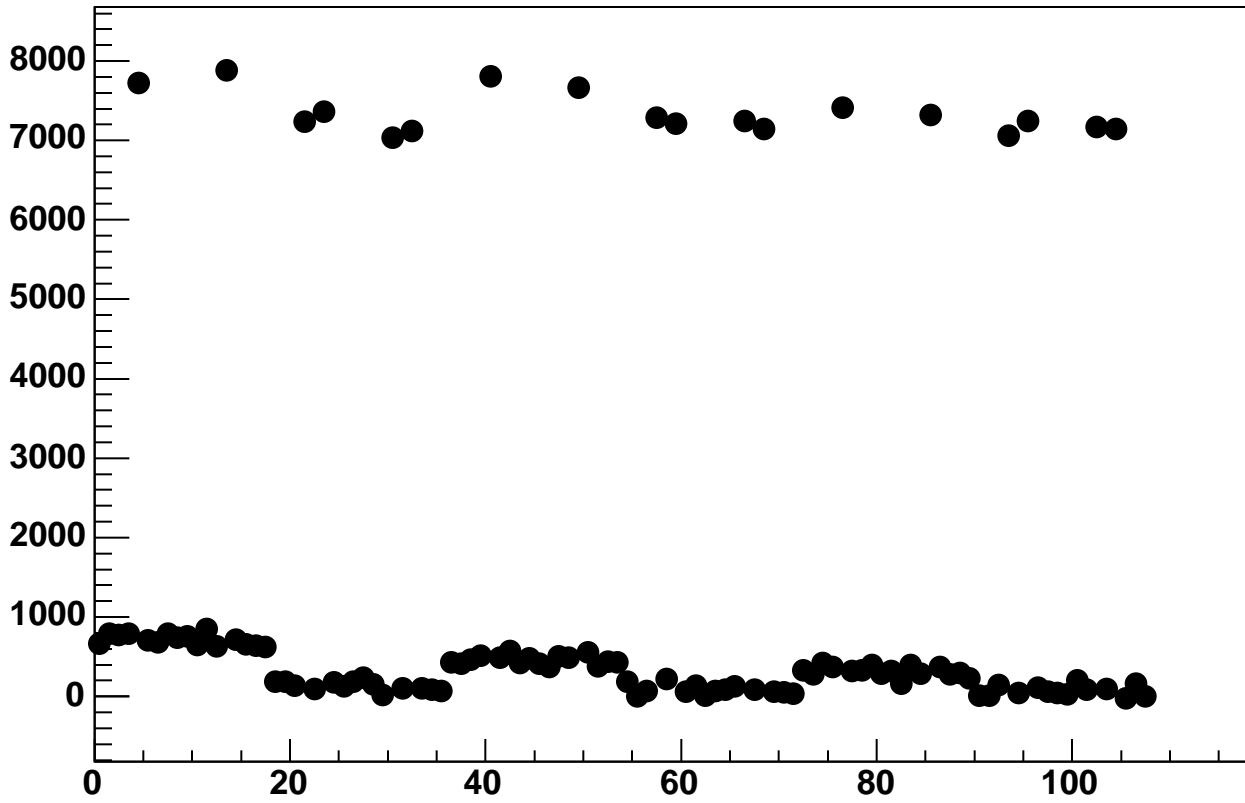
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



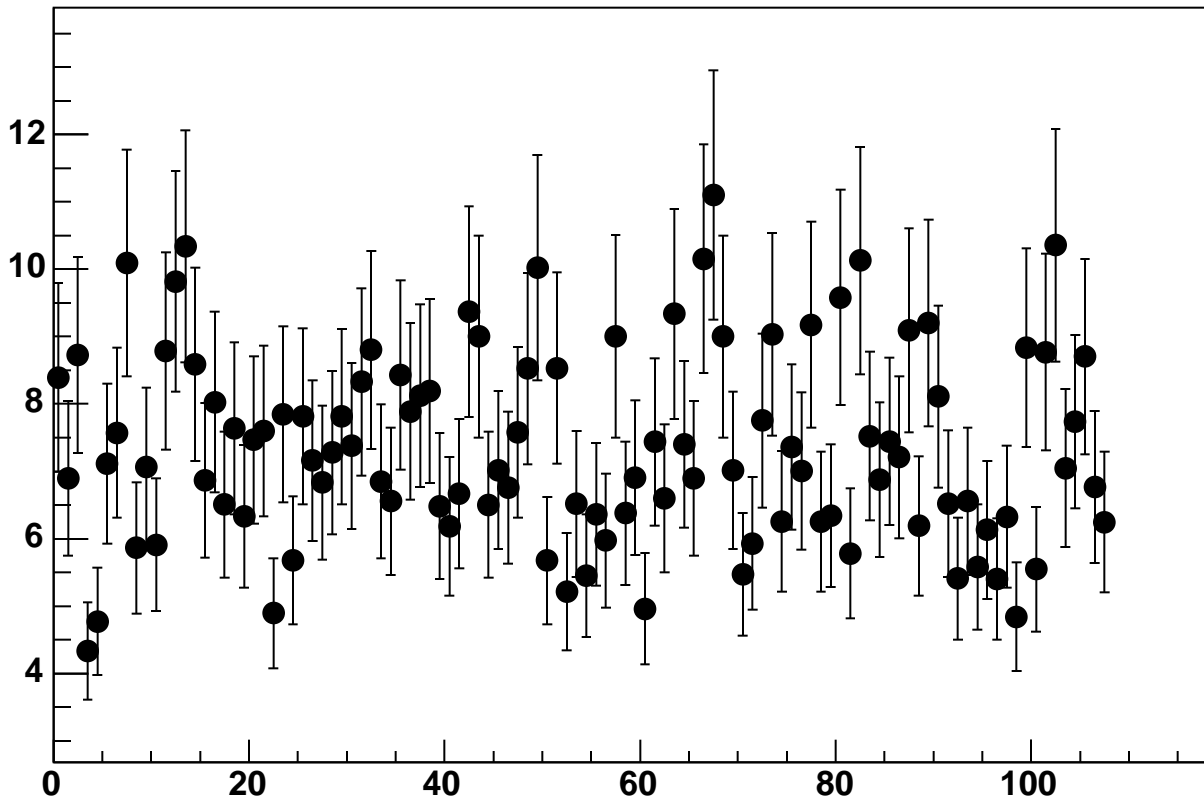
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



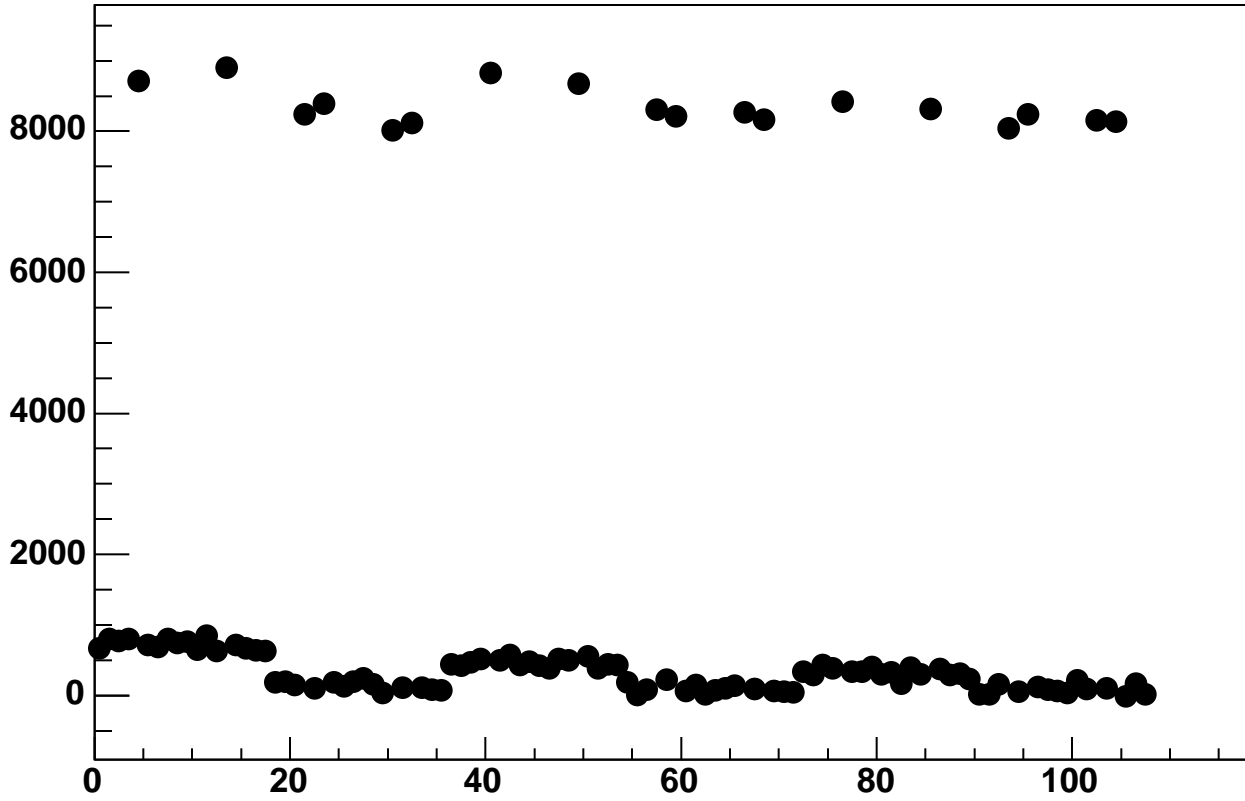
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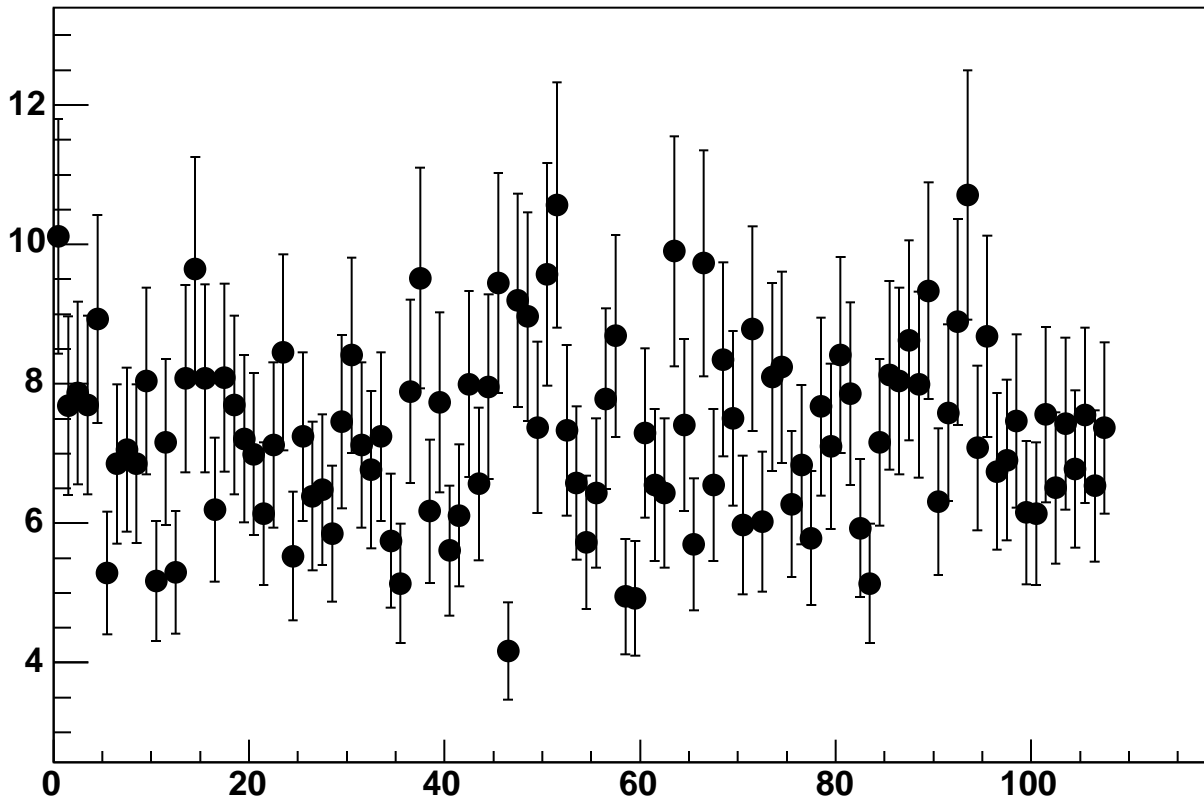
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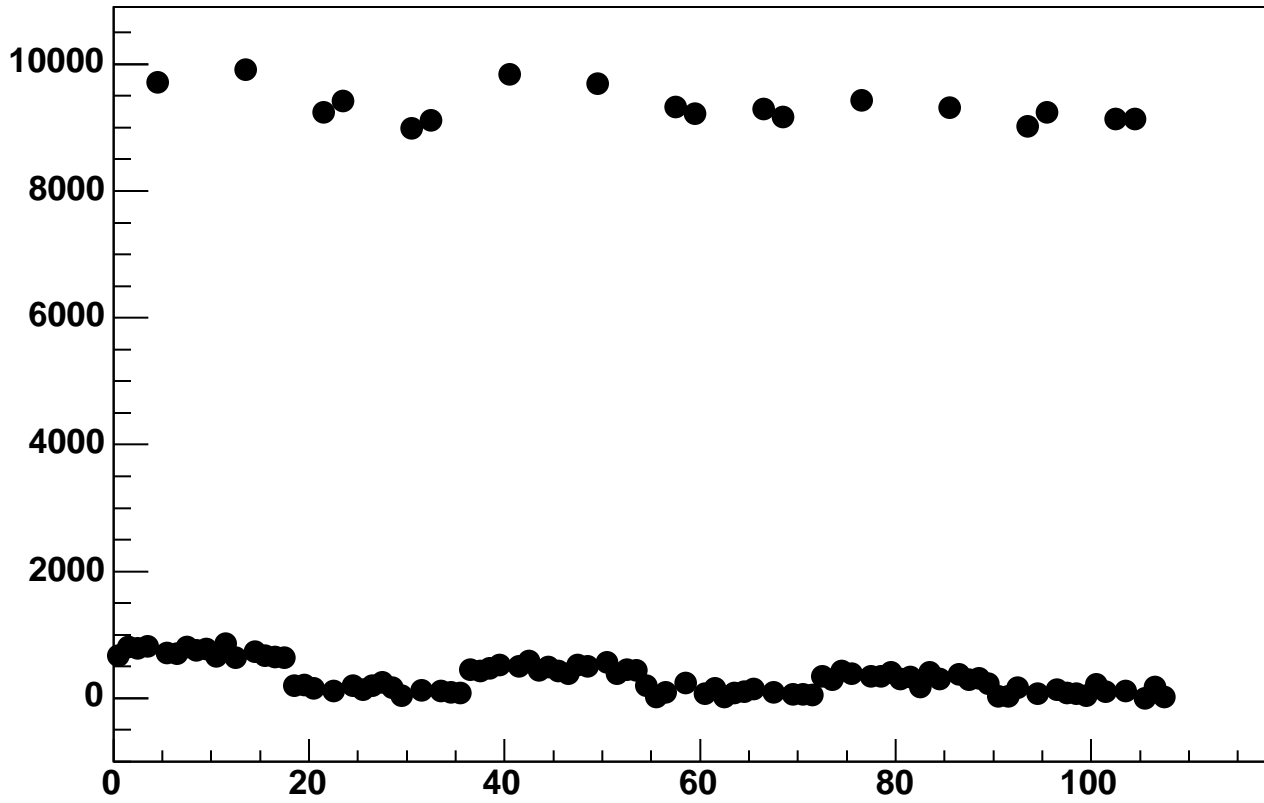
Enable 4, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



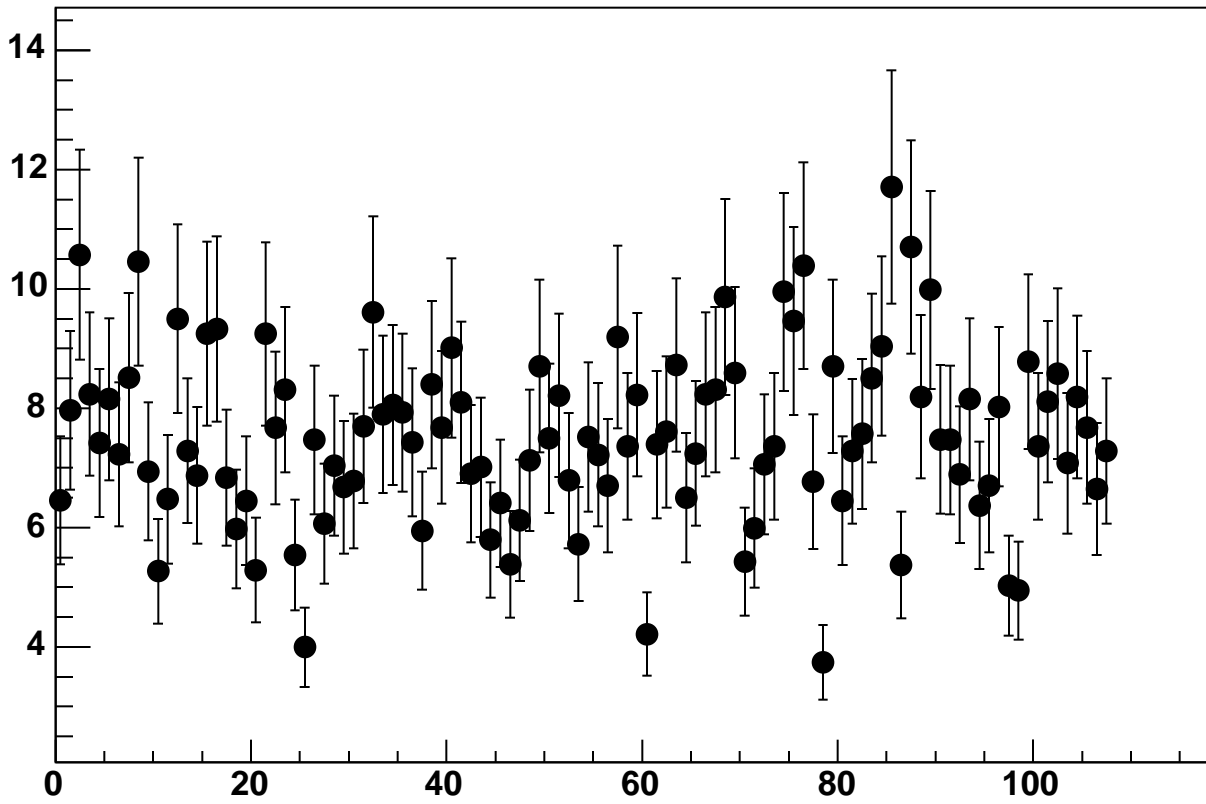
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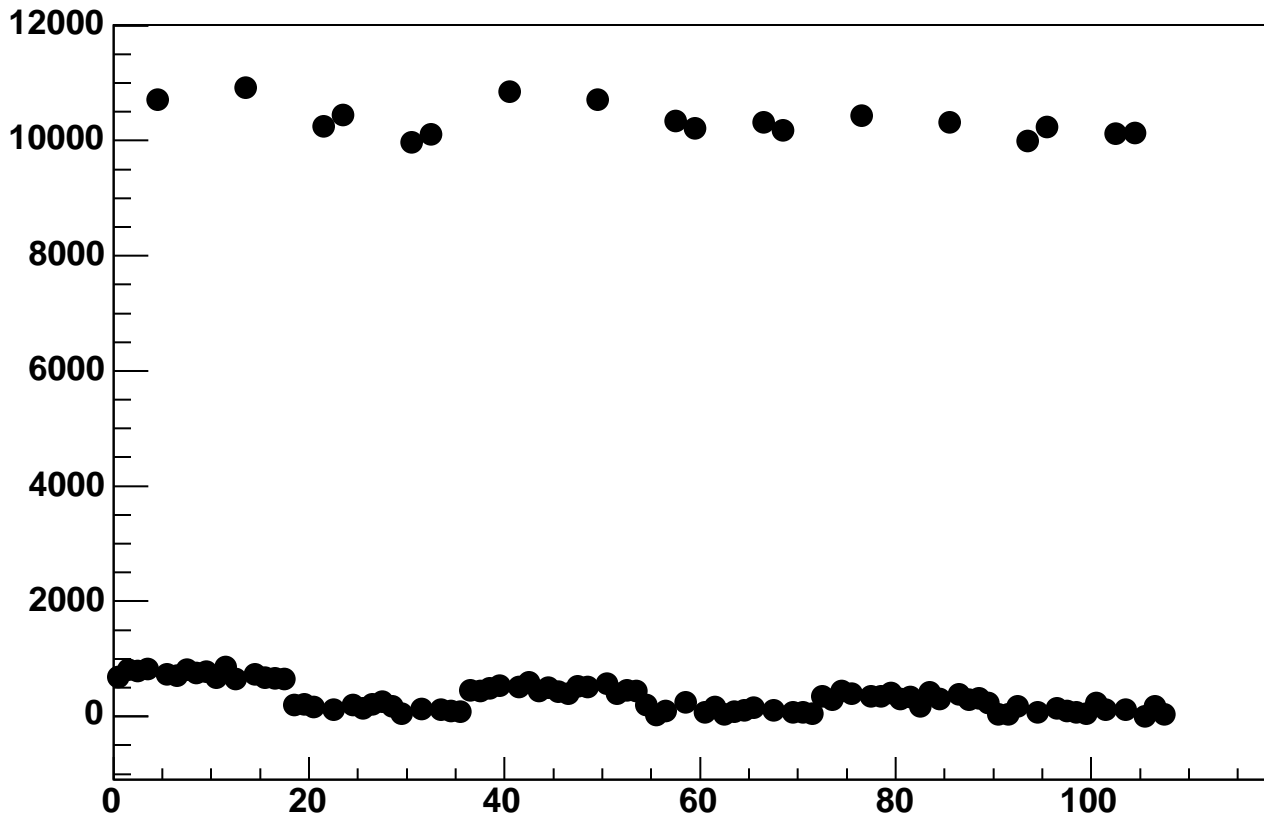
Enable 4, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



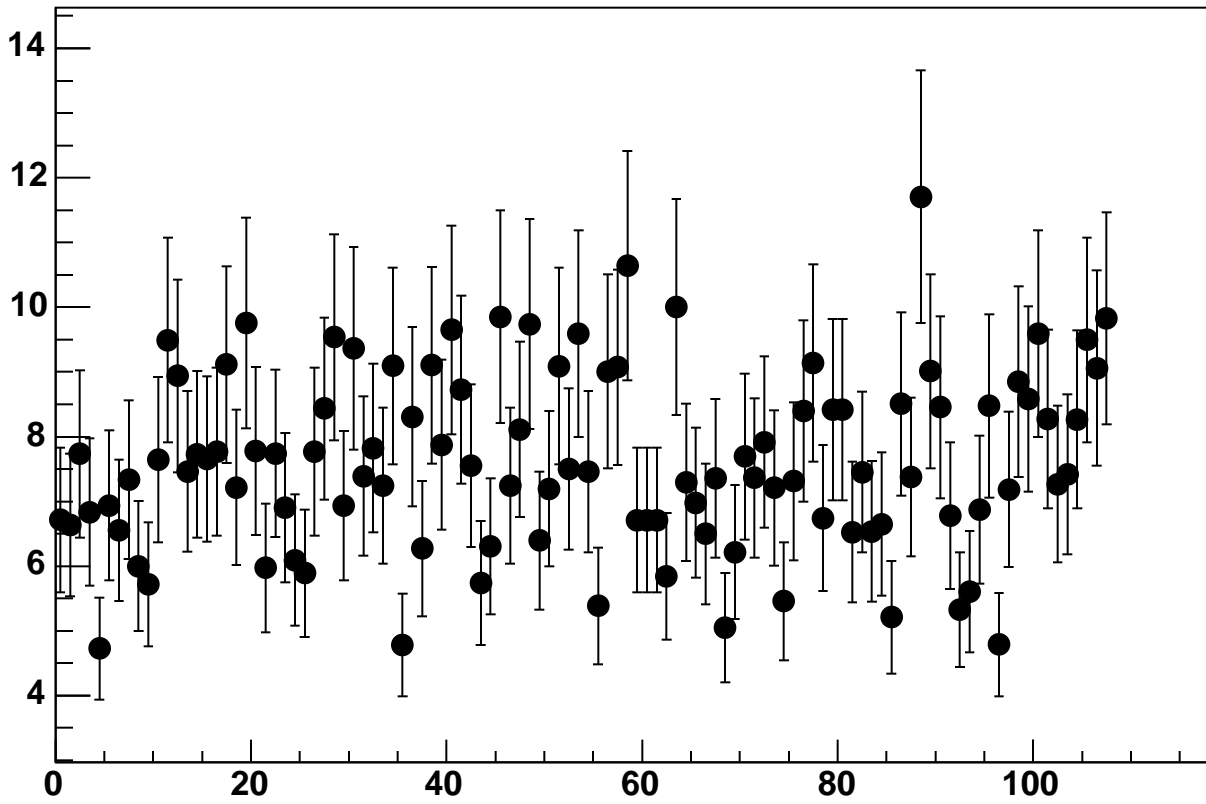
Enable 4, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



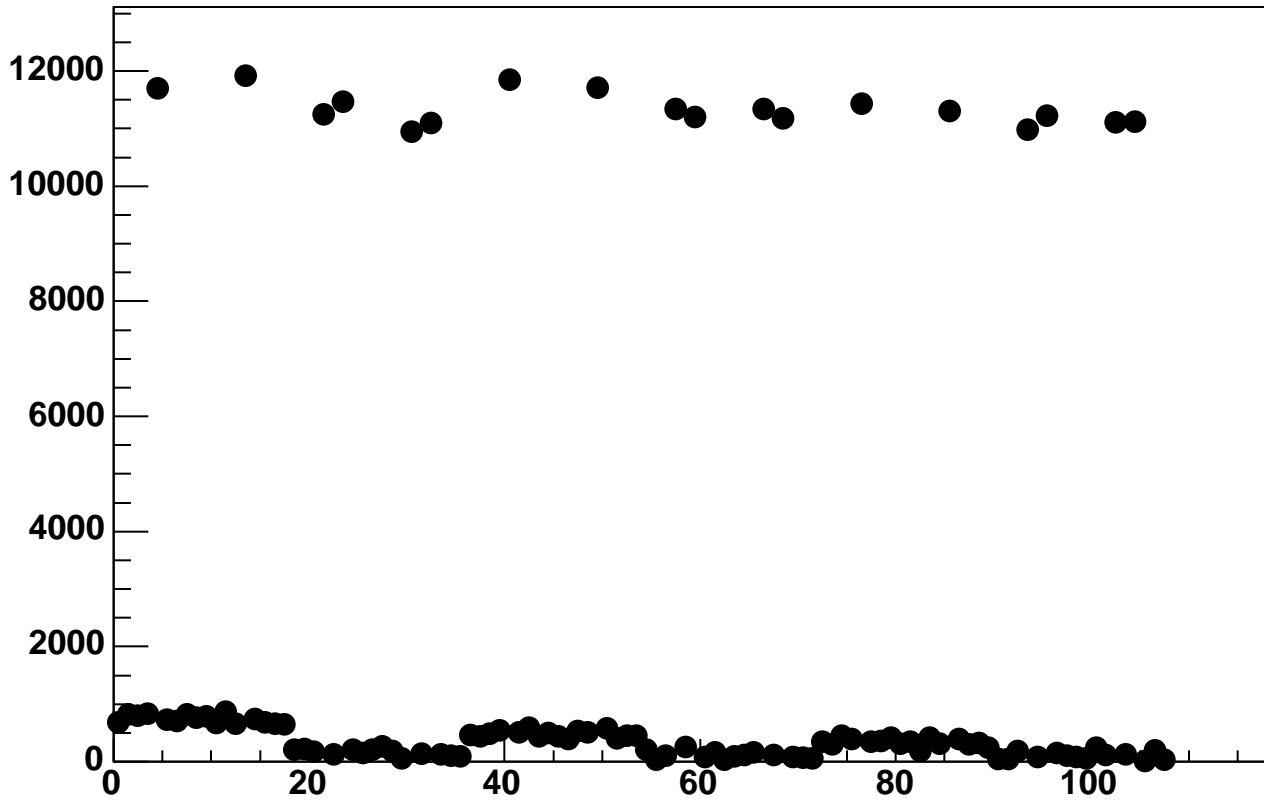
Enable 4, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



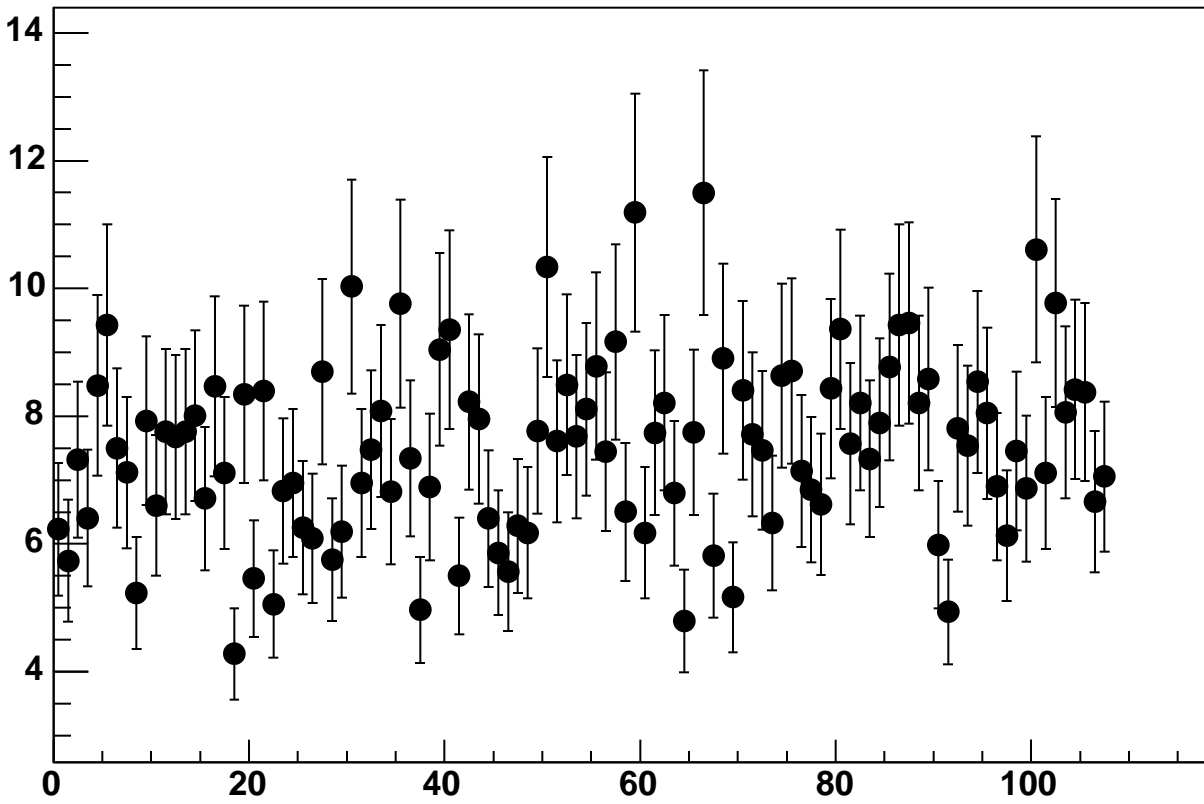
Enable 4, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



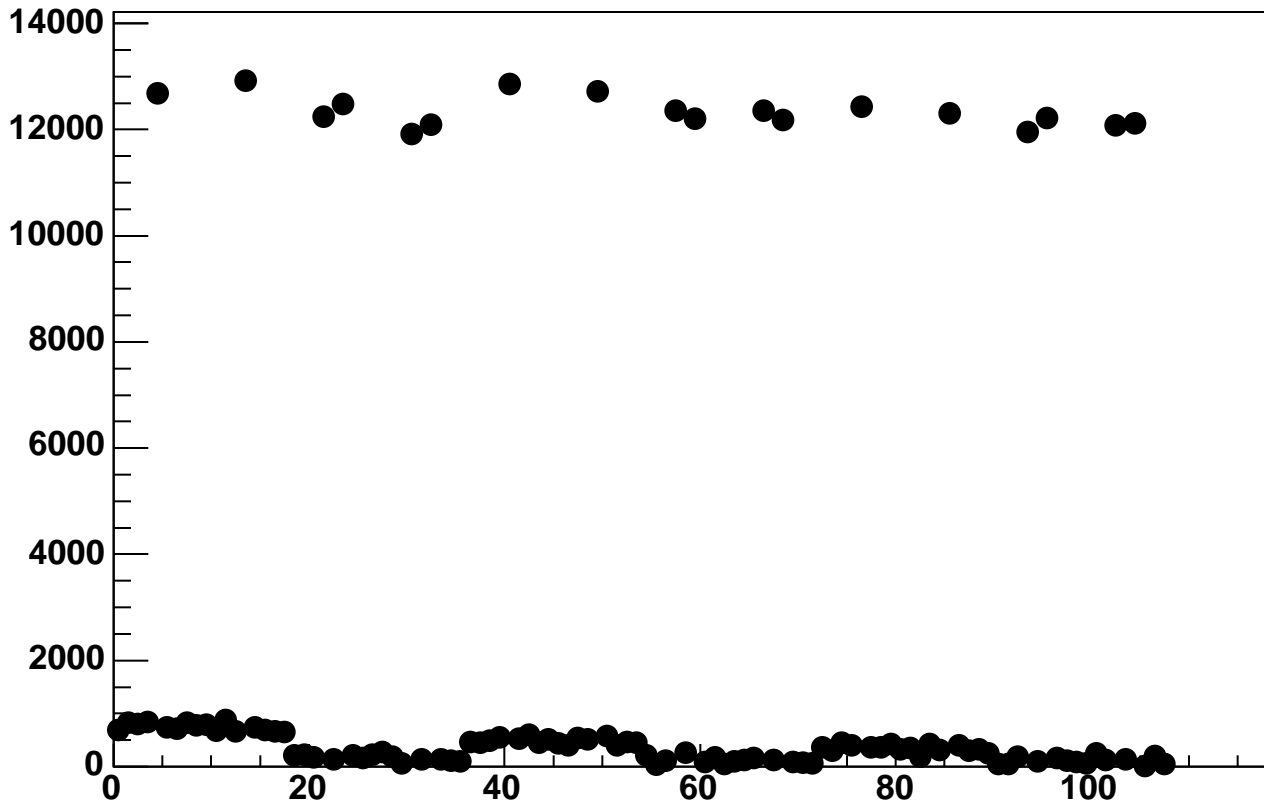
Enable 4, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



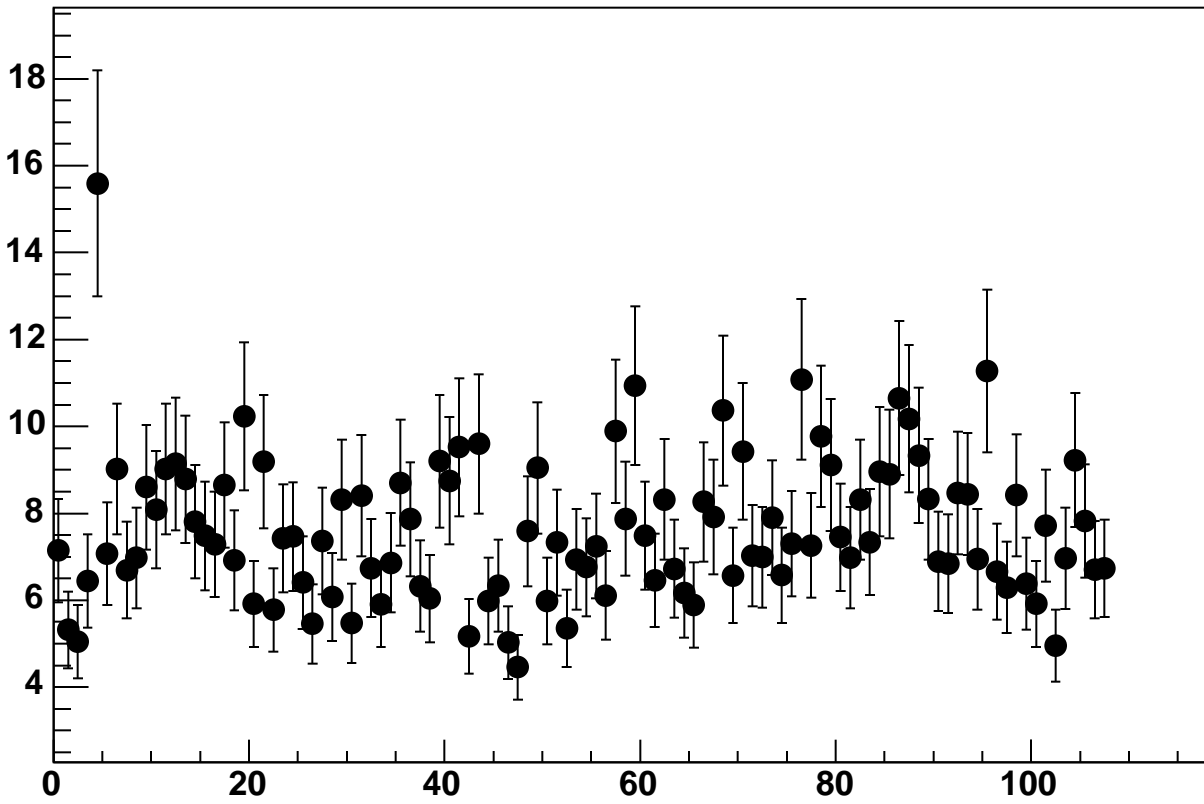
Enable 4, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



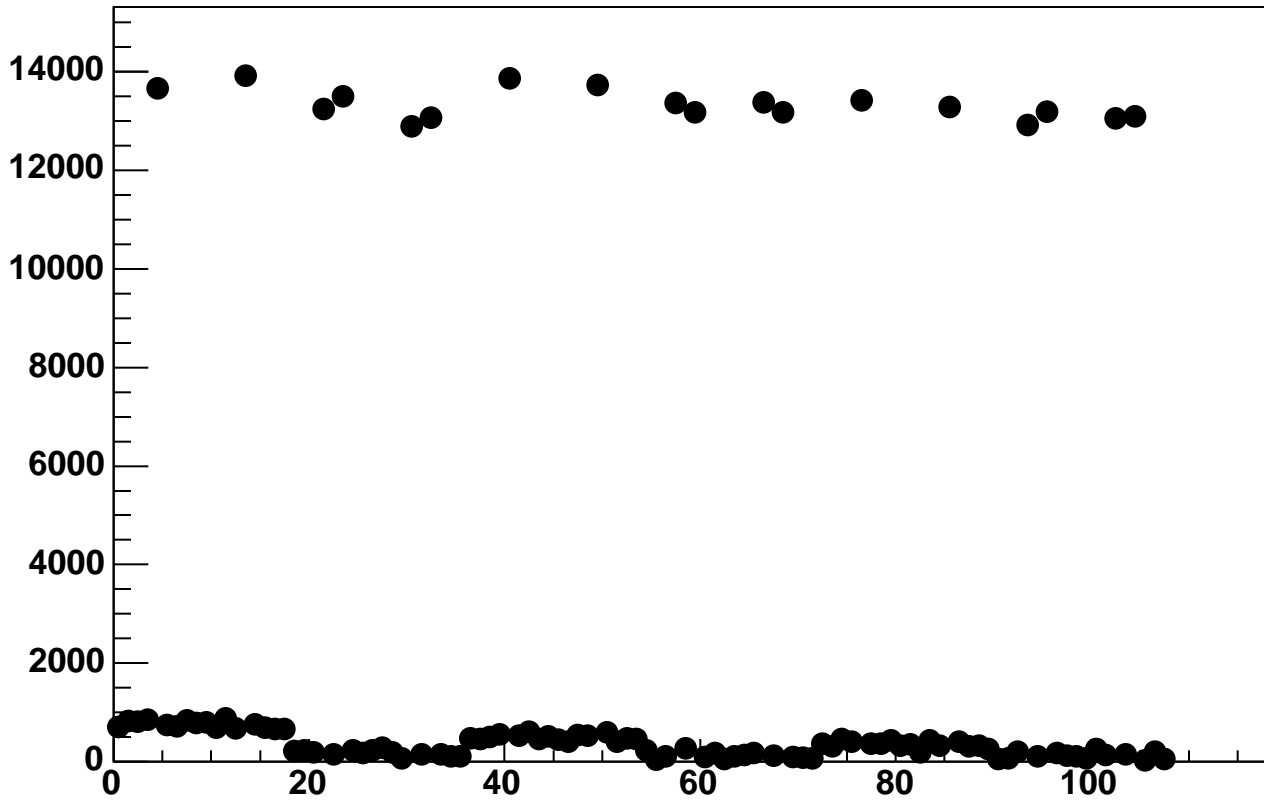
Enable 4, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



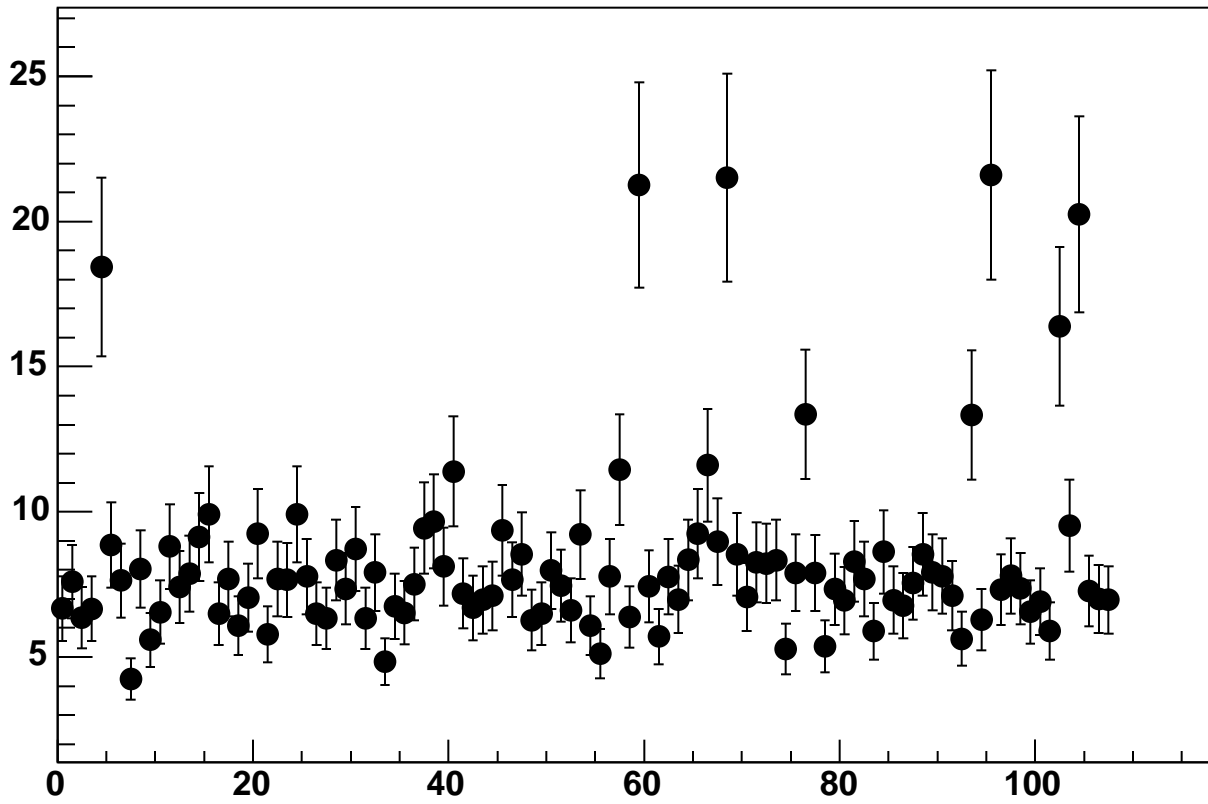
Enable 4, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



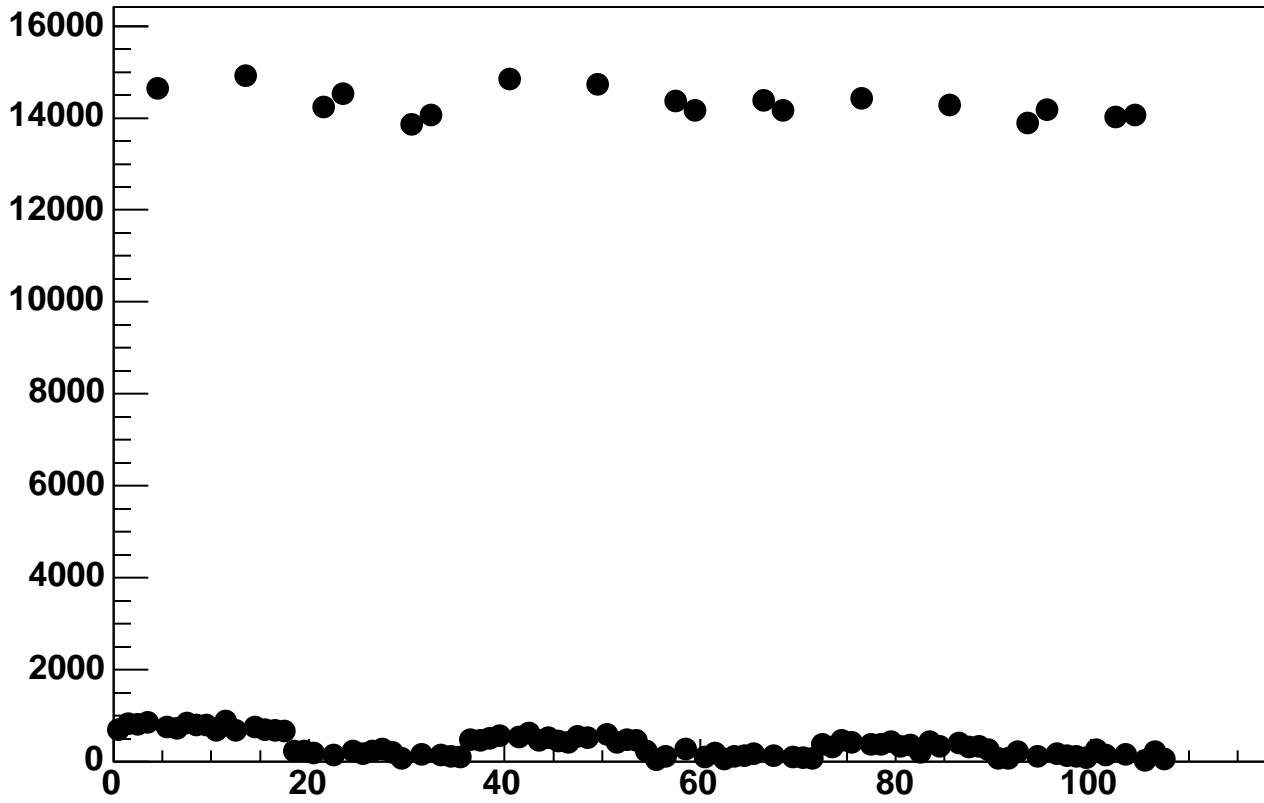
Enable 4, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



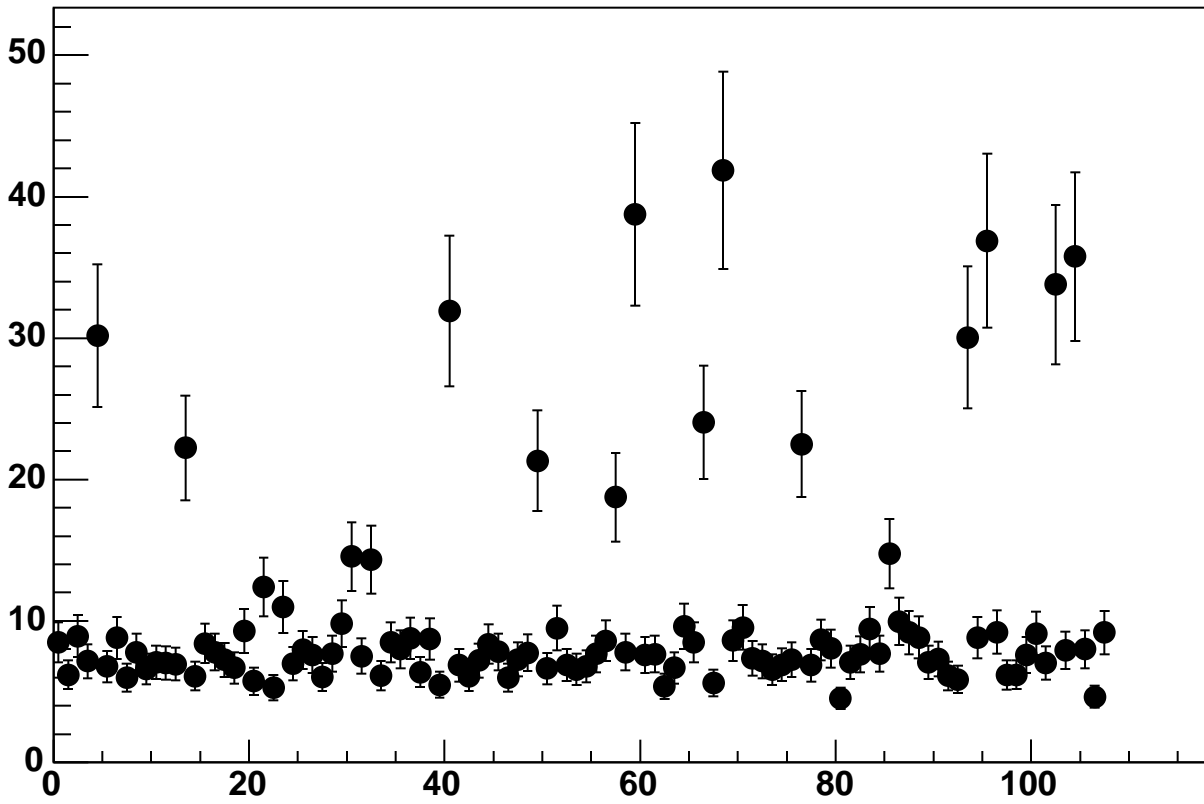
Enable 4, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



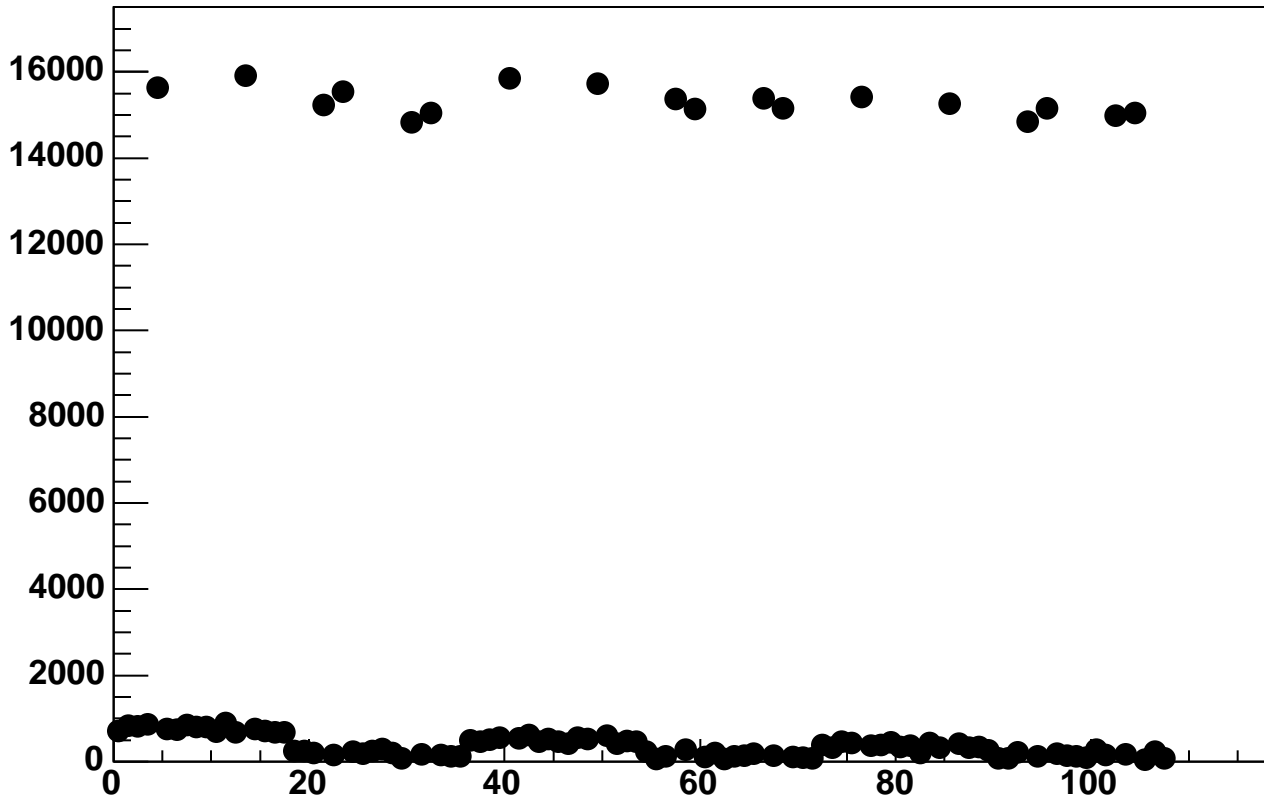
Enable 4, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



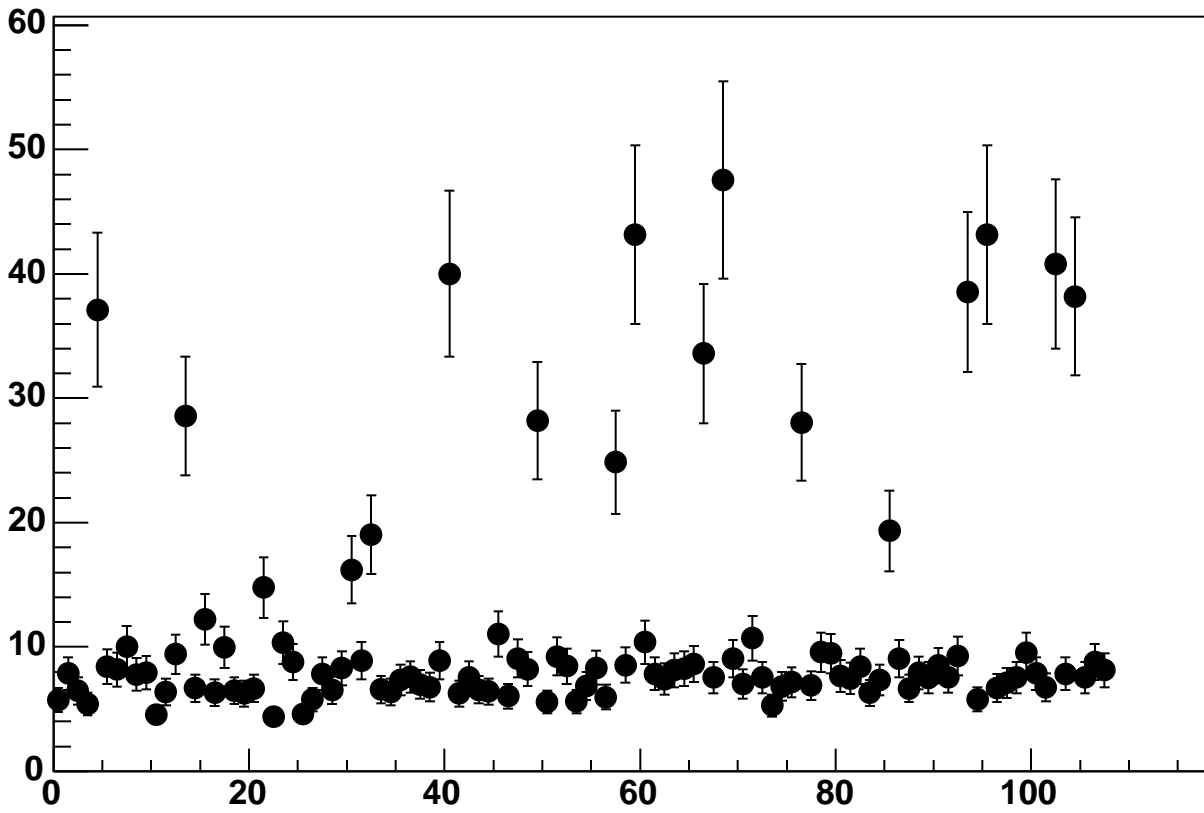
Enable 4, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



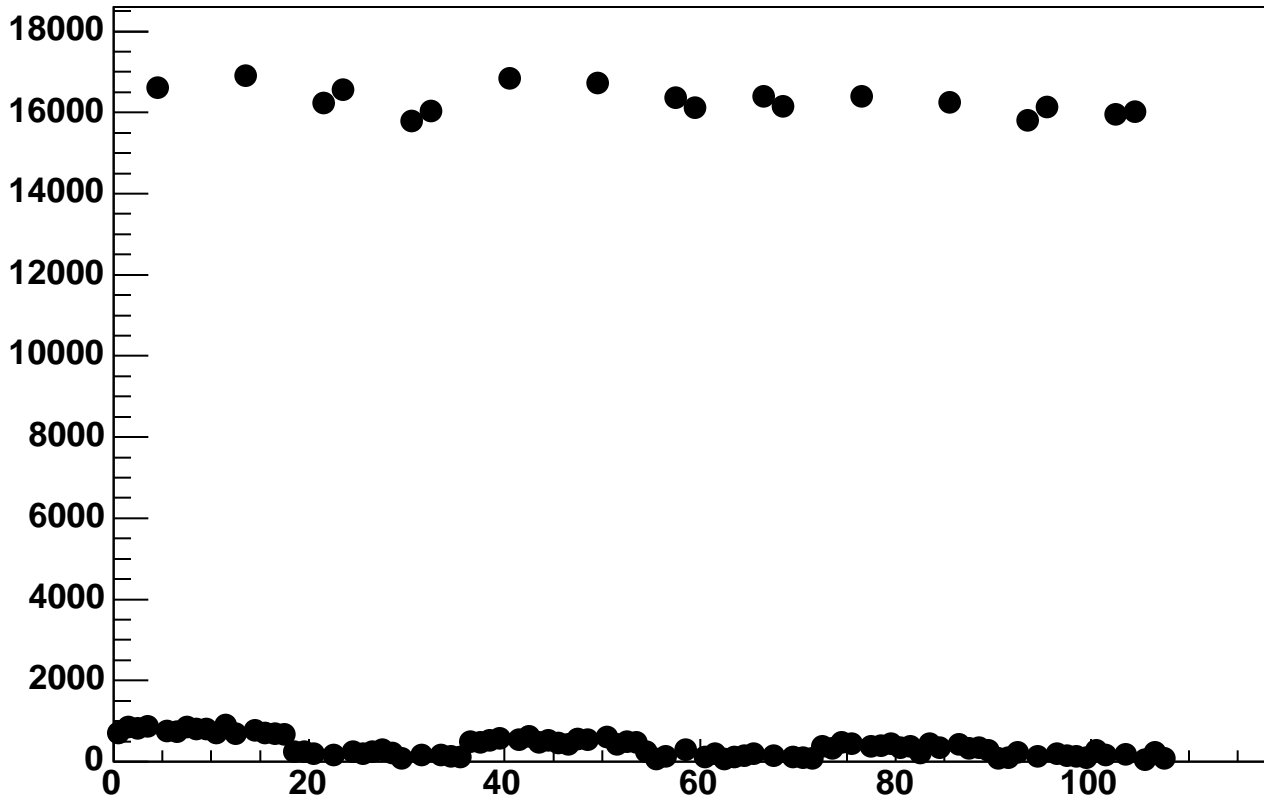
Enable 4, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



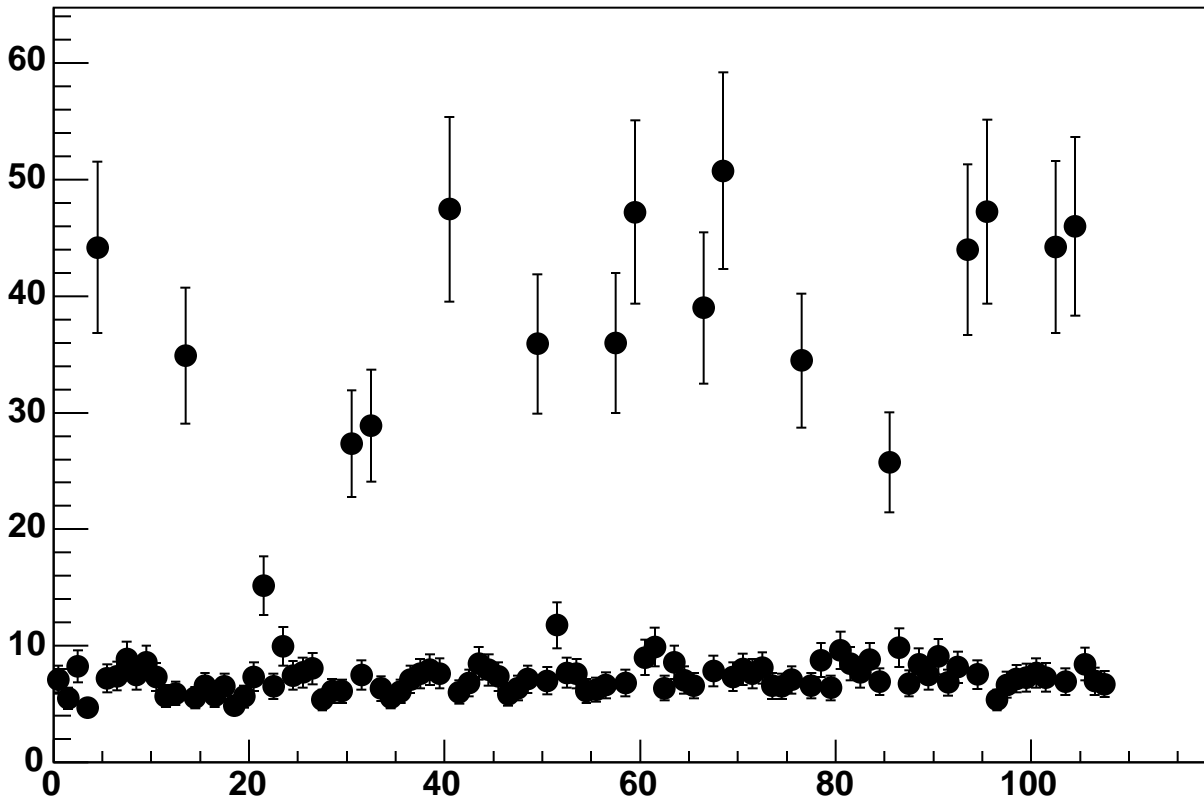
Enable 4, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



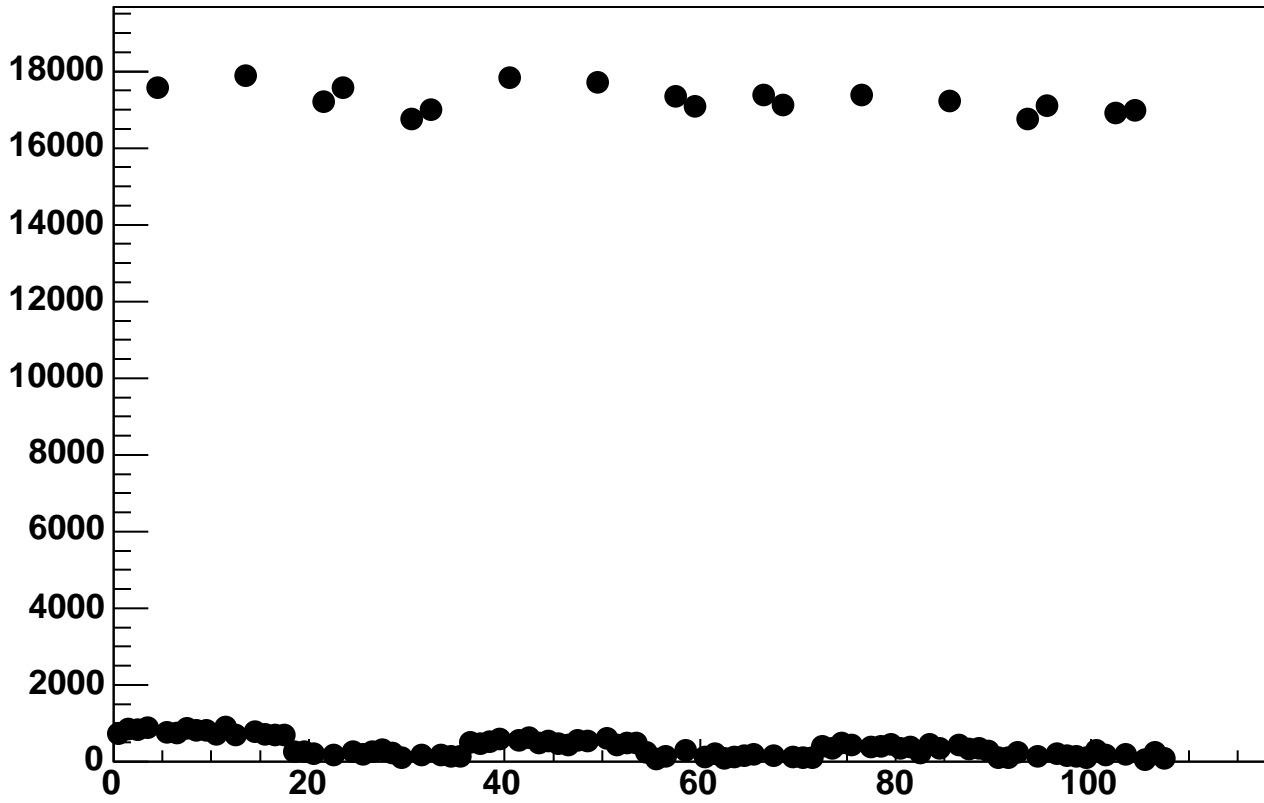
Enable 4, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



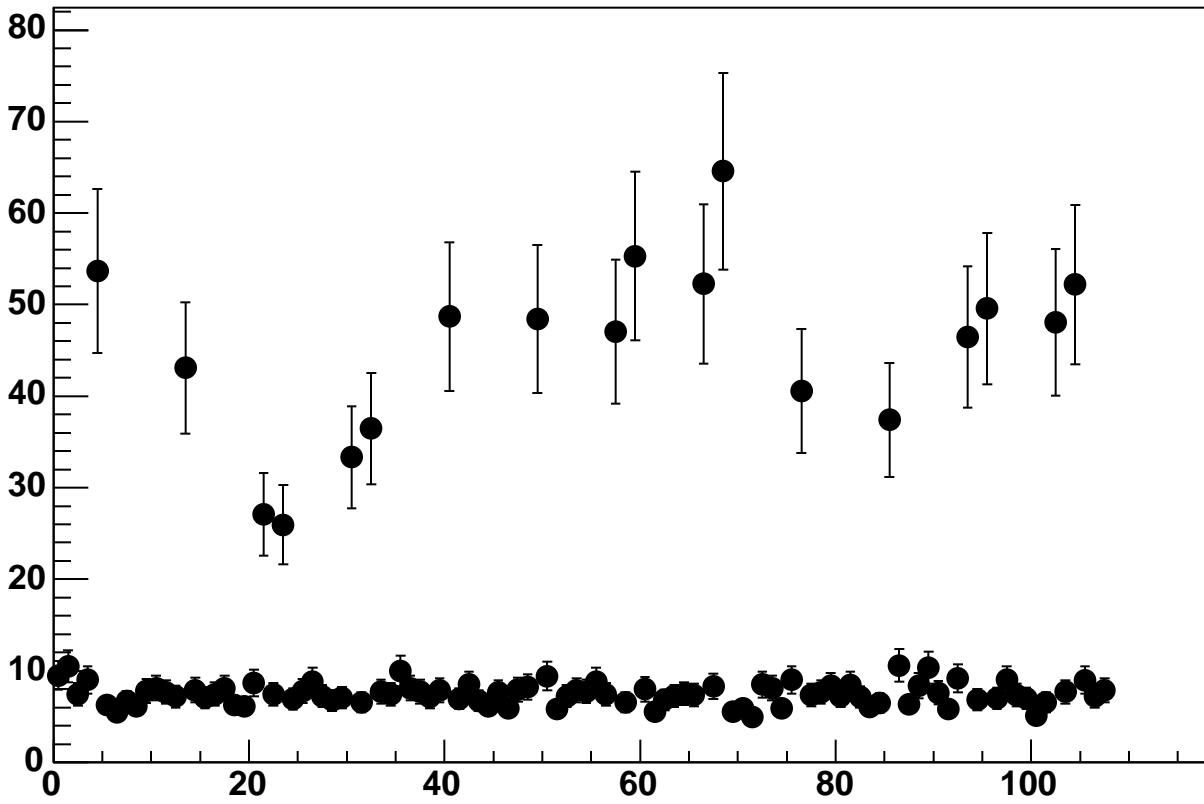
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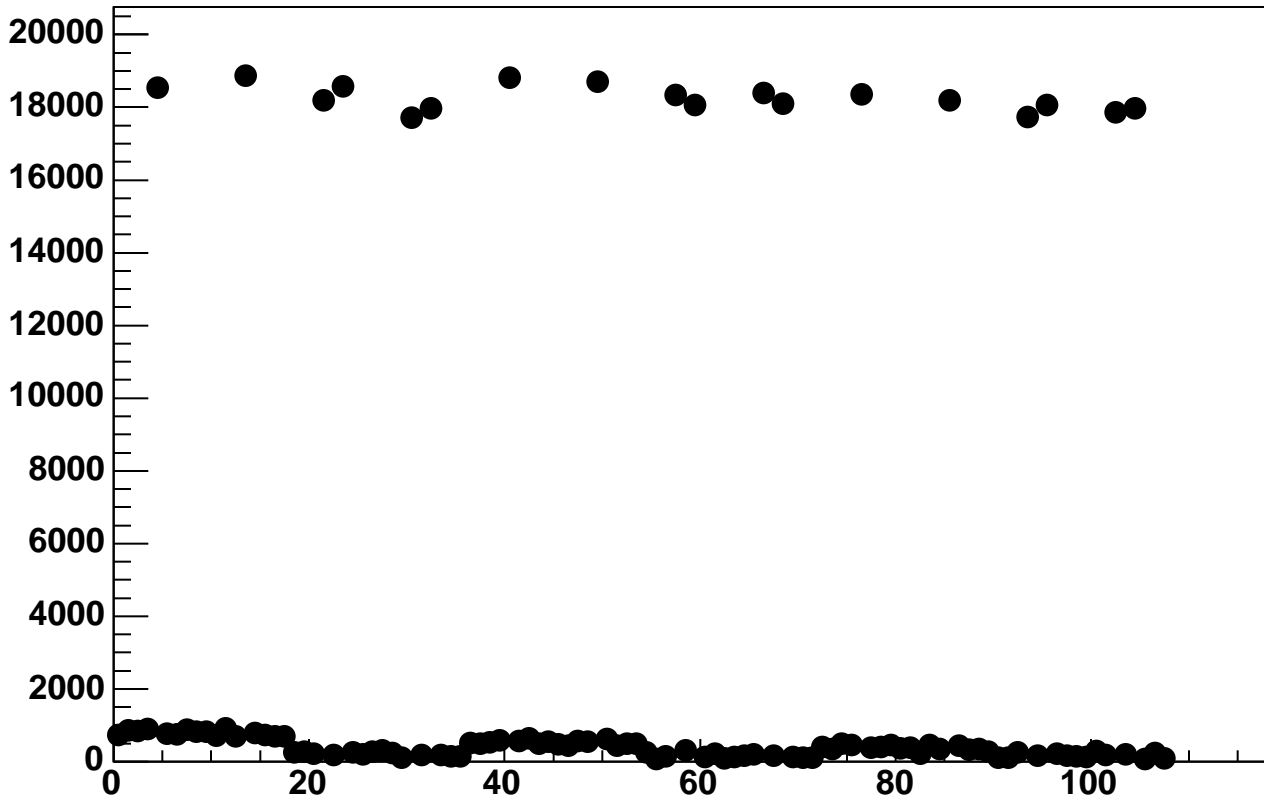
Enable 4, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



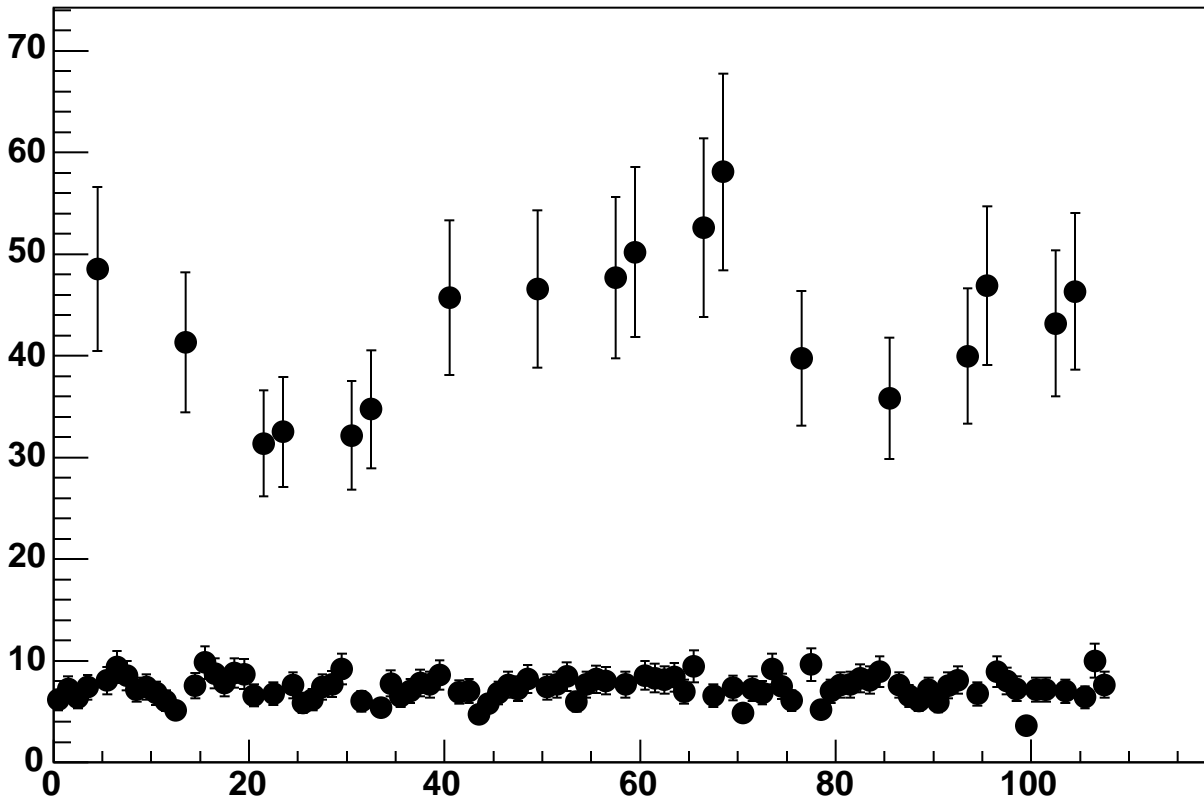
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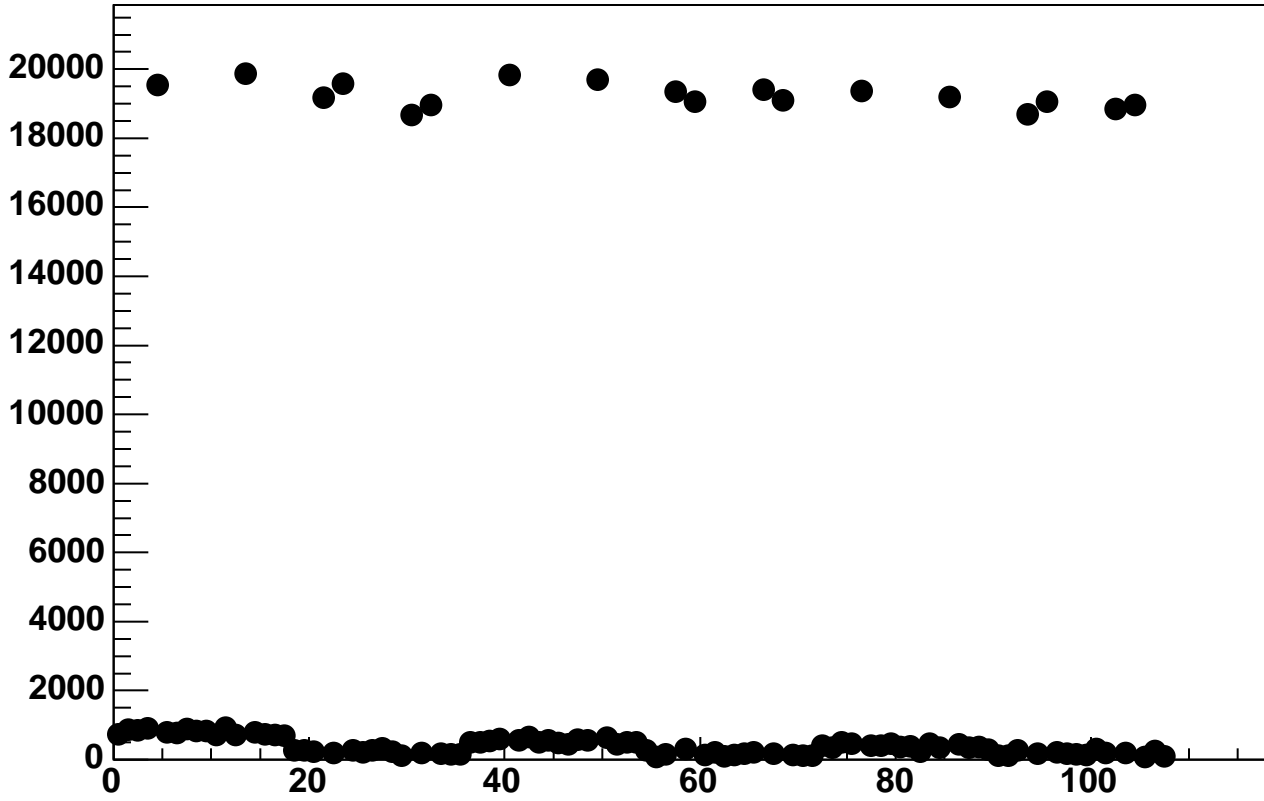
Enable 4, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



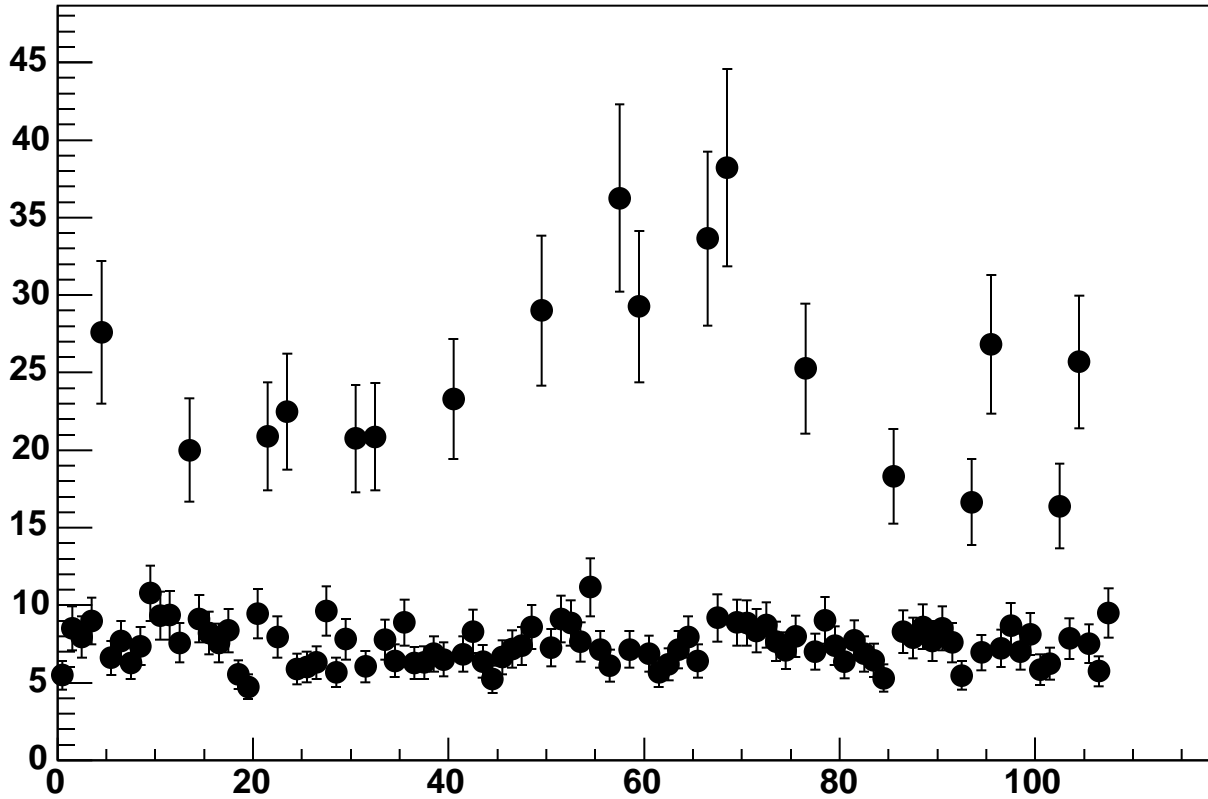
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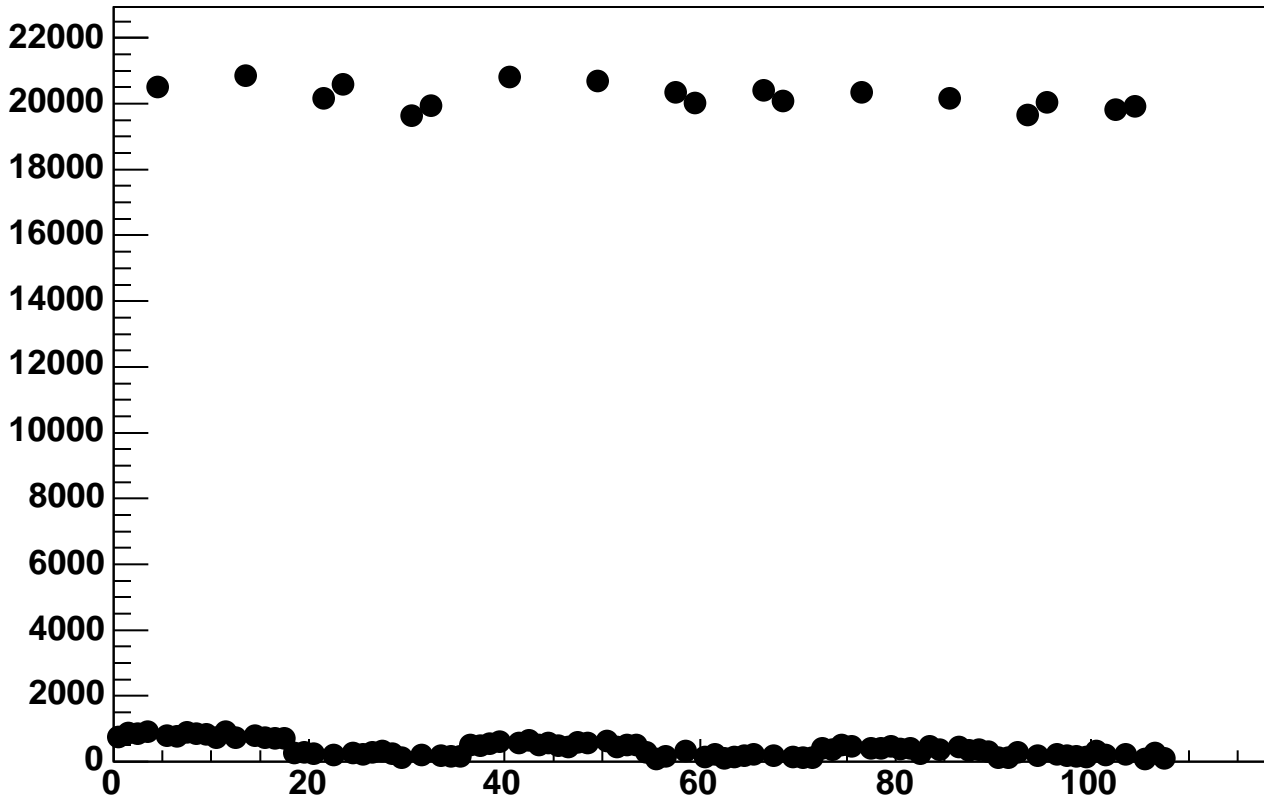
Enable 4, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



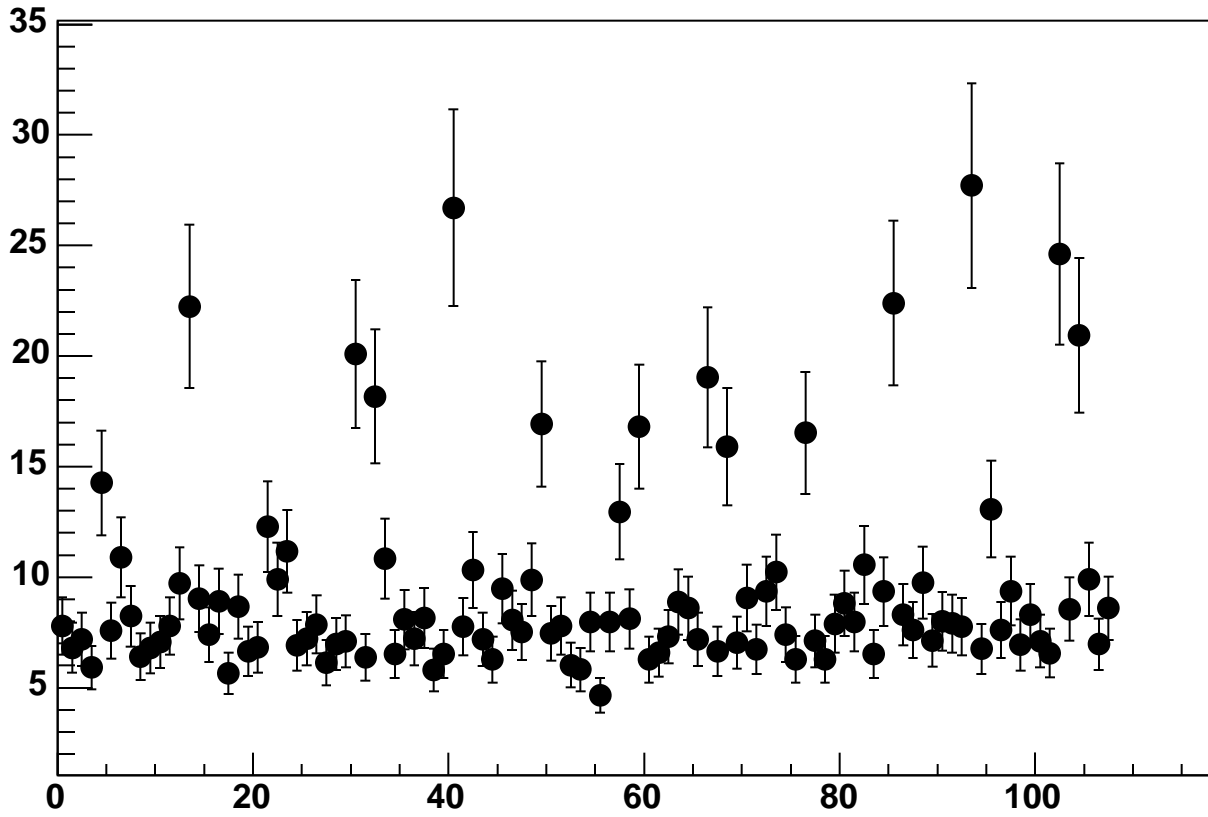
Enable 4, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



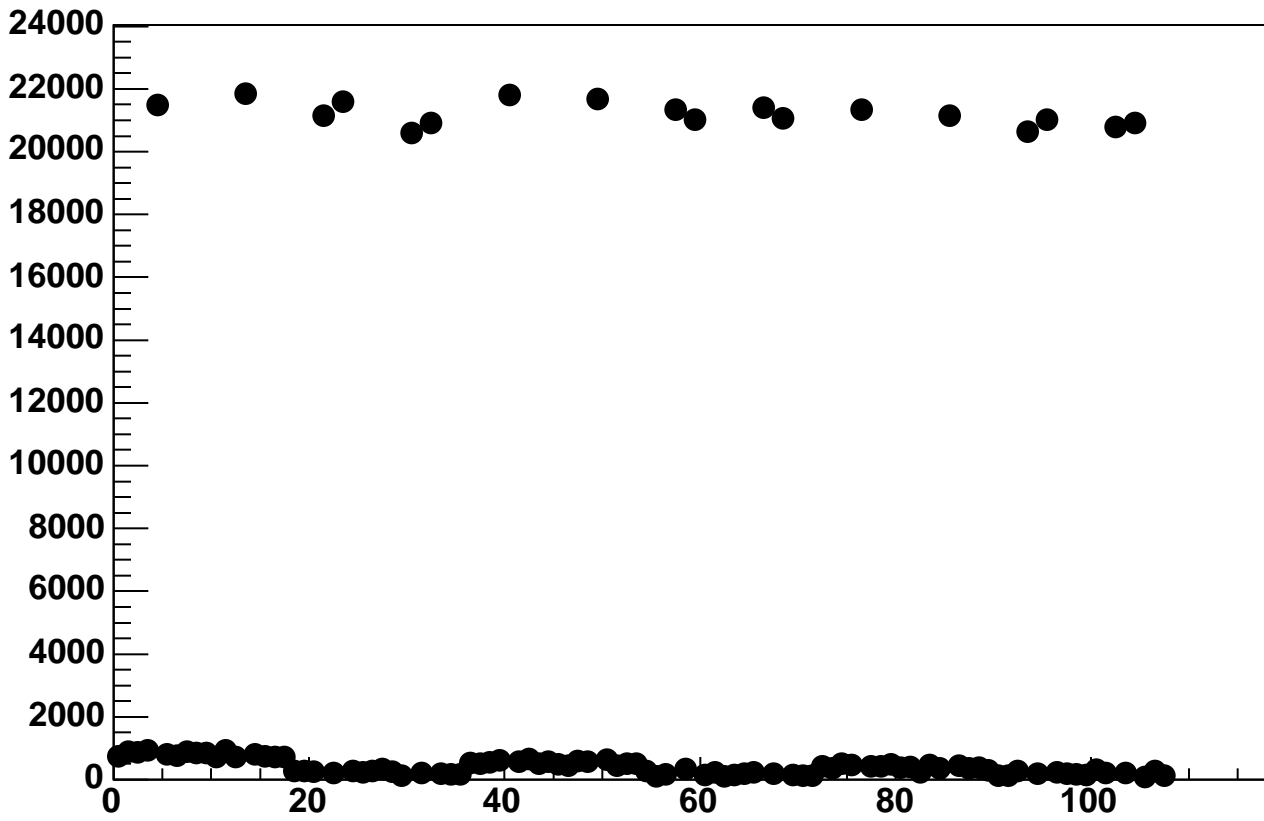
Enable 4, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



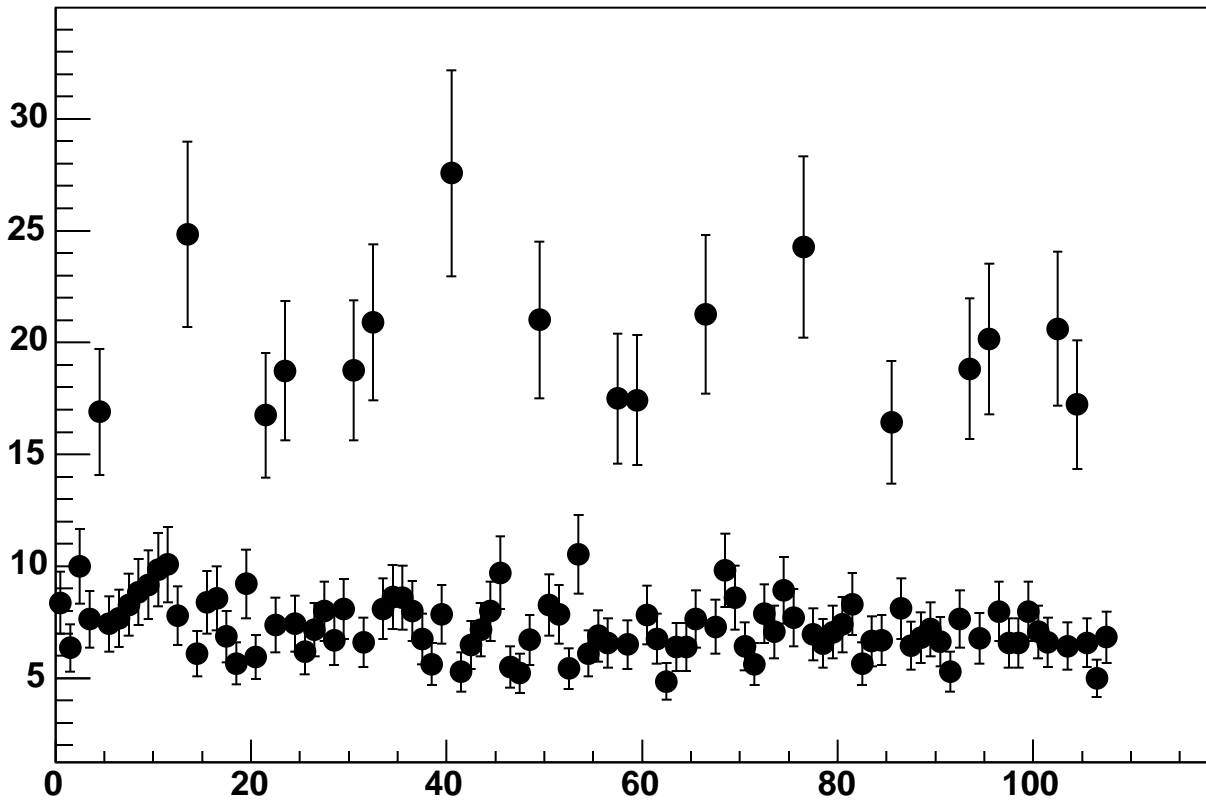
Enable 4, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



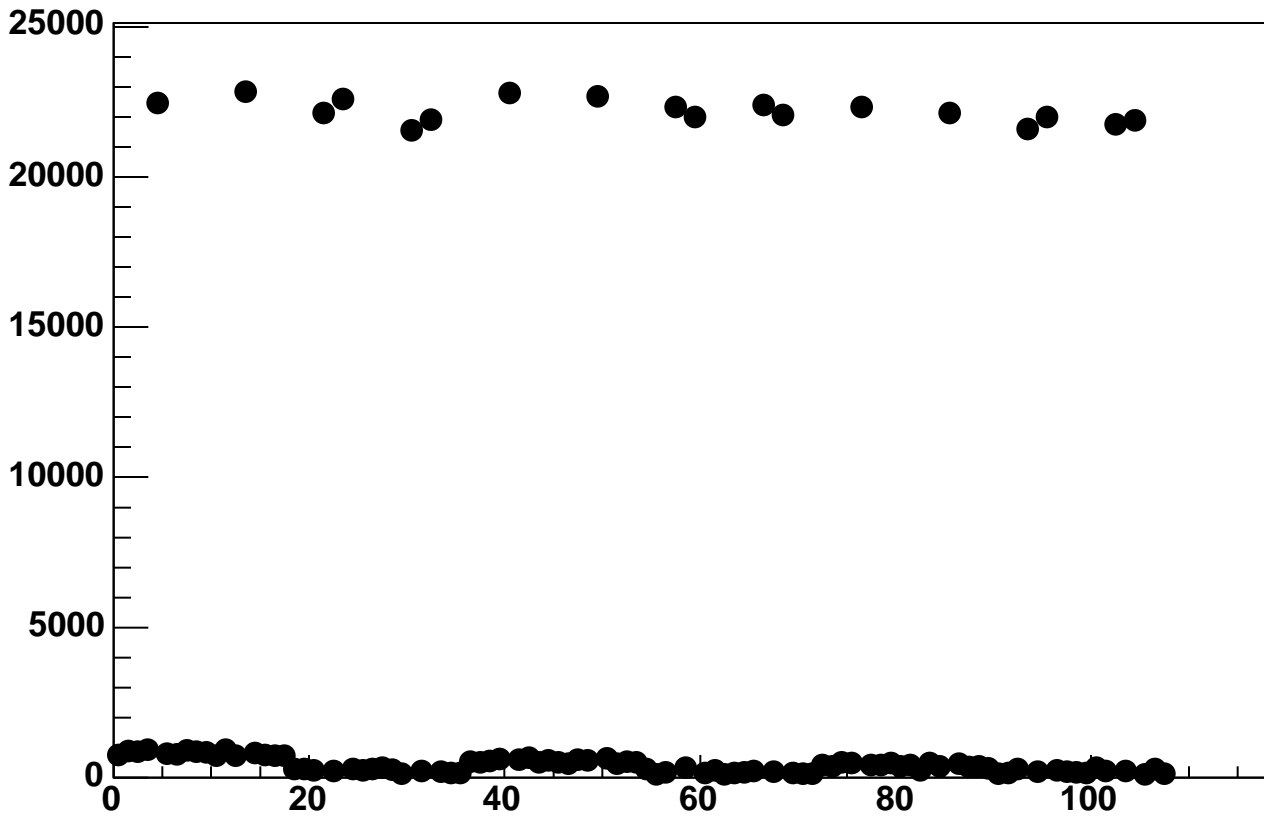
Enable 4, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



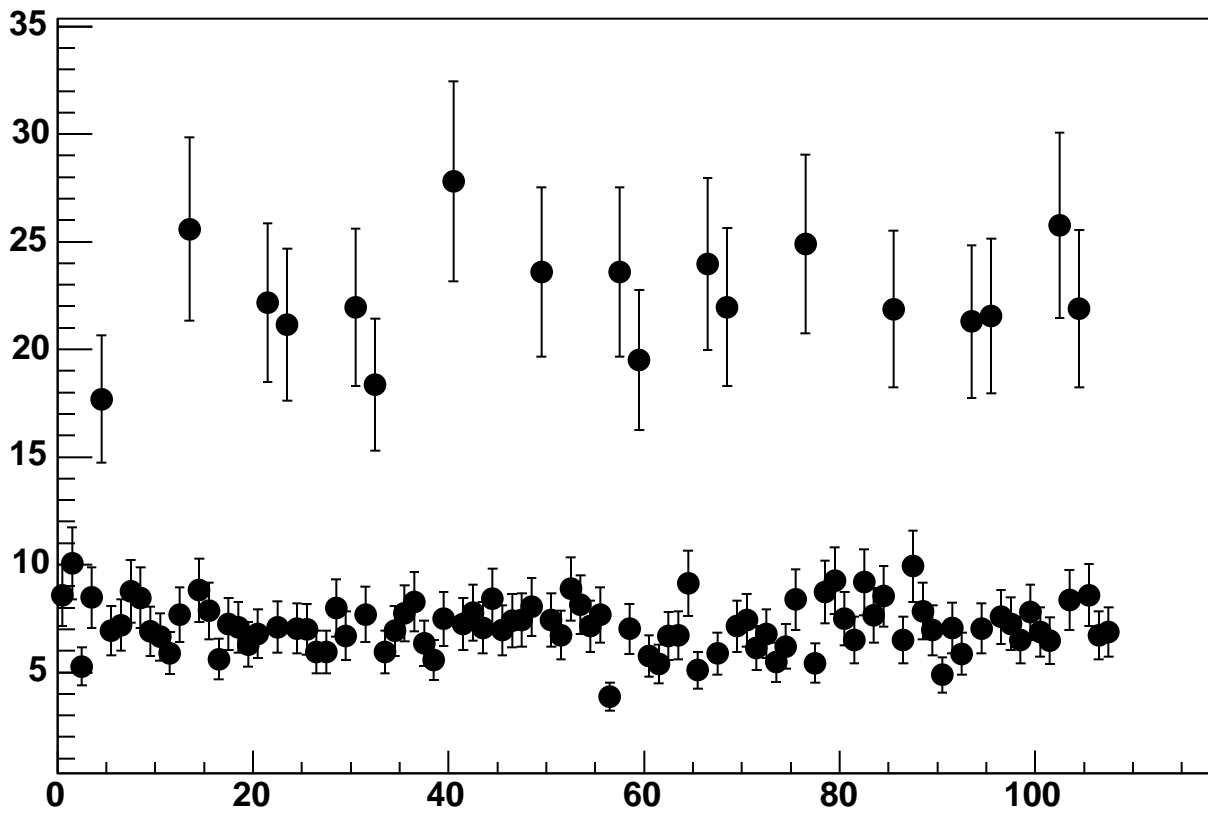
Enable 4, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



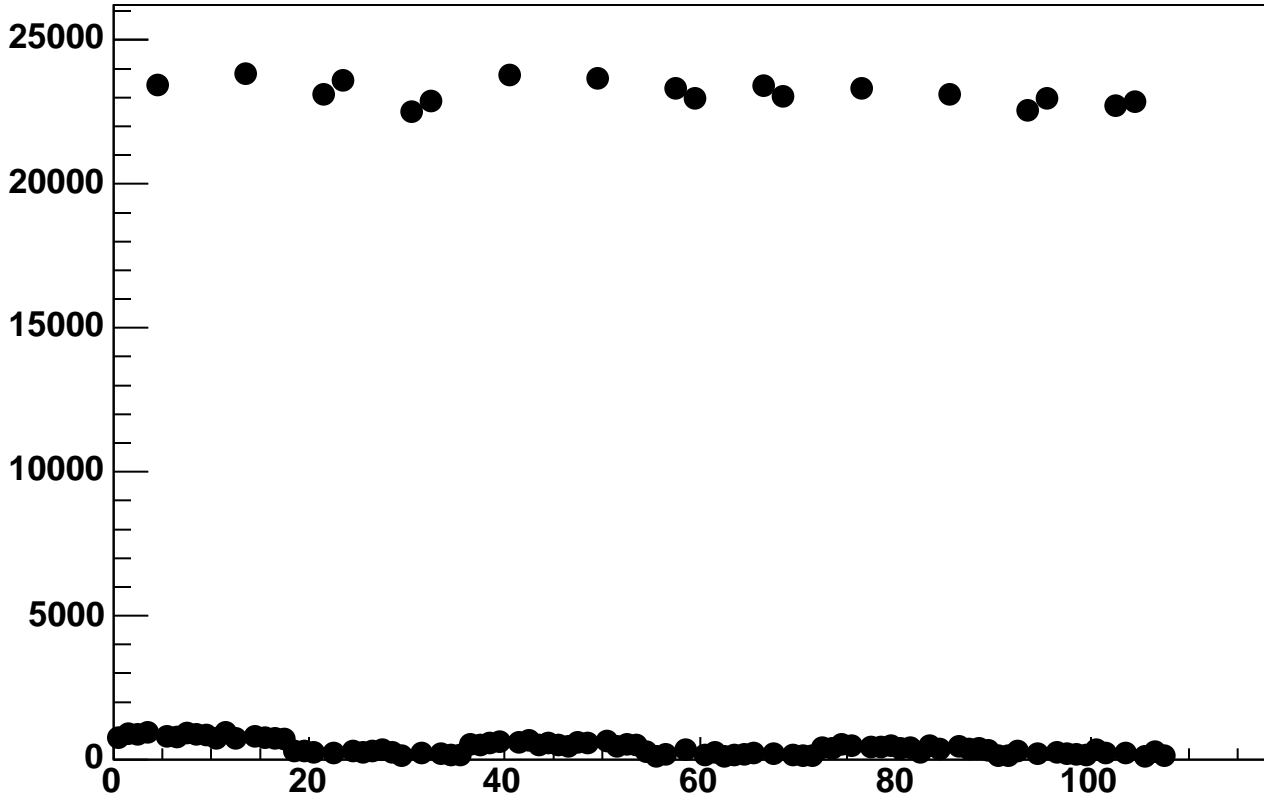
Enable 4, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



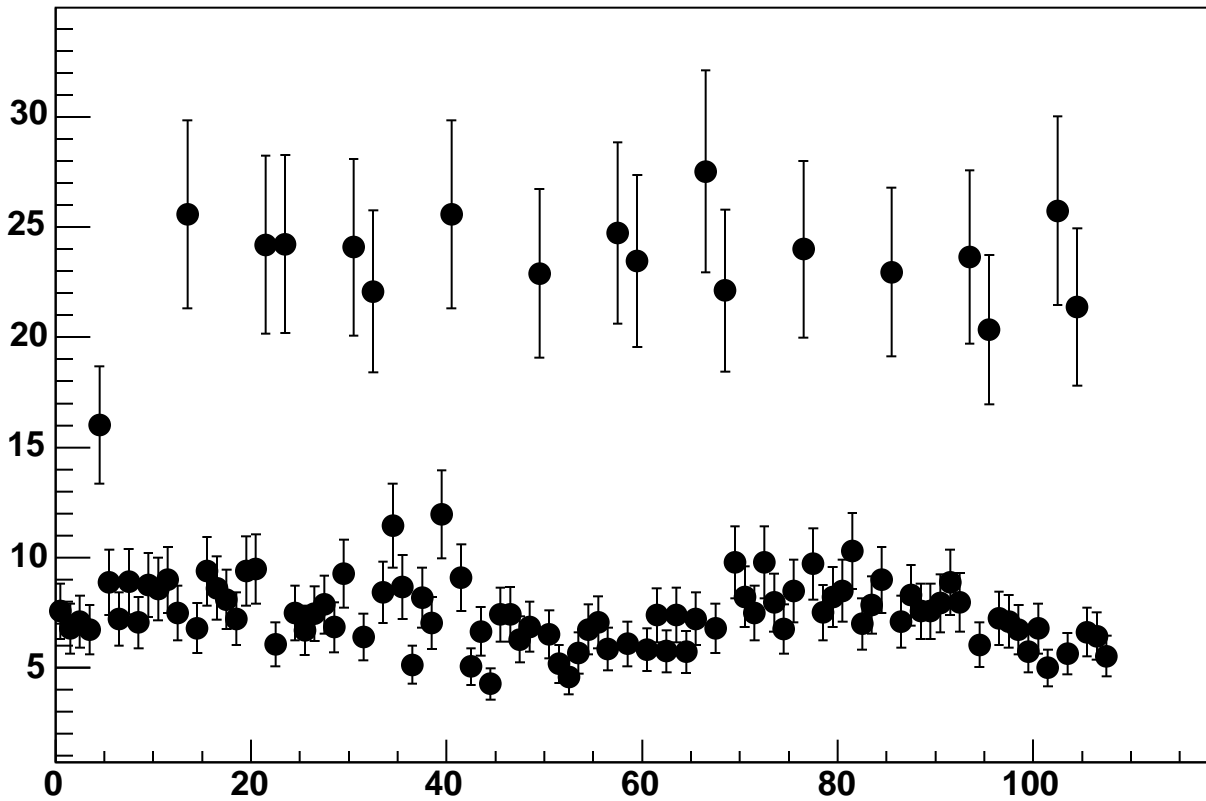
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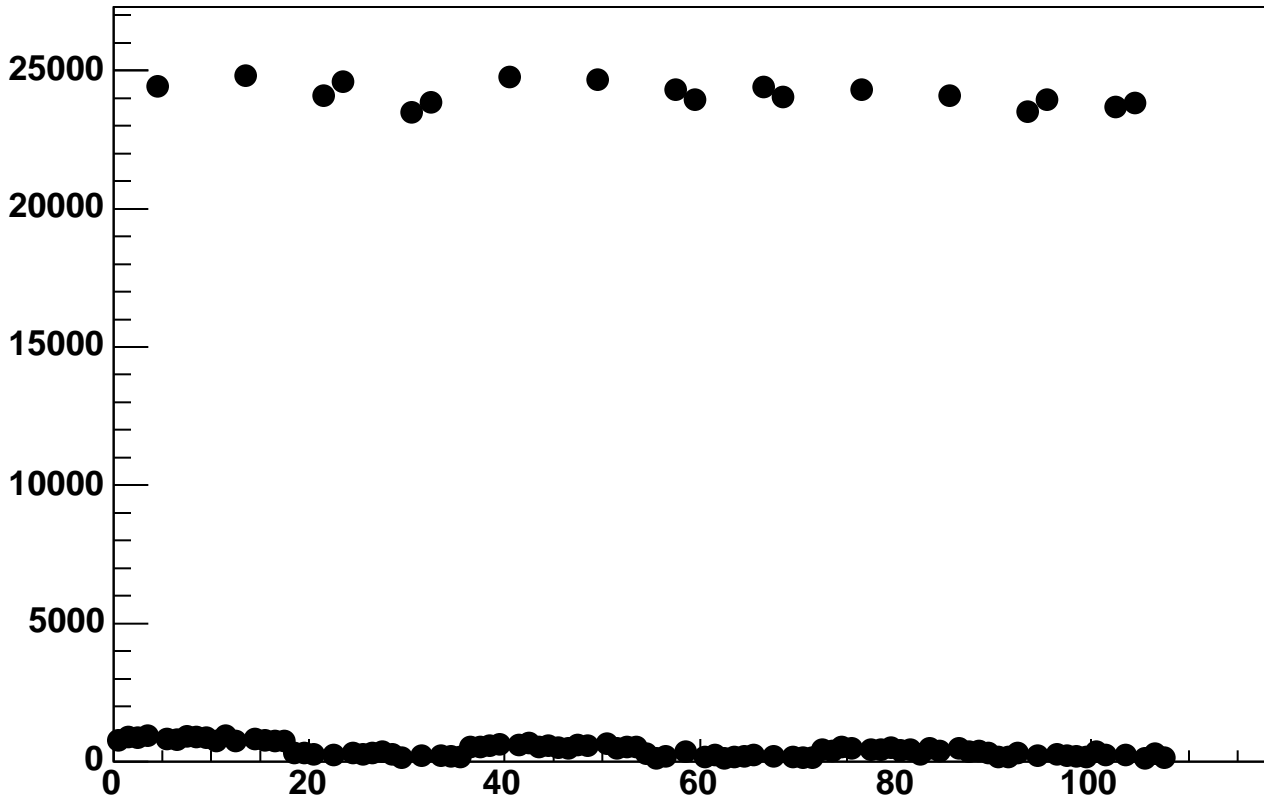
Enable 4, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



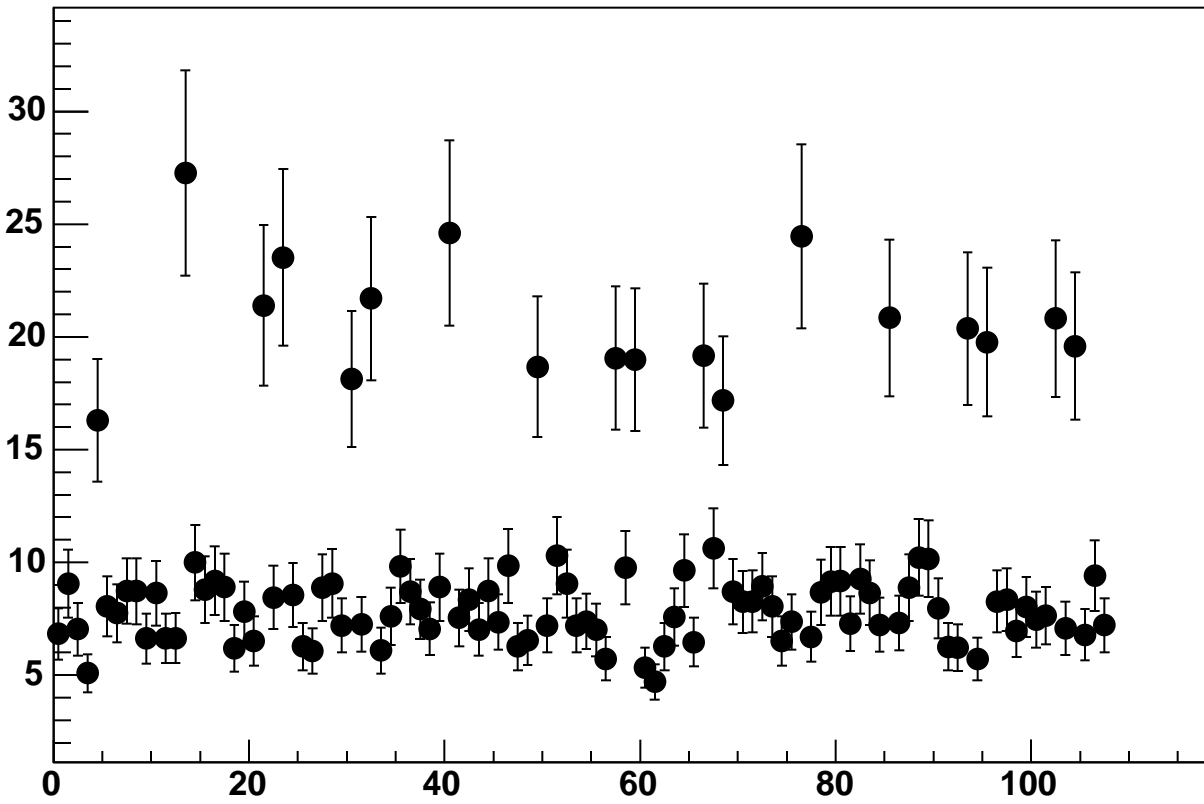
Enable 4, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



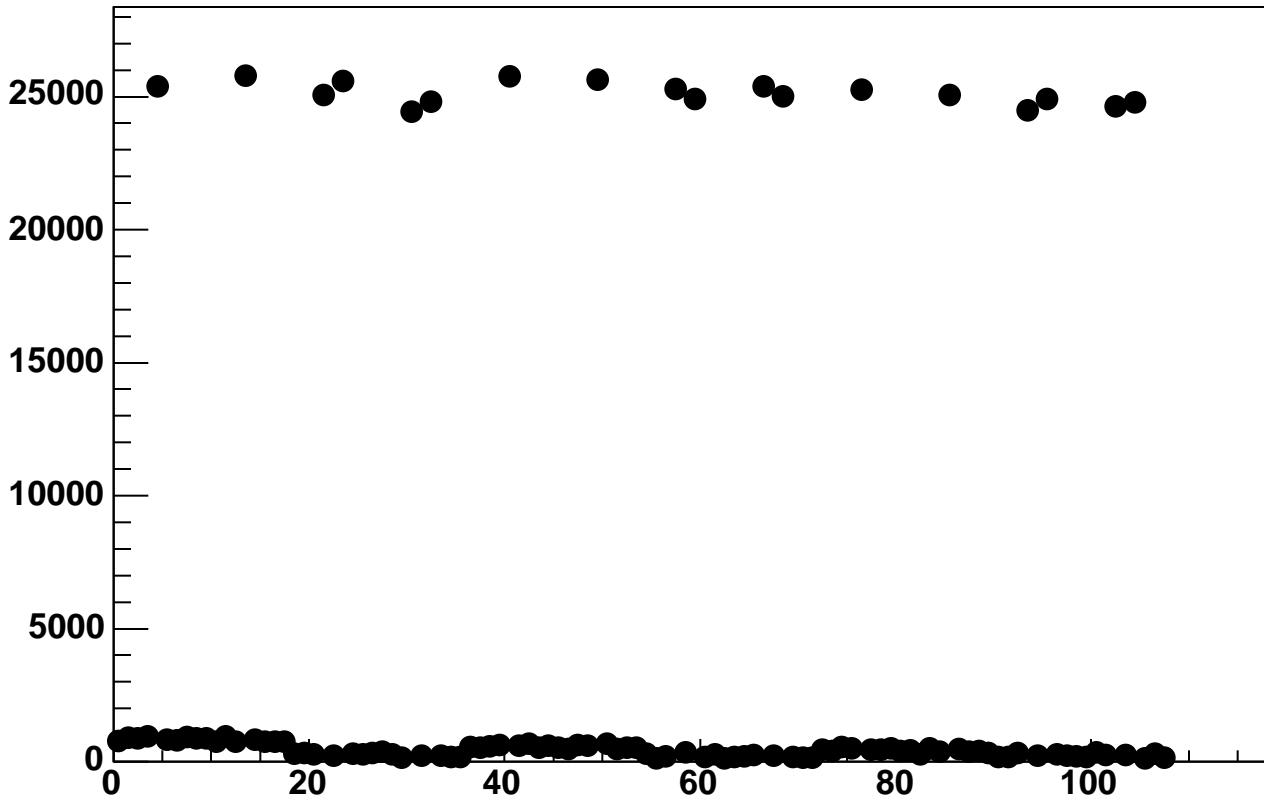
Enable 4, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



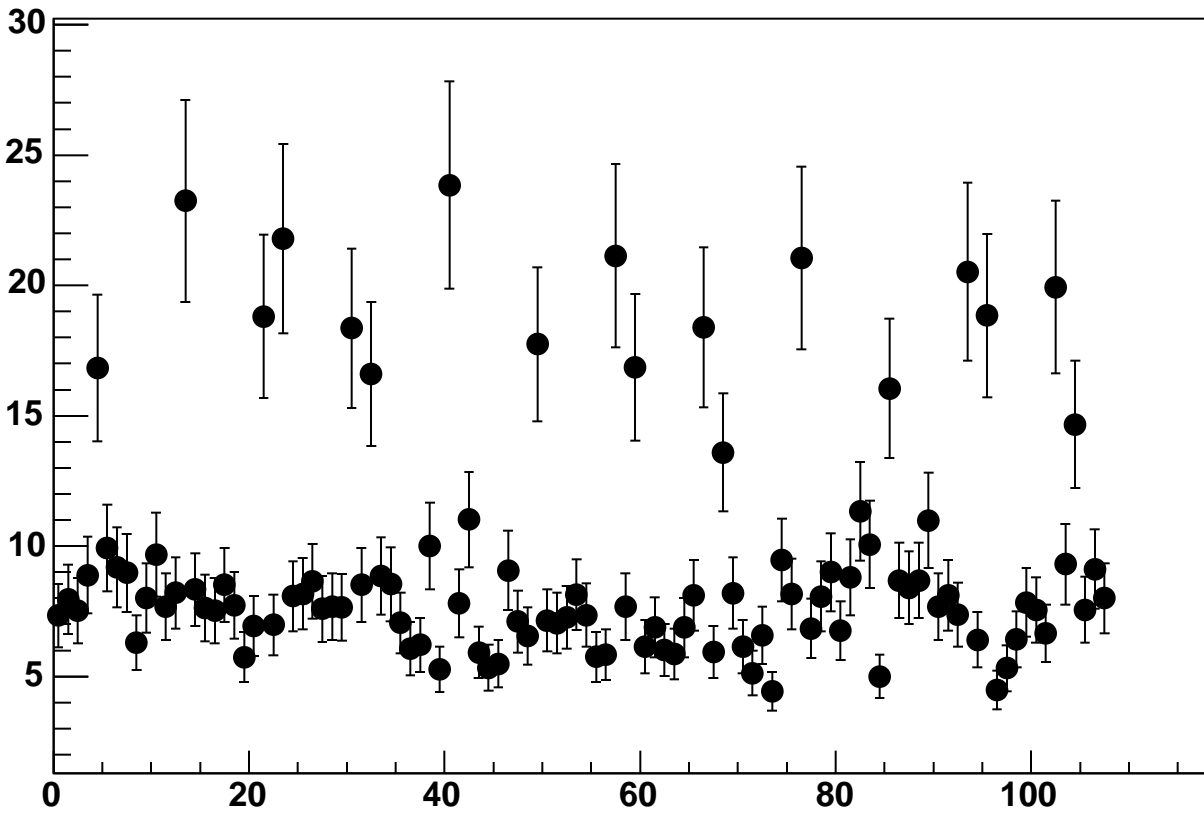
Enable 4, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



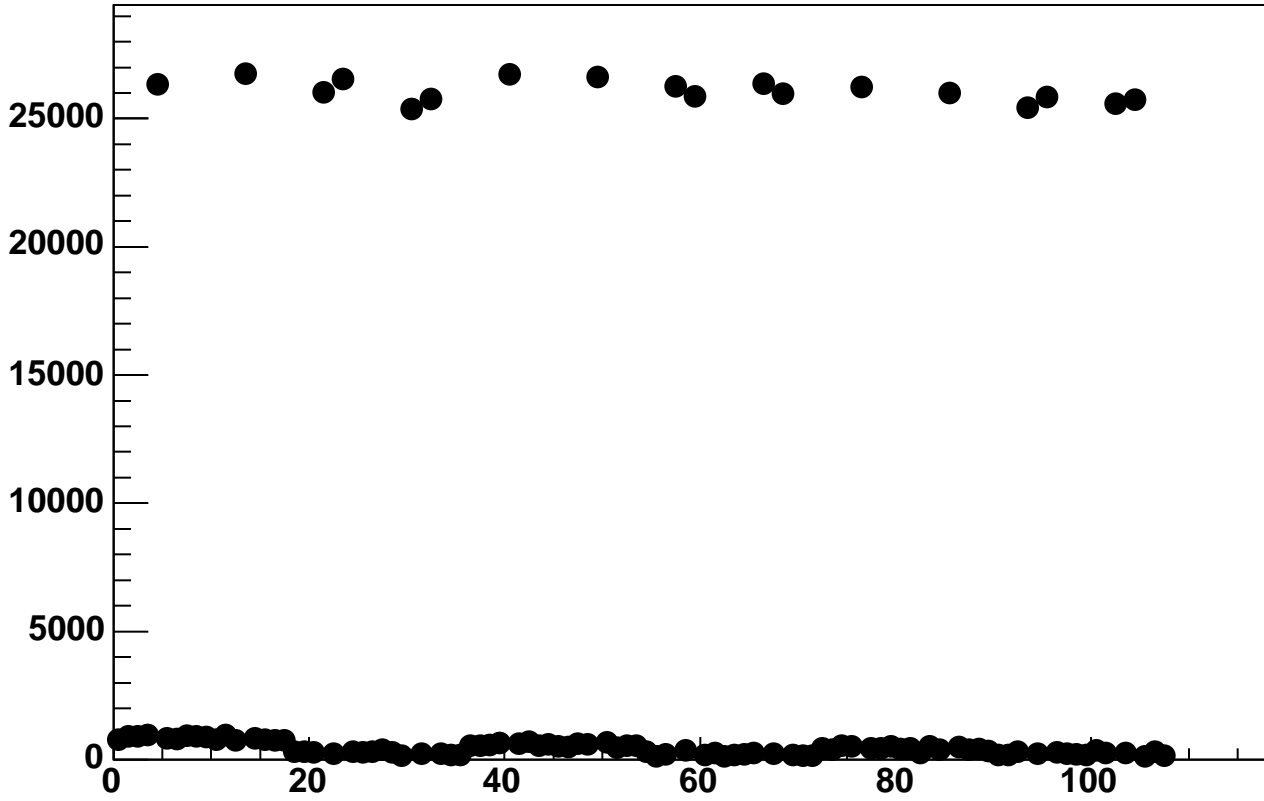
Enable 4, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



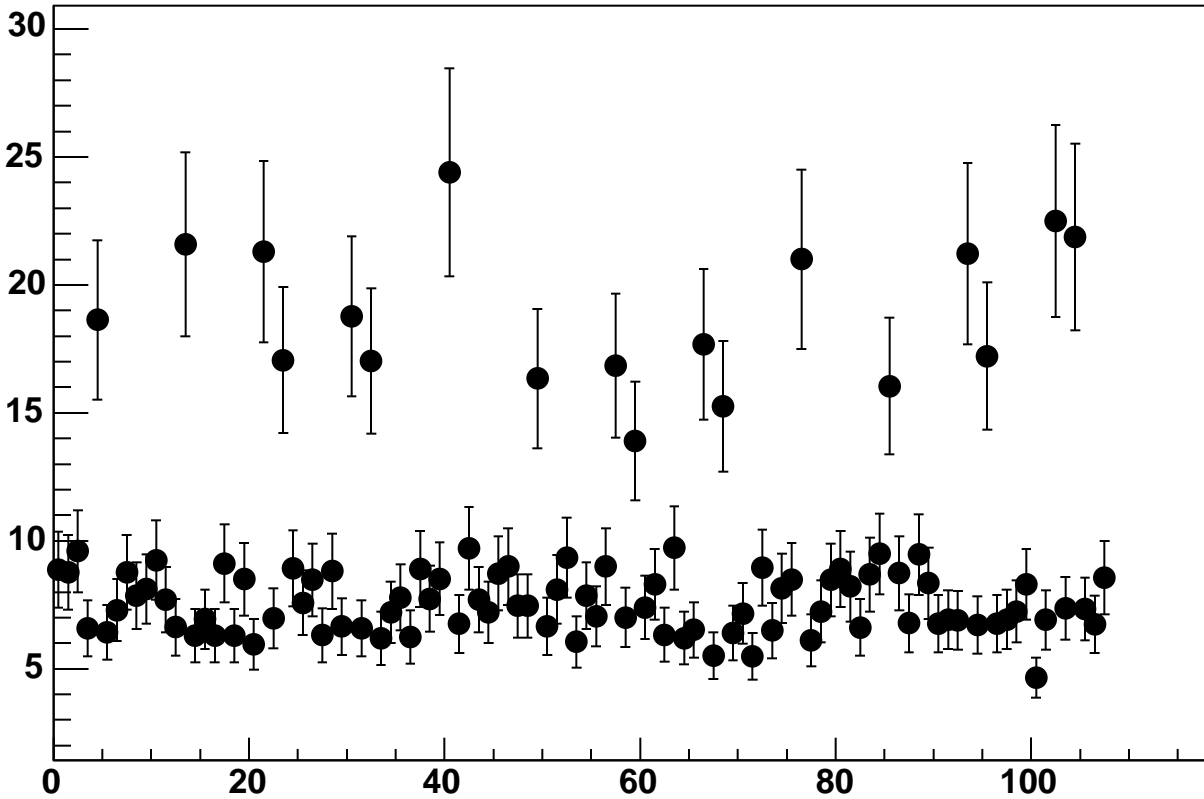
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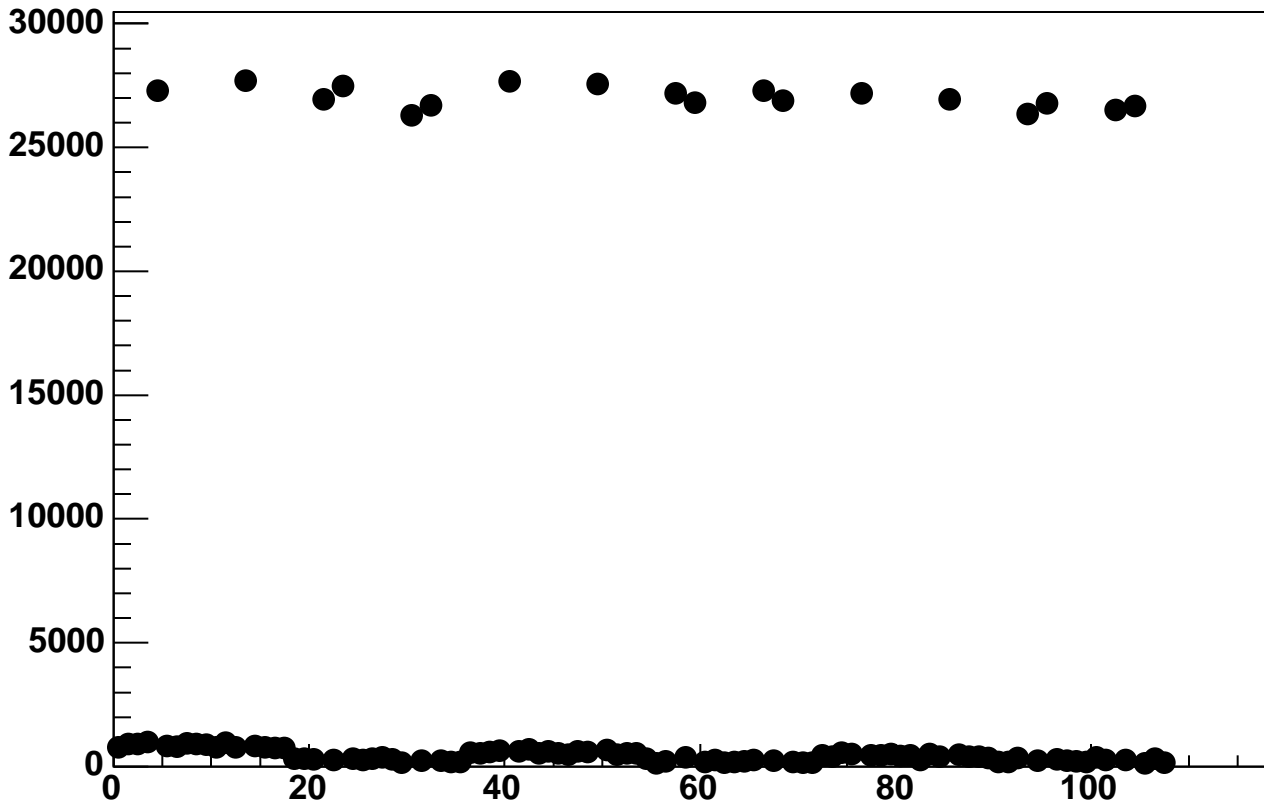
Enable 4, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



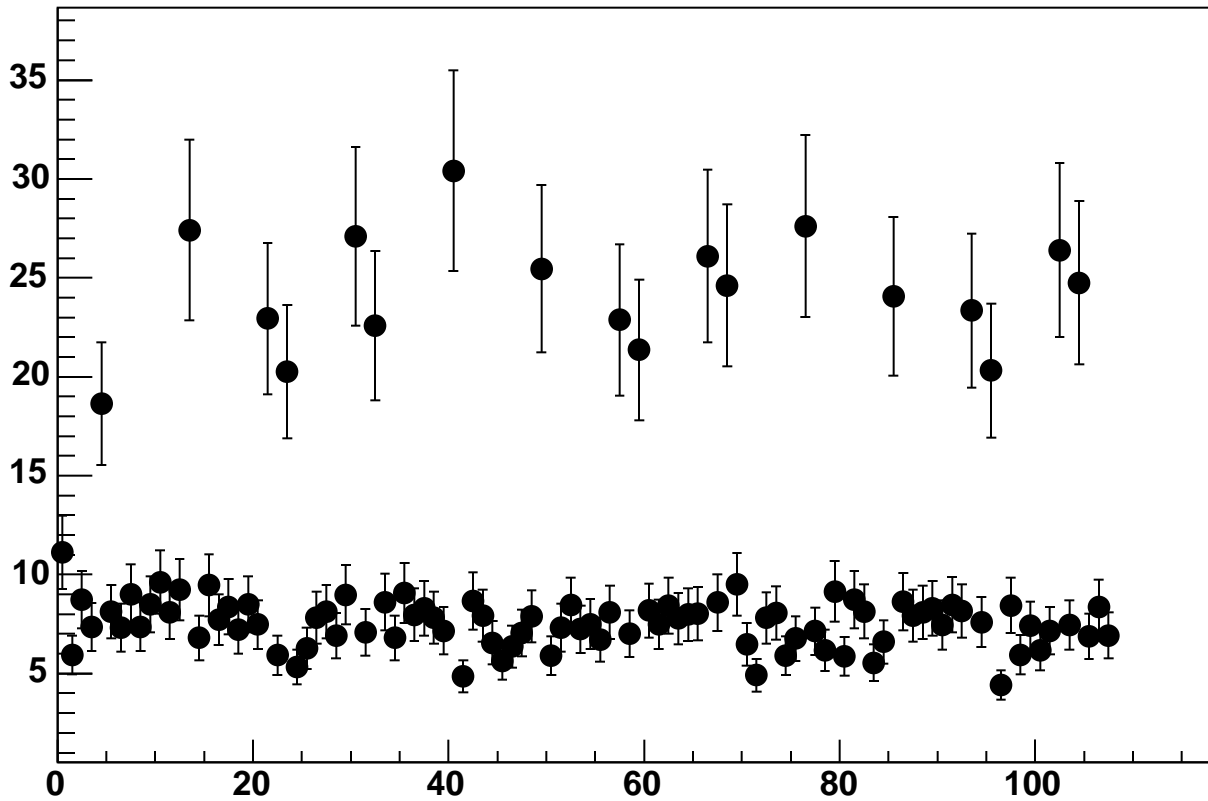
Enable 4, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



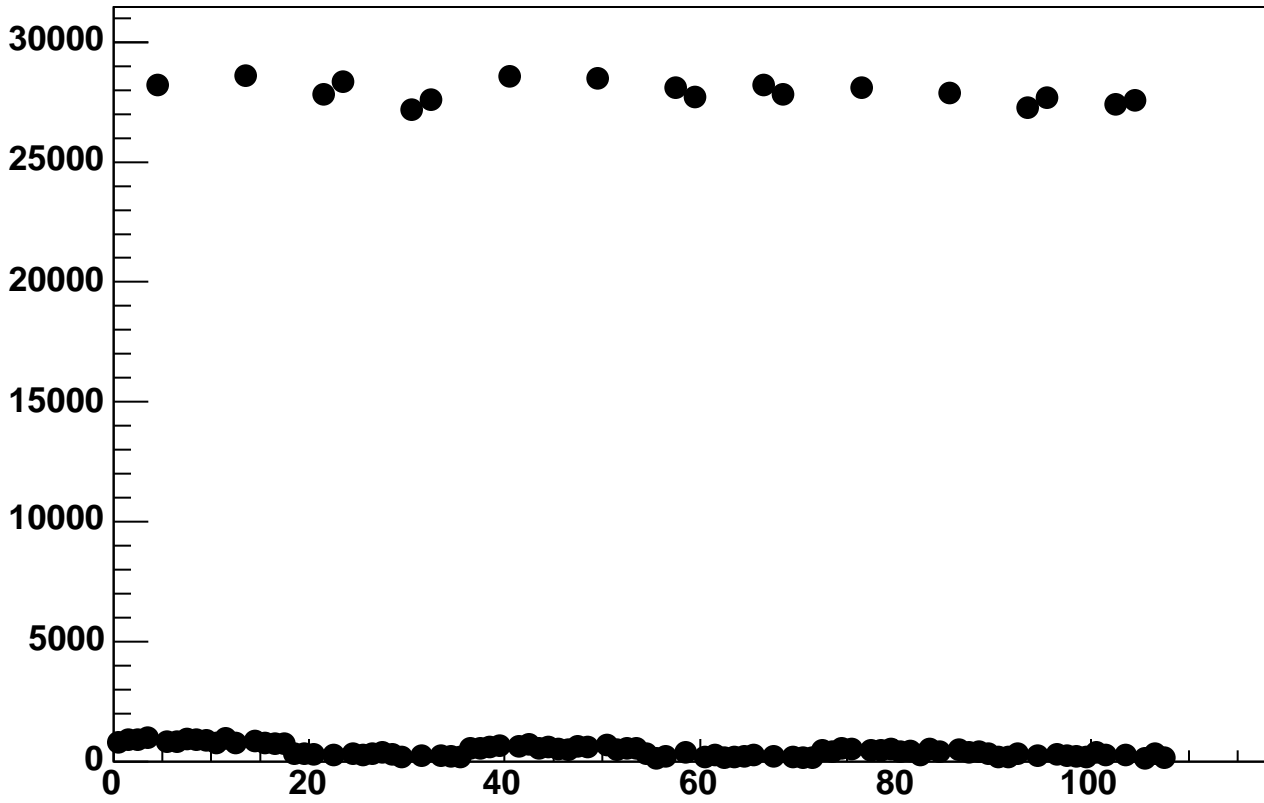
Enable 4, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



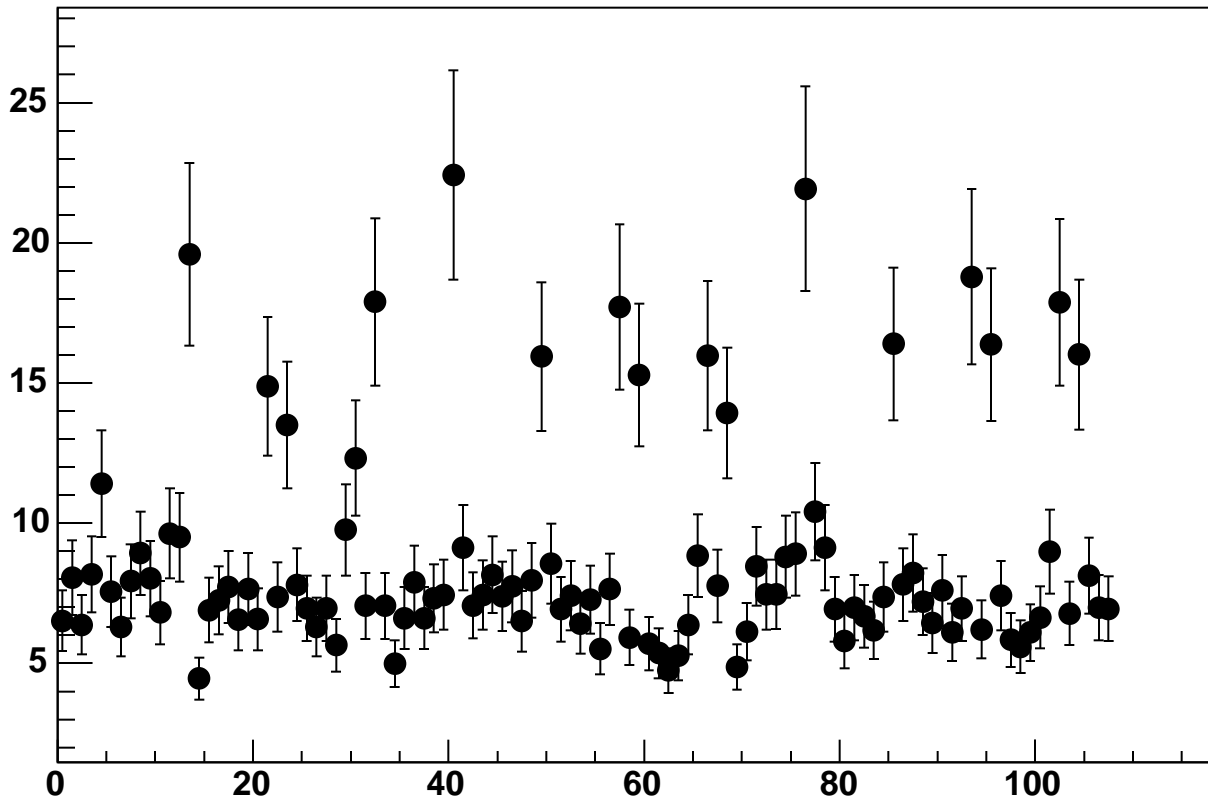
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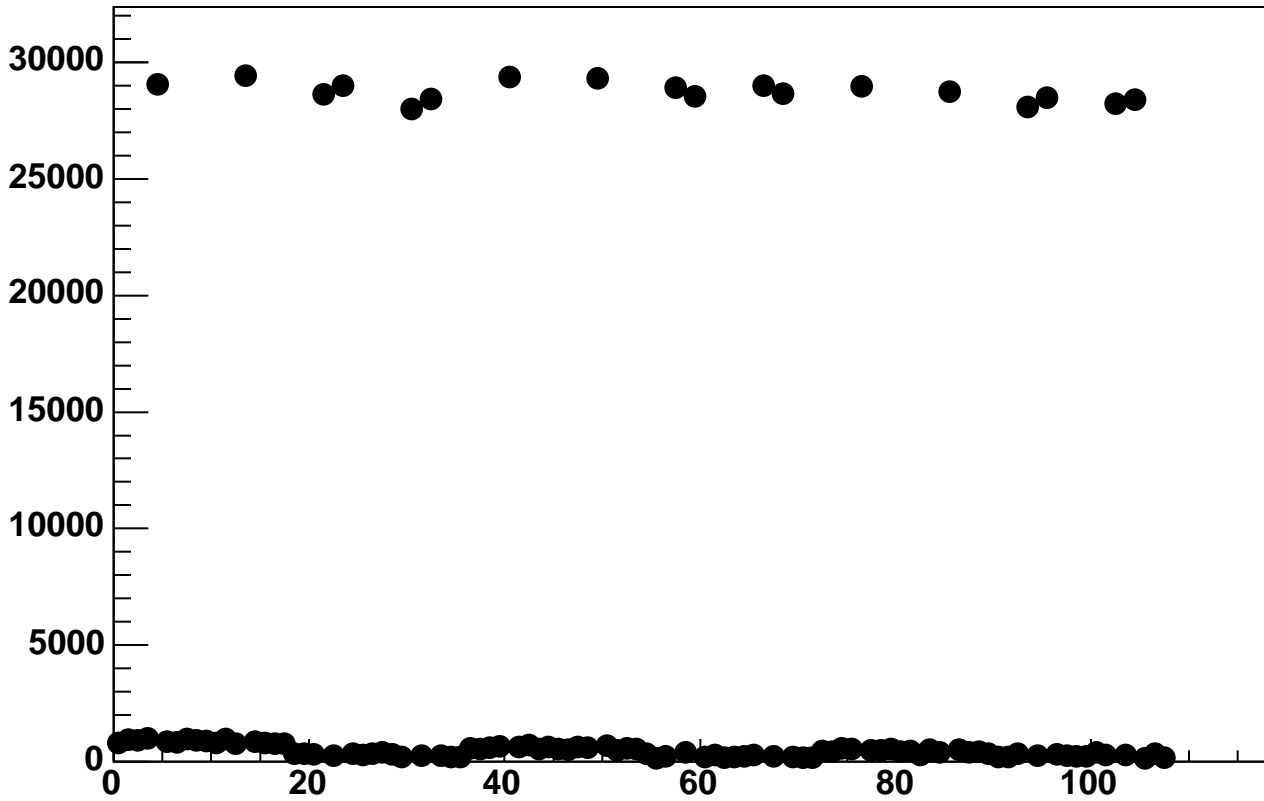
Enable 4, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



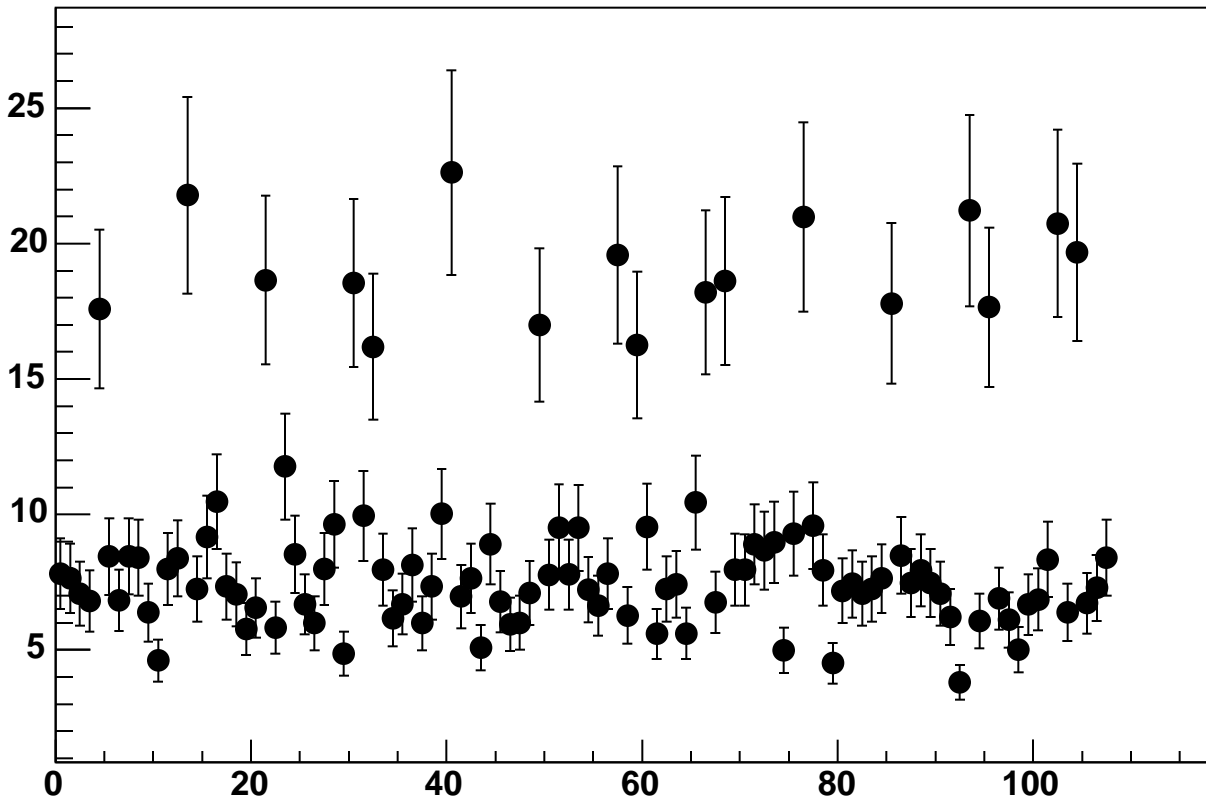
Enable 4, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



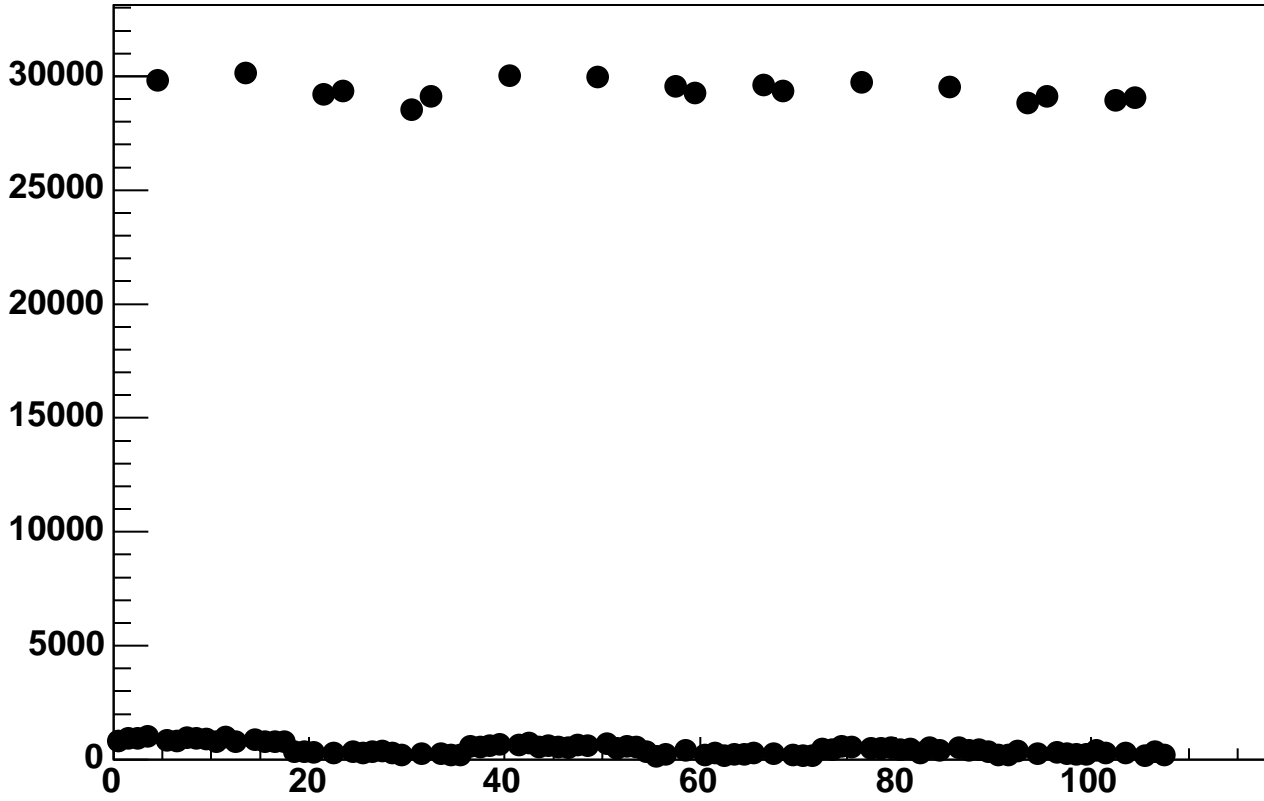
Enable 4, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



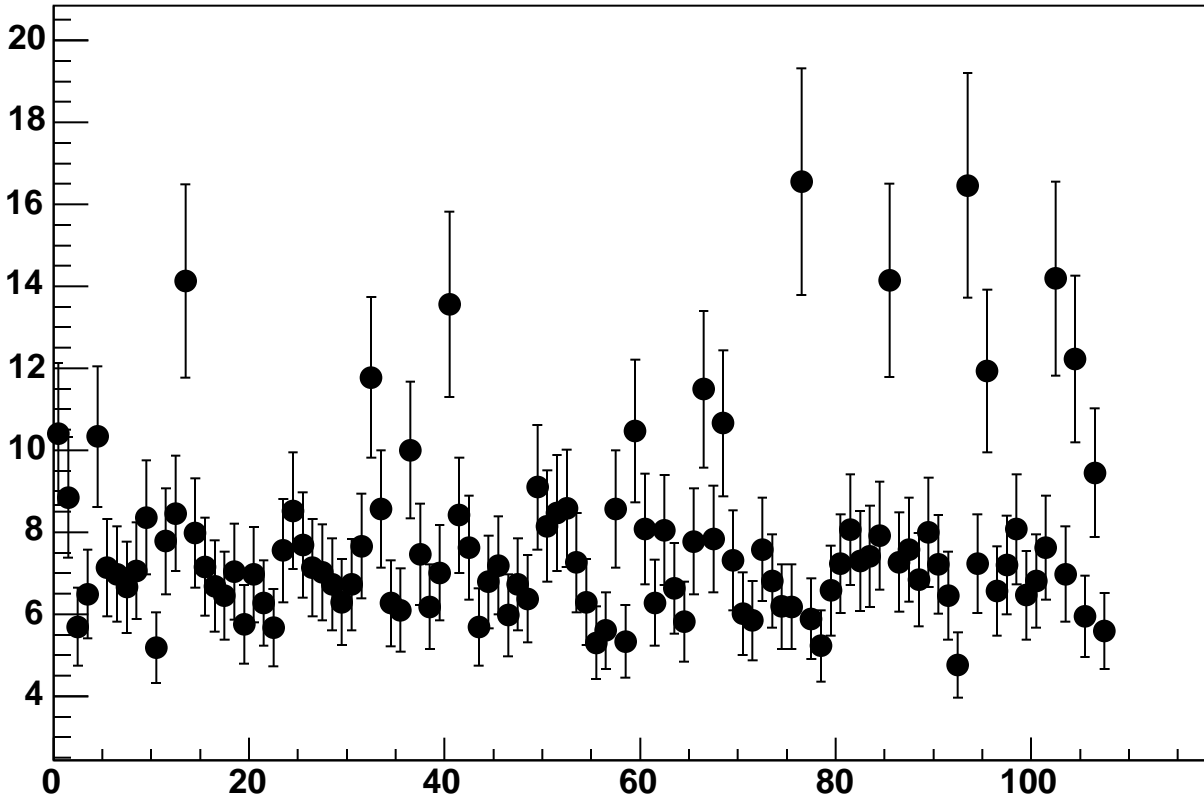
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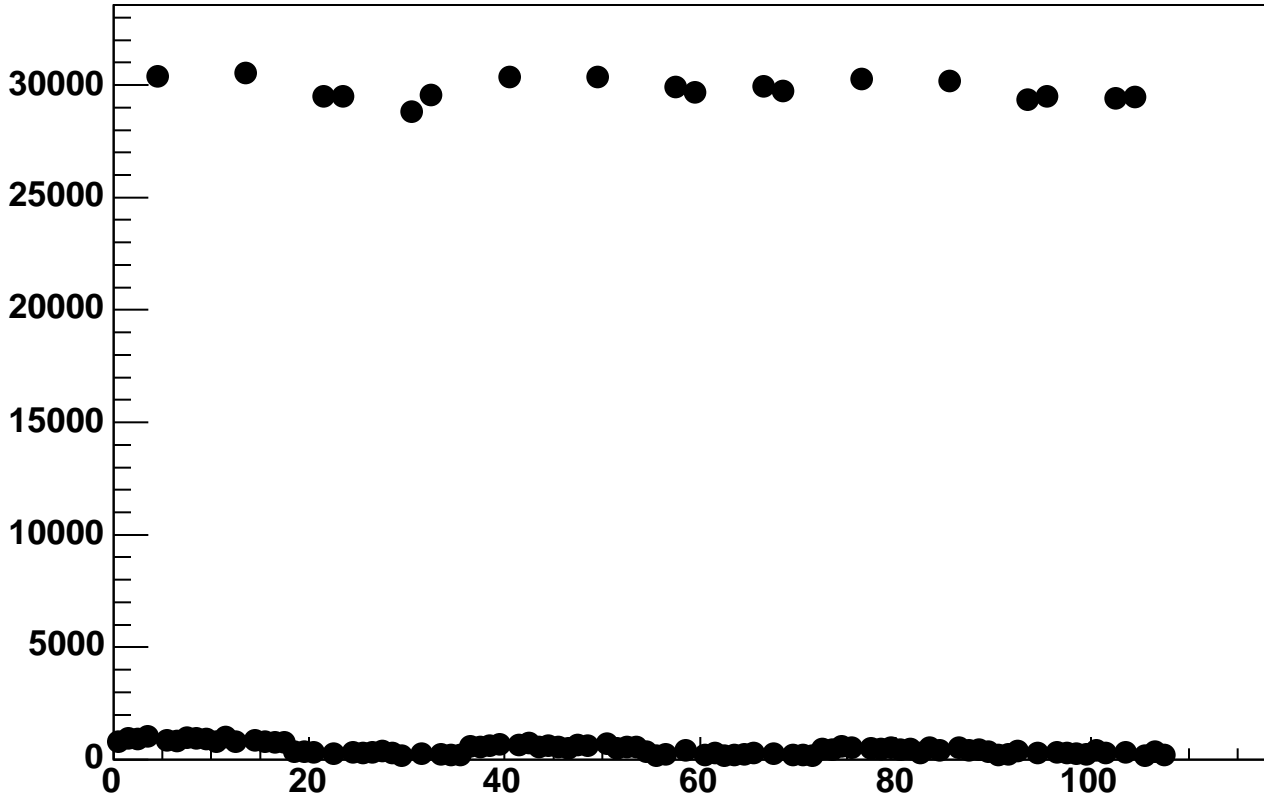
Enable 4, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



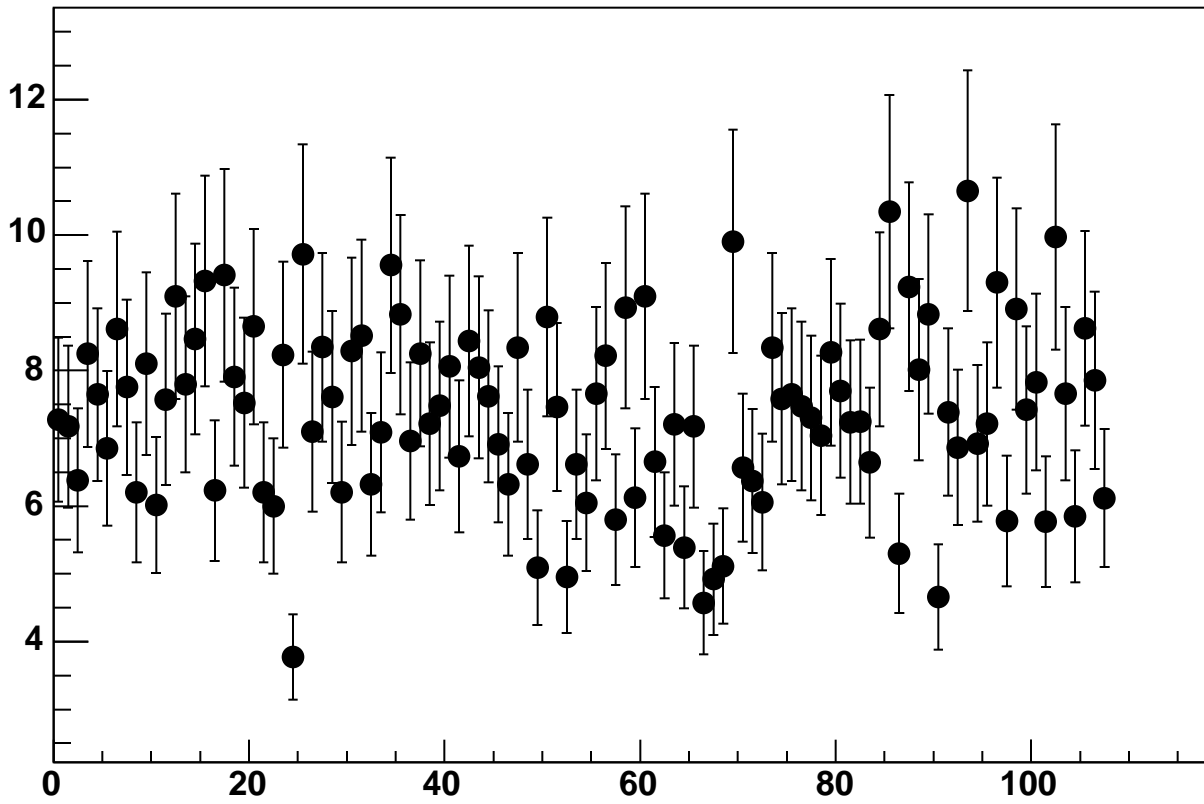
Enable 4, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



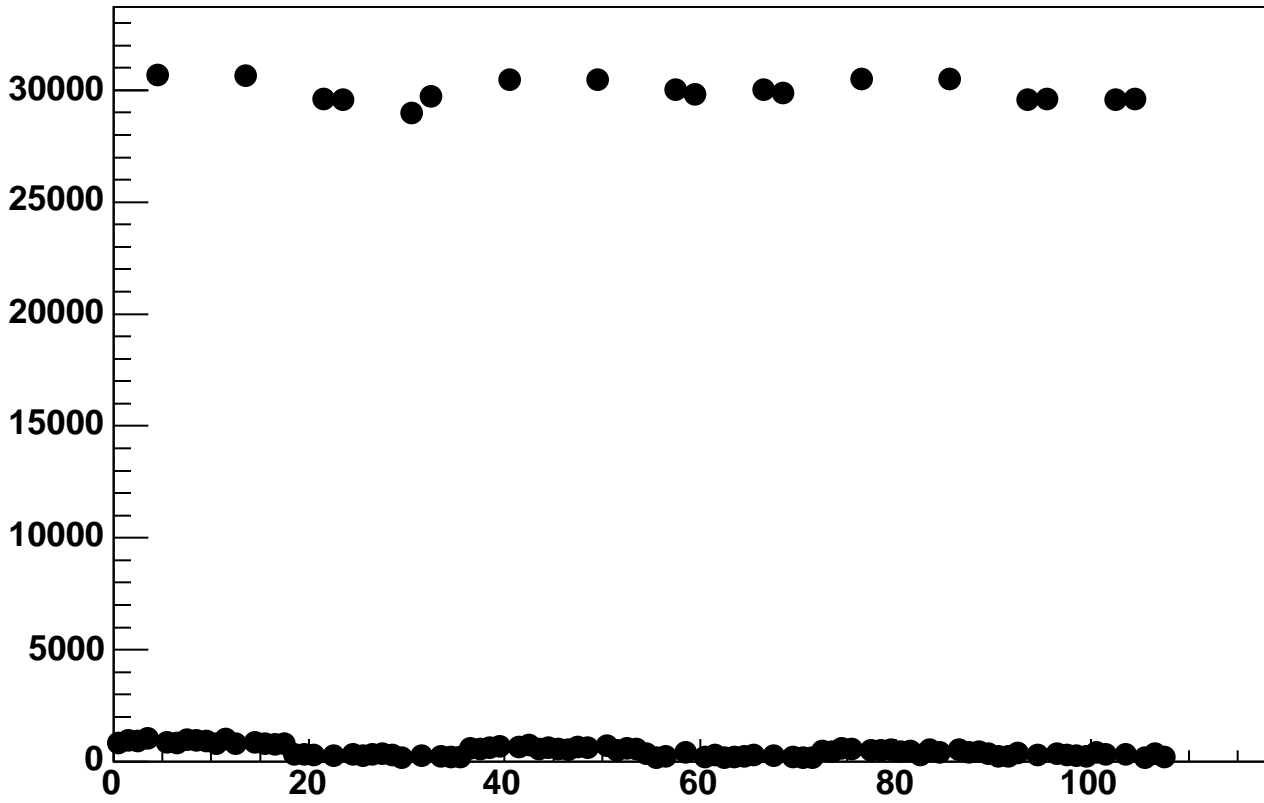
Enable 4, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



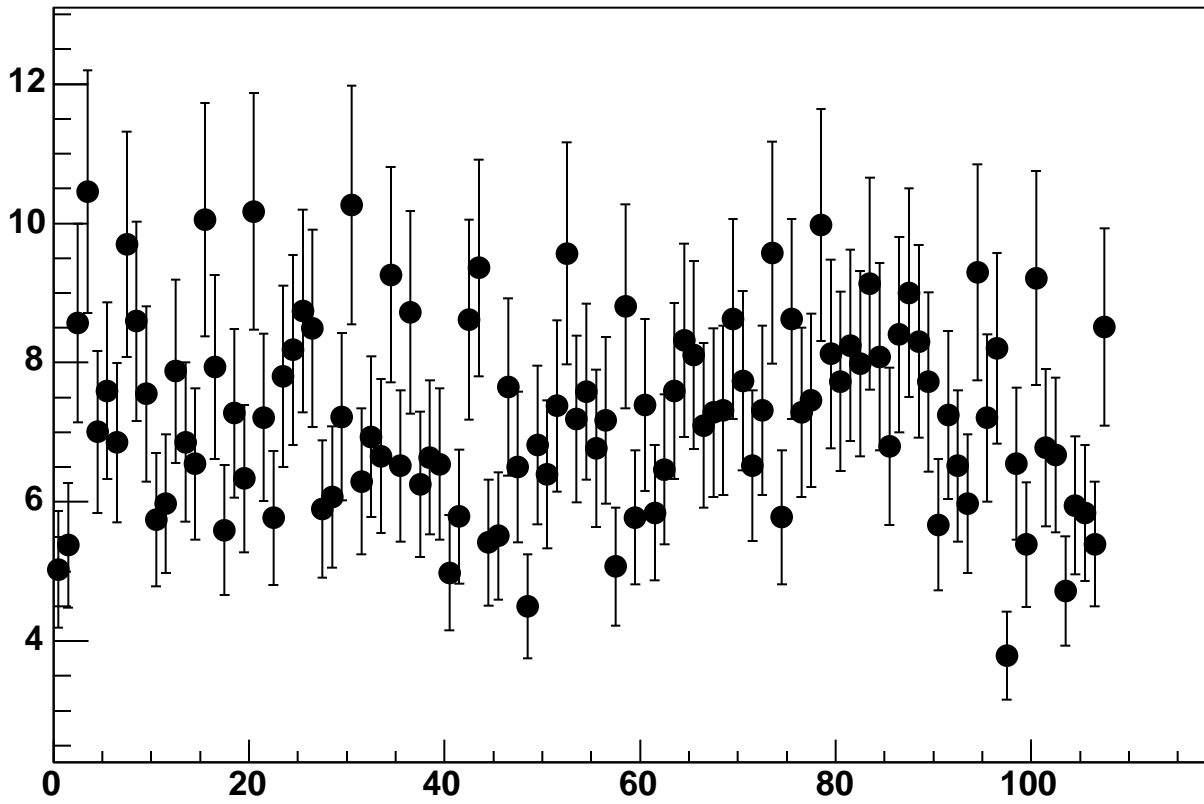
Enable 4, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



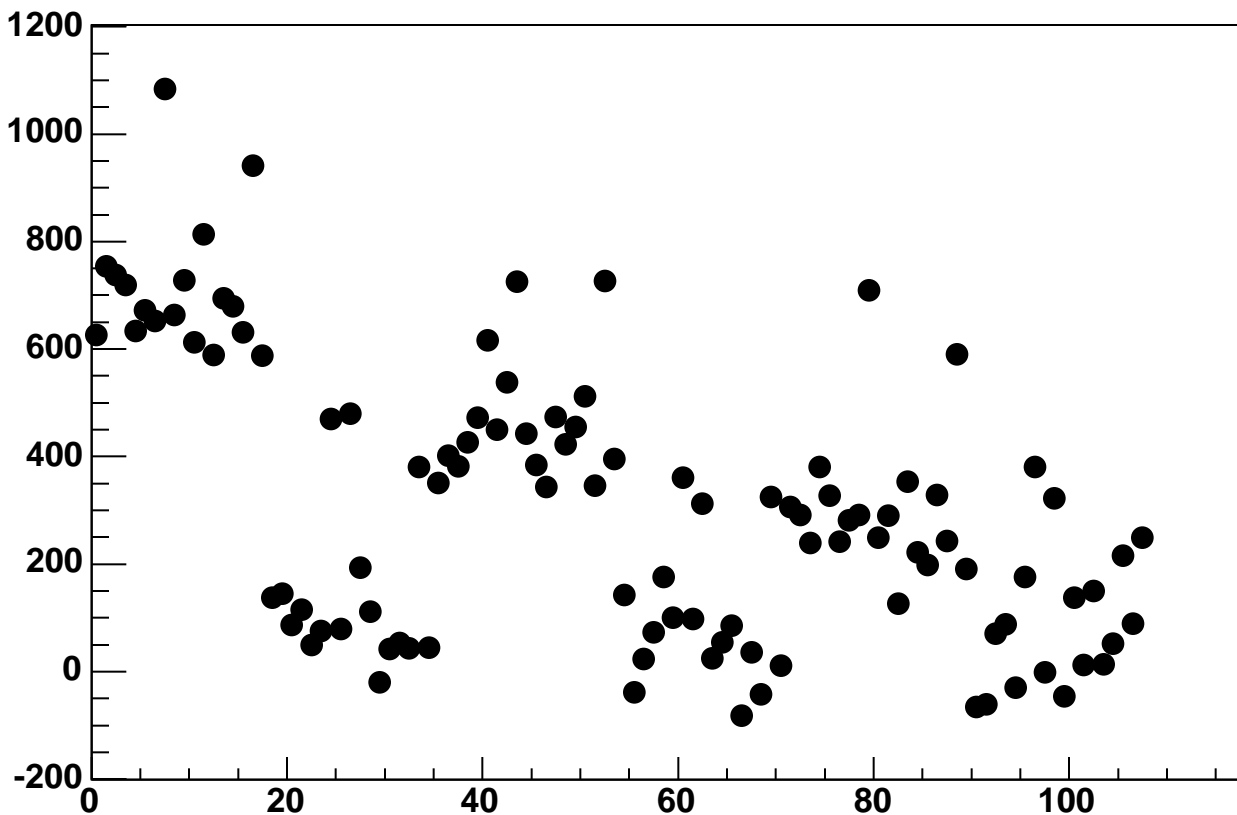
Enable 4, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



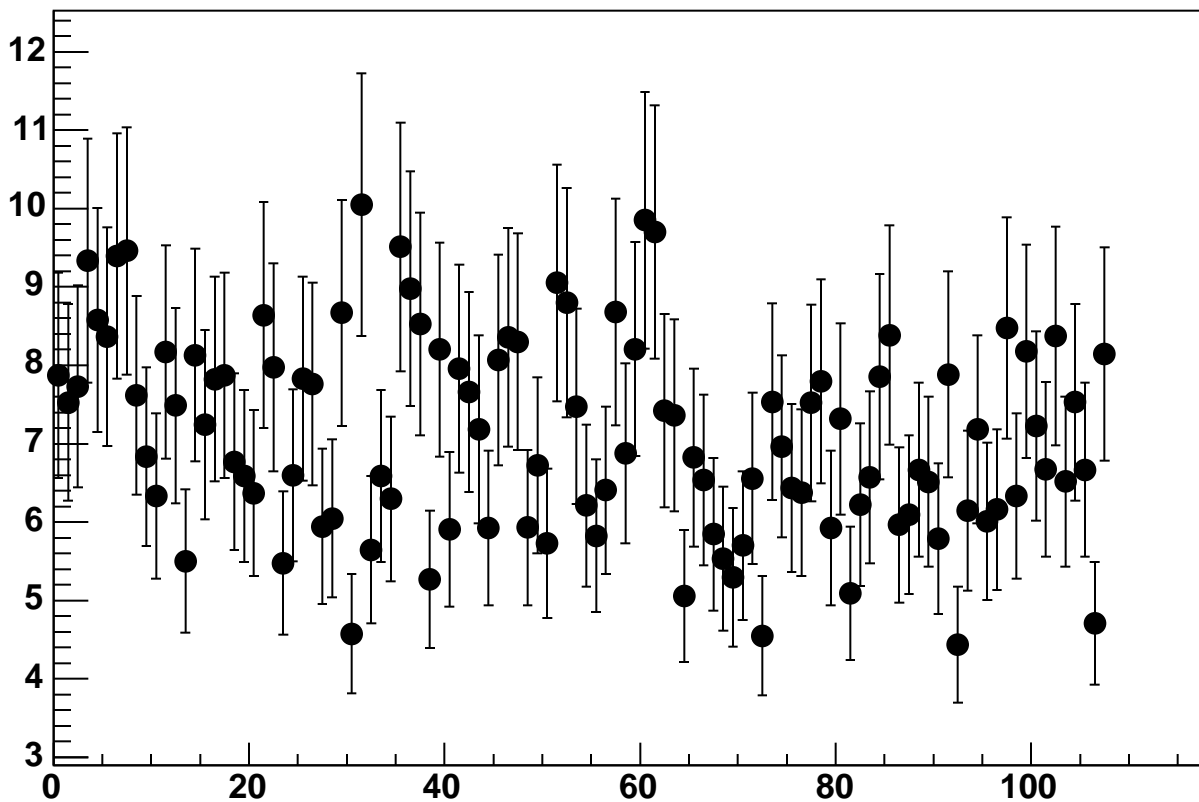
Enable 4, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



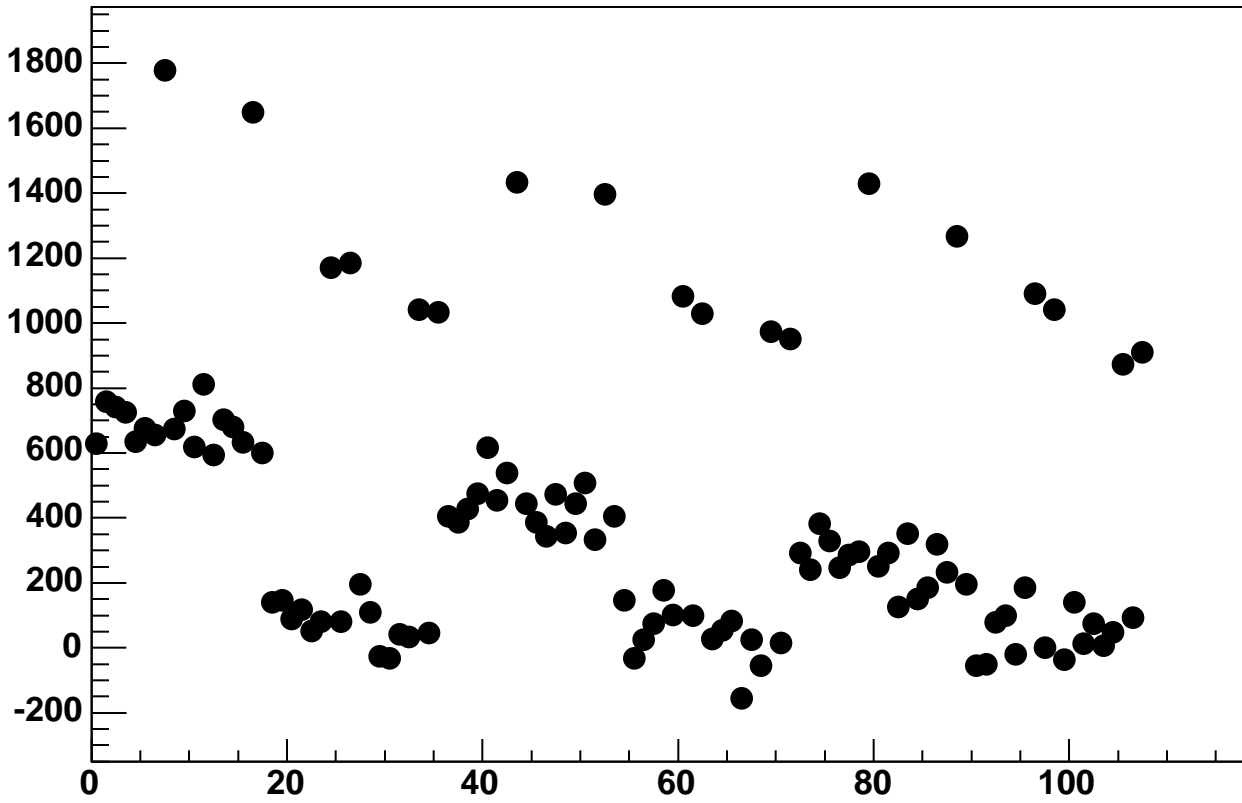
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



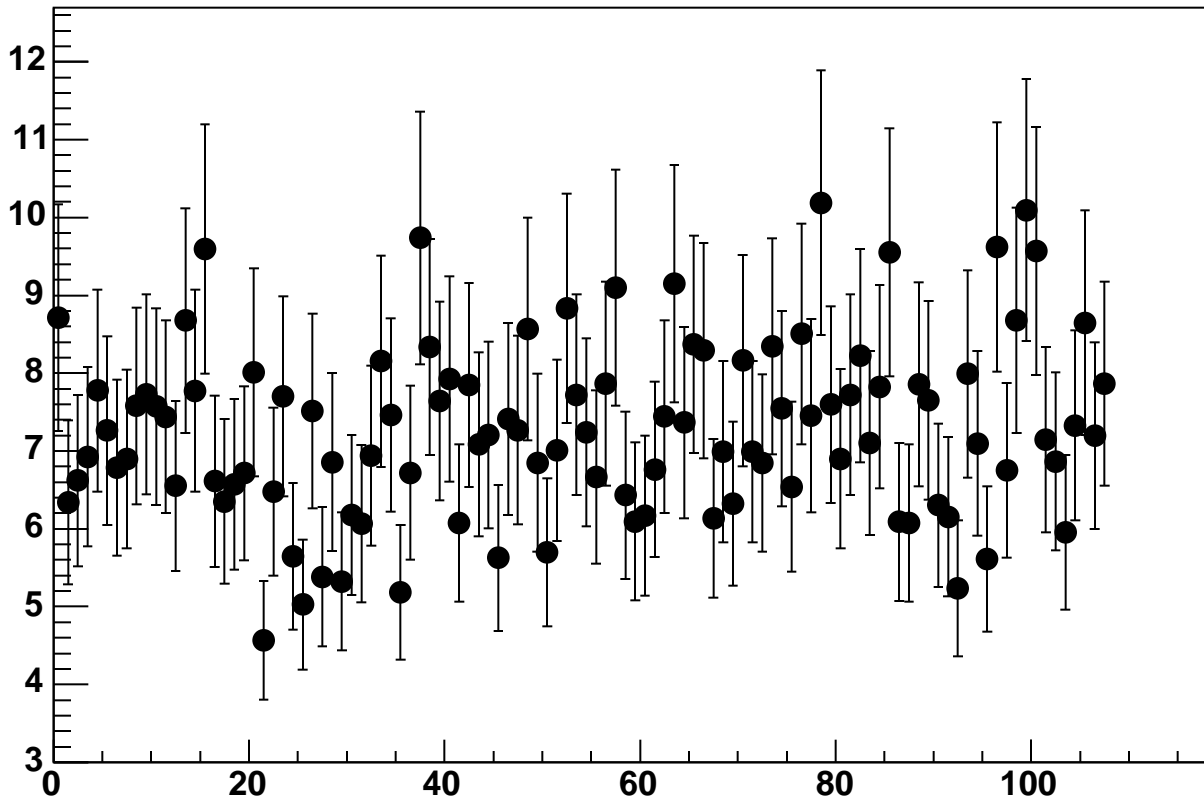
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



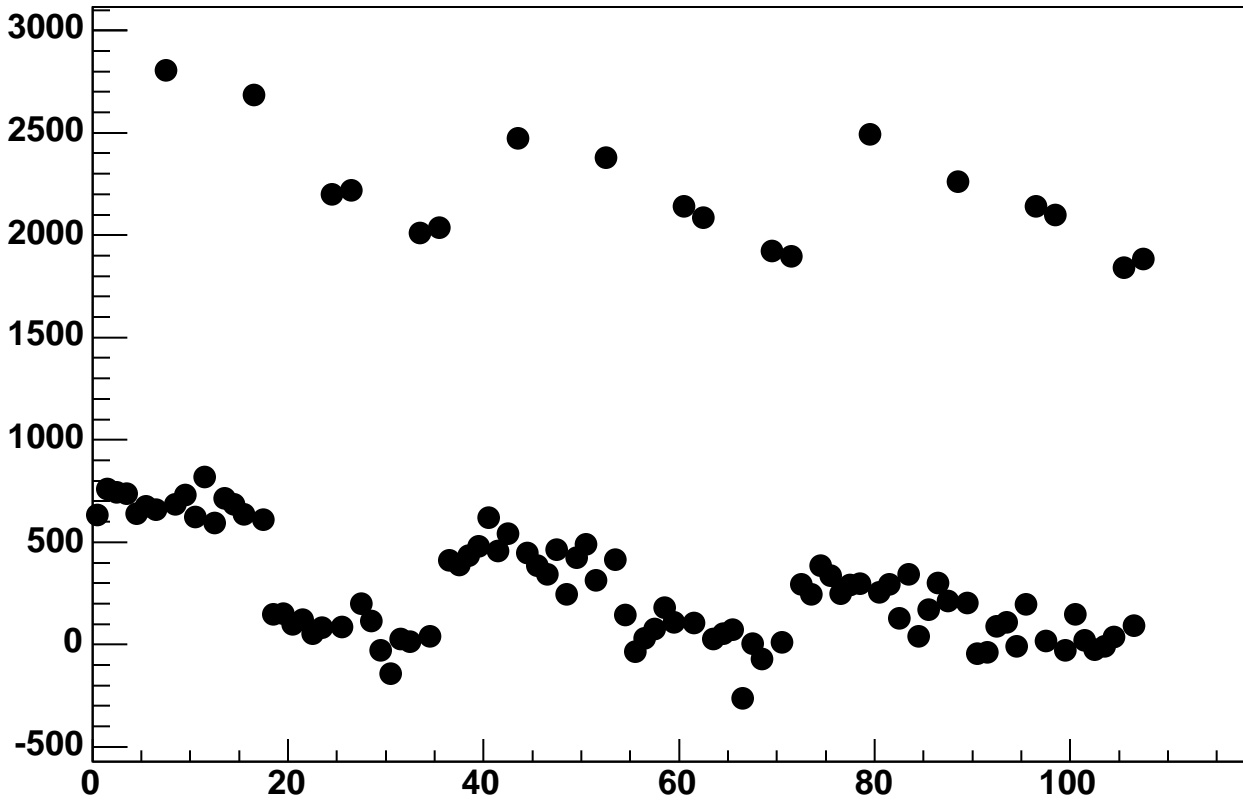
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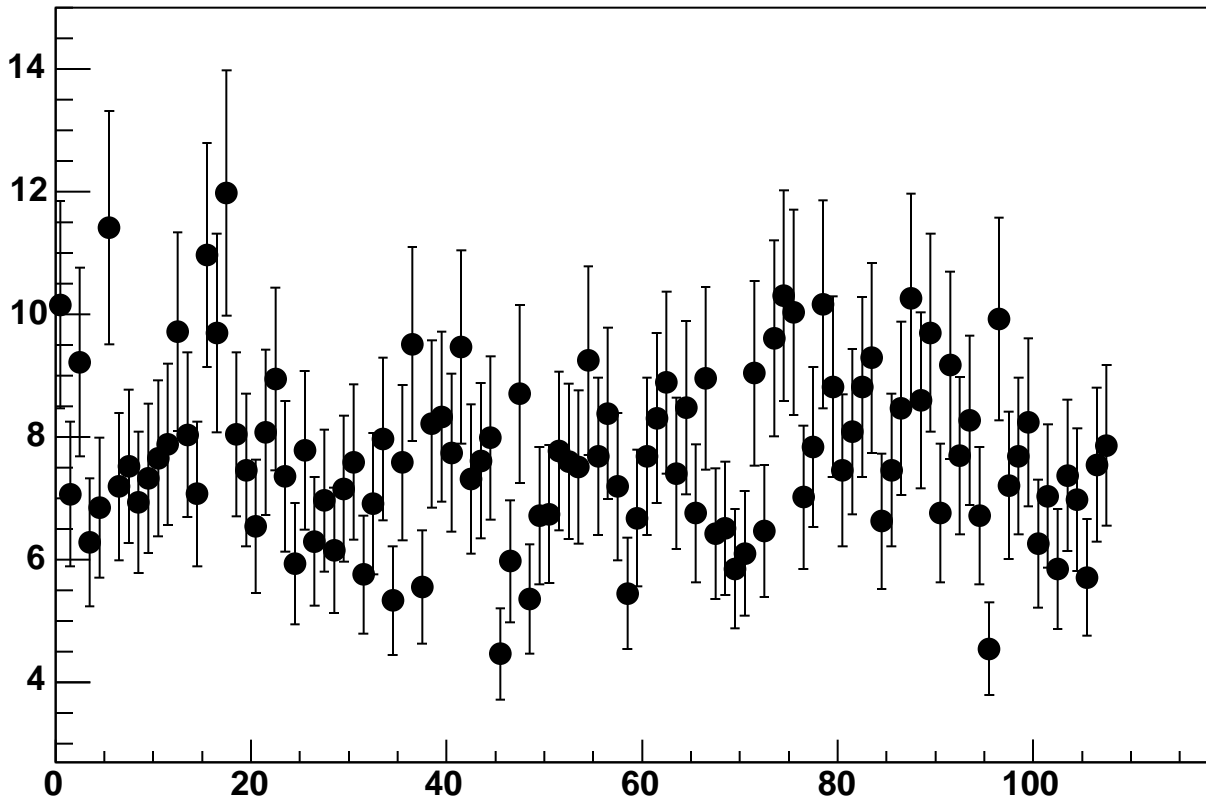
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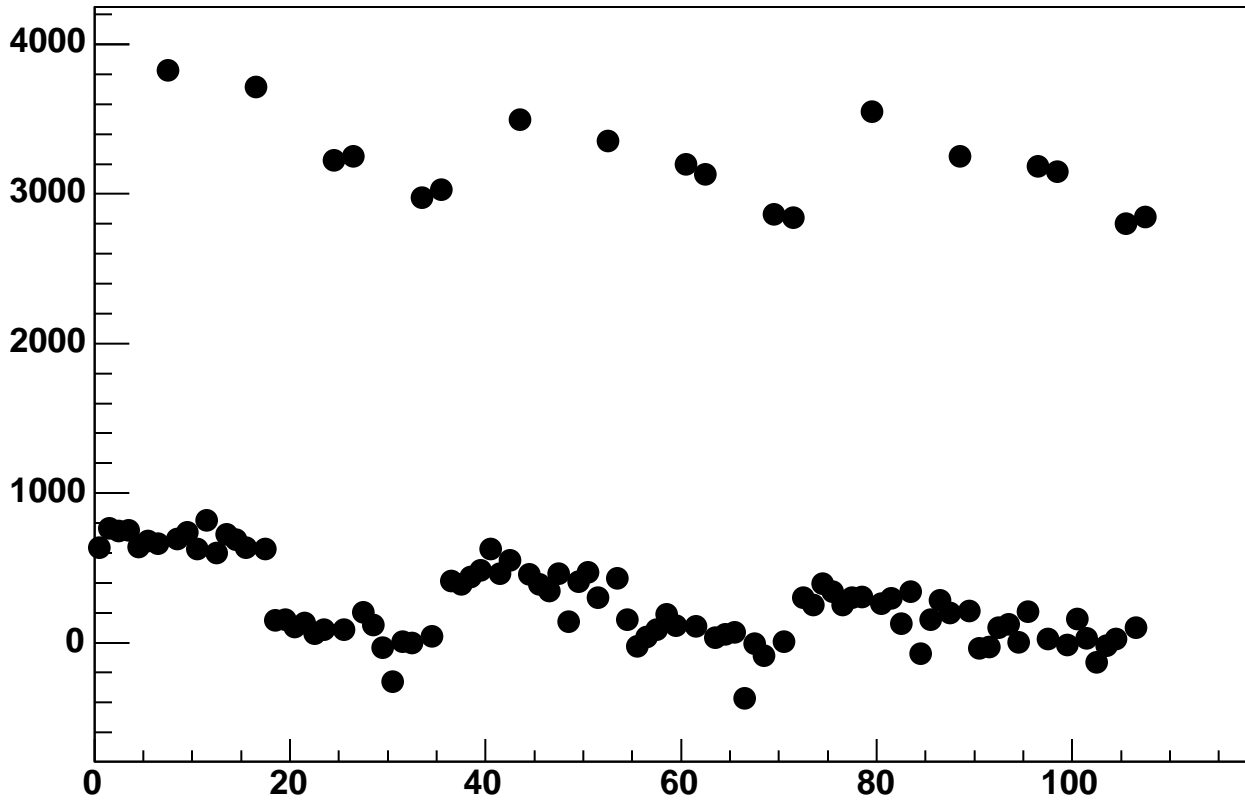
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



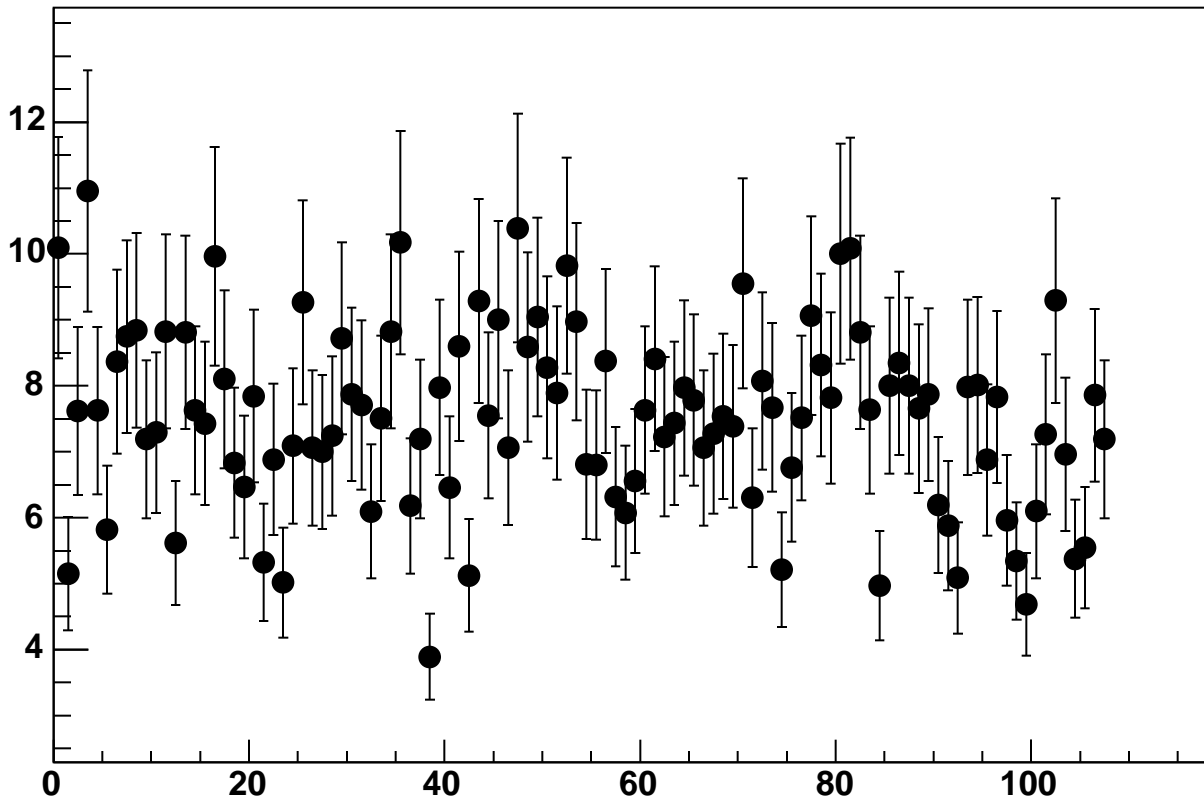
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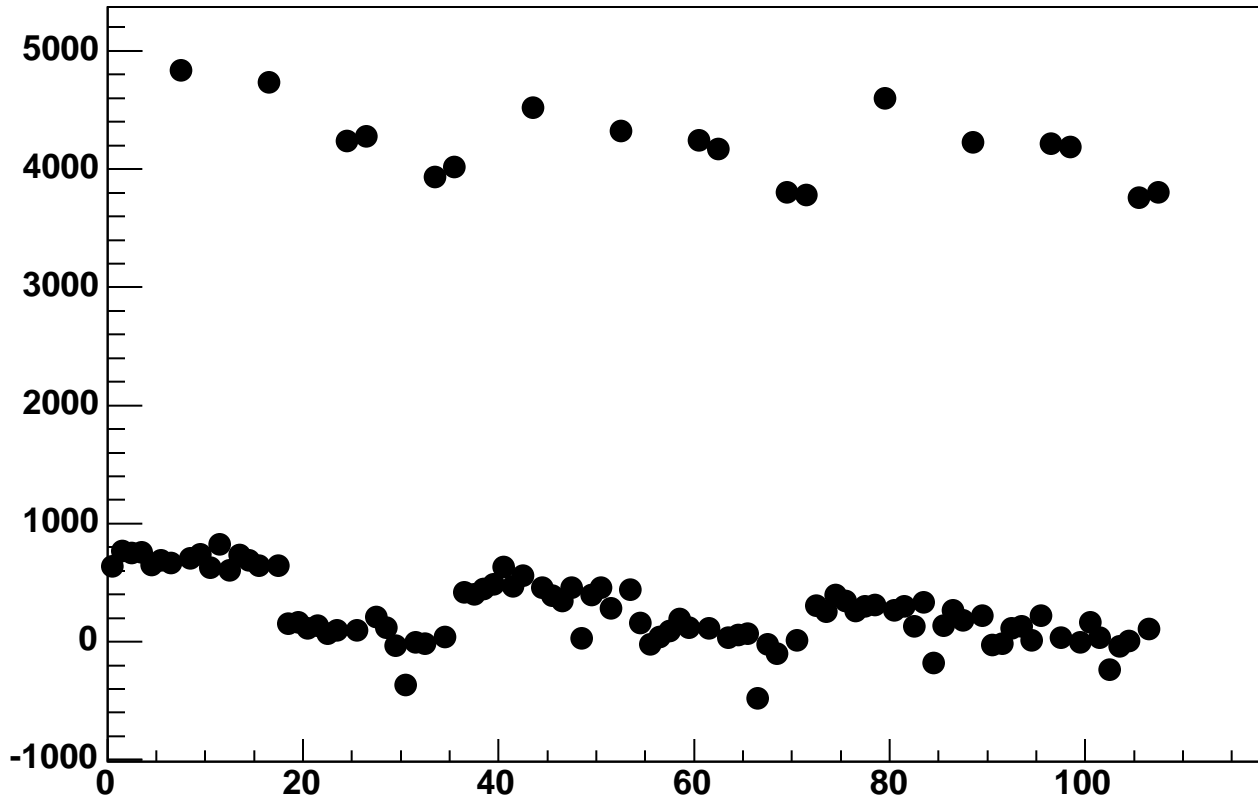
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



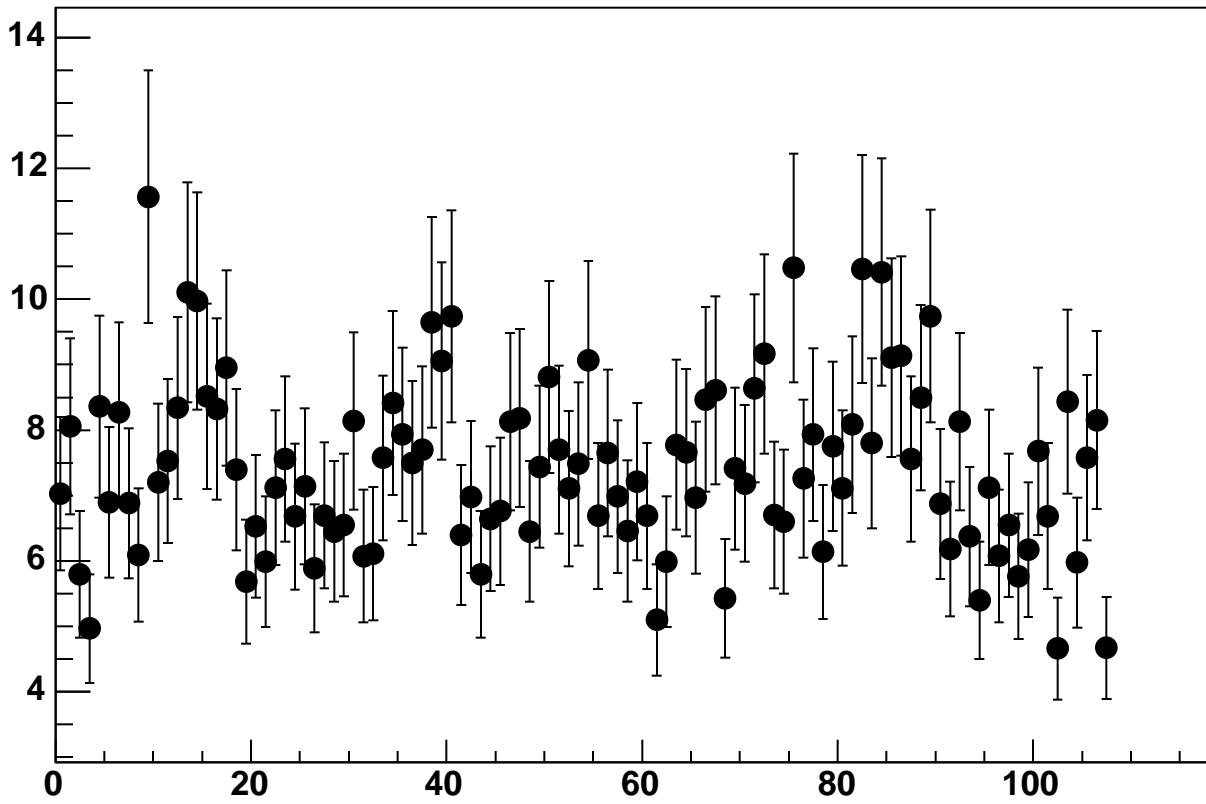
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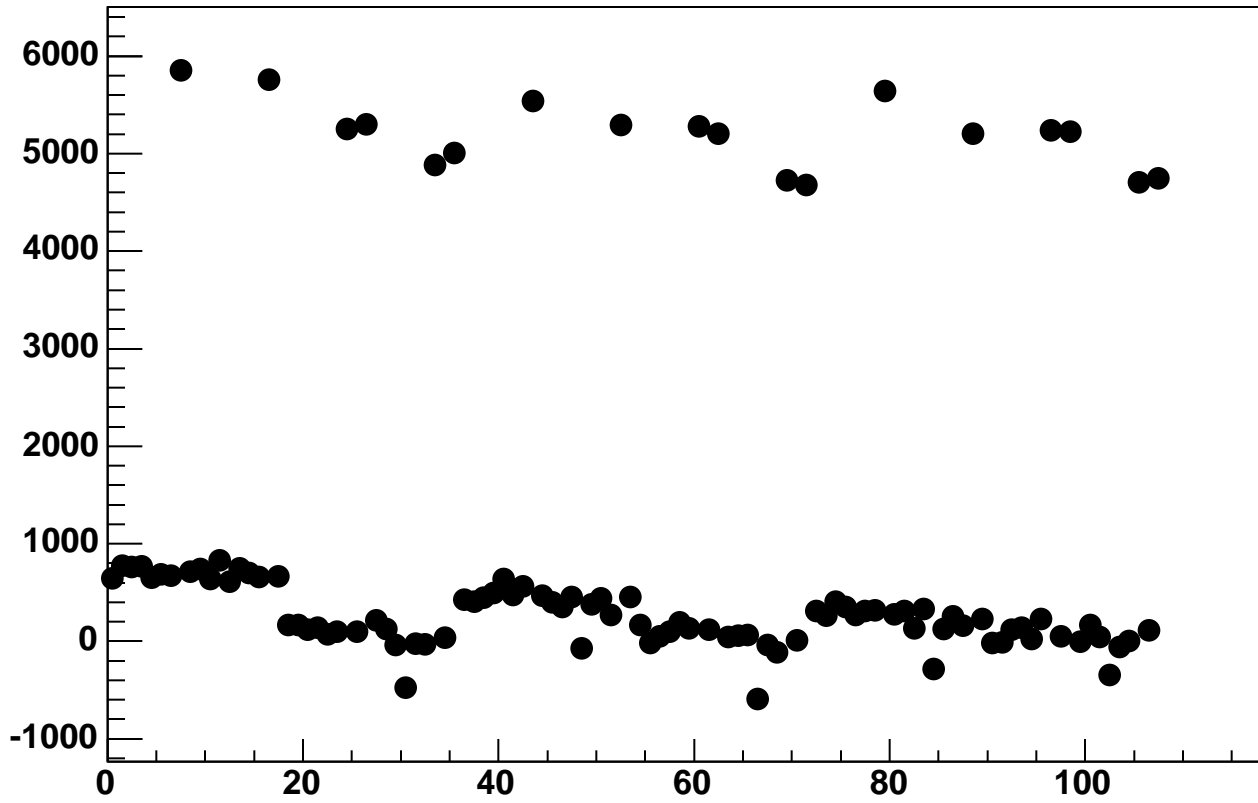
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



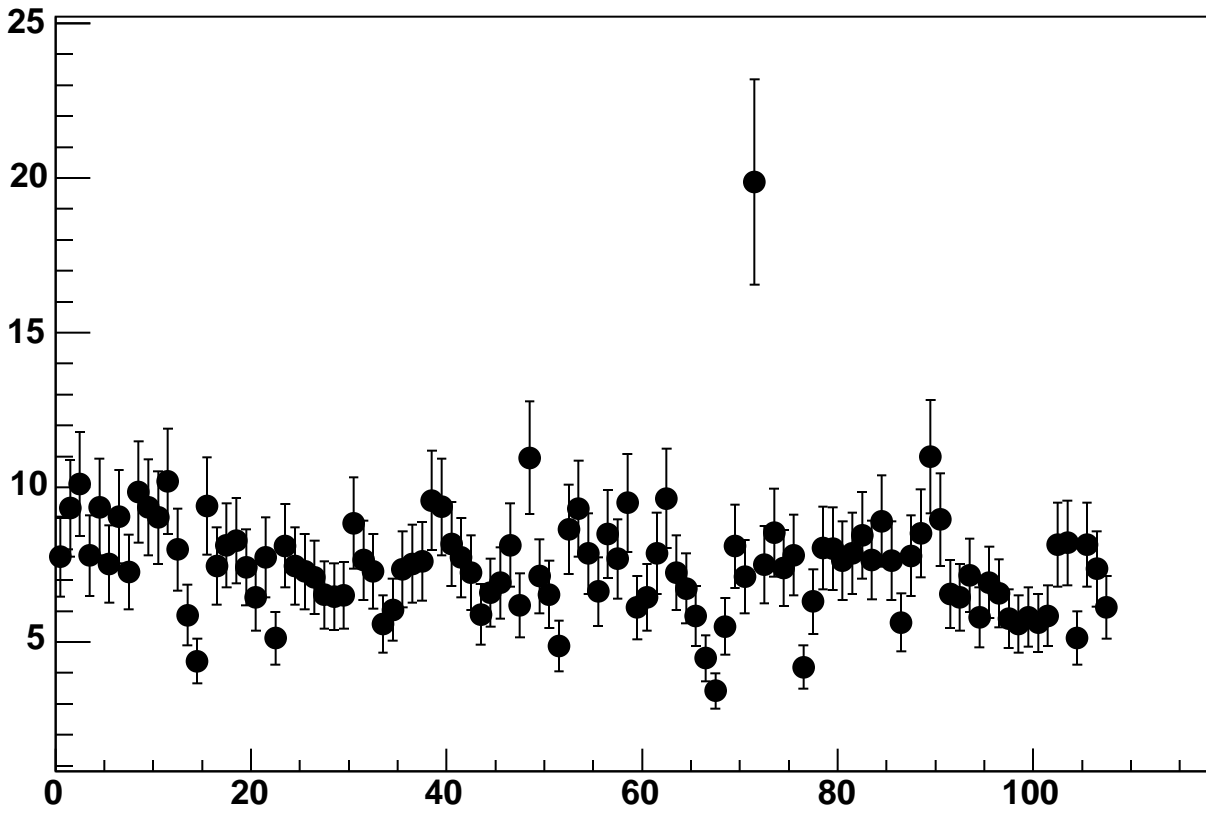
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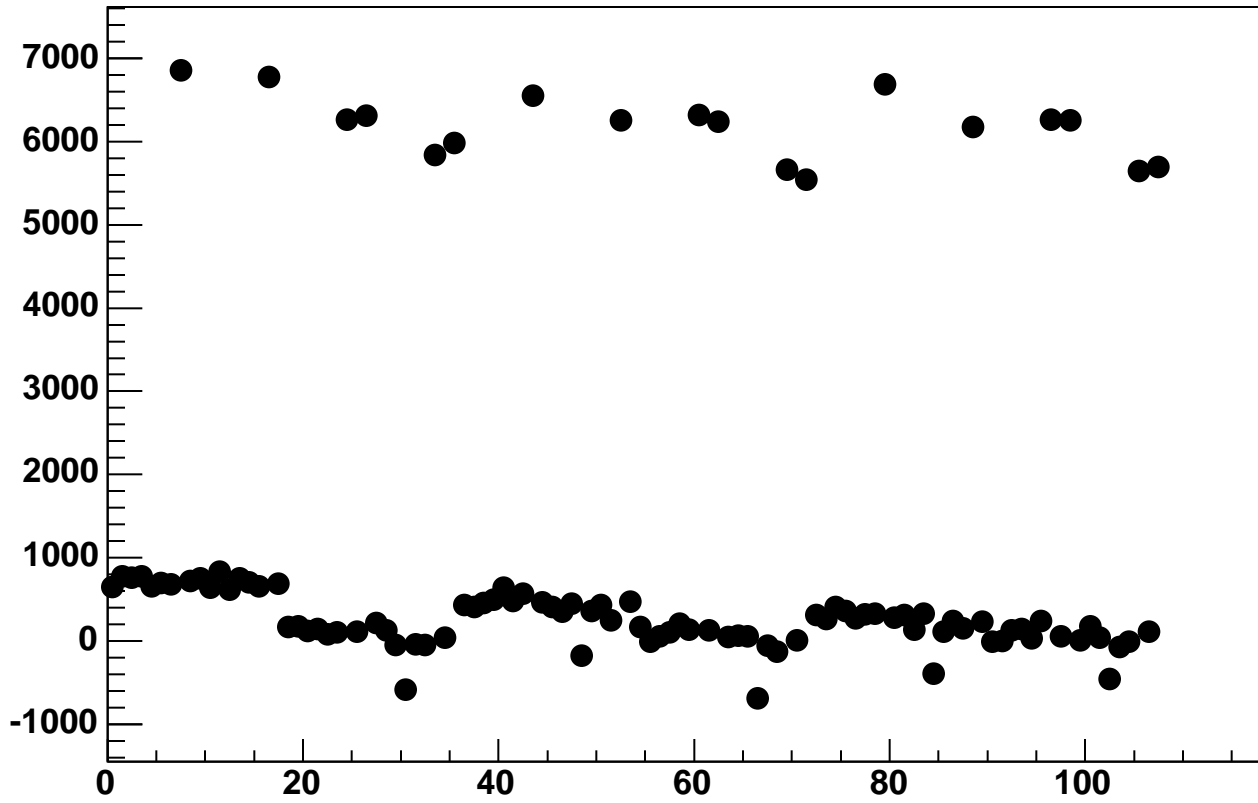
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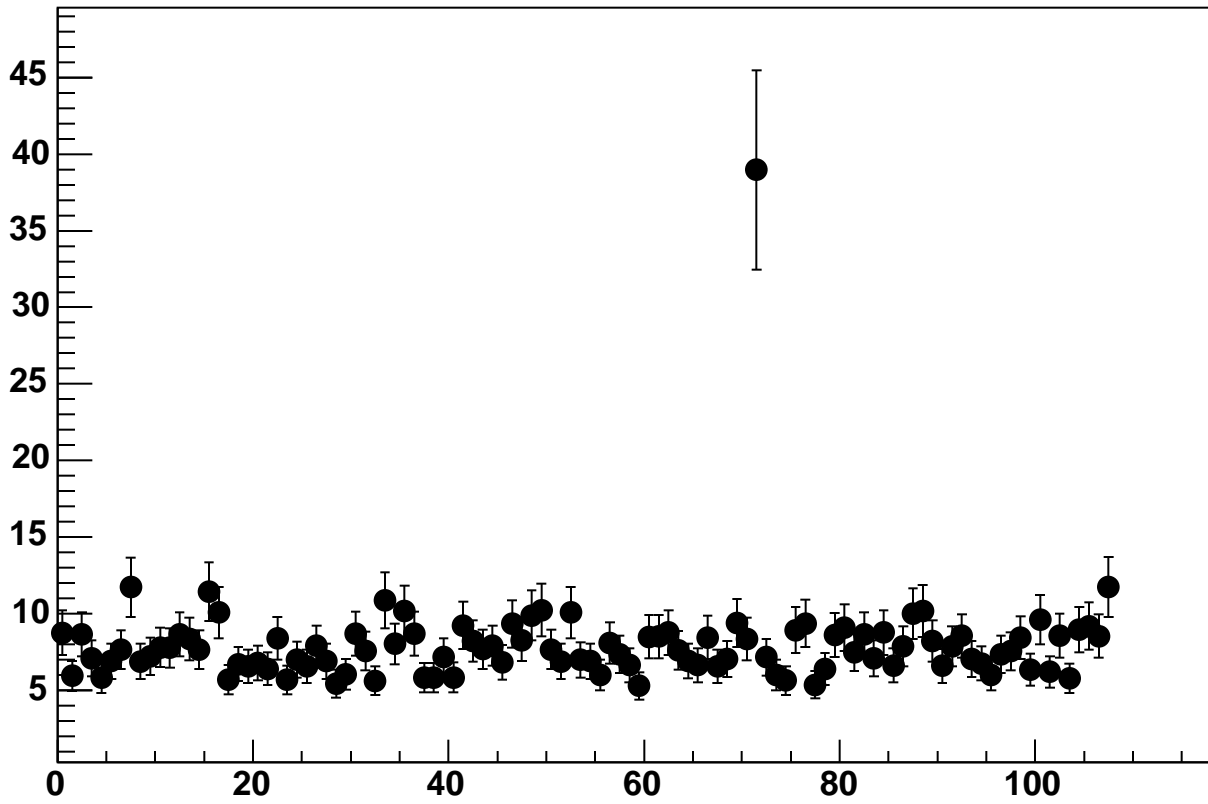
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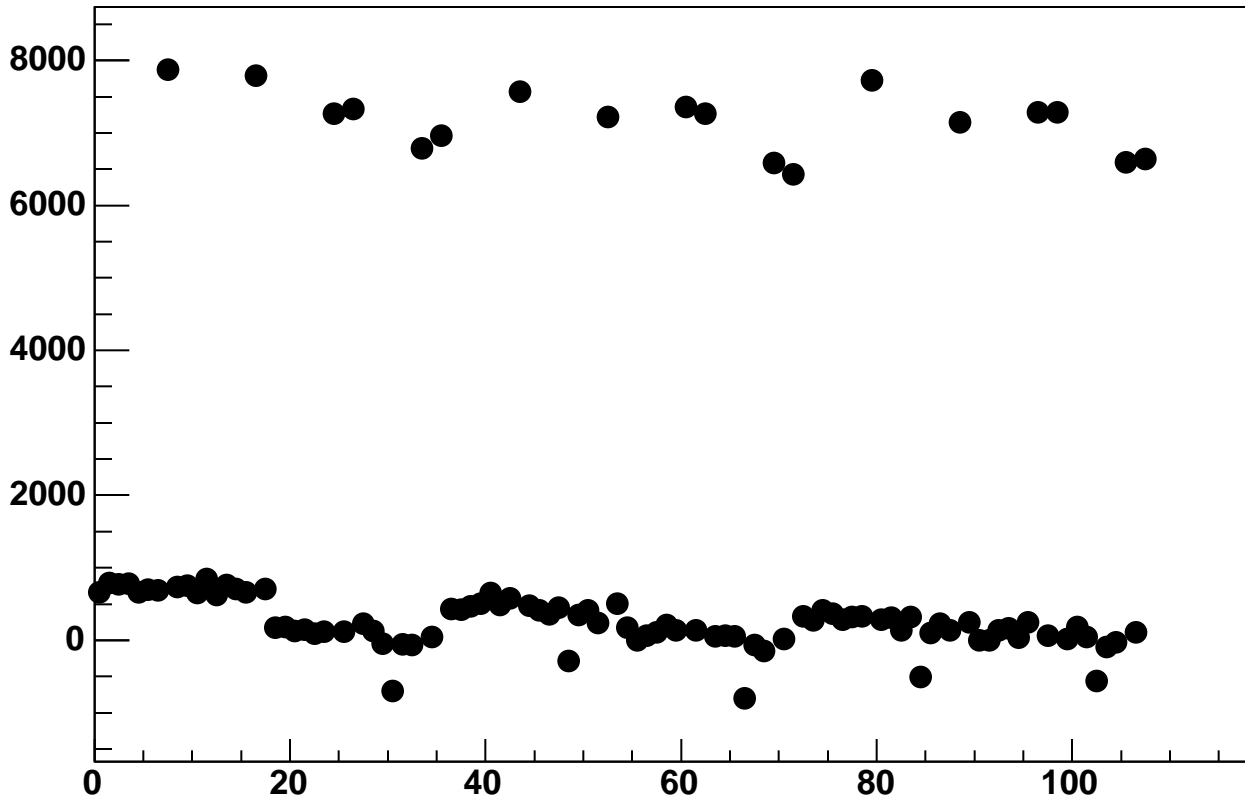
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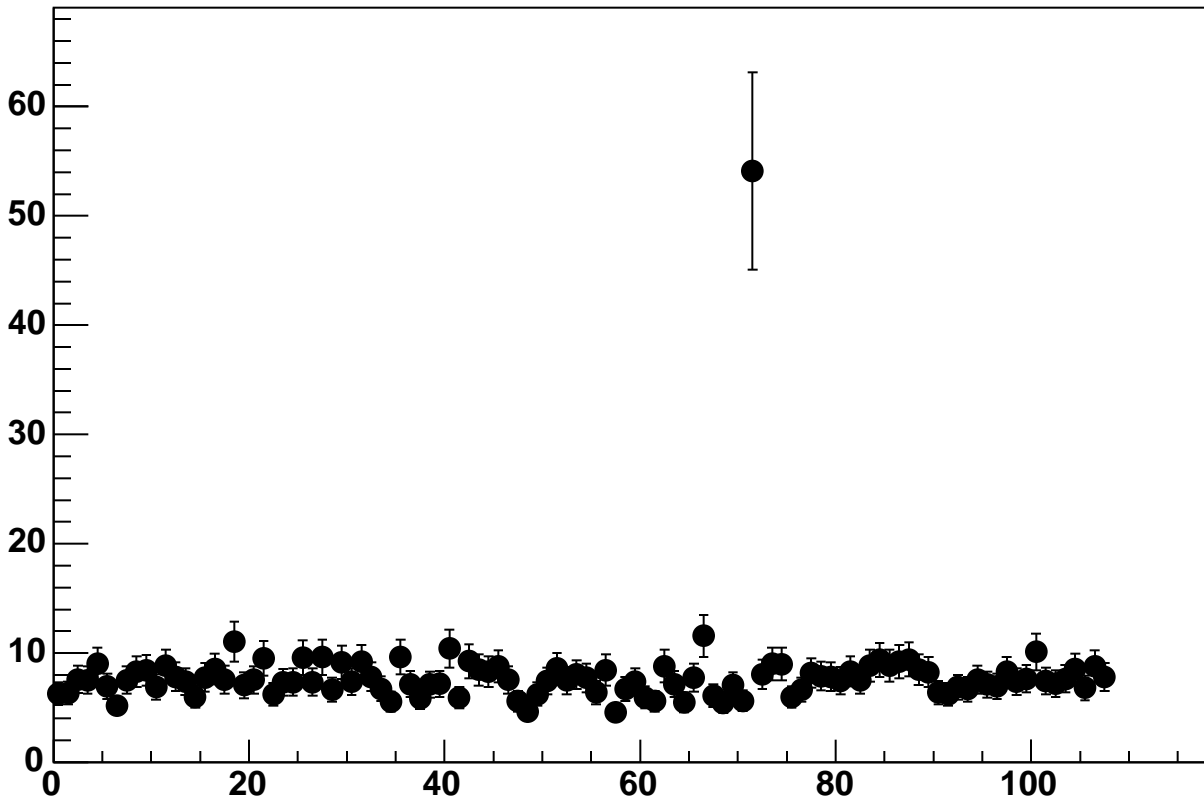
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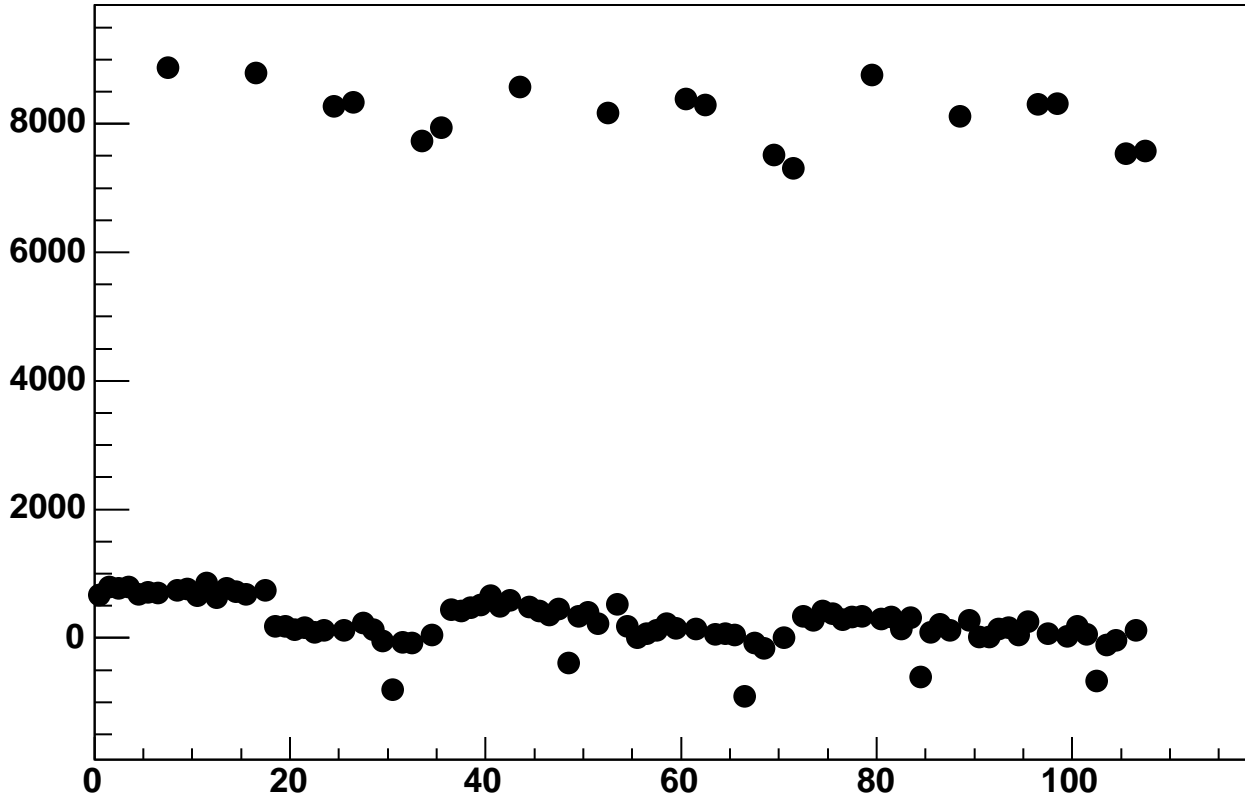
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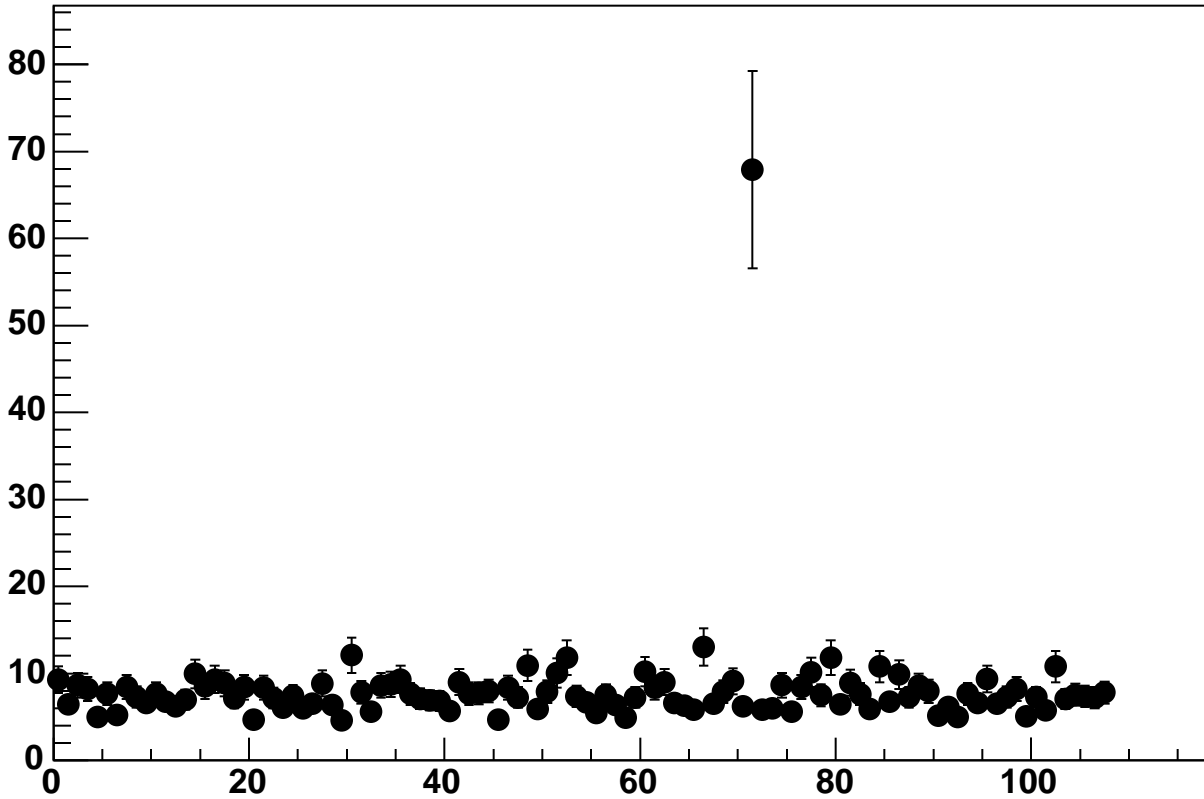
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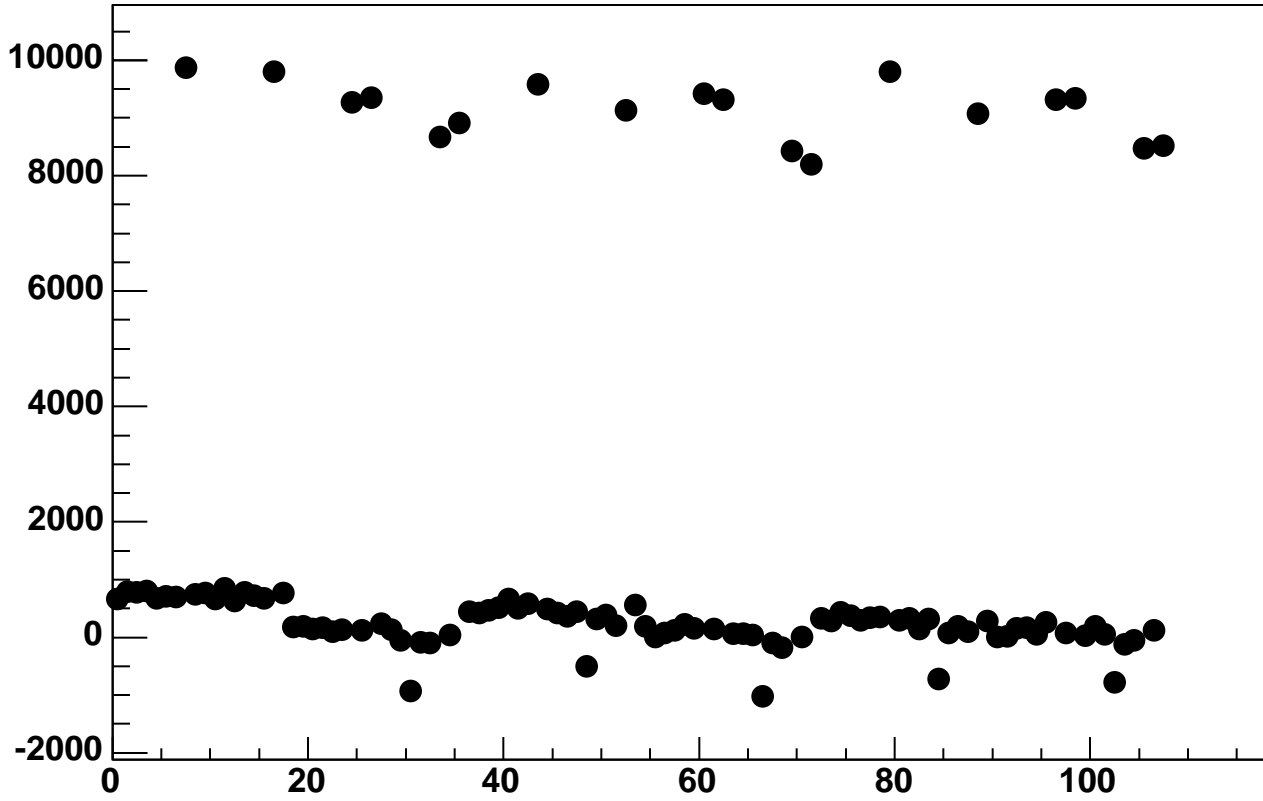
Enable 5, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



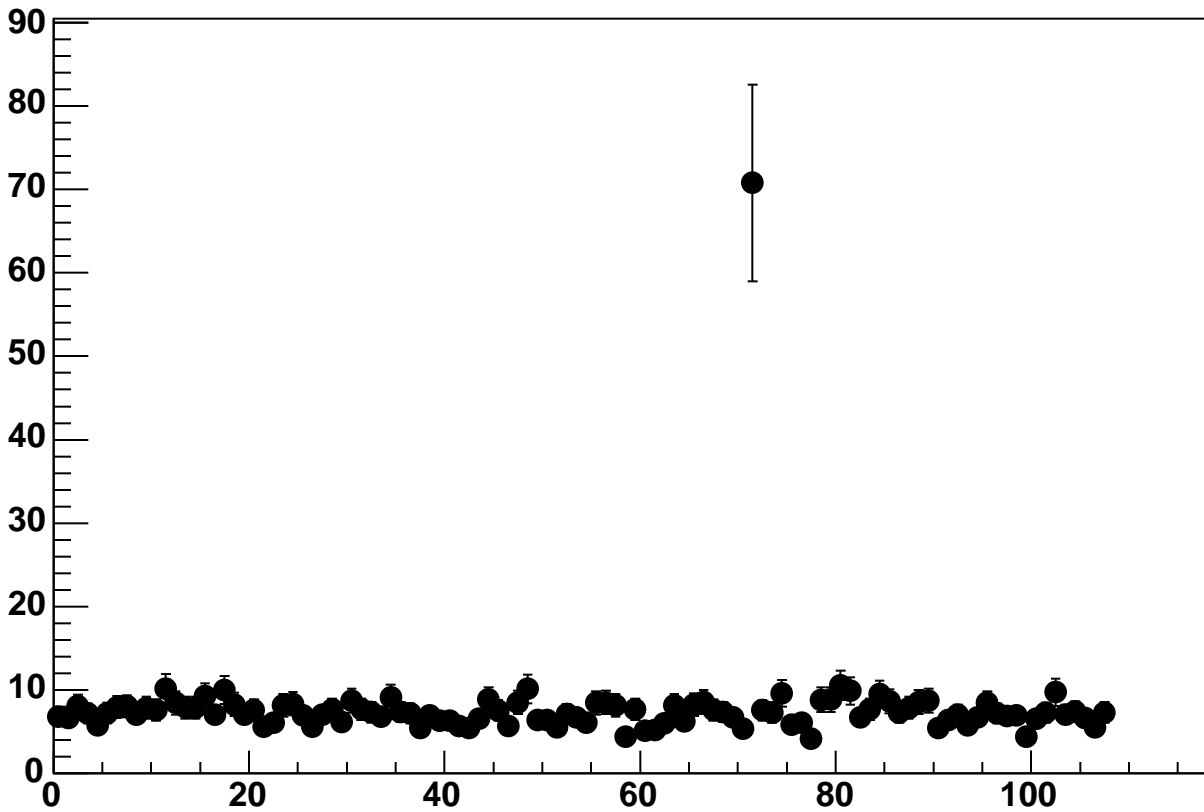
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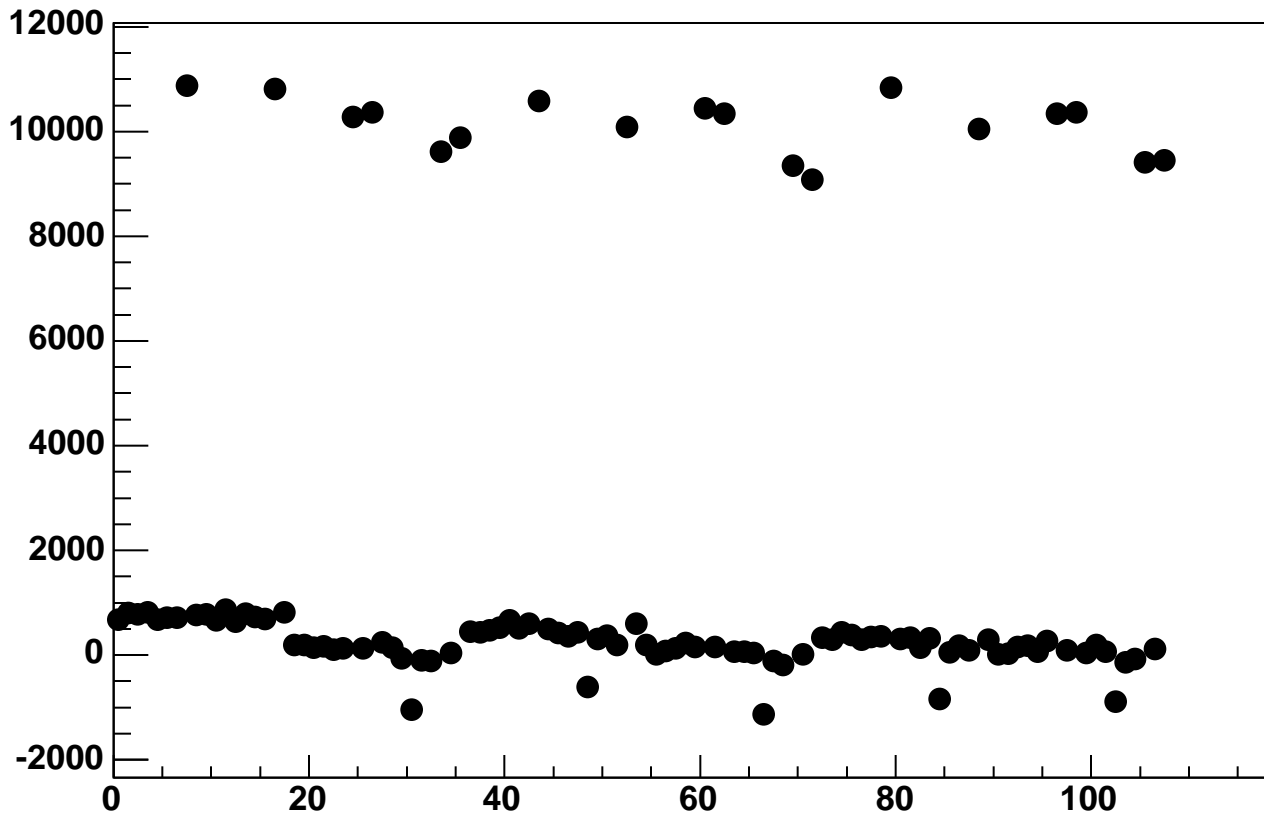
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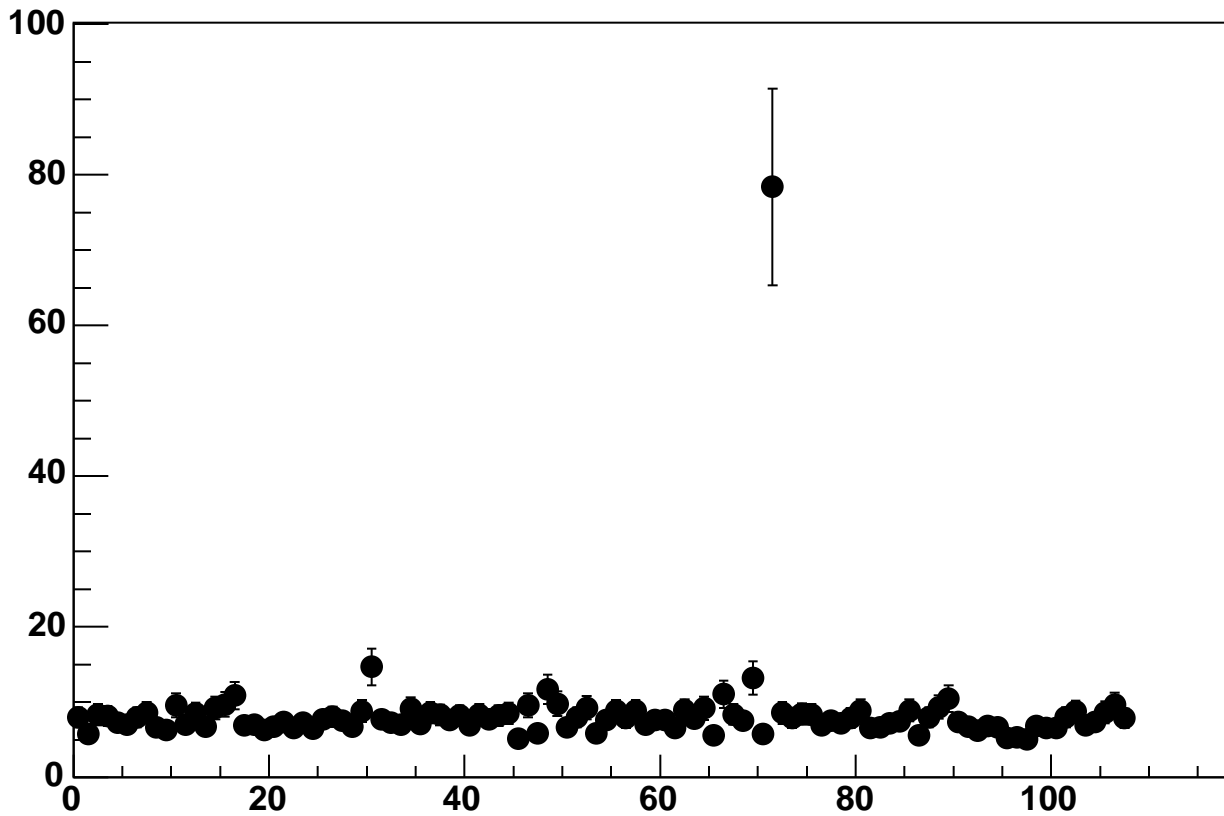
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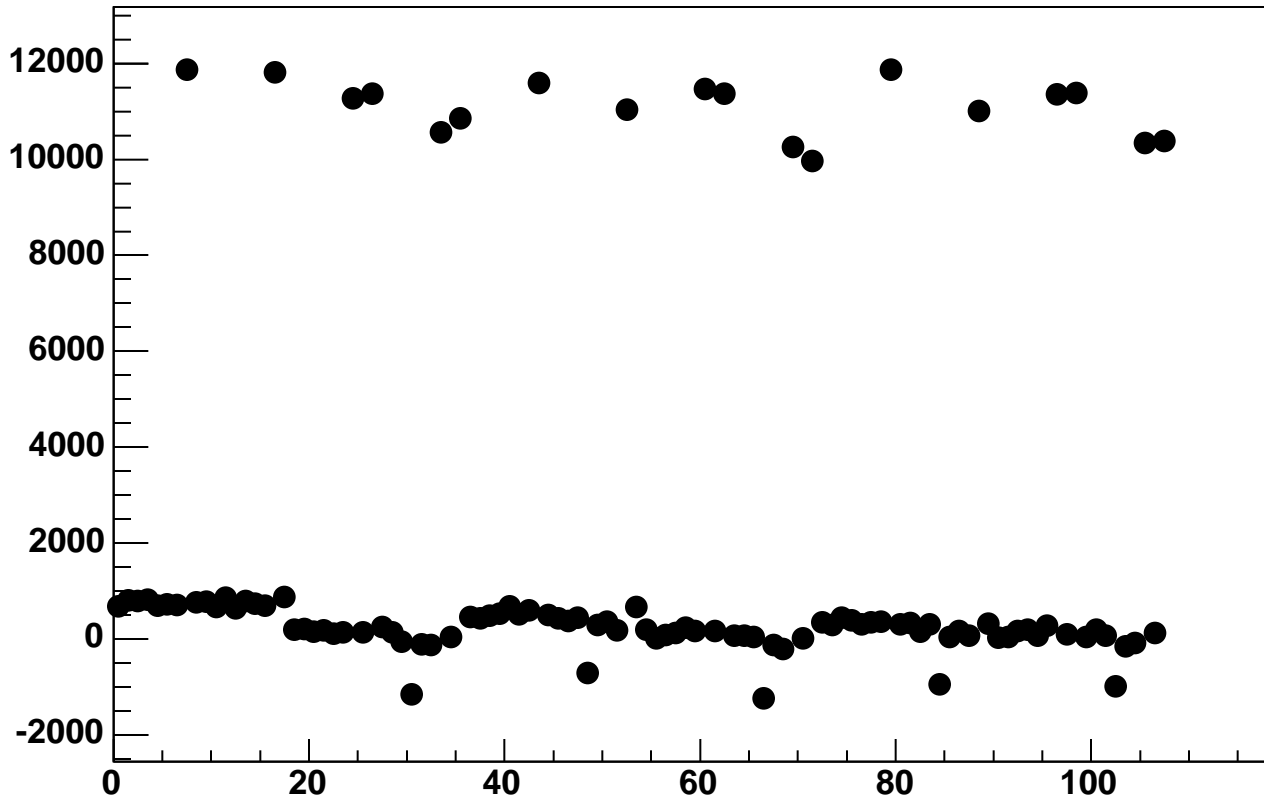
Enable 5, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



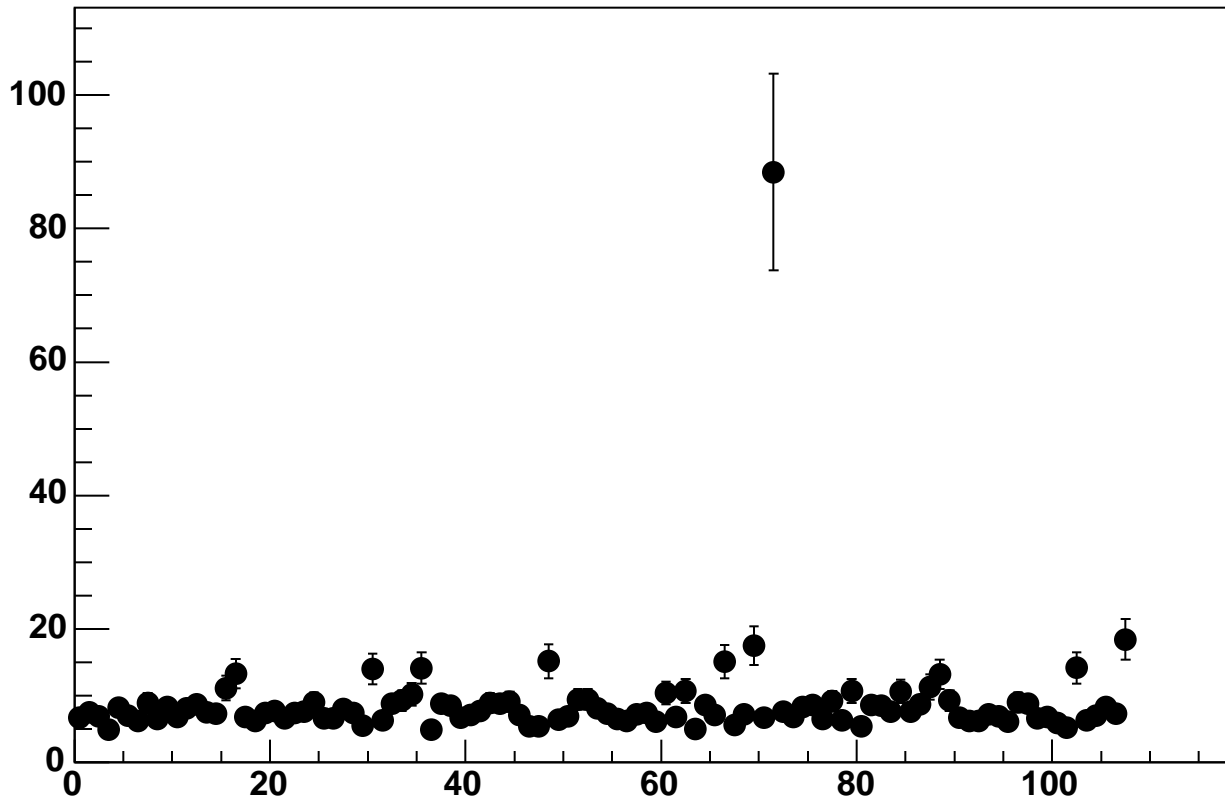
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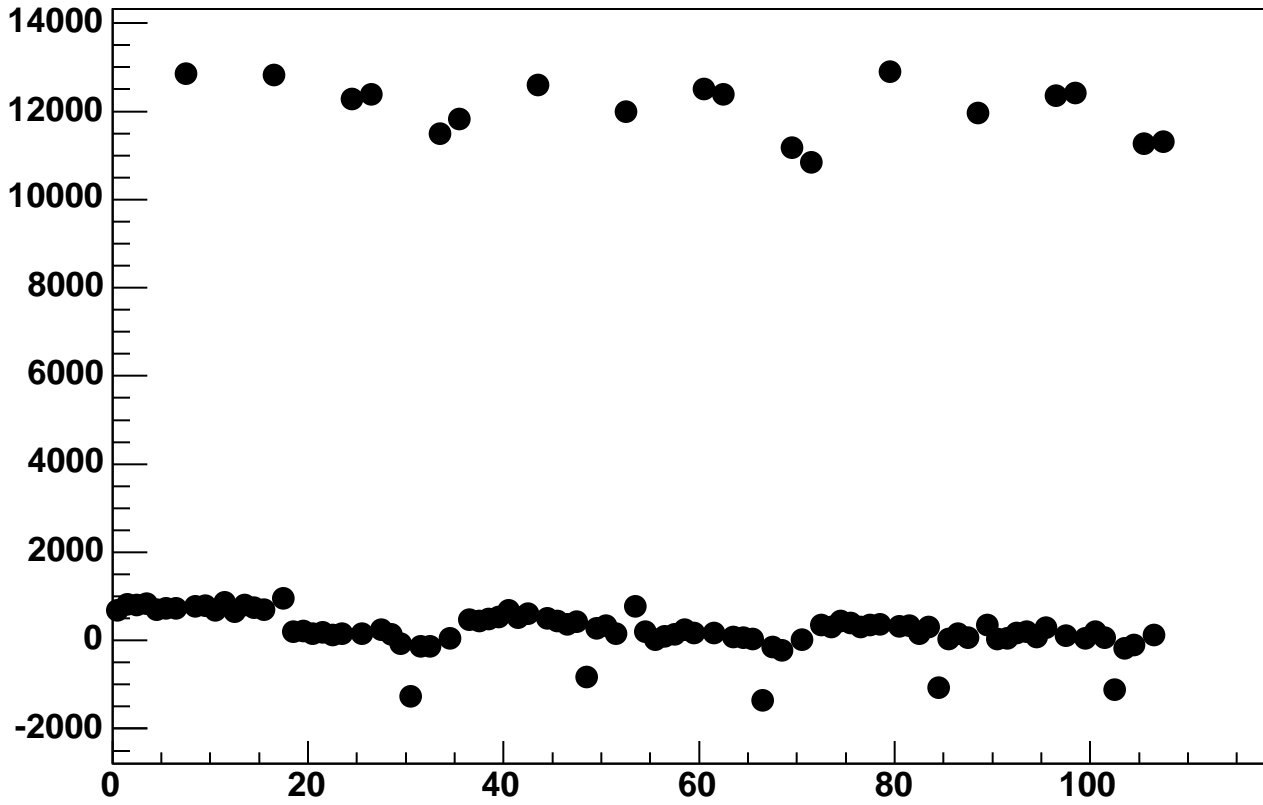
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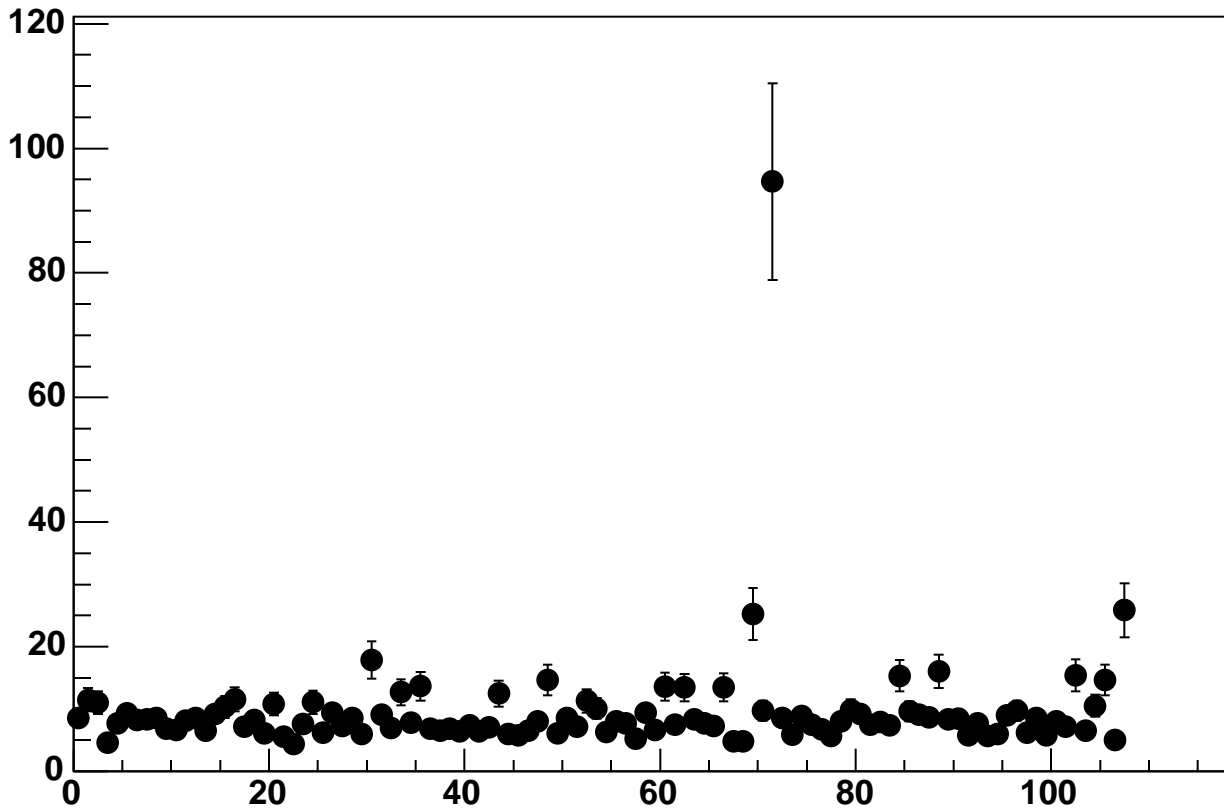
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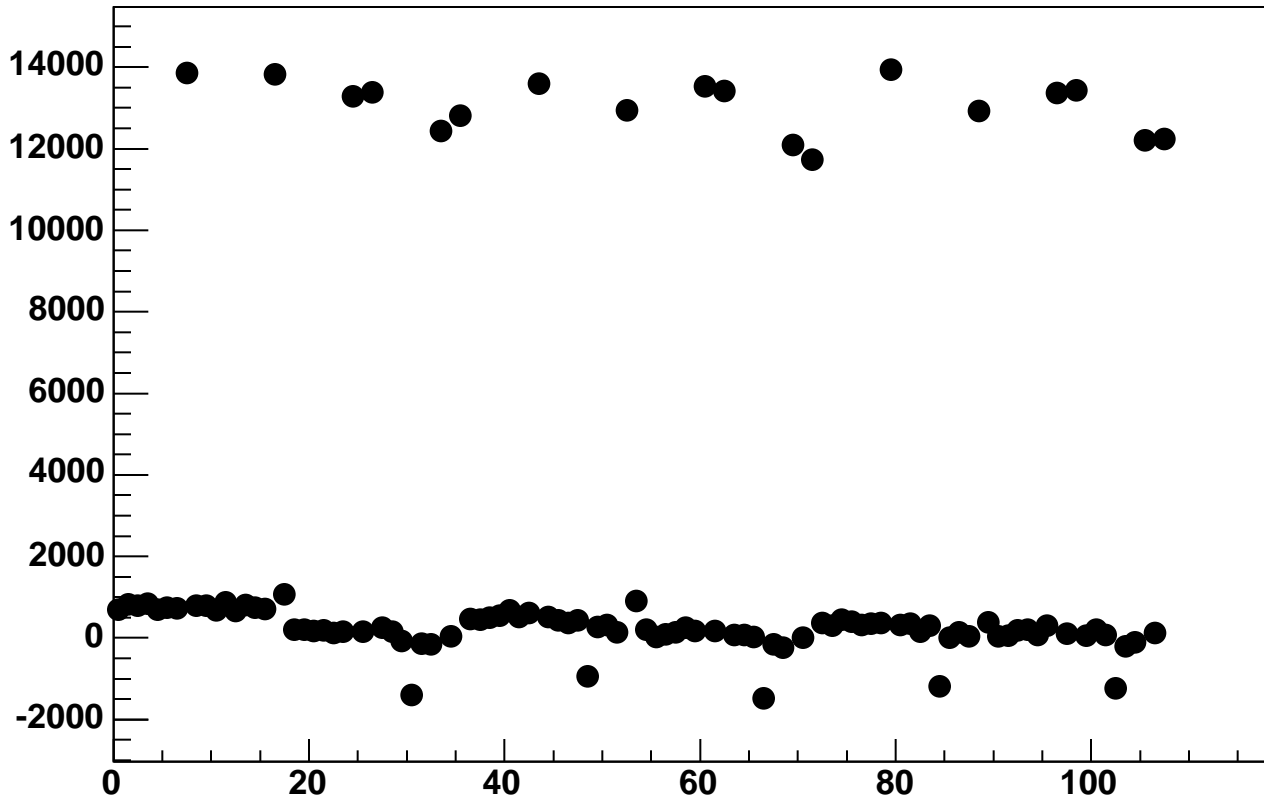
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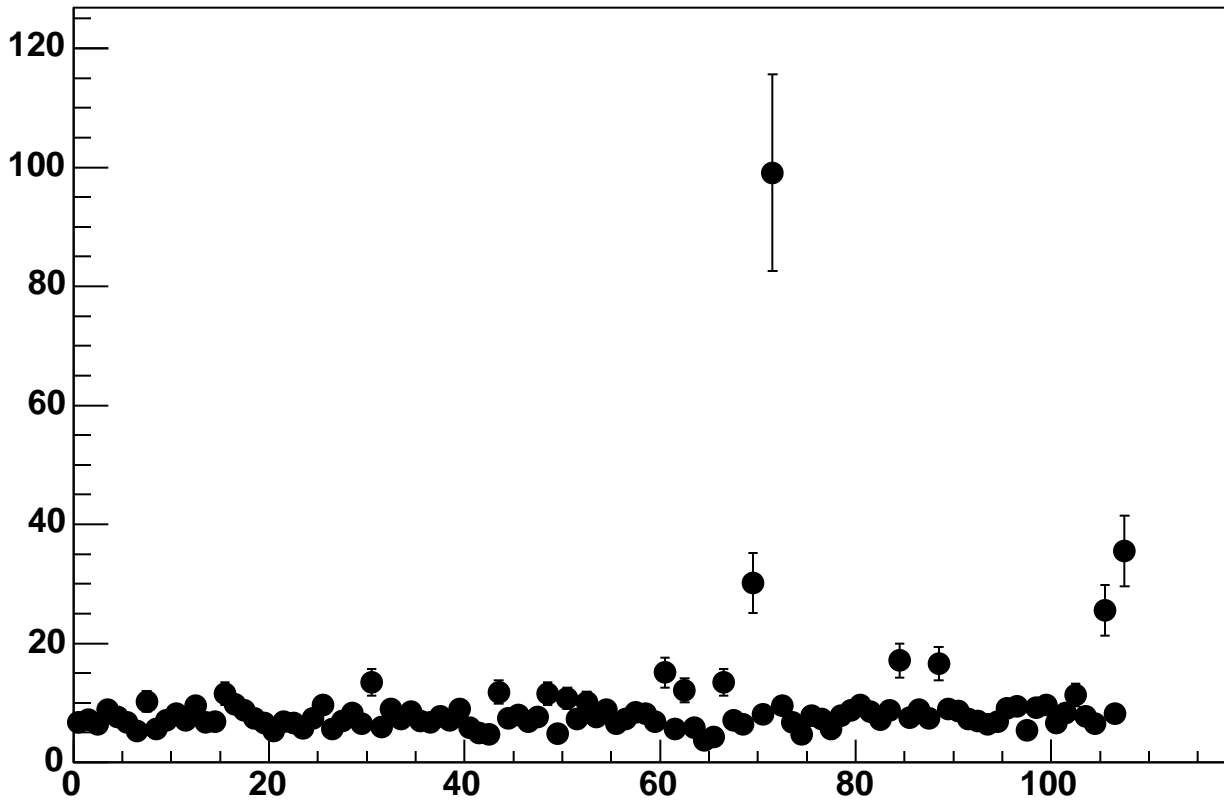
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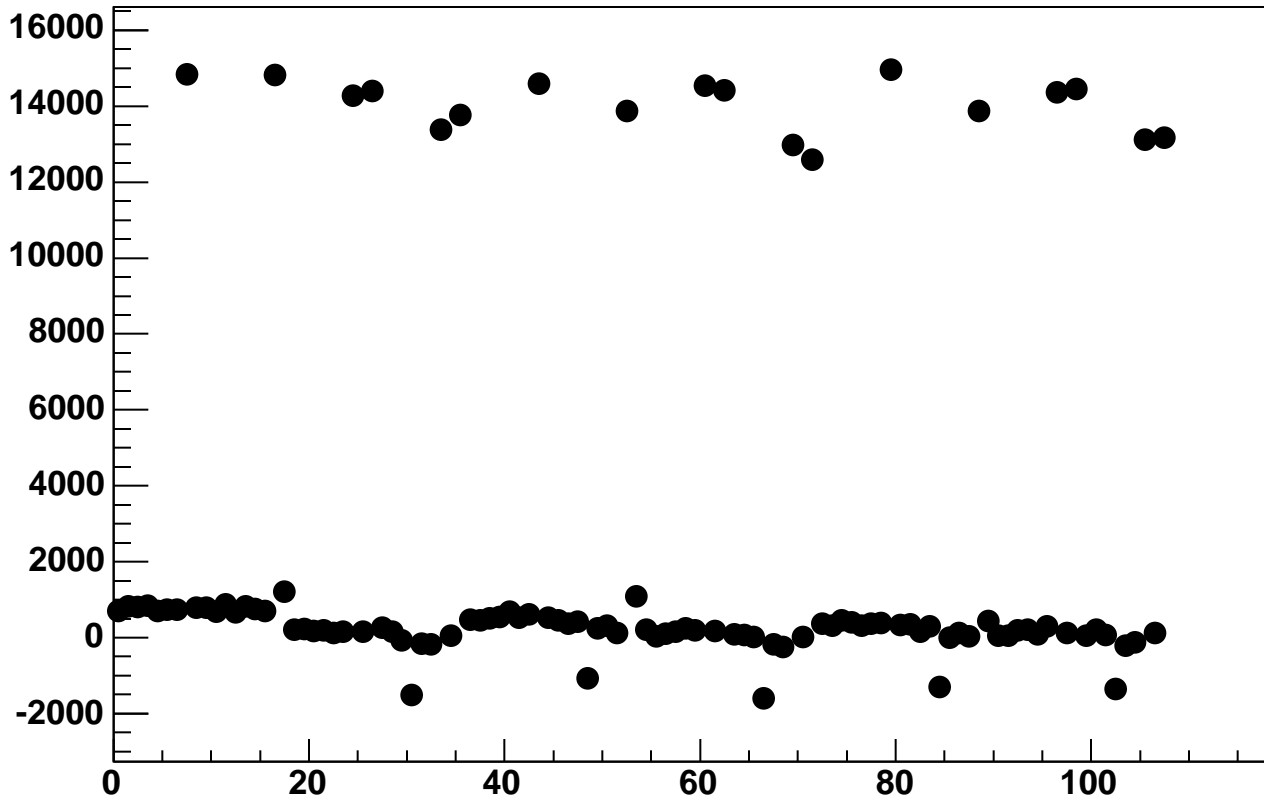
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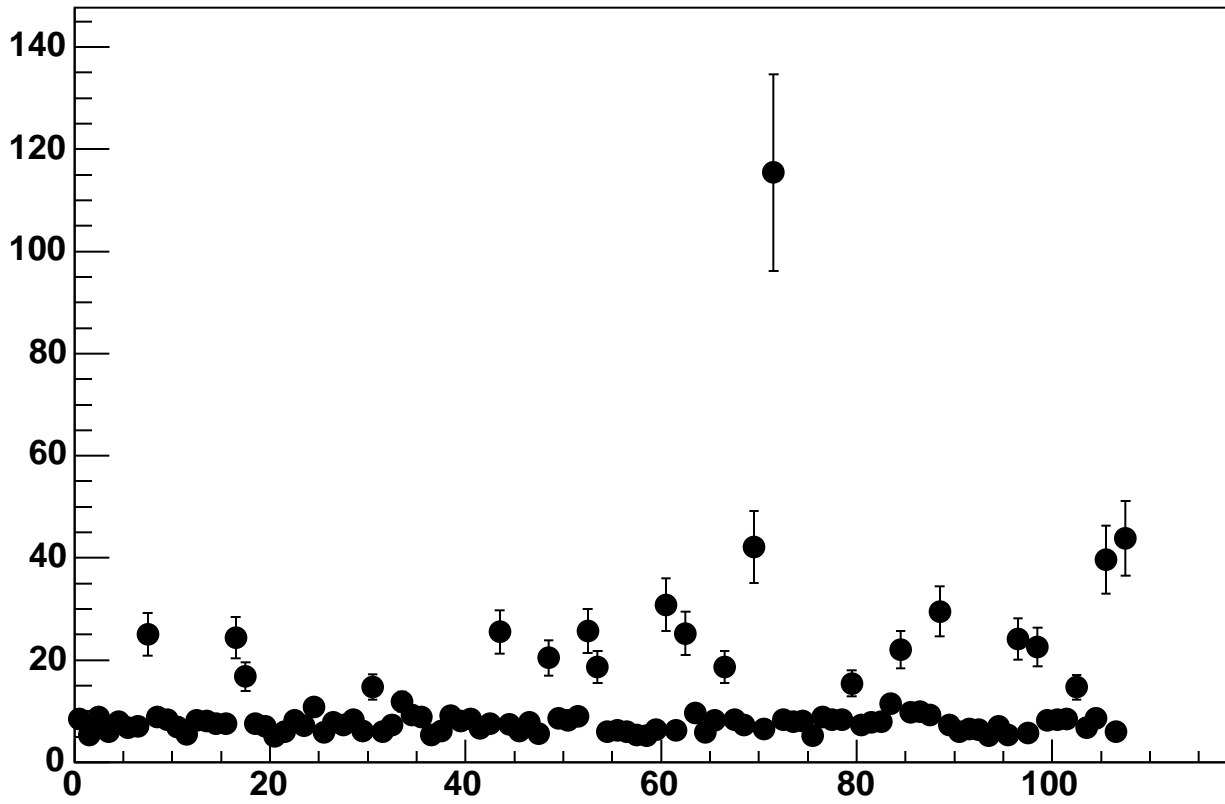
Enable 5, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



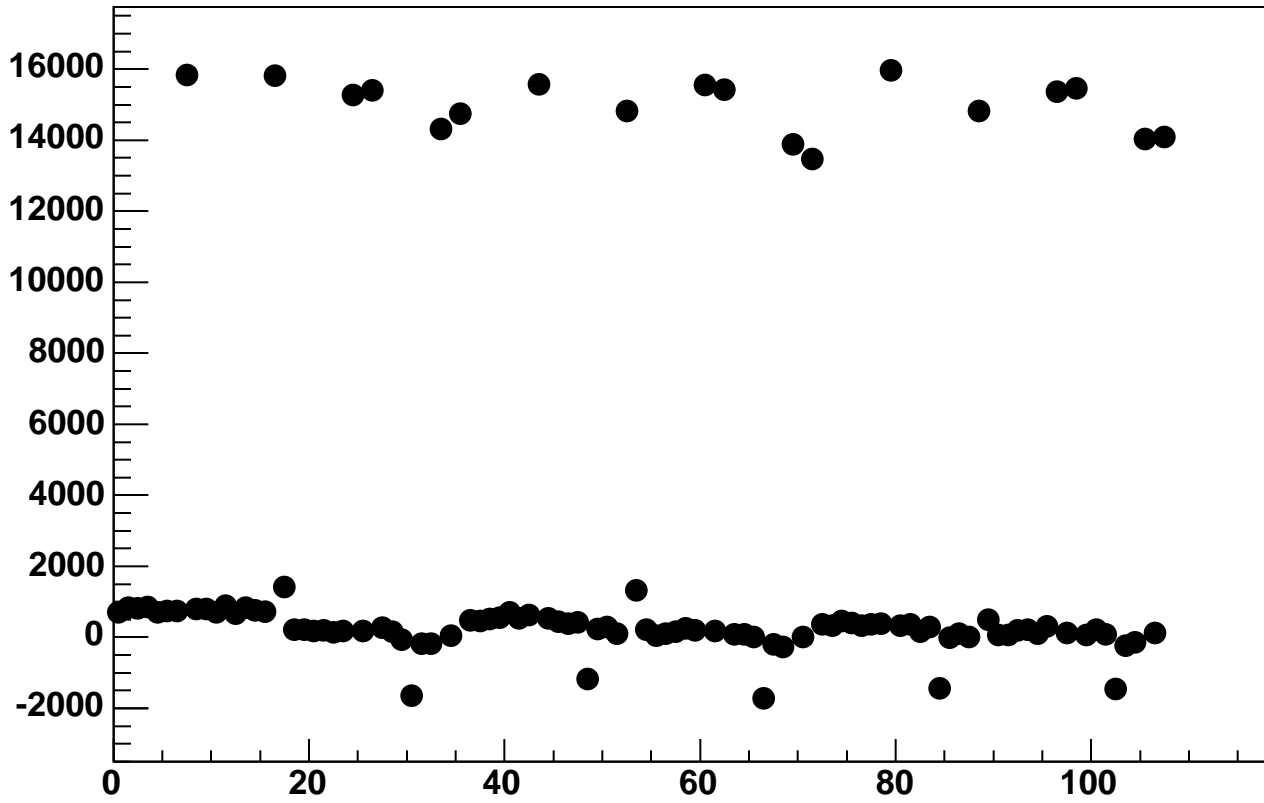
Enable 5, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



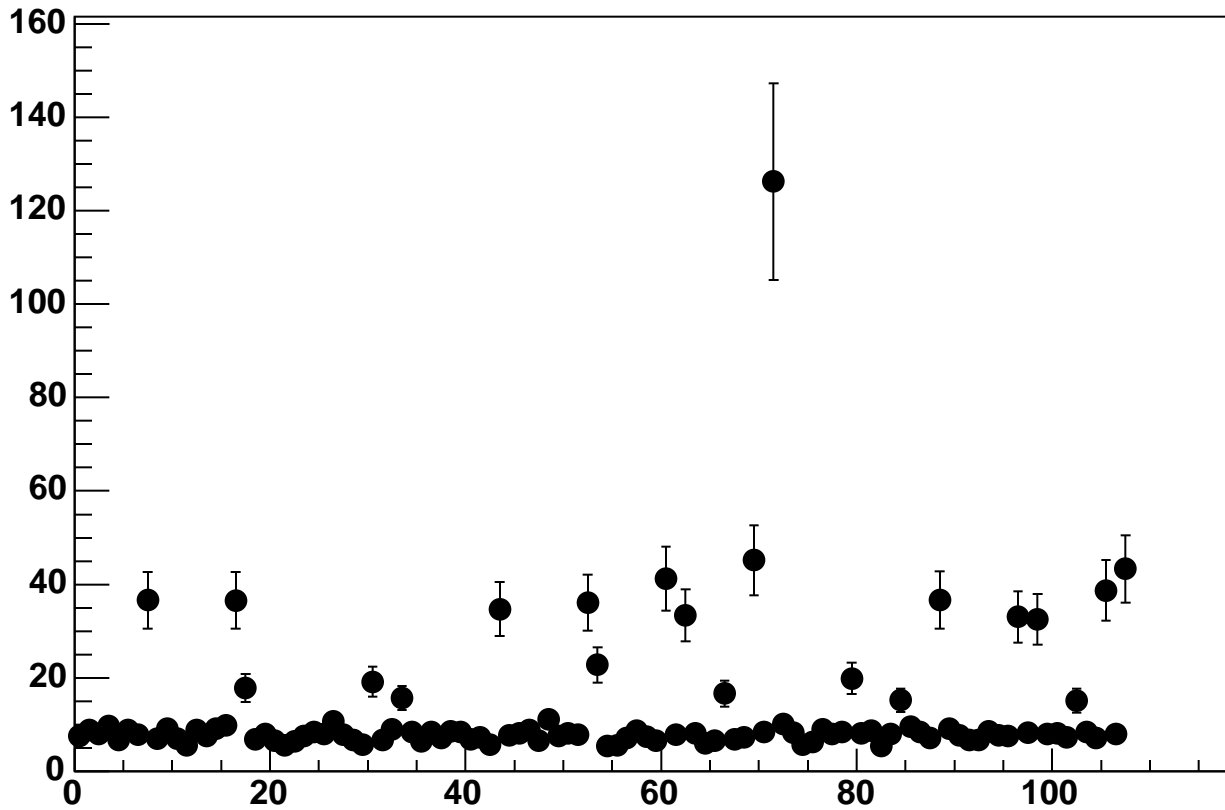
Enable 5, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



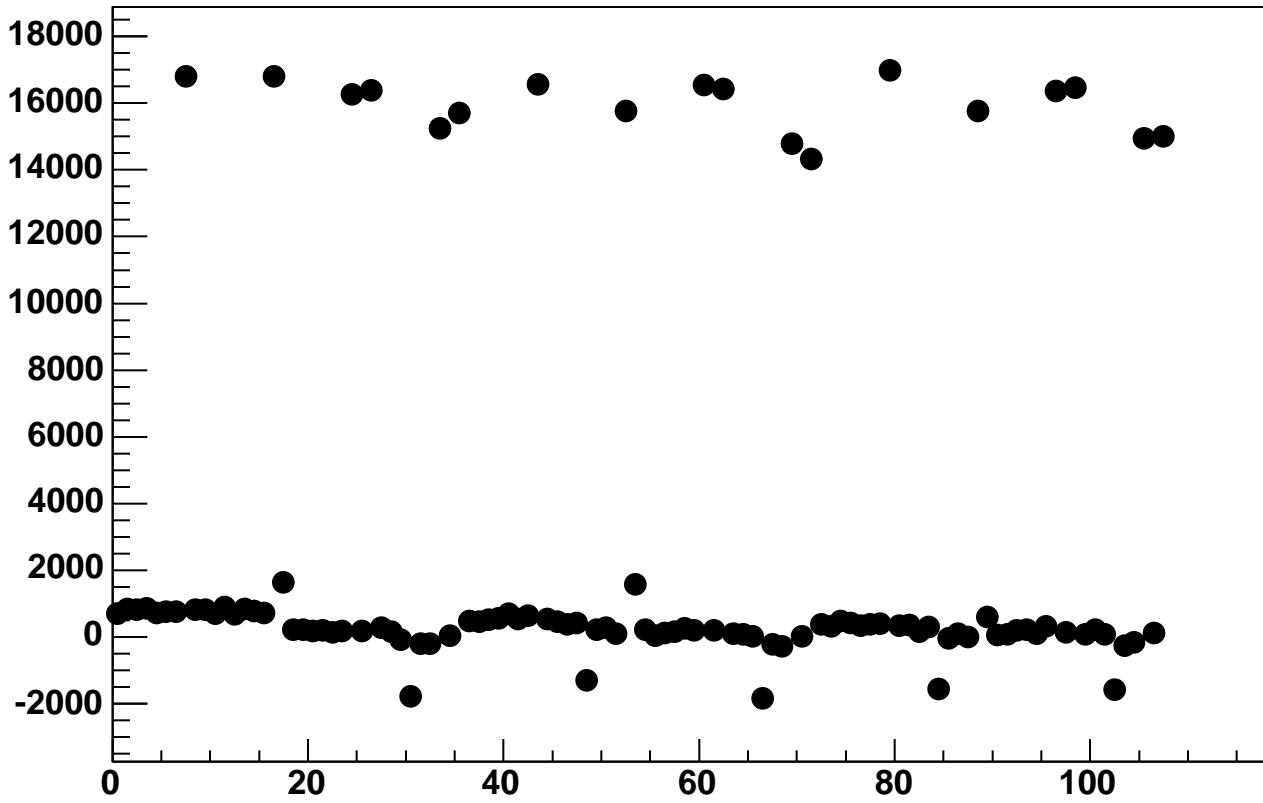
Enable 5, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



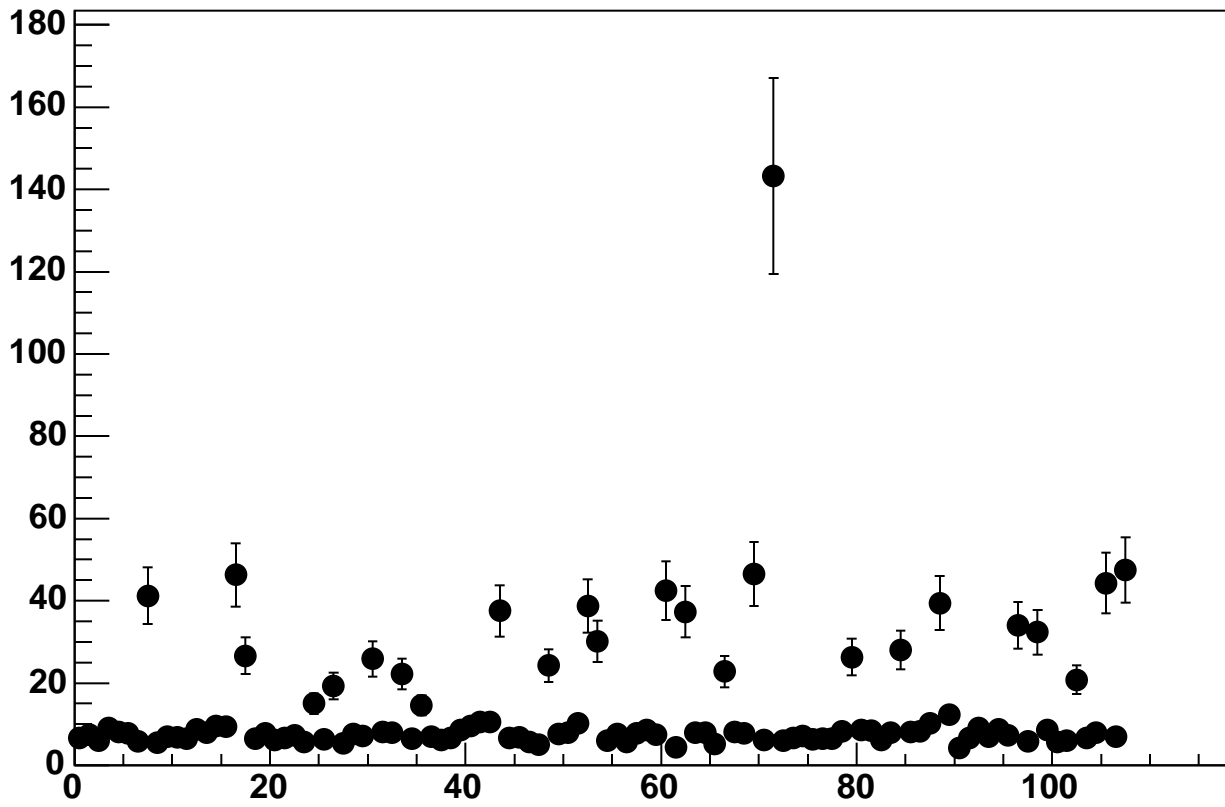
Enable 5, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



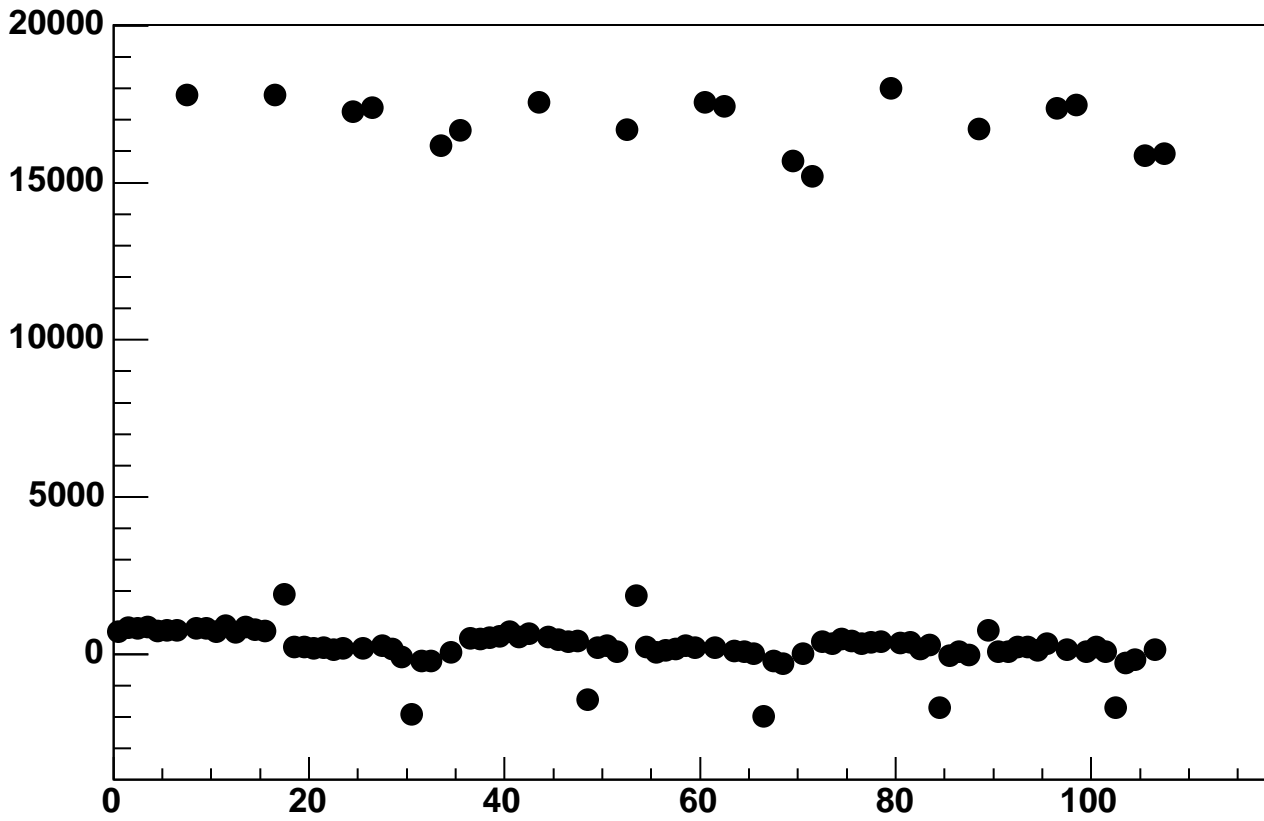
Enable 5, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



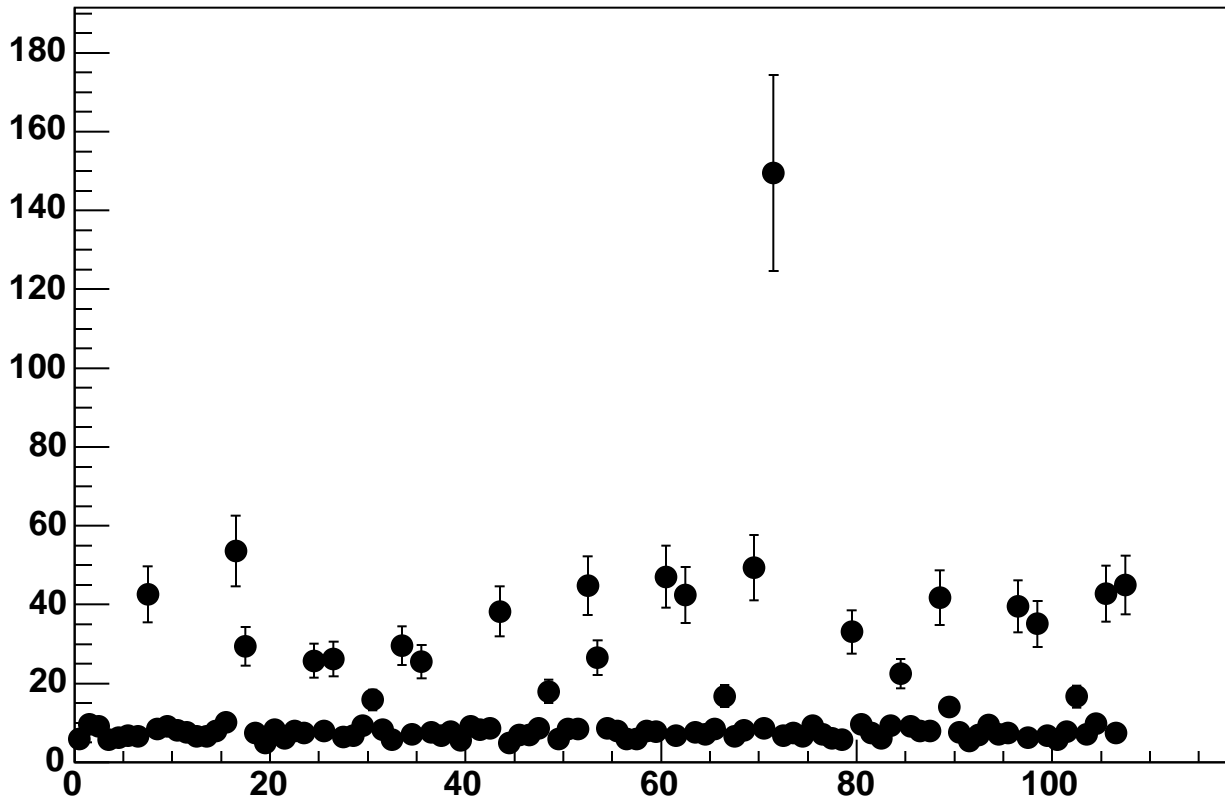
Enable 5, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



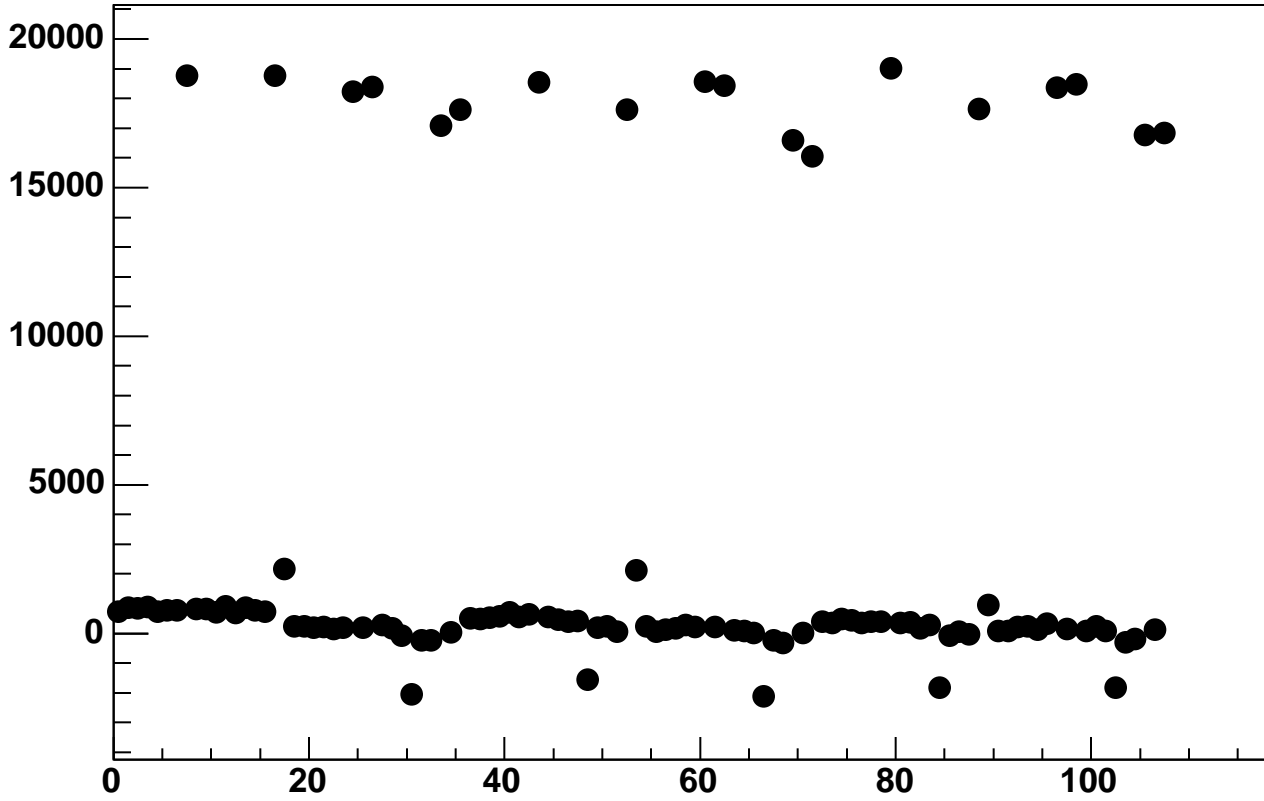
Enable 5, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



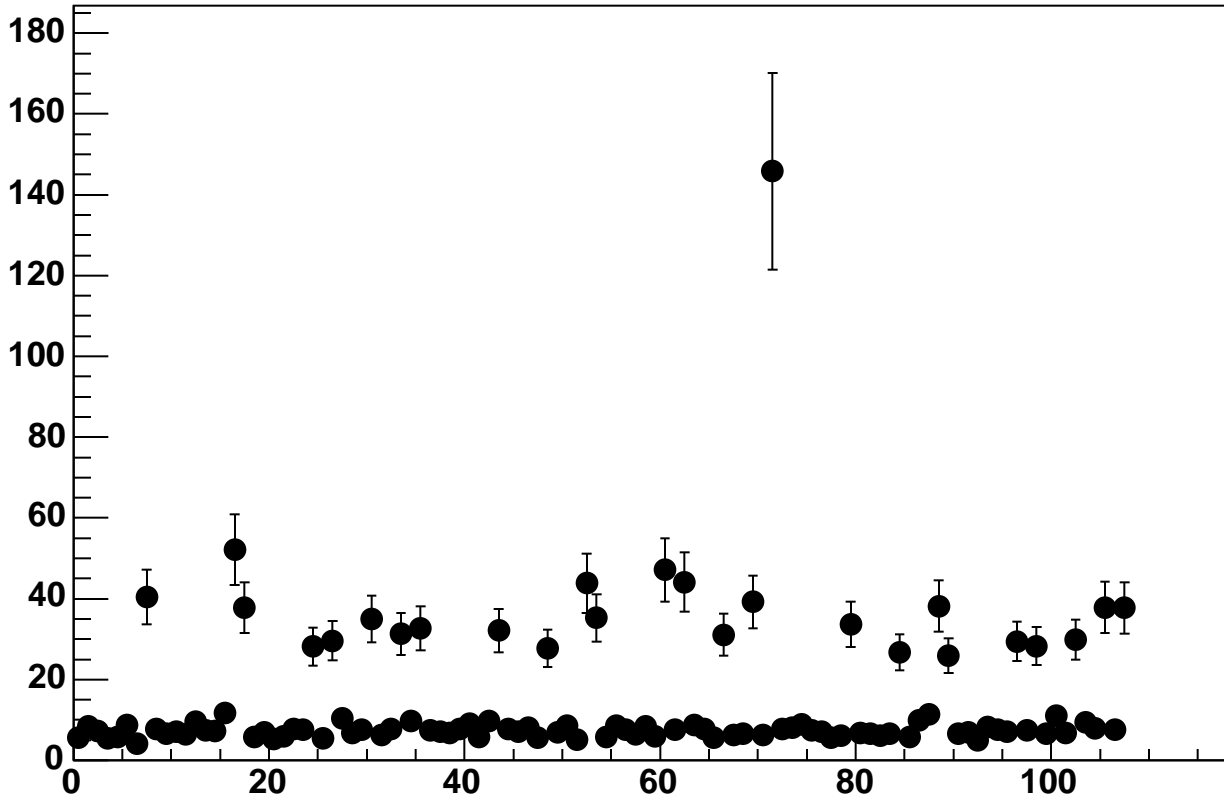
Enable 5, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



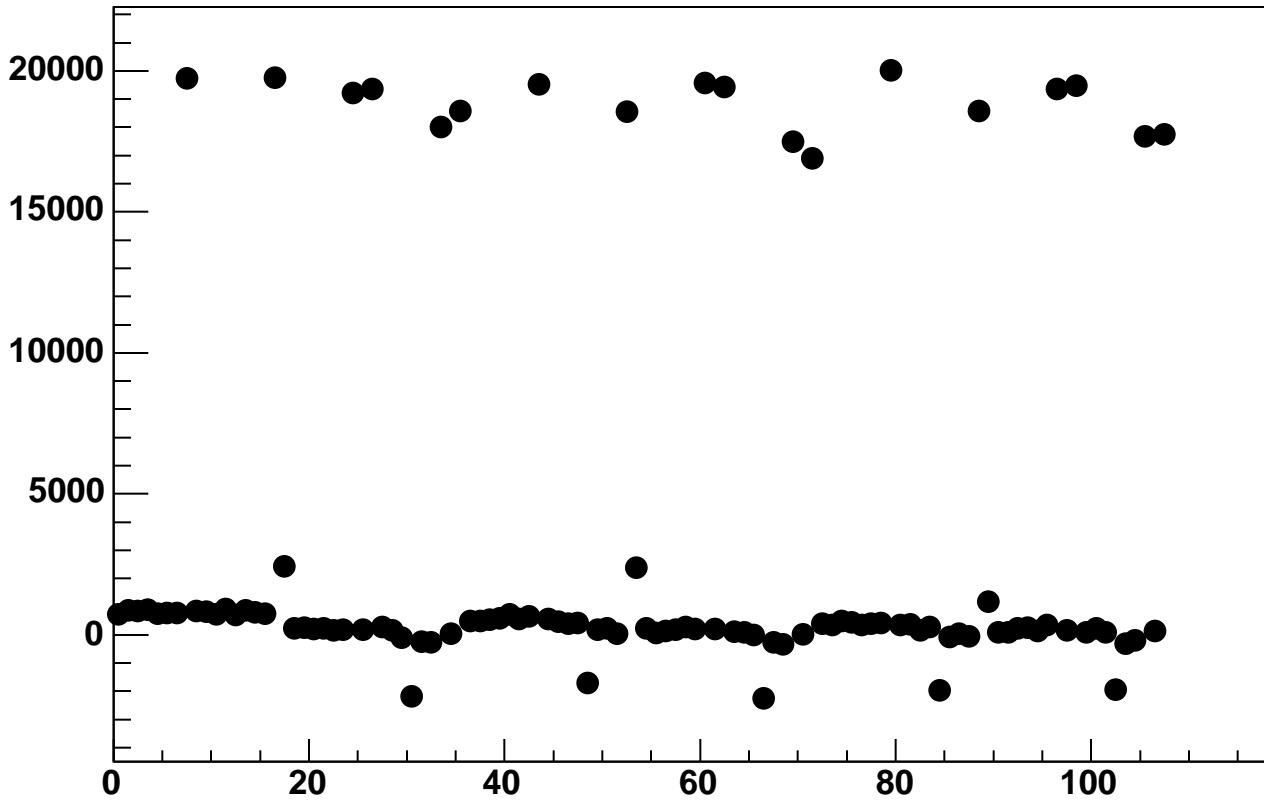
Enable 5, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



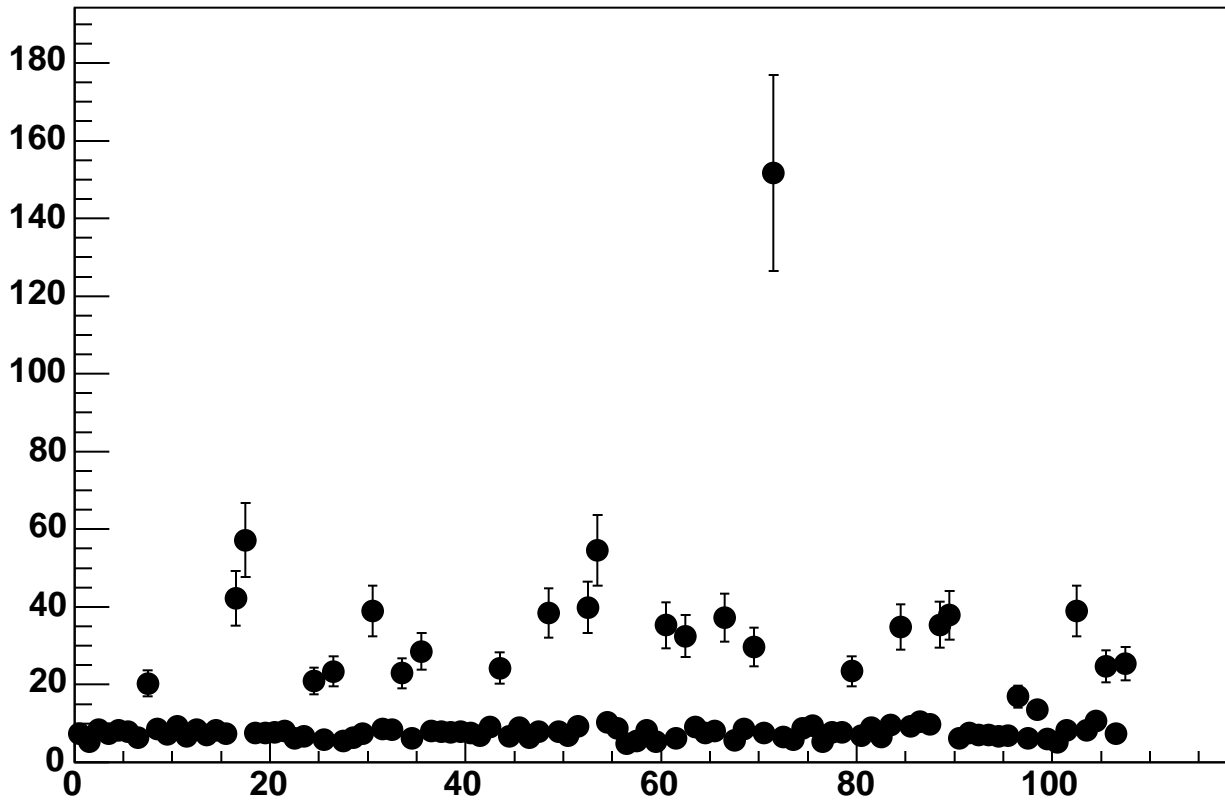
Enable 5, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



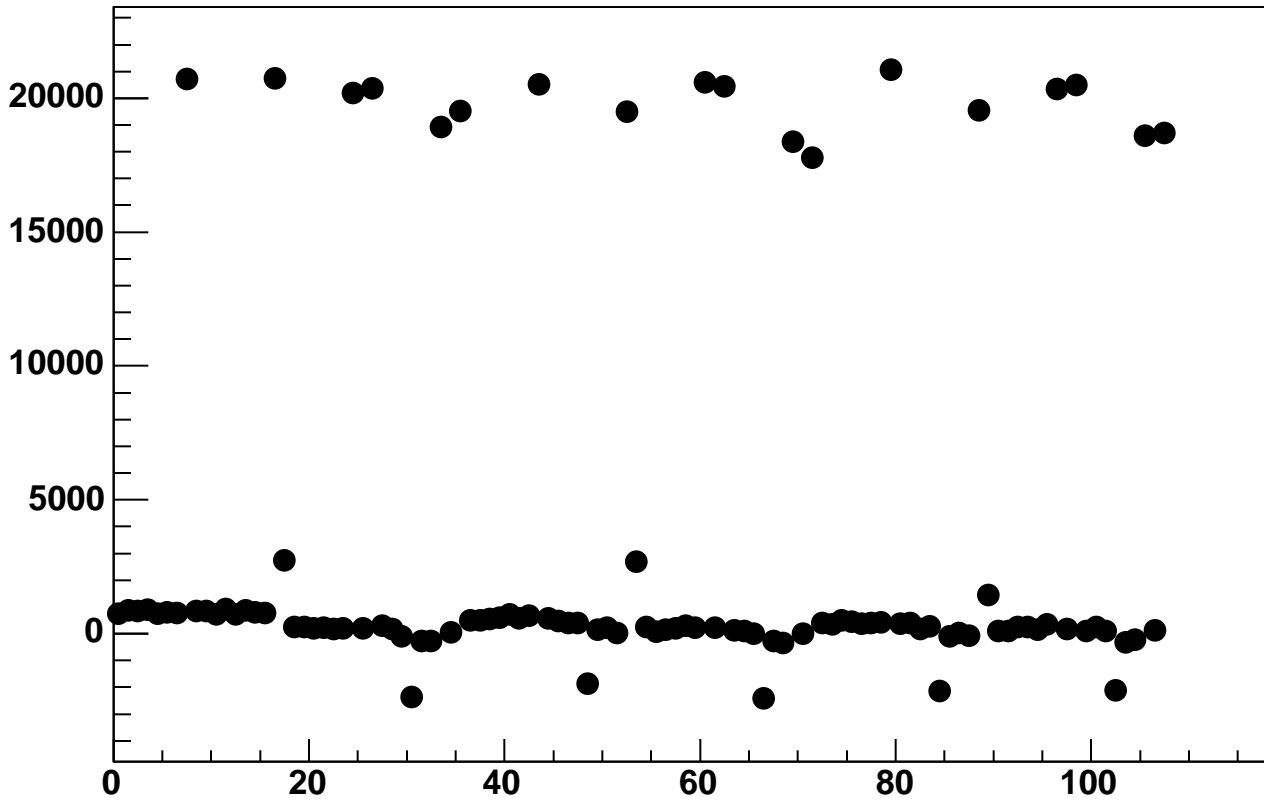
Enable 5, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



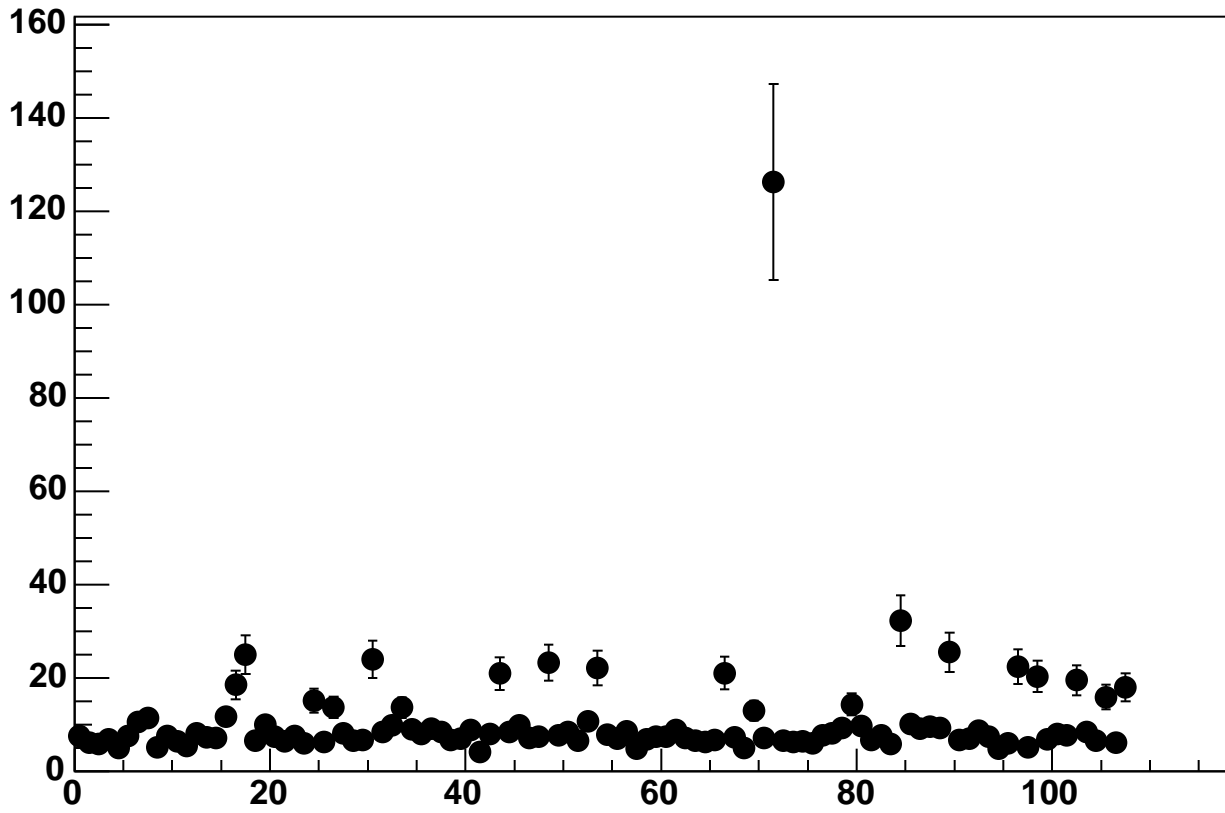
Enable 5, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



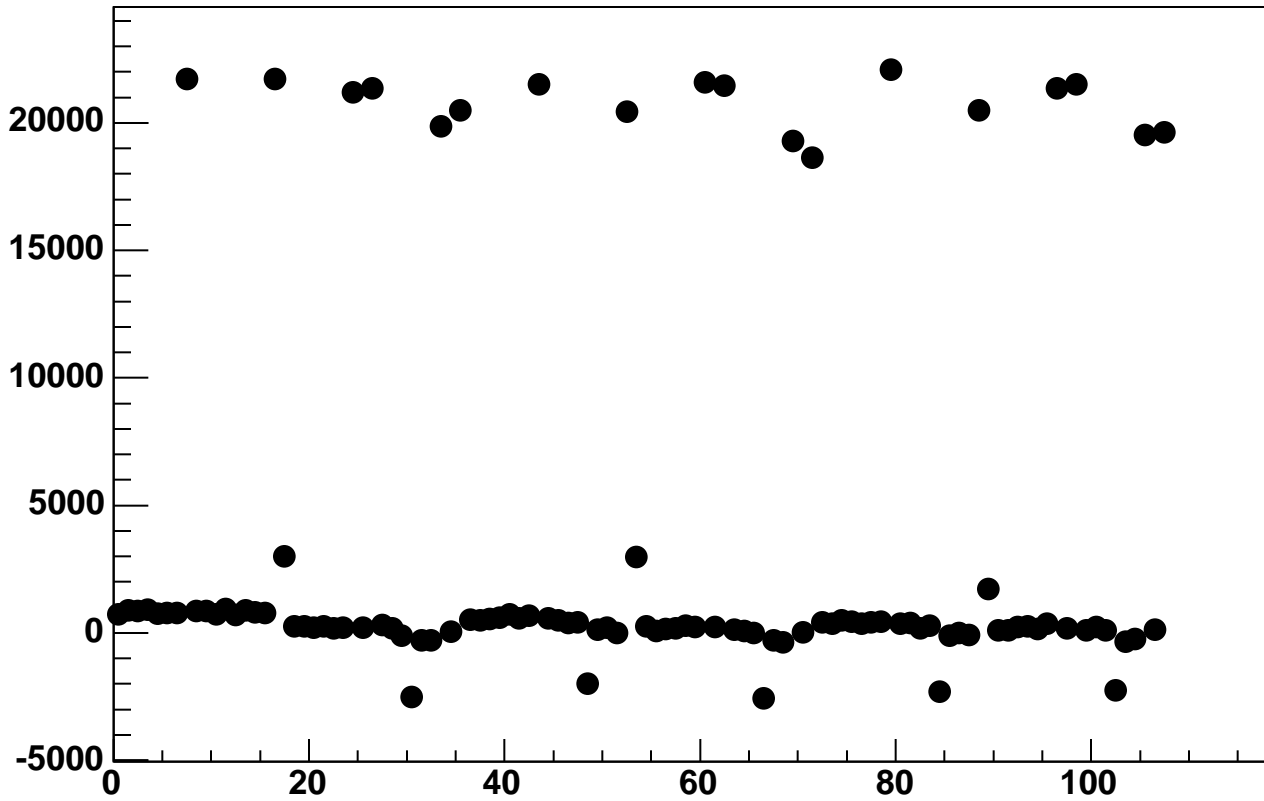
Enable 5, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



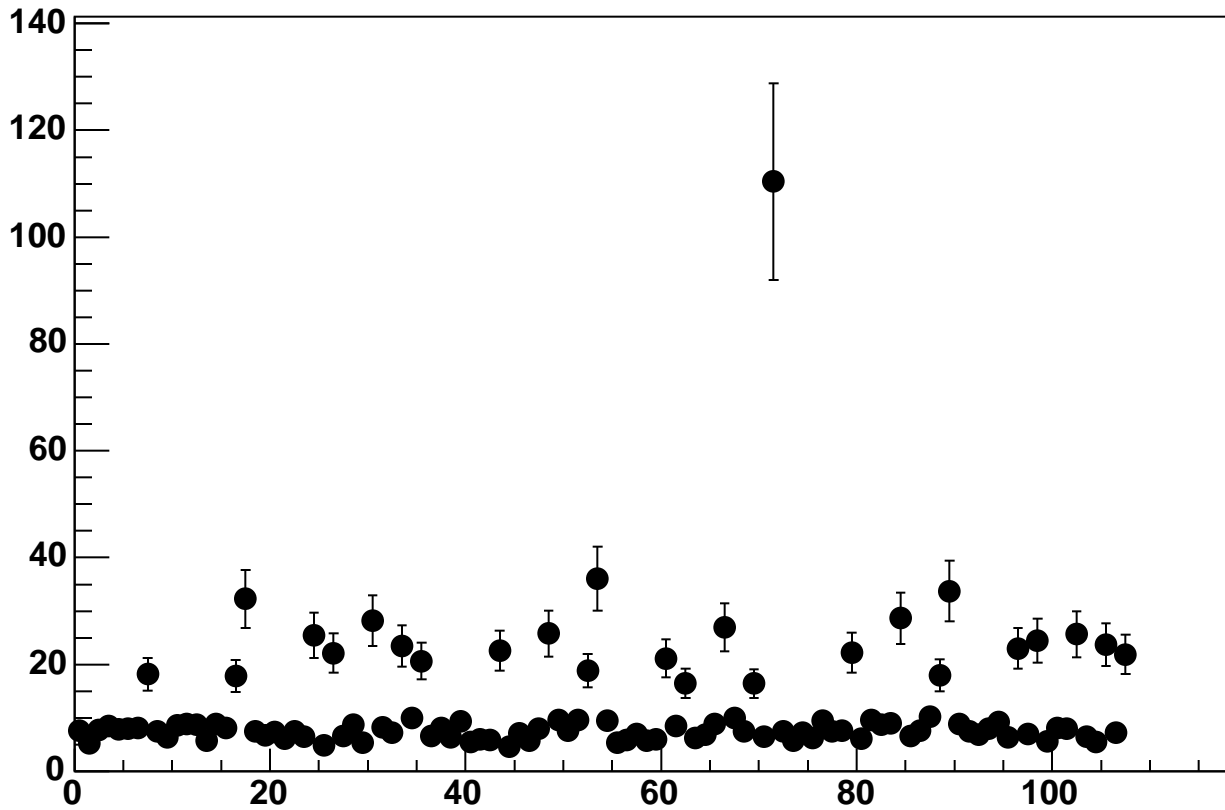
Enable 5, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



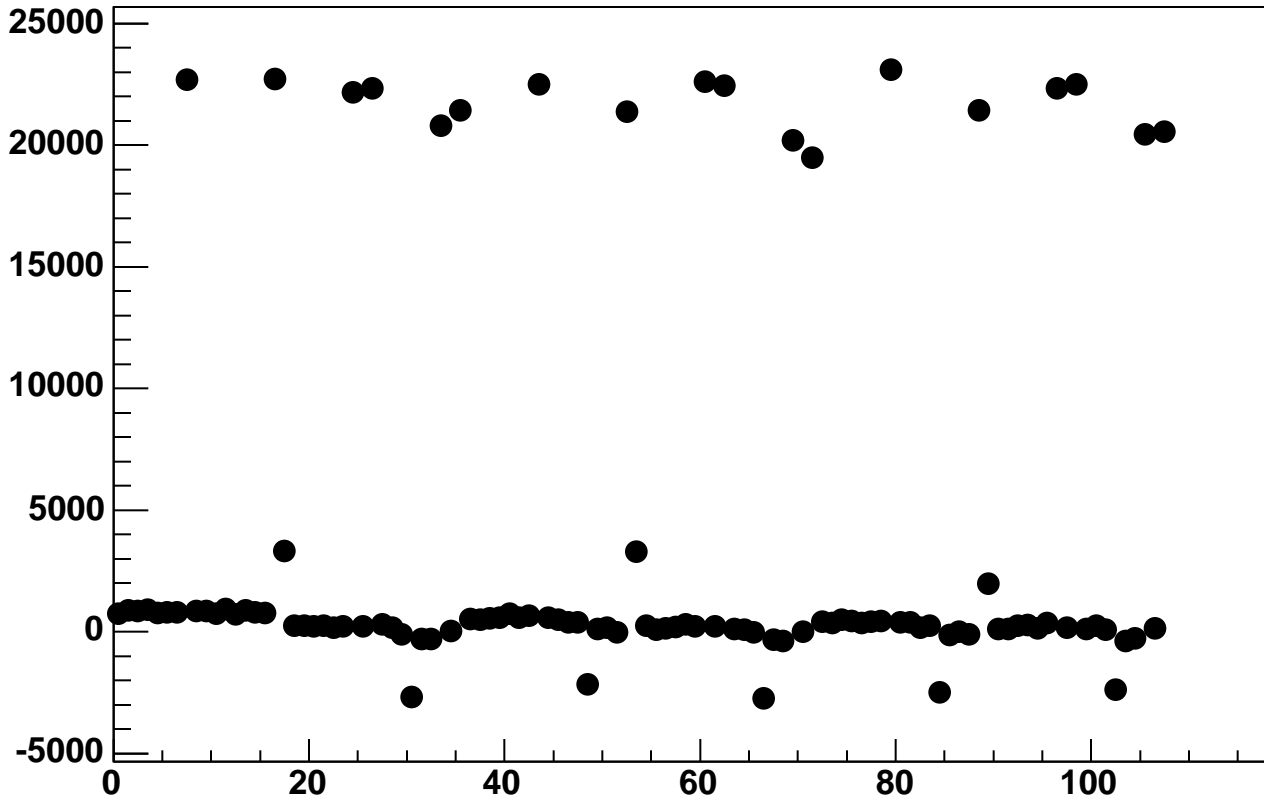
Enable 5, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



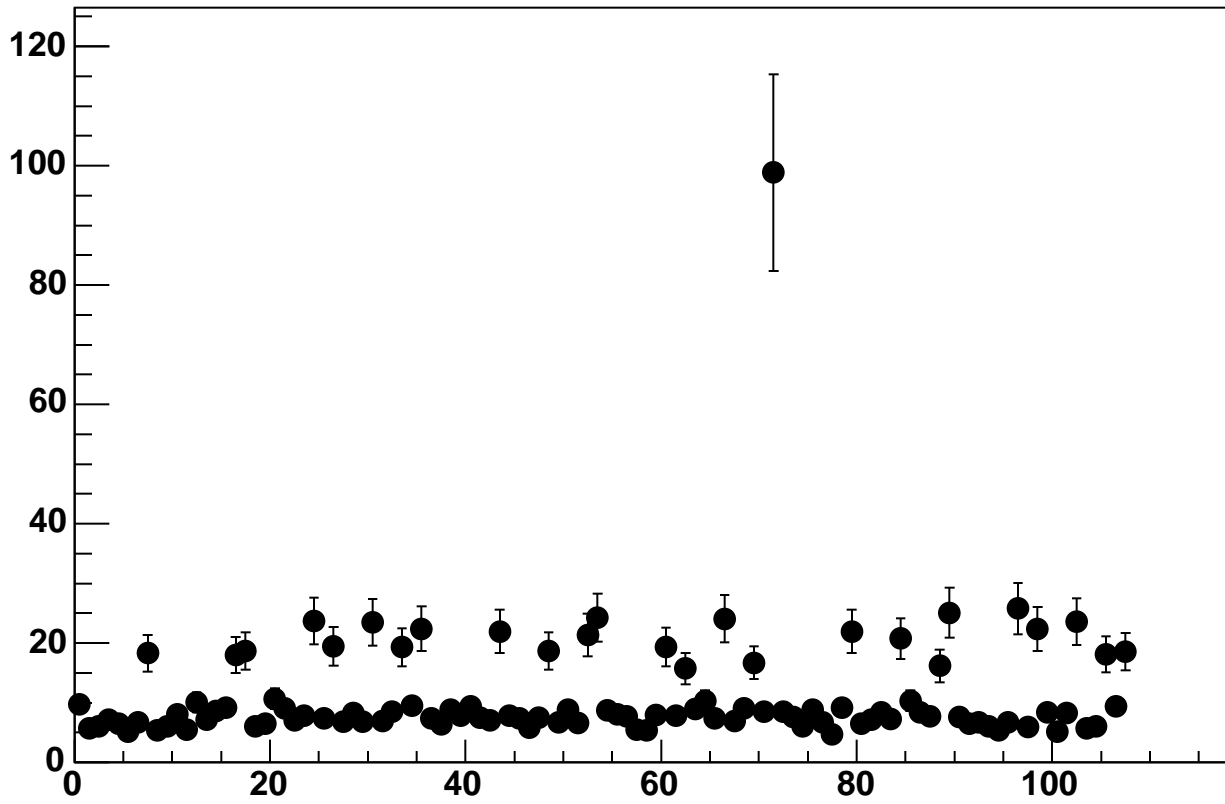
Enable 5, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



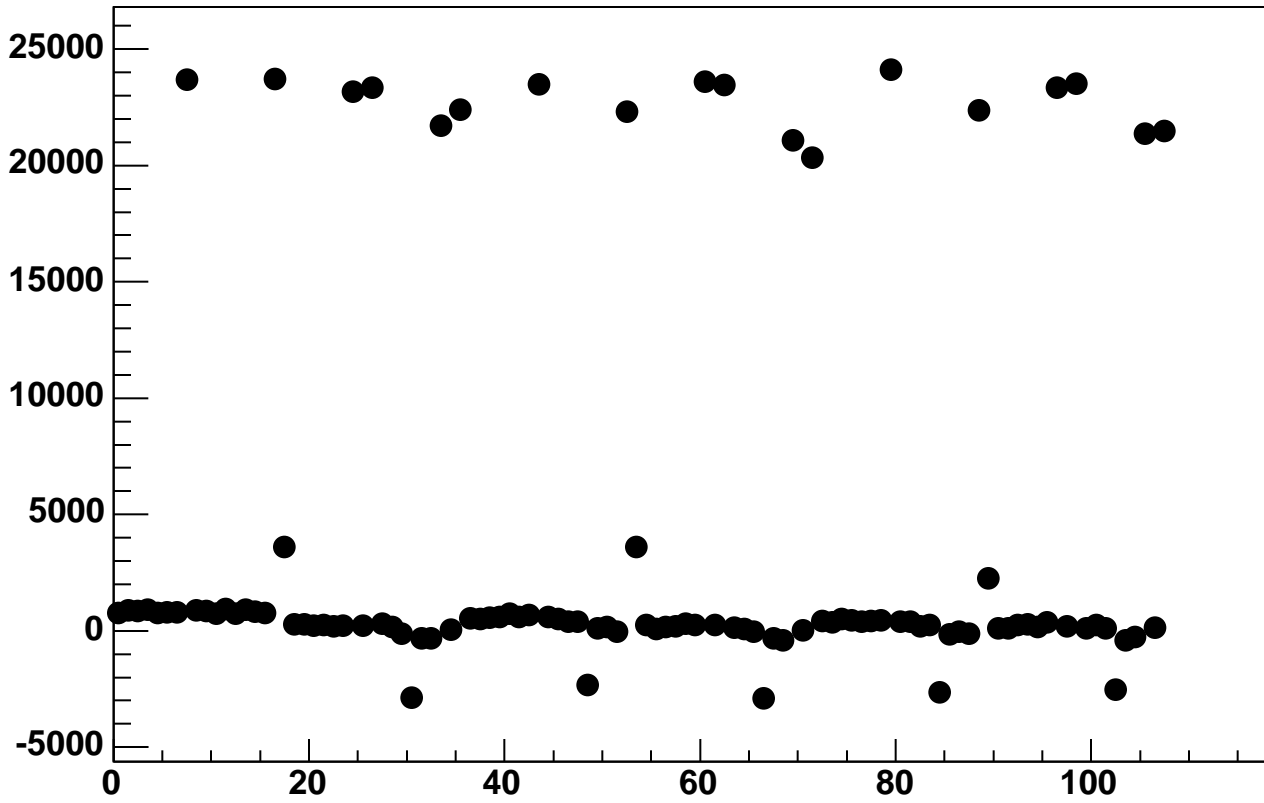
Enable 5, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



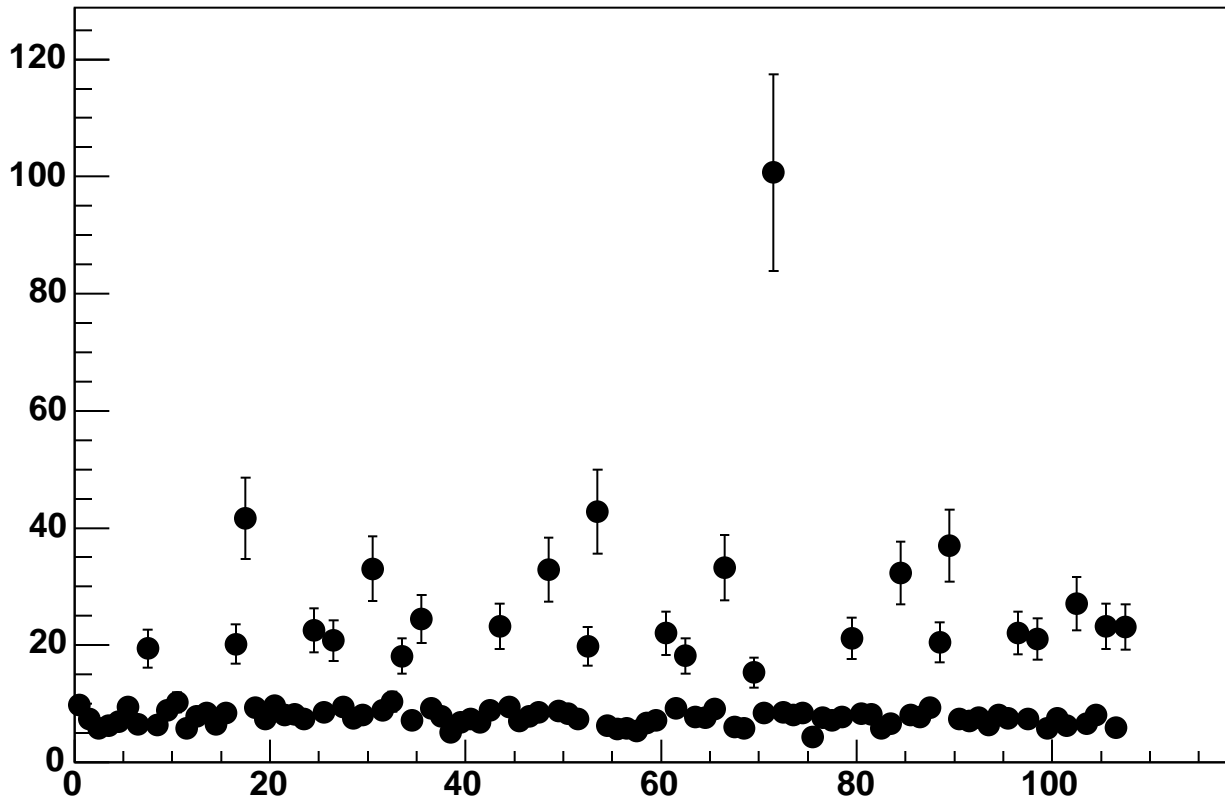
Enable 5, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



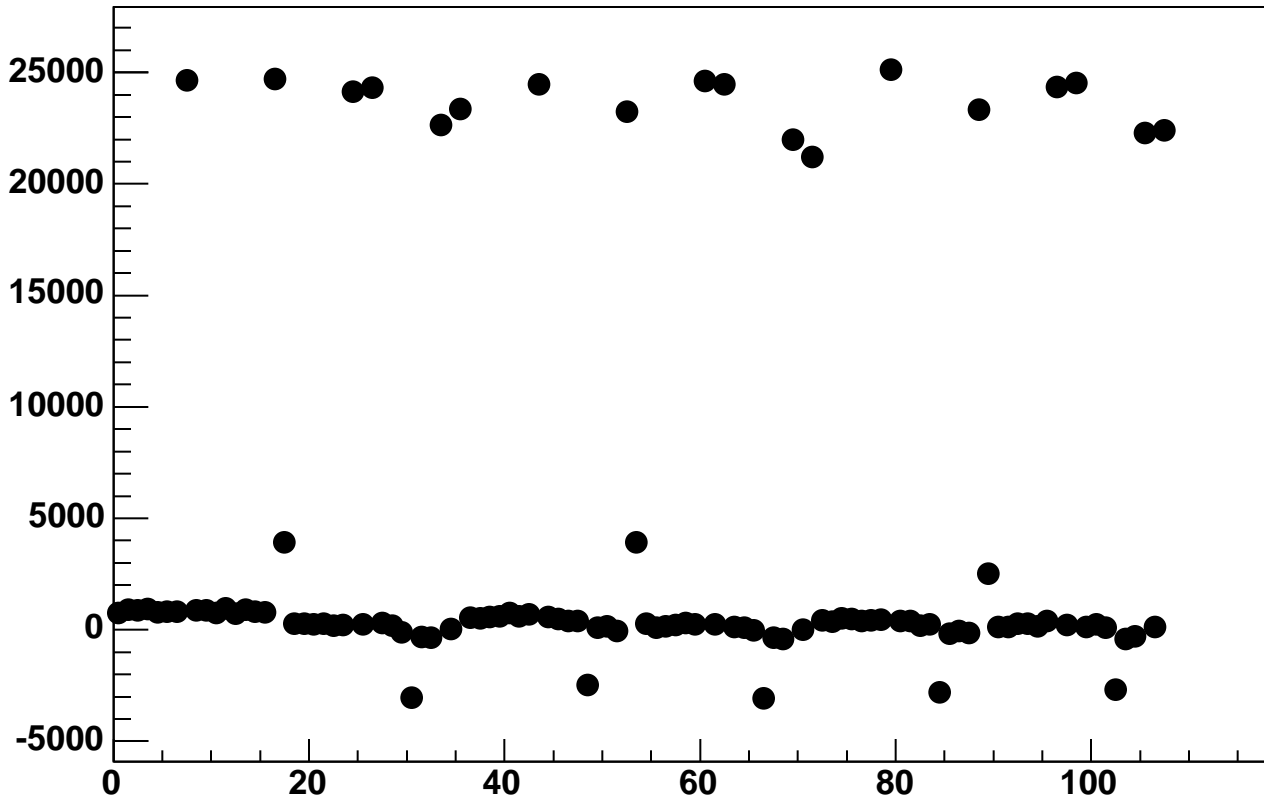
Enable 5, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



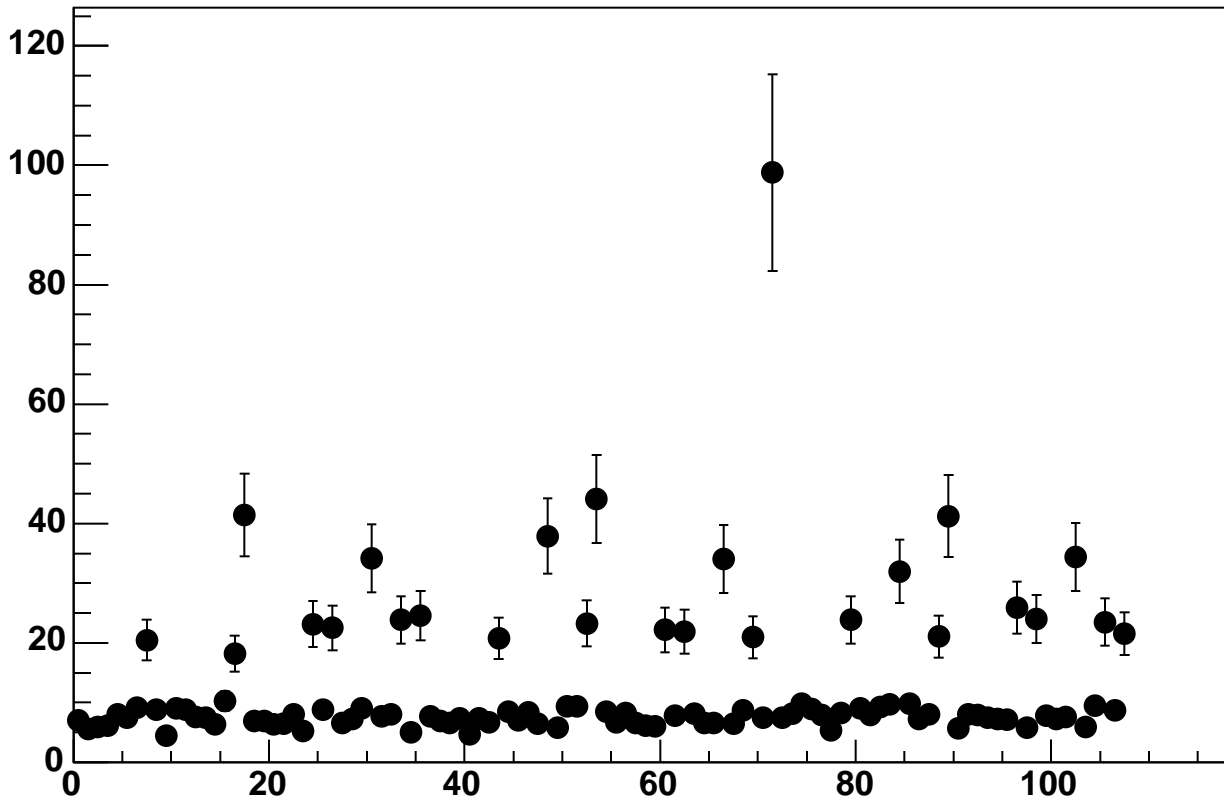
Enable 5, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



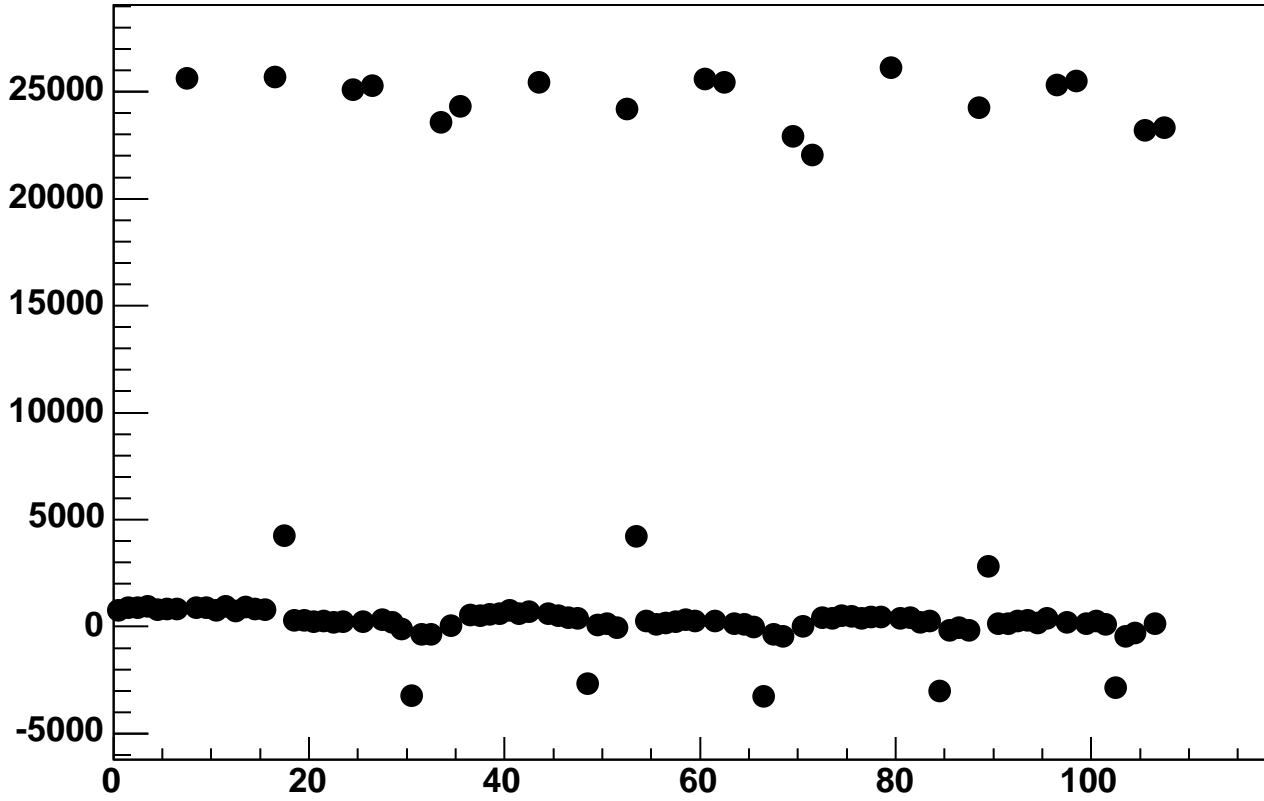
Enable 5, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



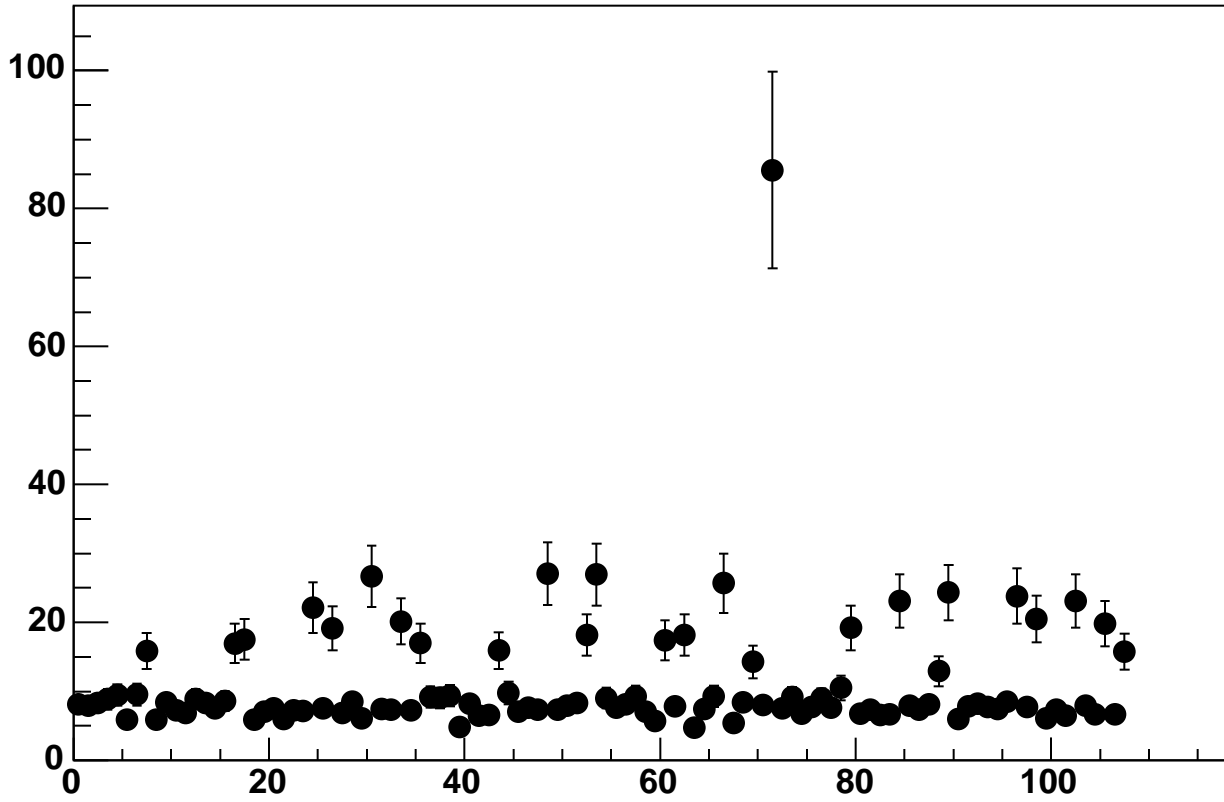
Enable 5, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



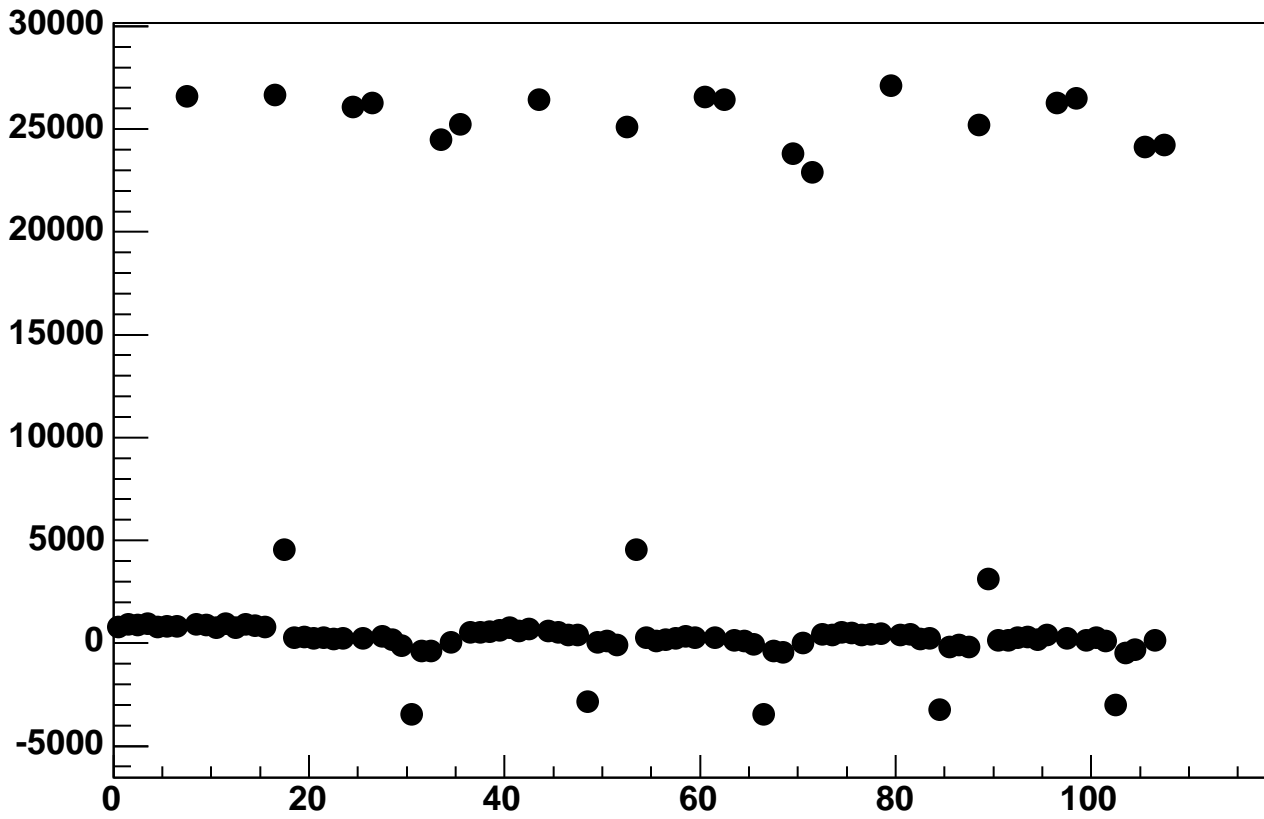
Enable 5, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



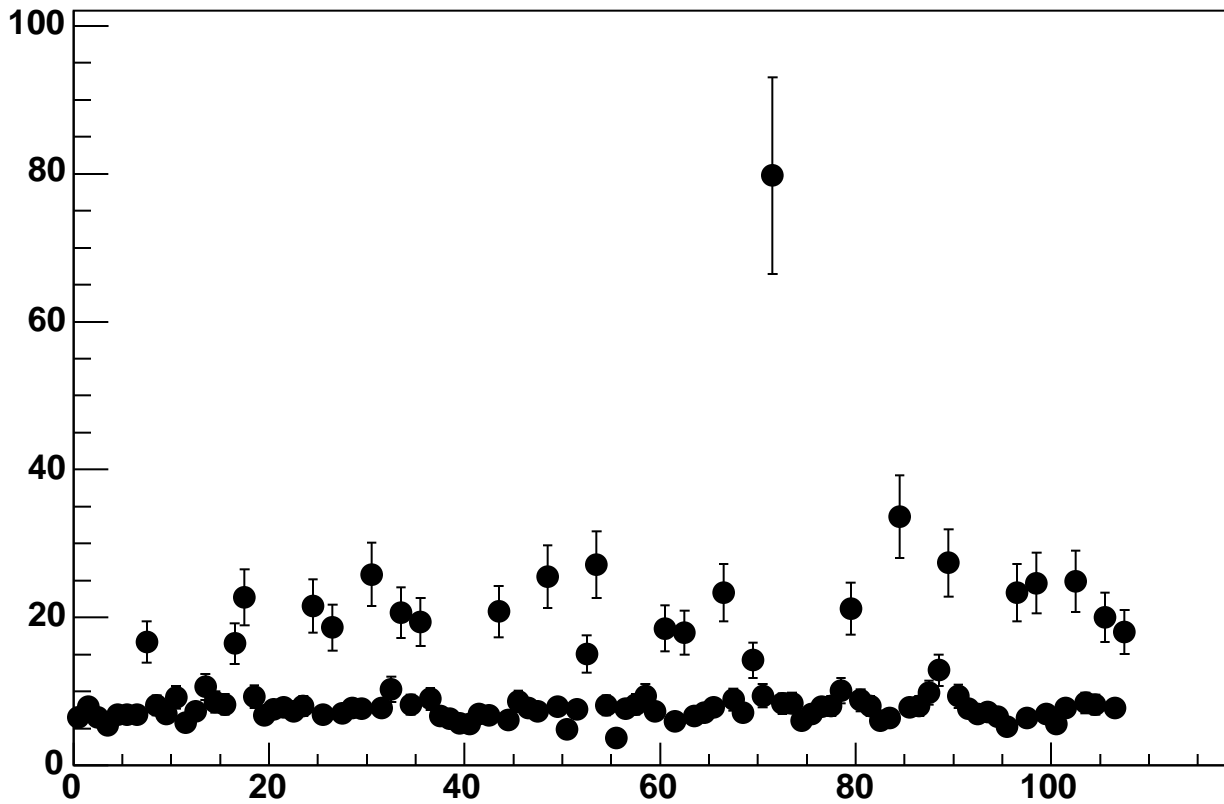
Enable 5, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



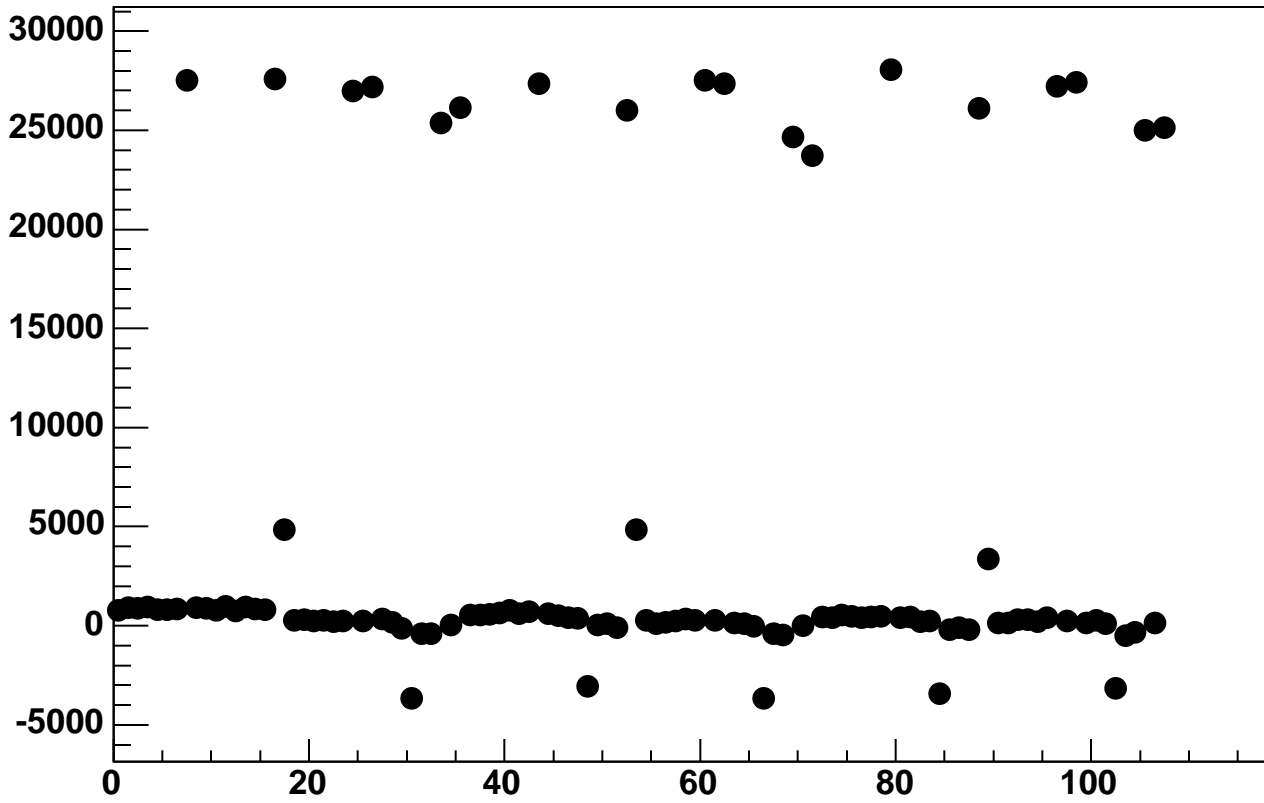
Enable 5, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



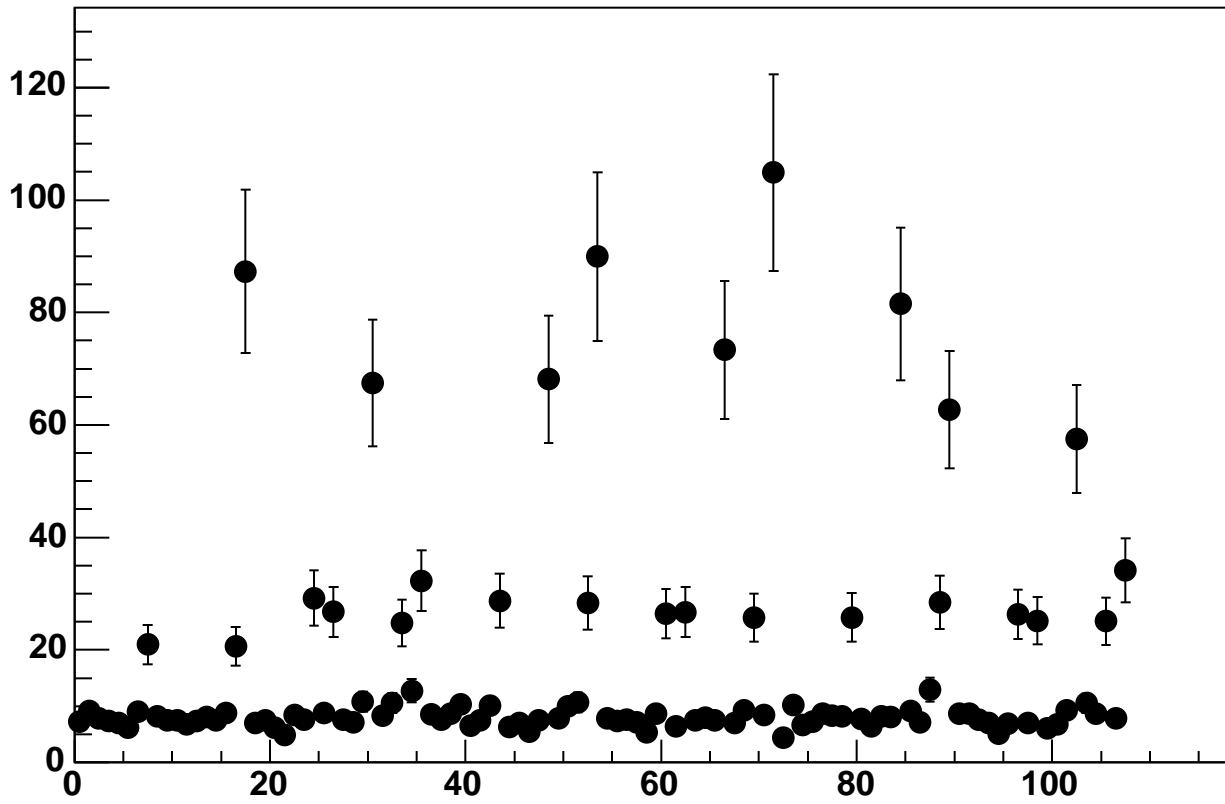
Enable 5, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



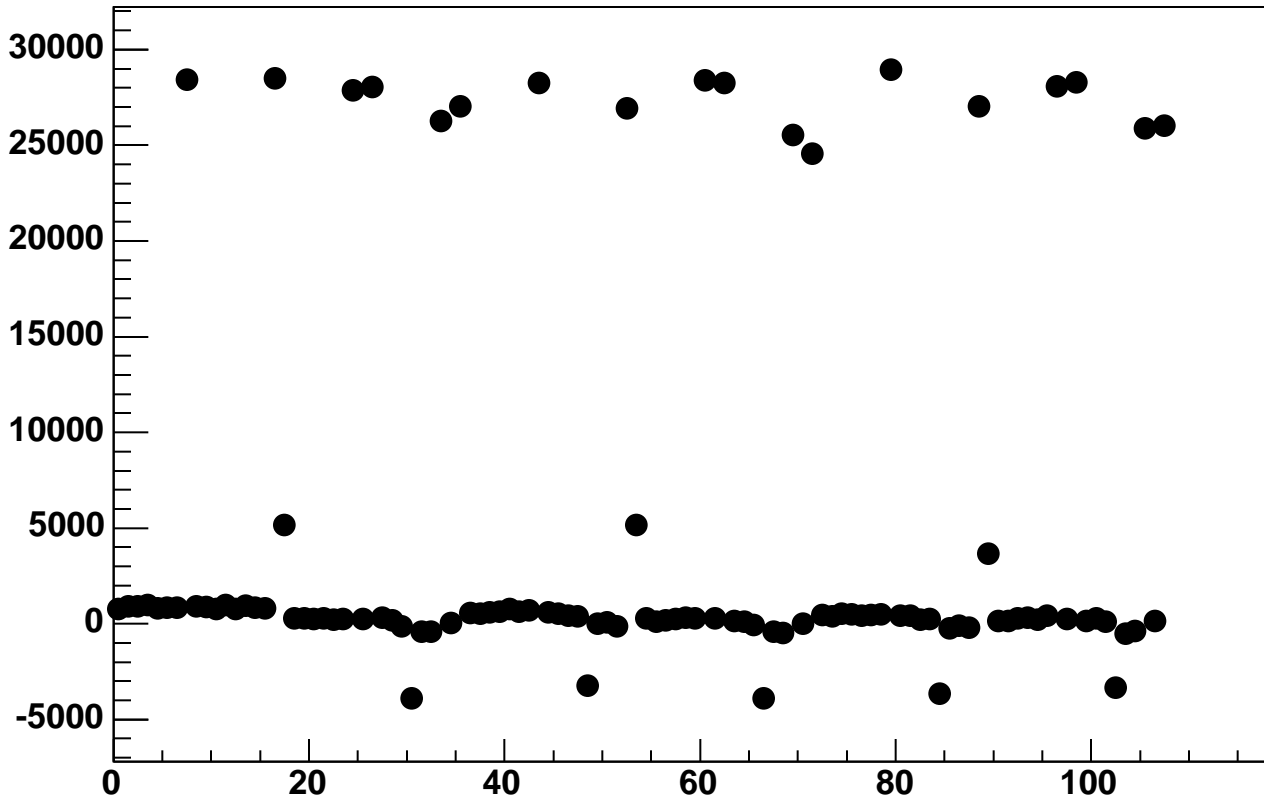
Enable 5, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



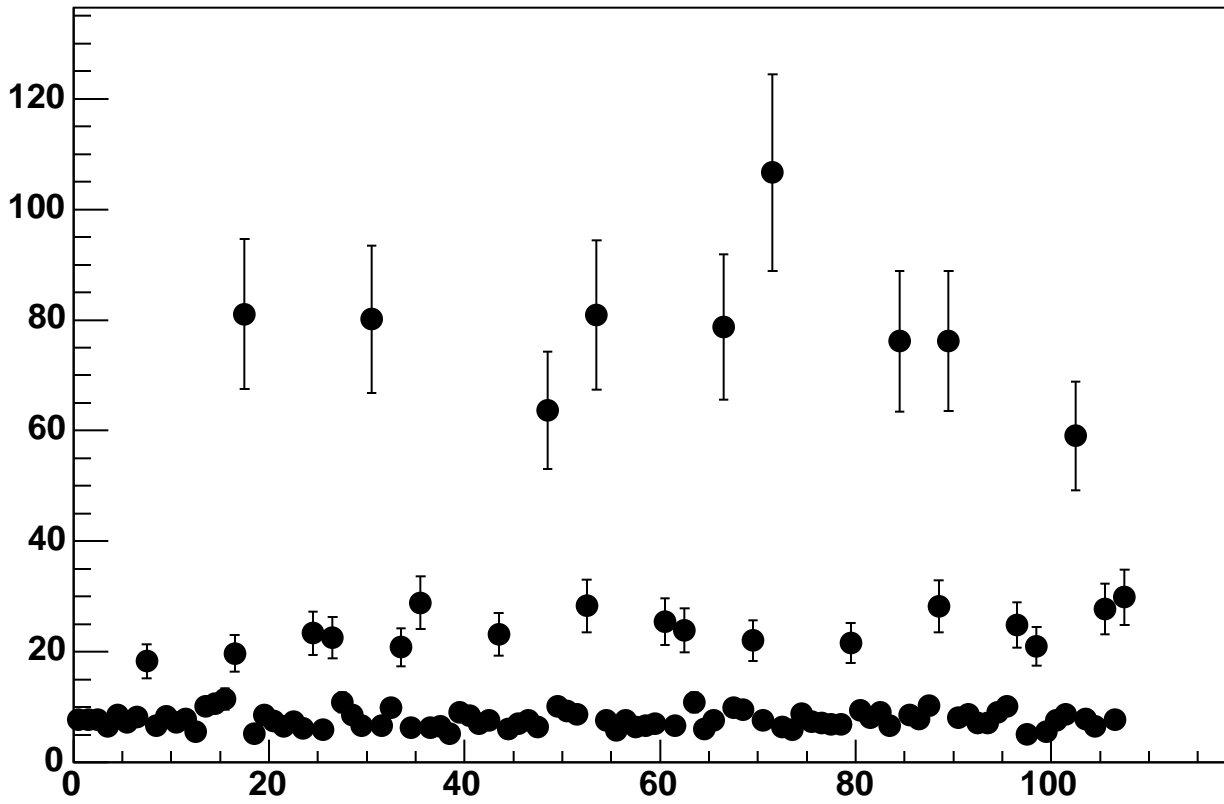
Enable 5, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



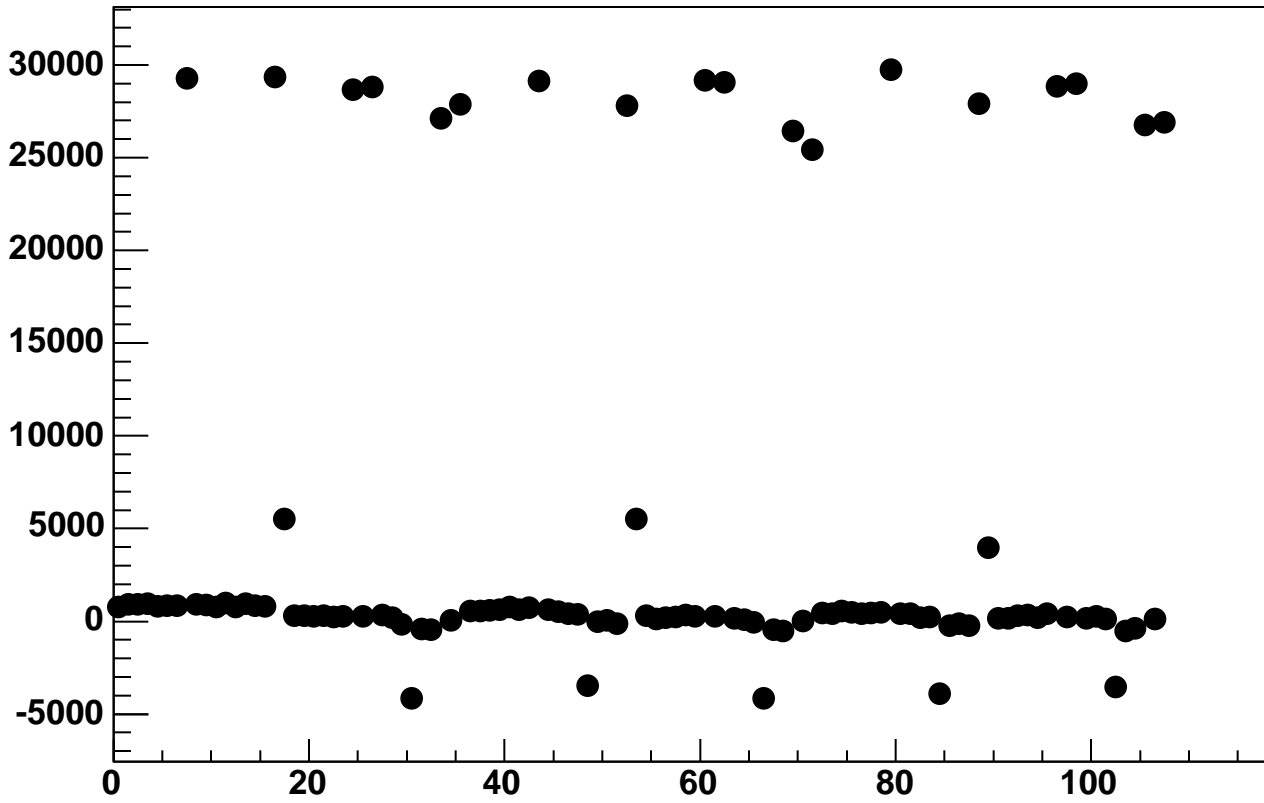
Enable 5, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



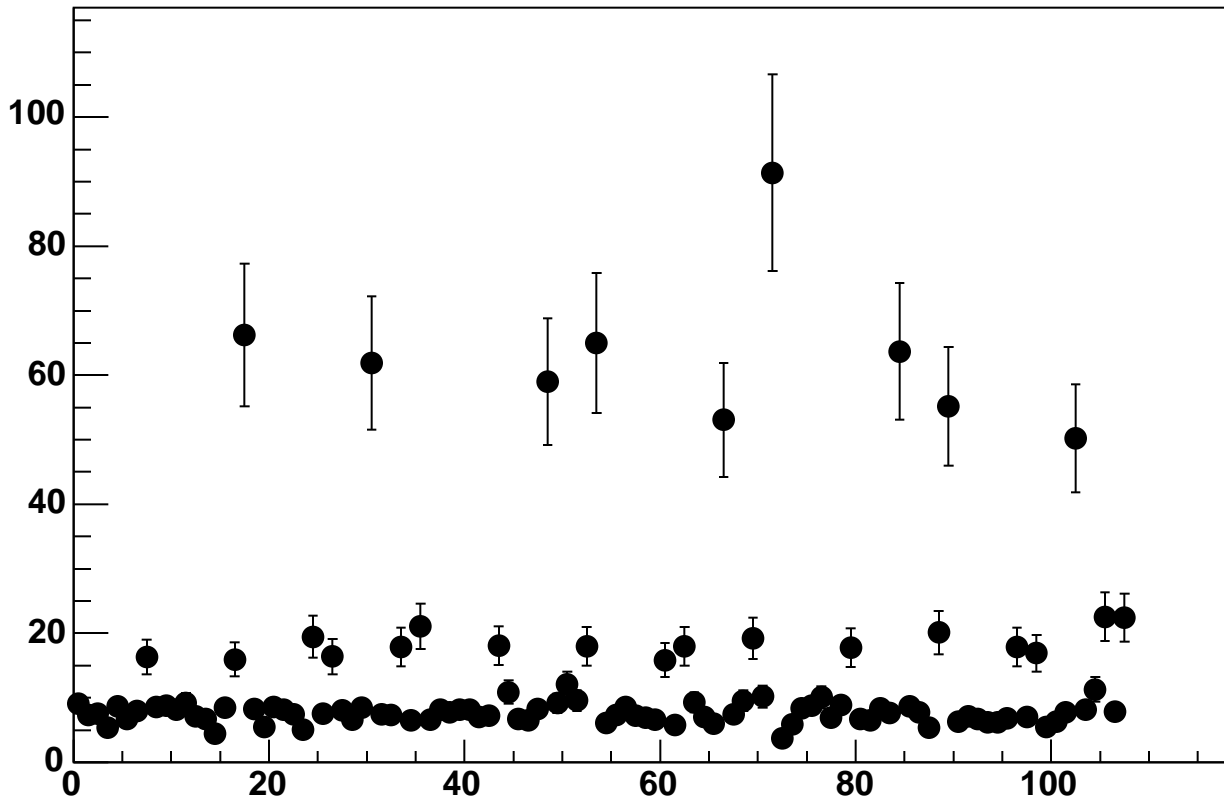
Enable 5, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



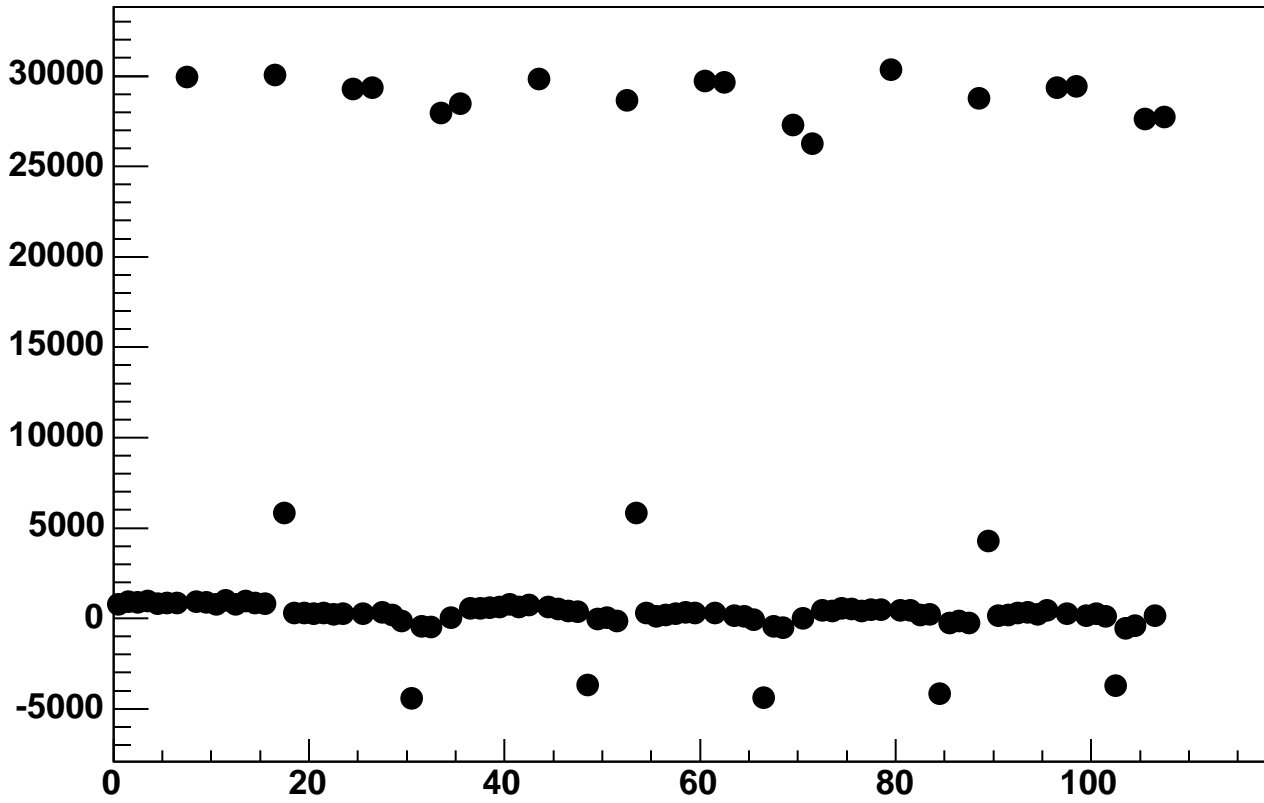
Enable 5, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



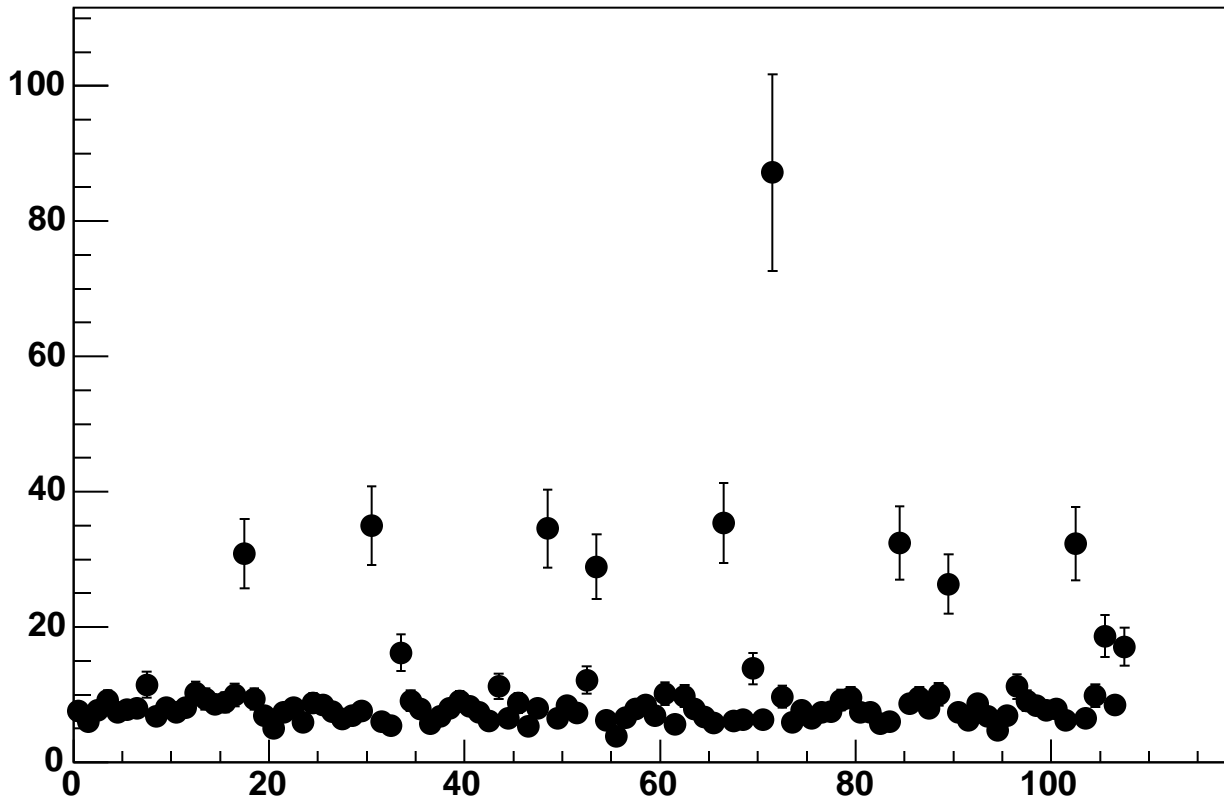
Enable 5, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



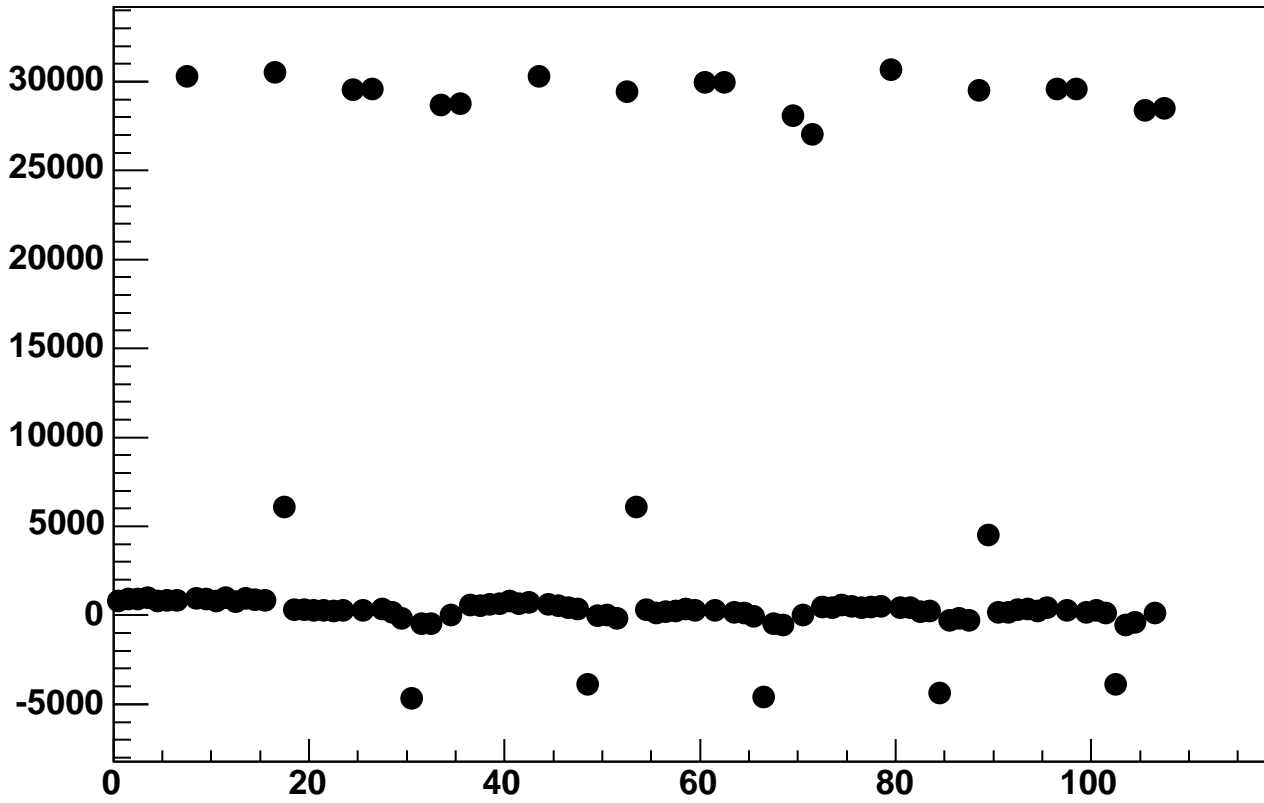
Enable 5, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



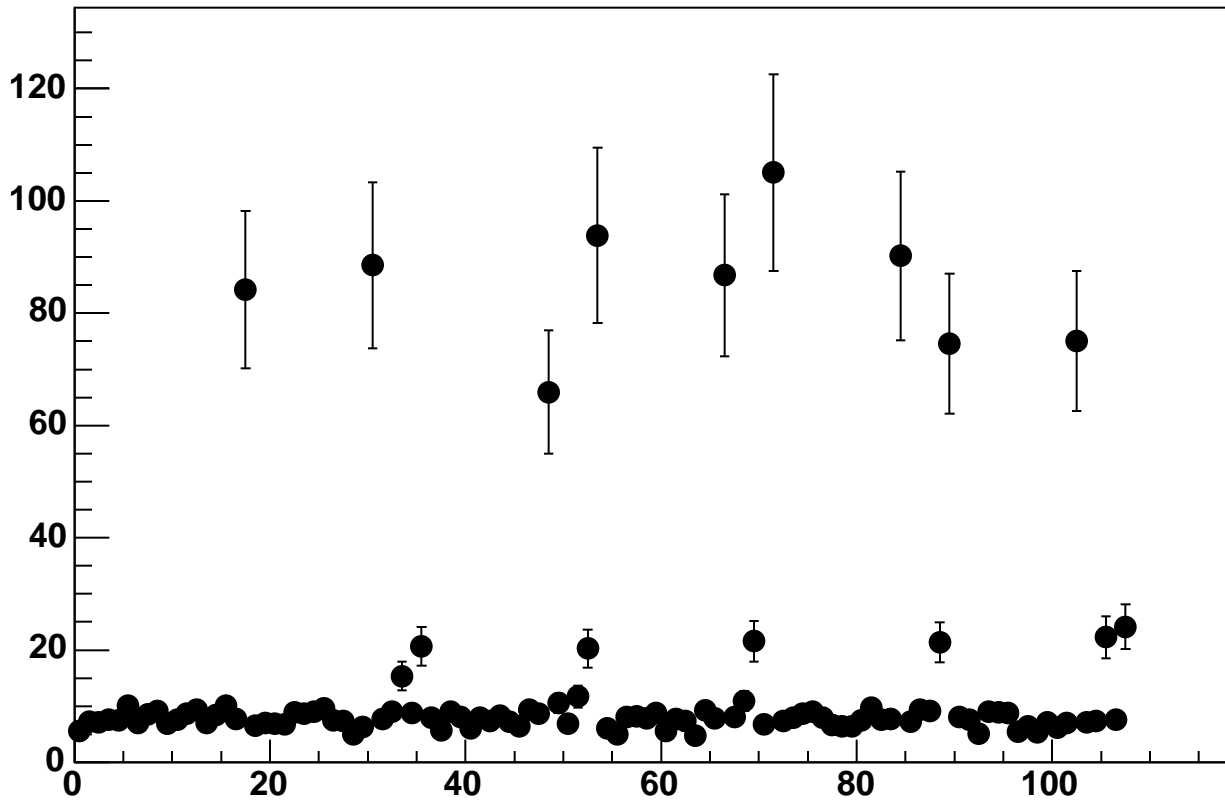
Enable 5, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



Enable 5, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan

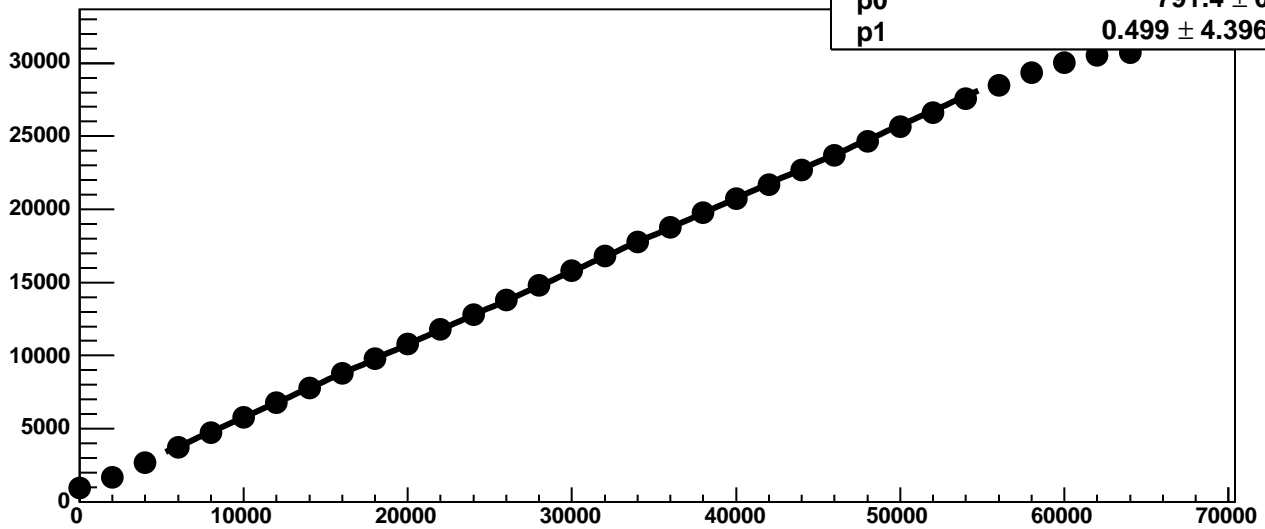


Enable 5, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan

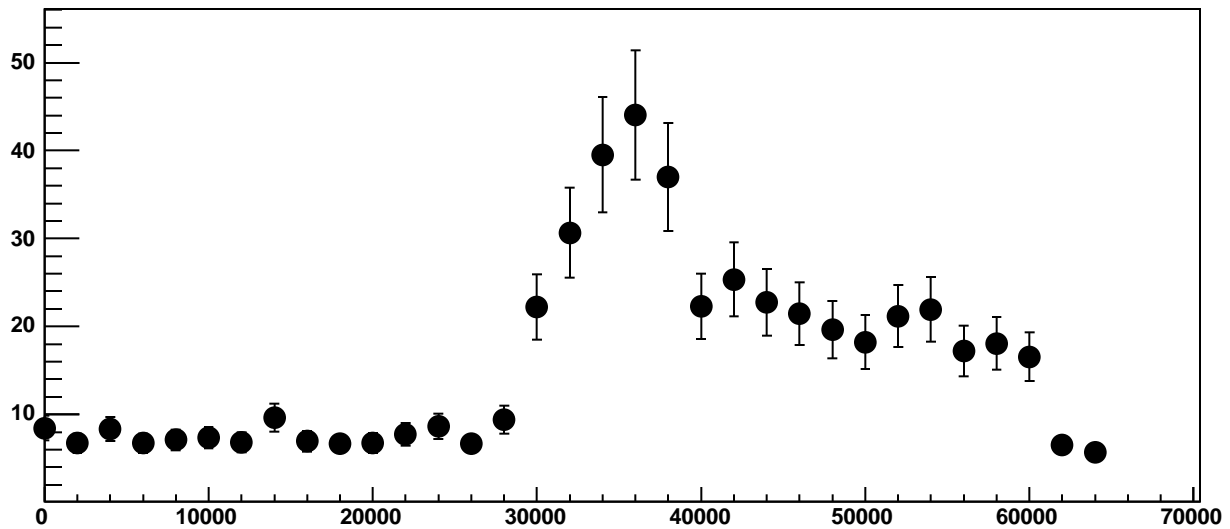


Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC

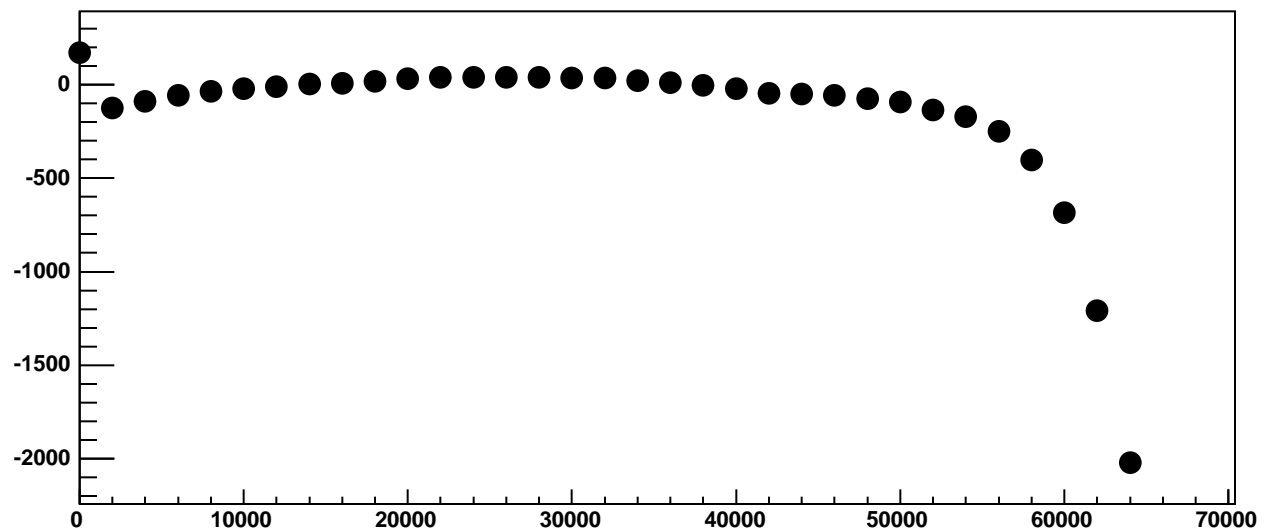
χ^2 / ndf 7547 / 23
p0 791.4 ± 0.956
p1 $0.499 \pm 4.396\text{e-}05$



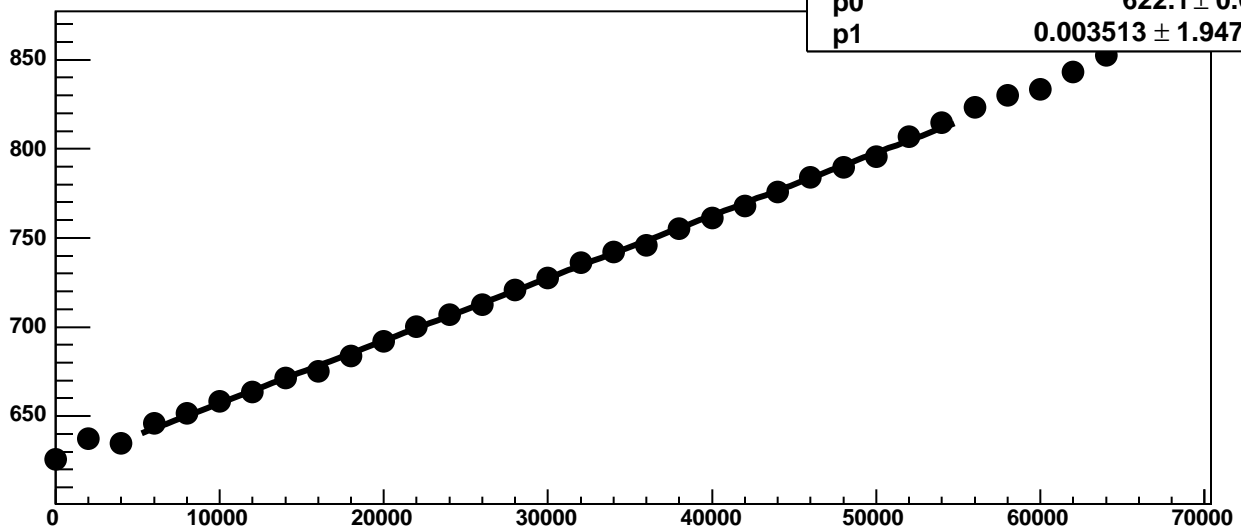
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

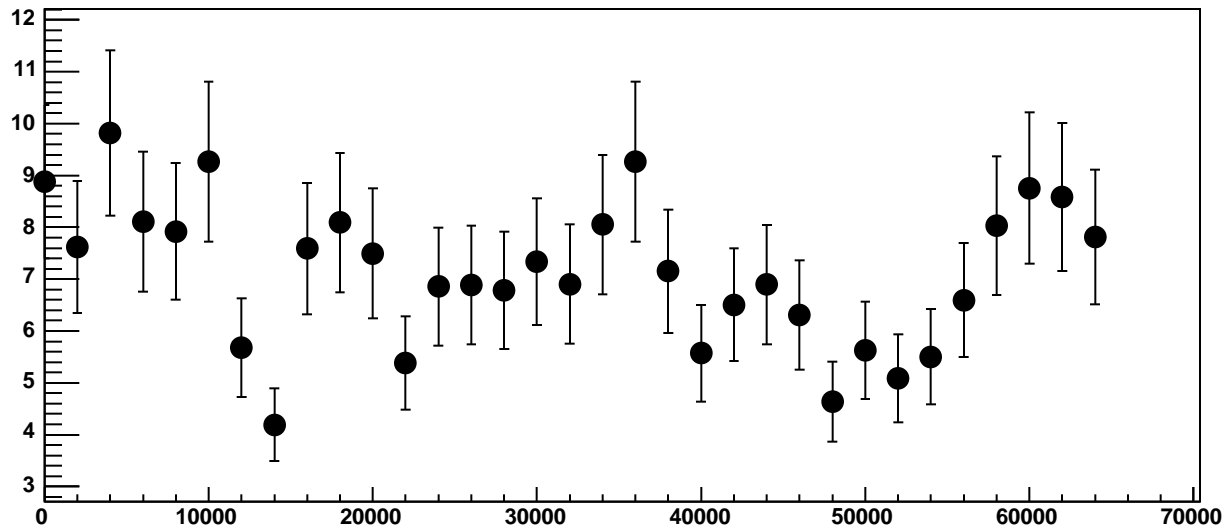


Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

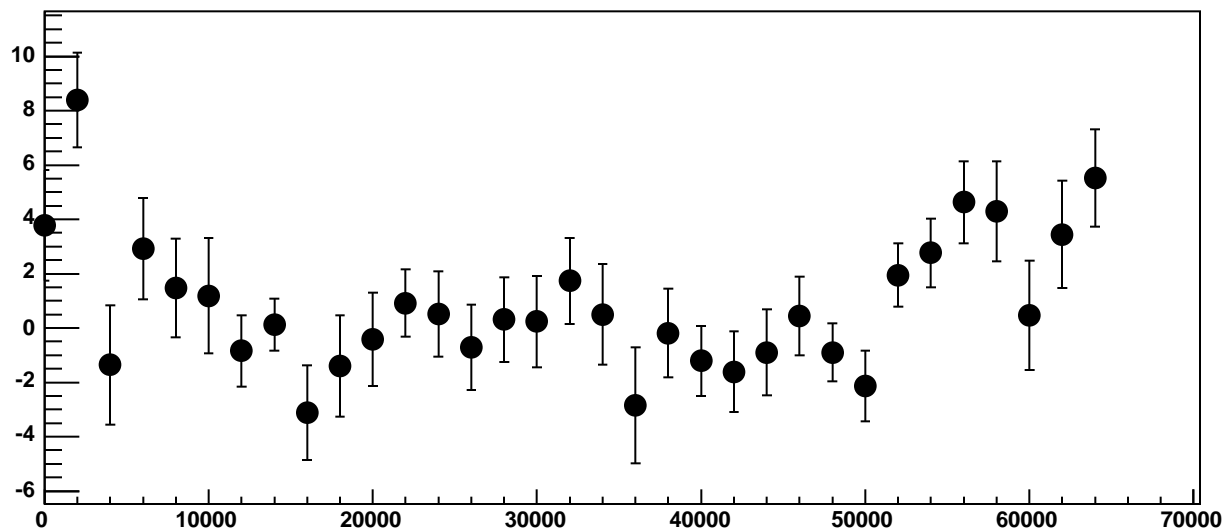


χ^2 / ndf 25.16 / 23
p0 622.1 ± 0.6867
p1 $0.003513 \pm 1.947e-05$

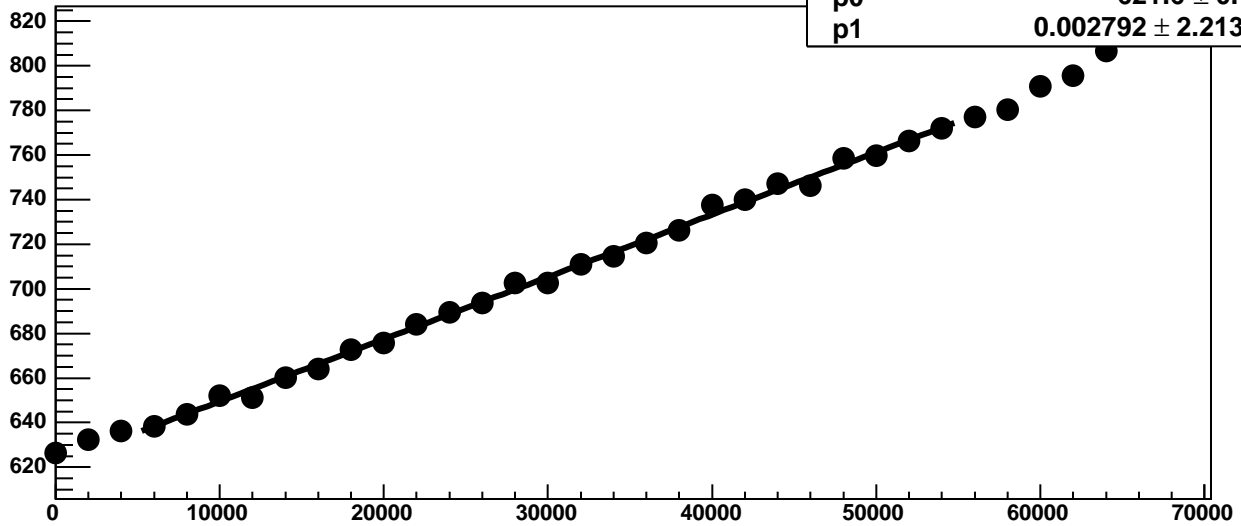
Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



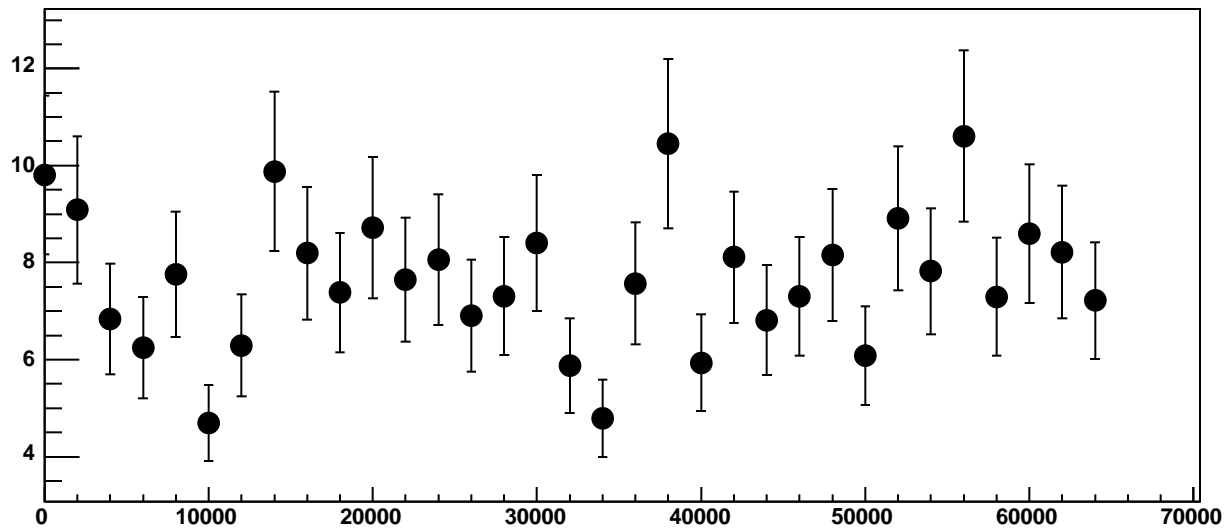
Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



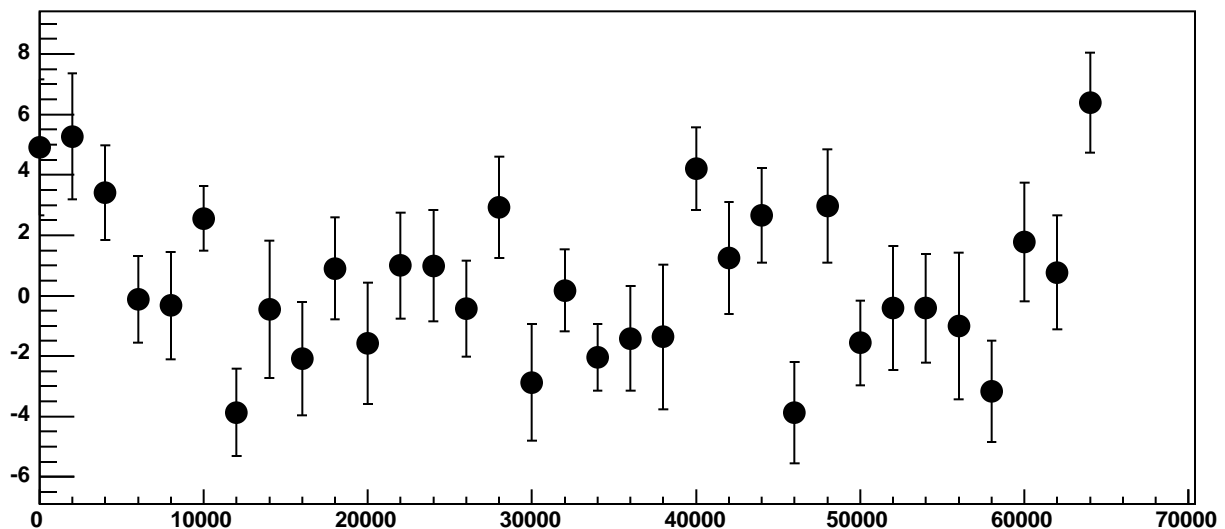
Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



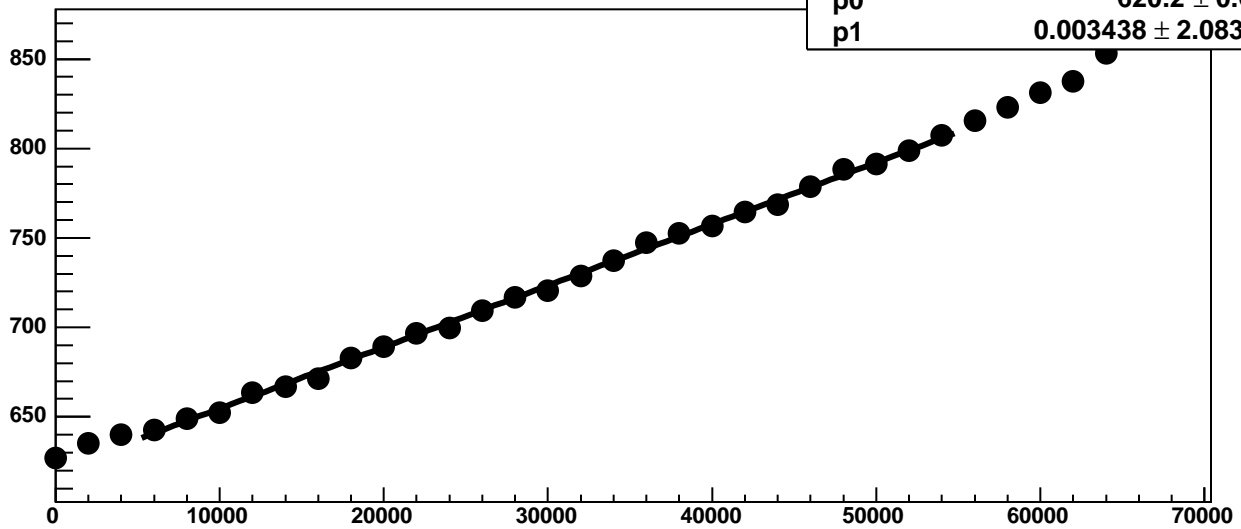
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.66 / 23

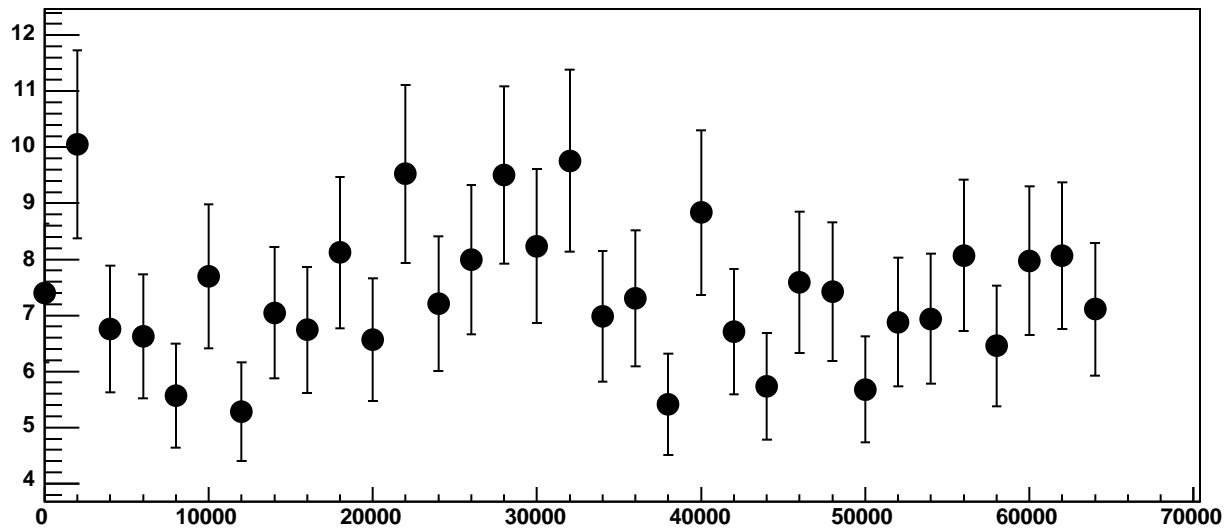
p0

620.2 ± 0.6988

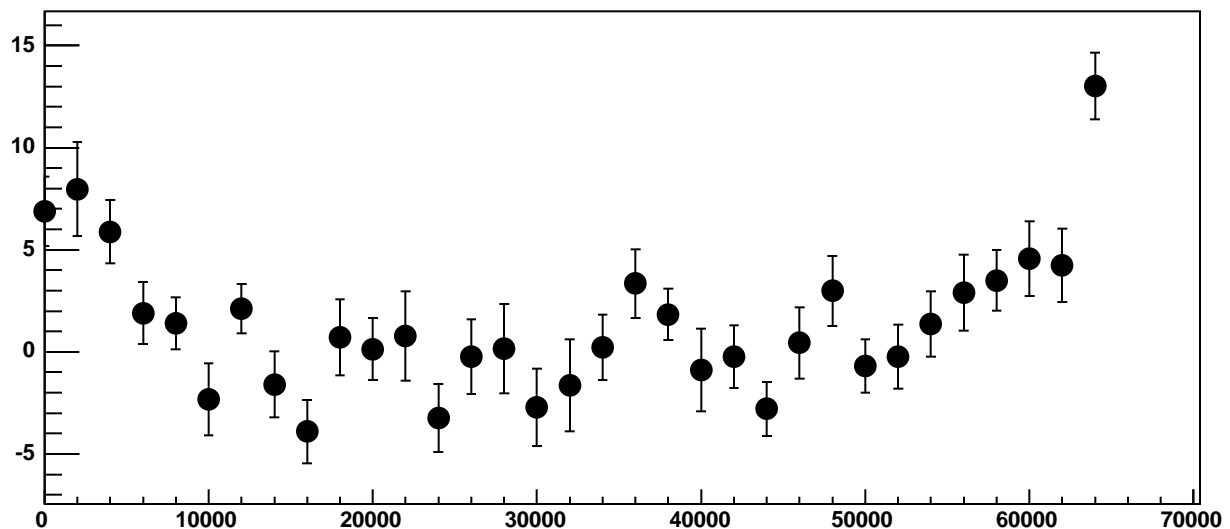
p1

$0.003438 \pm 2.083e-05$

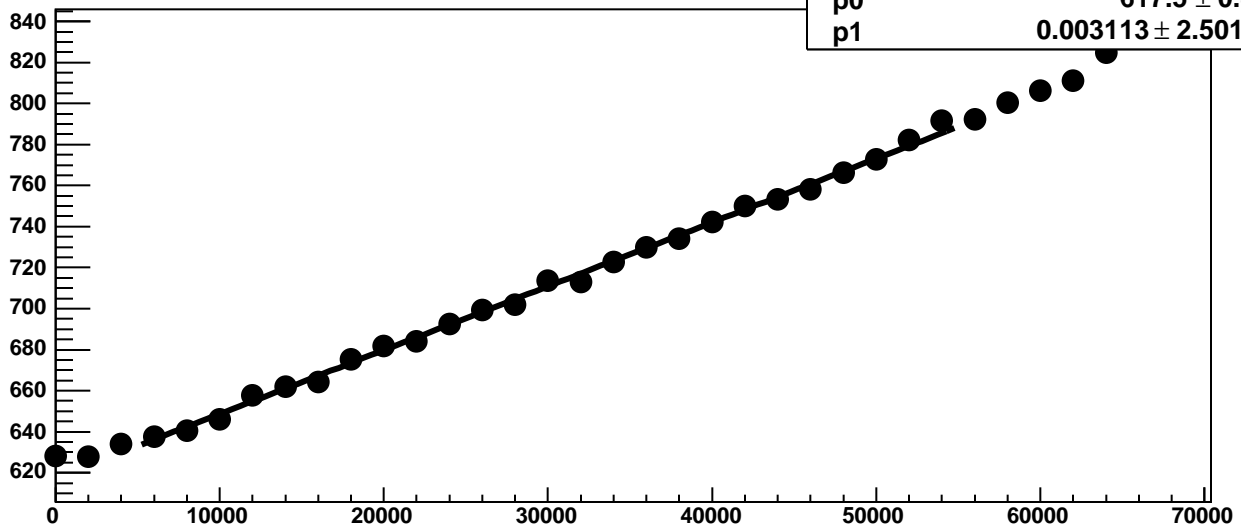
Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.35 / 23

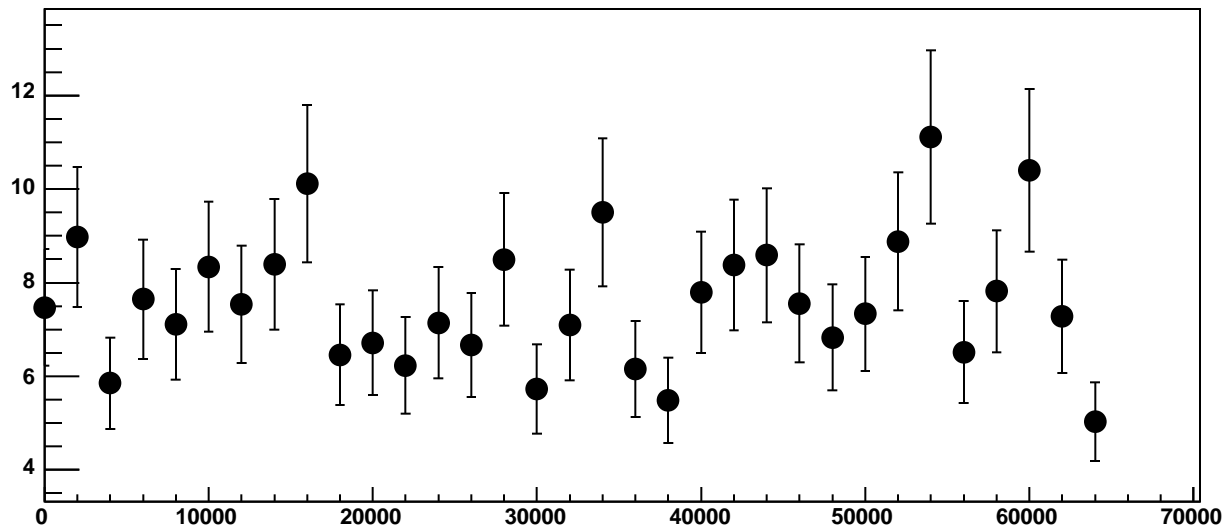
p0

617.5 ± 0.8117

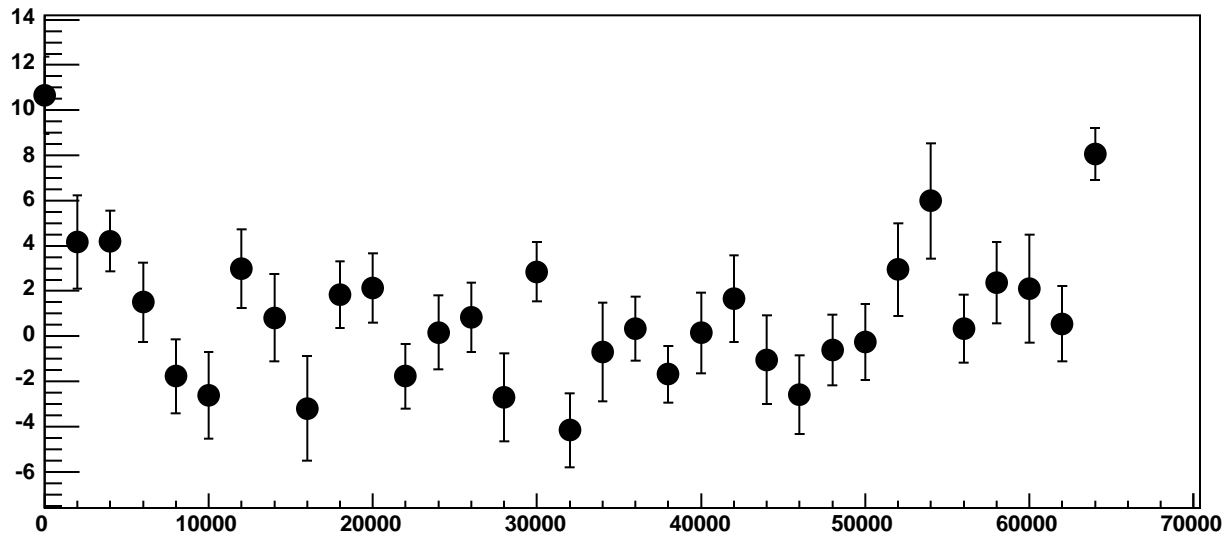
p1

$0.003113 \pm 2.501e-05$

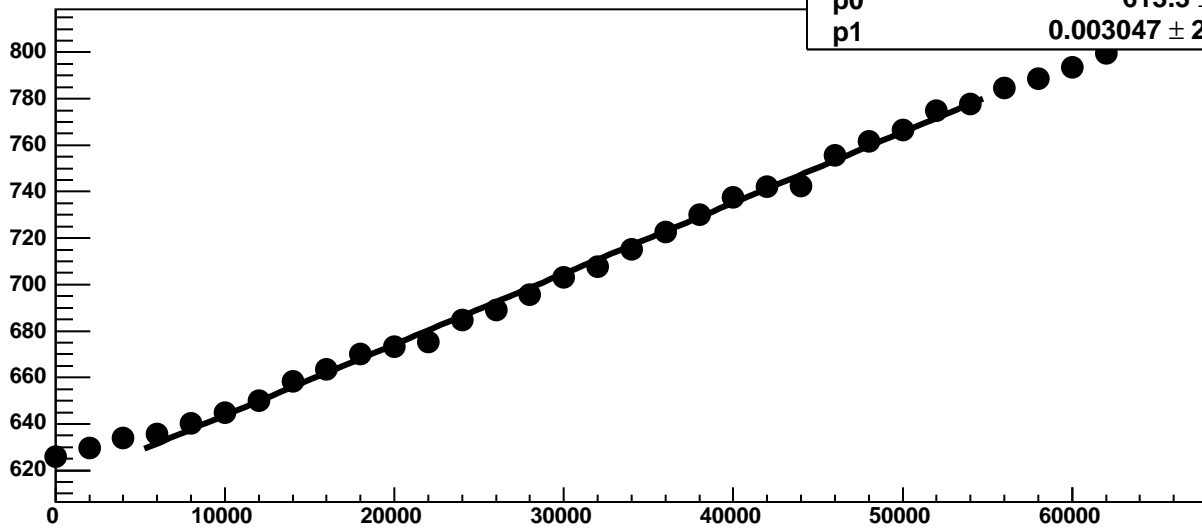
Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

50.69 / 23

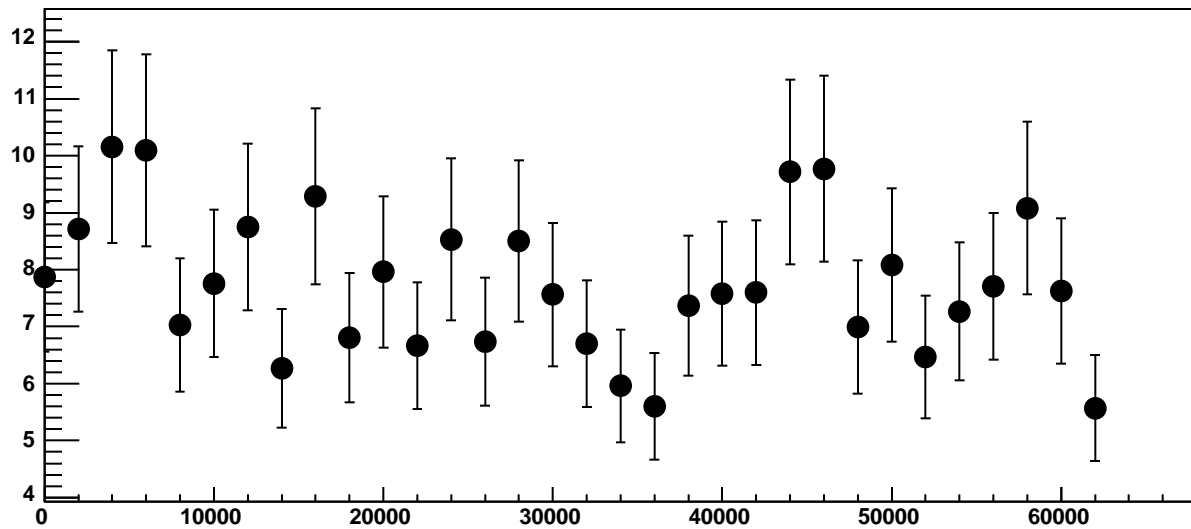
p0

613.3 ± 0.8151

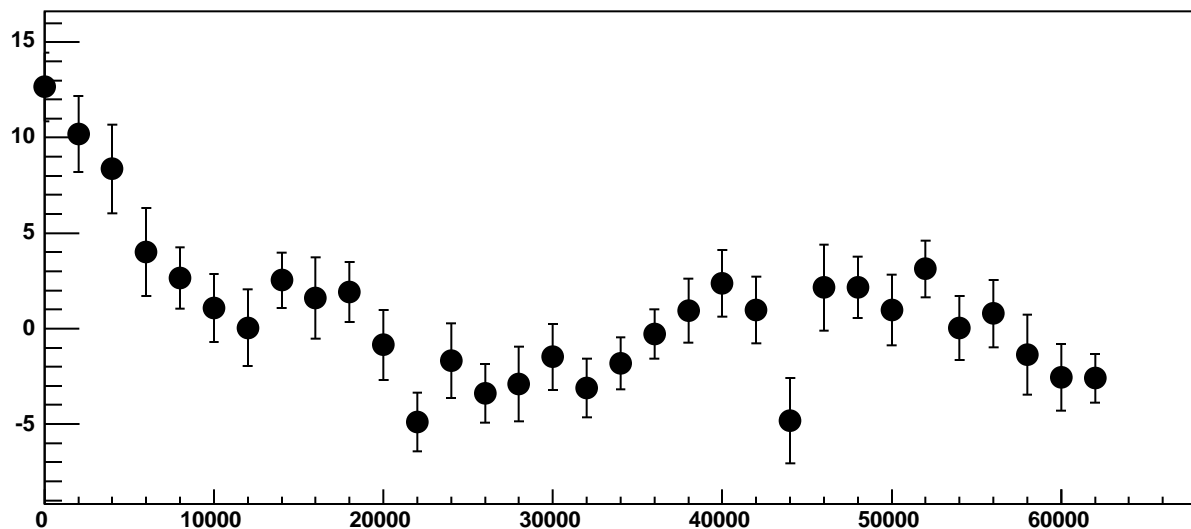
p1

$0.003047 \pm 2.43e-05$

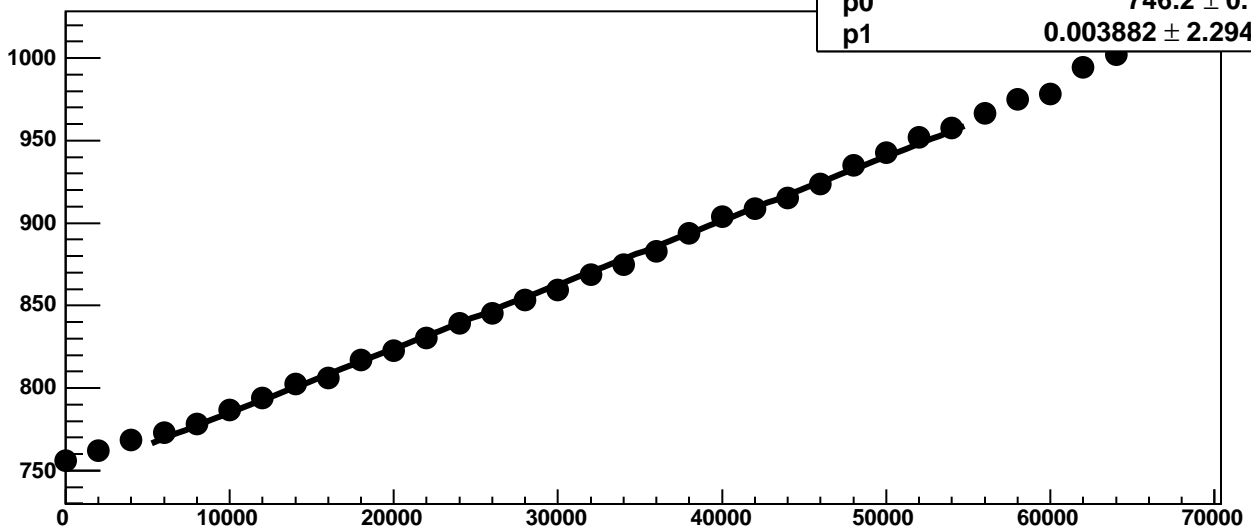
Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

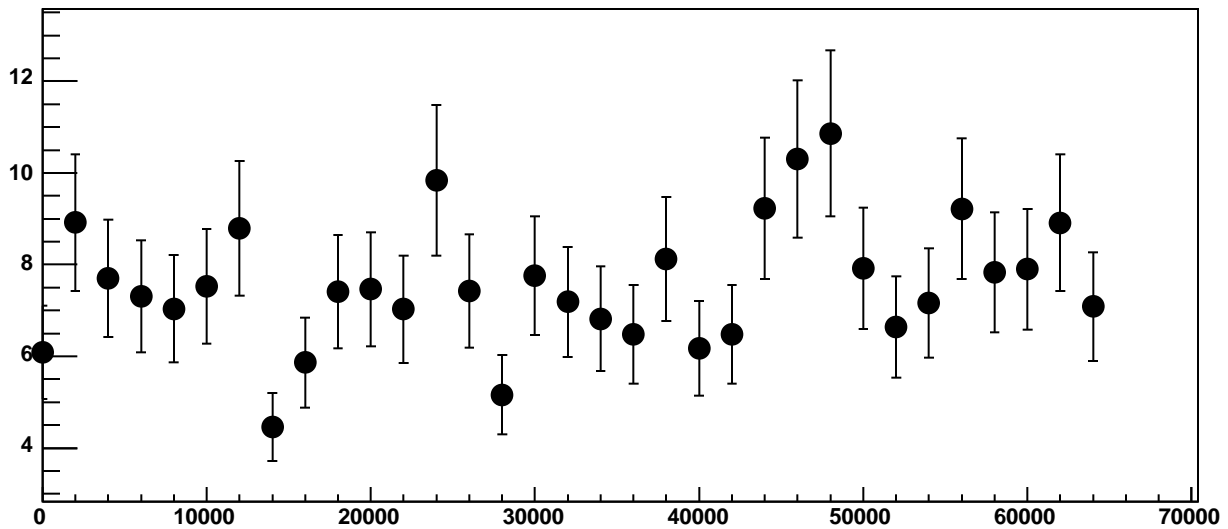


Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

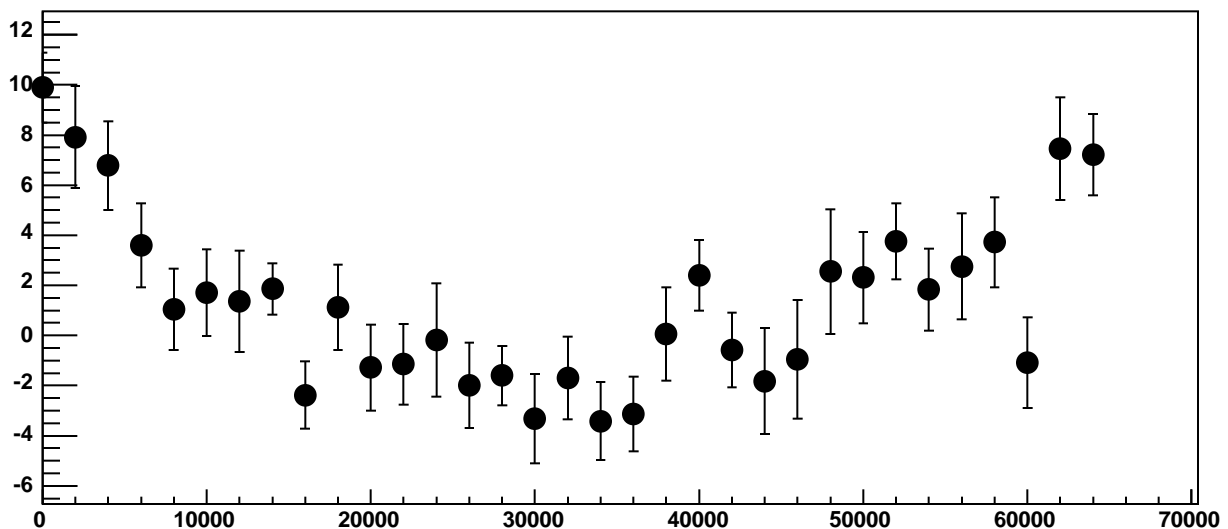


χ^2 / ndf 45.19 / 23
p0 746.2 \pm 0.7286
p1 0.003882 \pm 2.294e-05

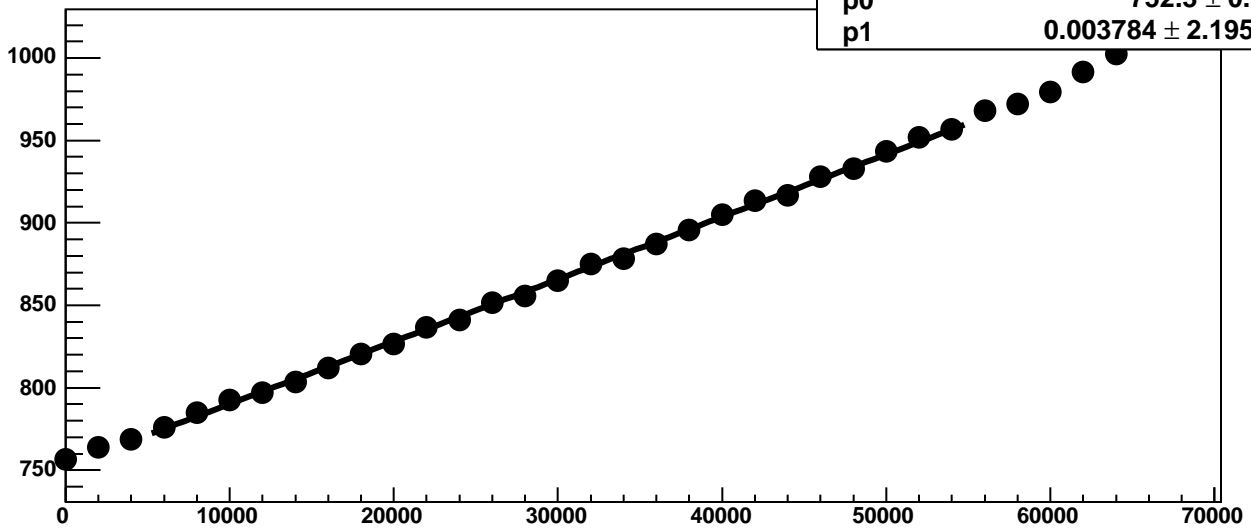
Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

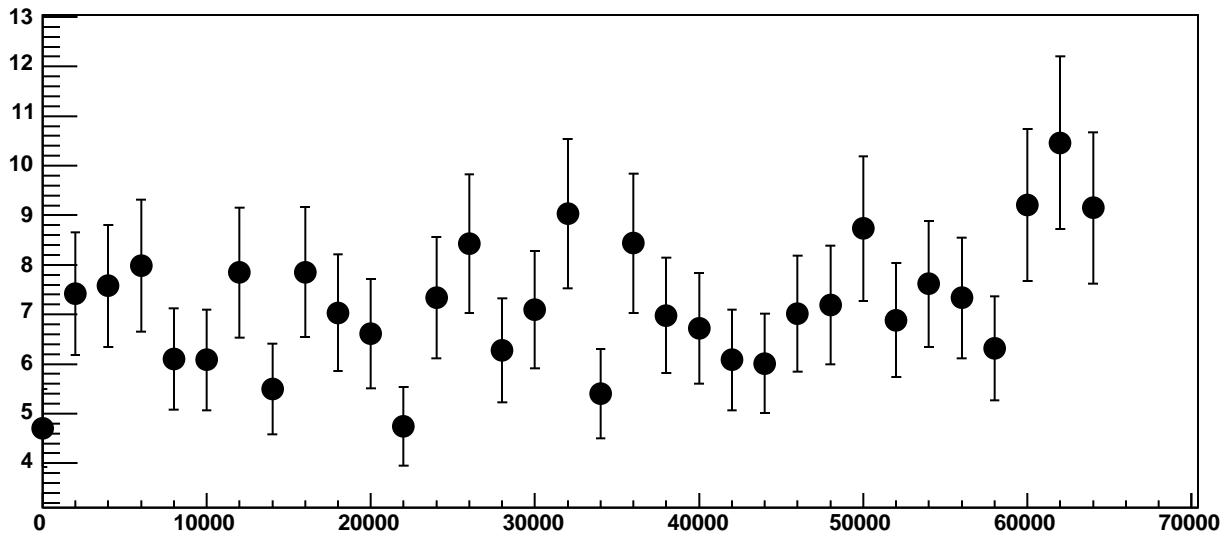


Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

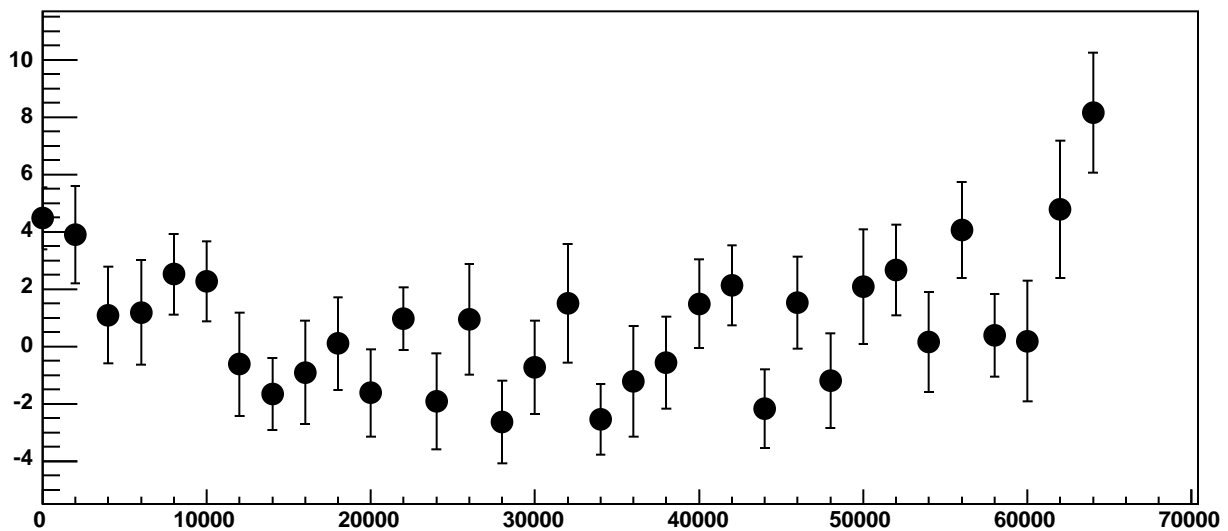


χ^2 / ndf 31.77 / 23
p0 752.3 ± 0.7117
p1 0.003784 ± 2.195e-05

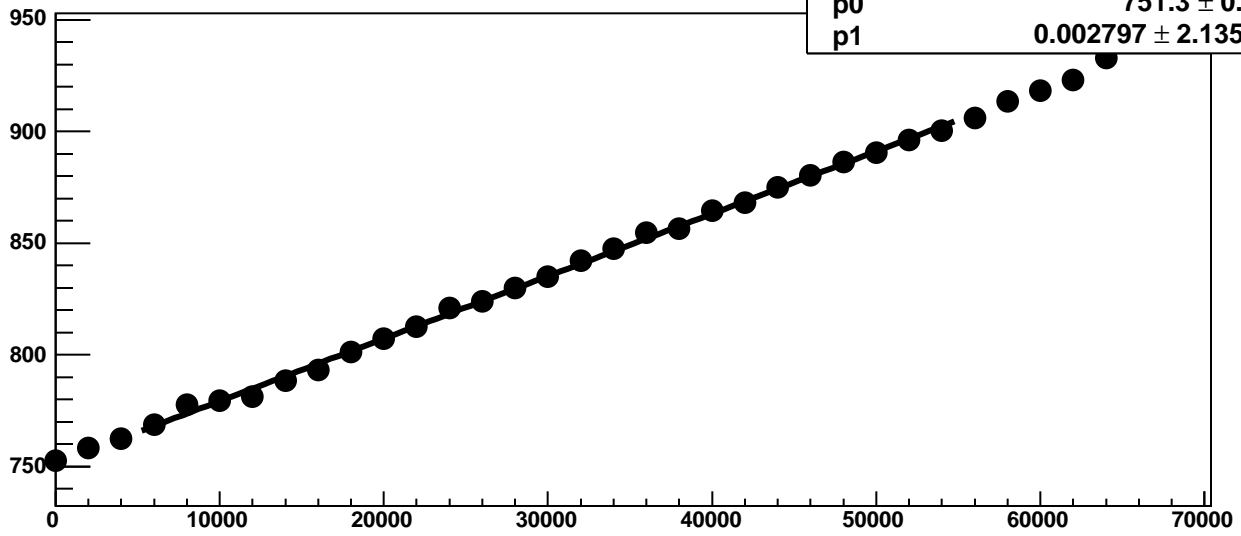
Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



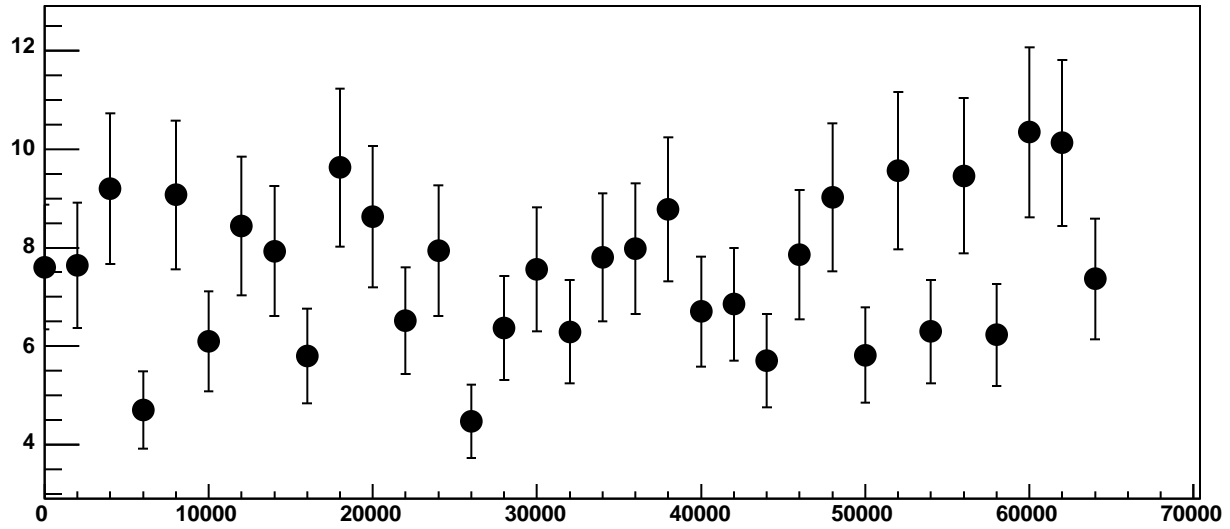
Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



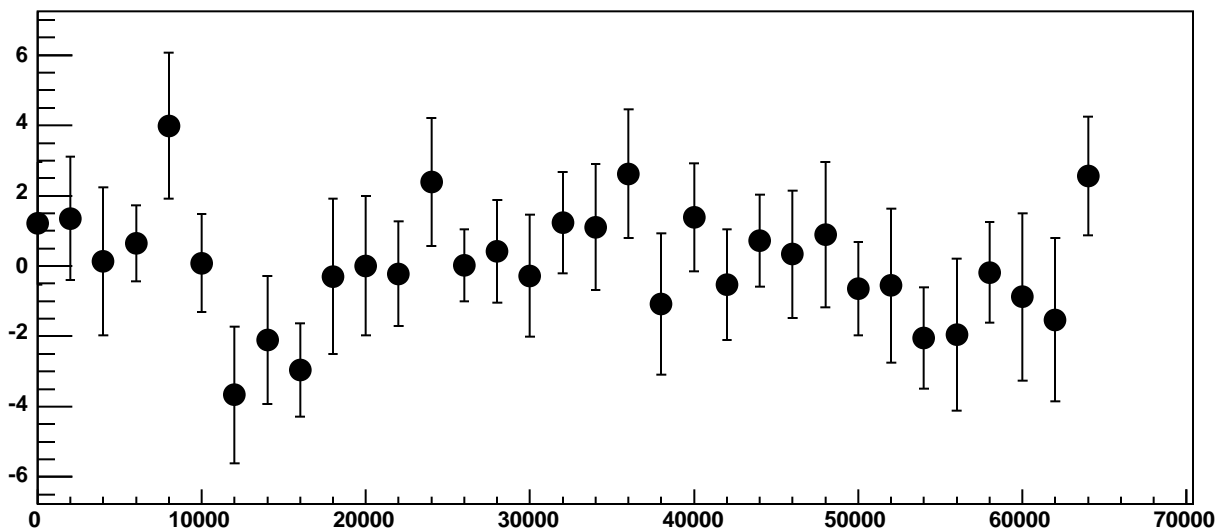
Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

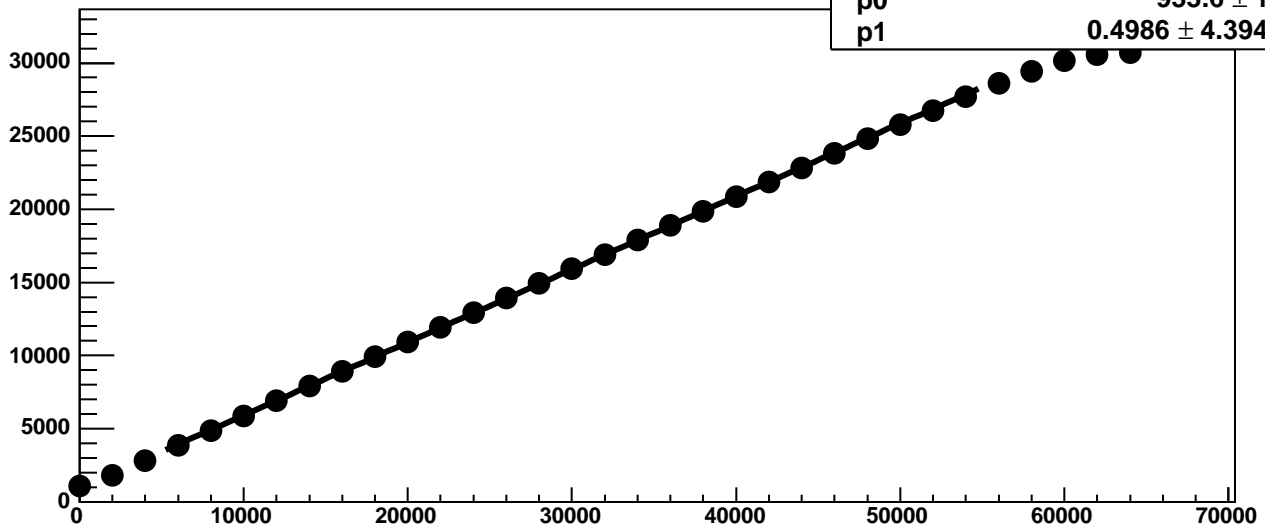


Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

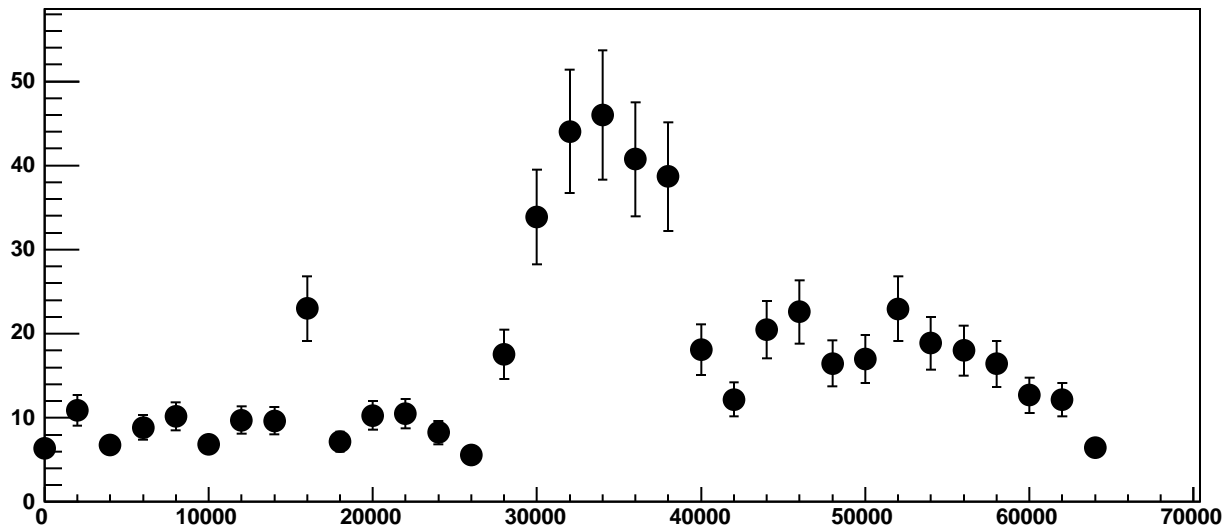


Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

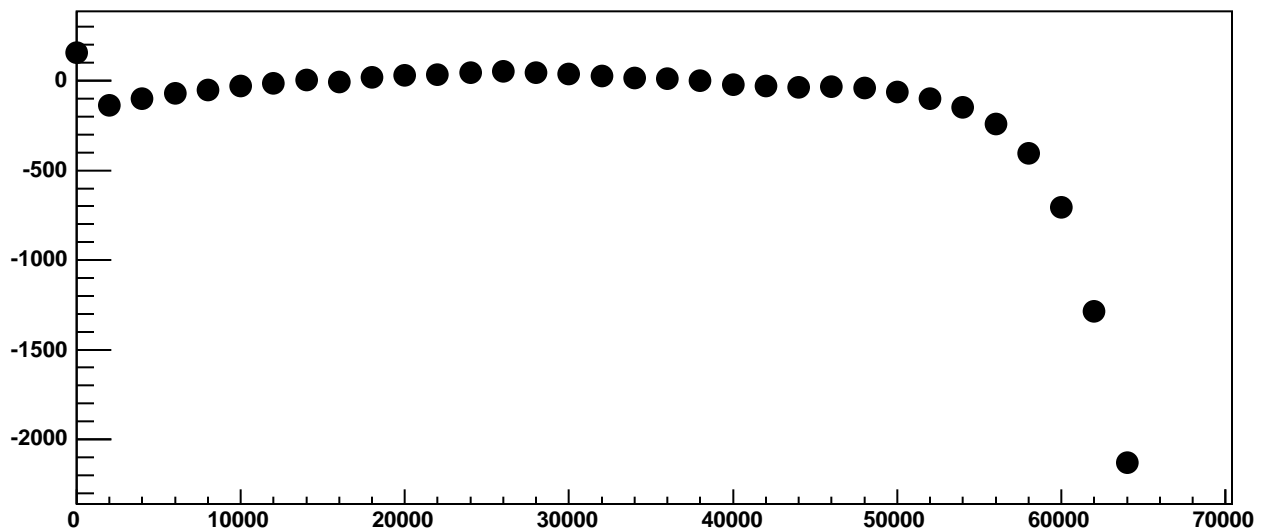
χ^2 / ndf 7072 / 23
p0 933.6 ± 1.095
p1 $0.4986 \pm 4.394e-05$



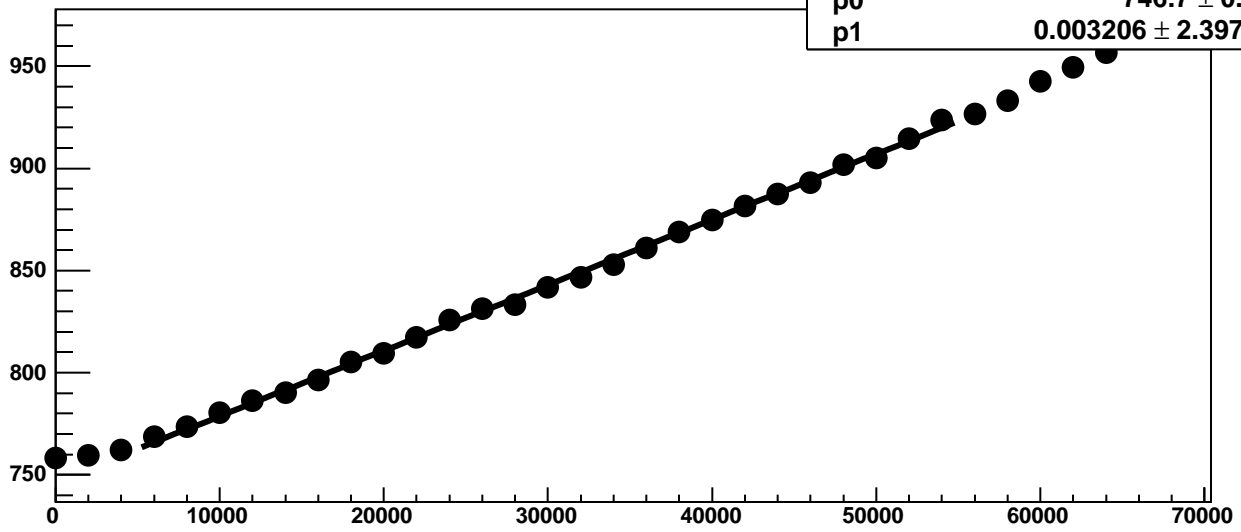
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.35 / 23

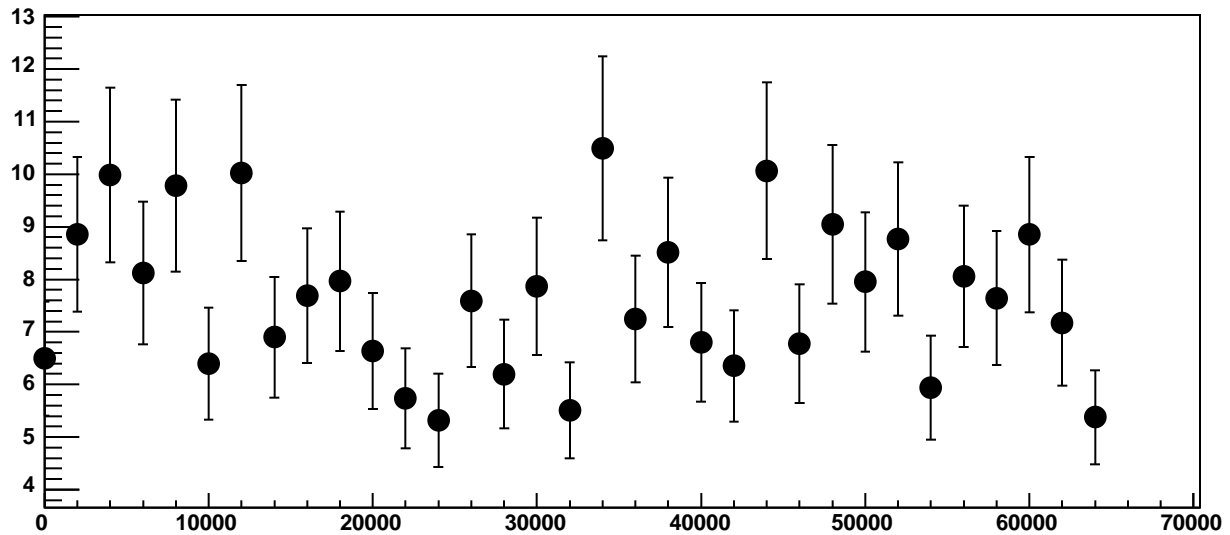
p0

746.7 ± 0.7911

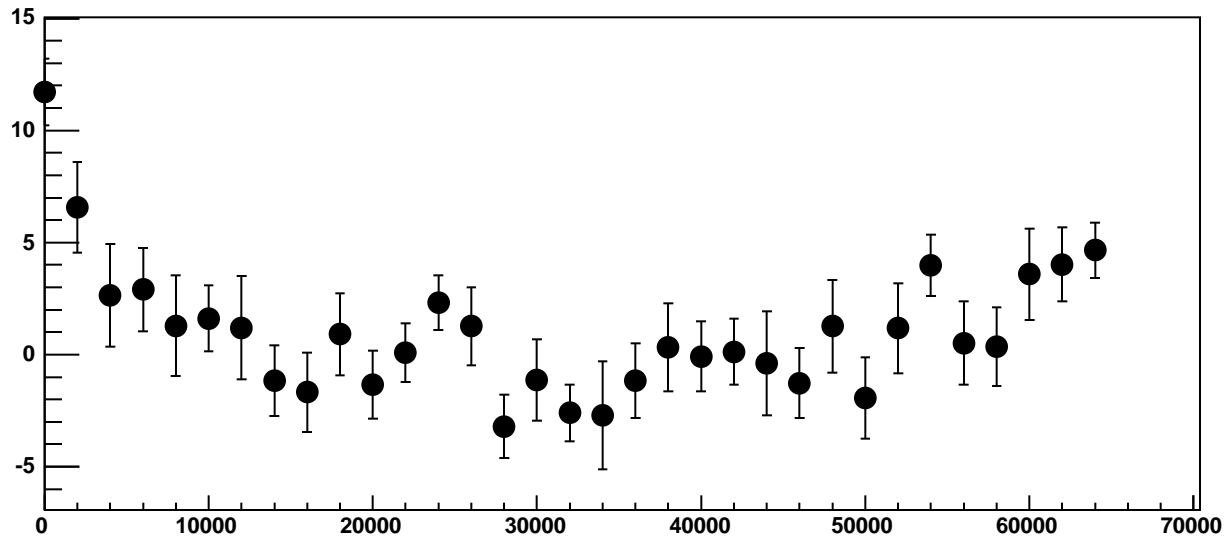
p1

$0.003206 \pm 2.397\text{e-}05$

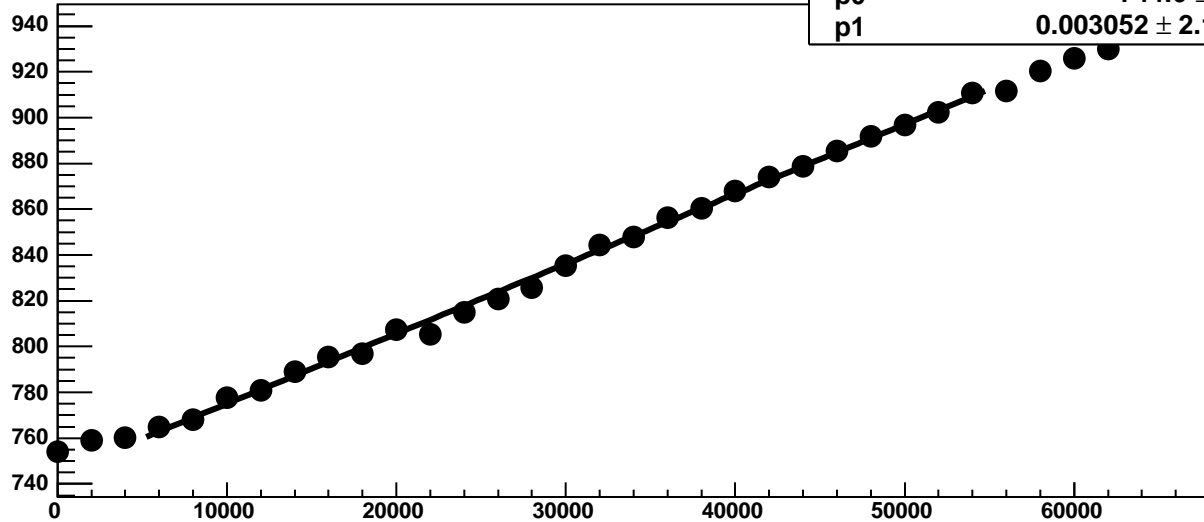
Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.3 / 23

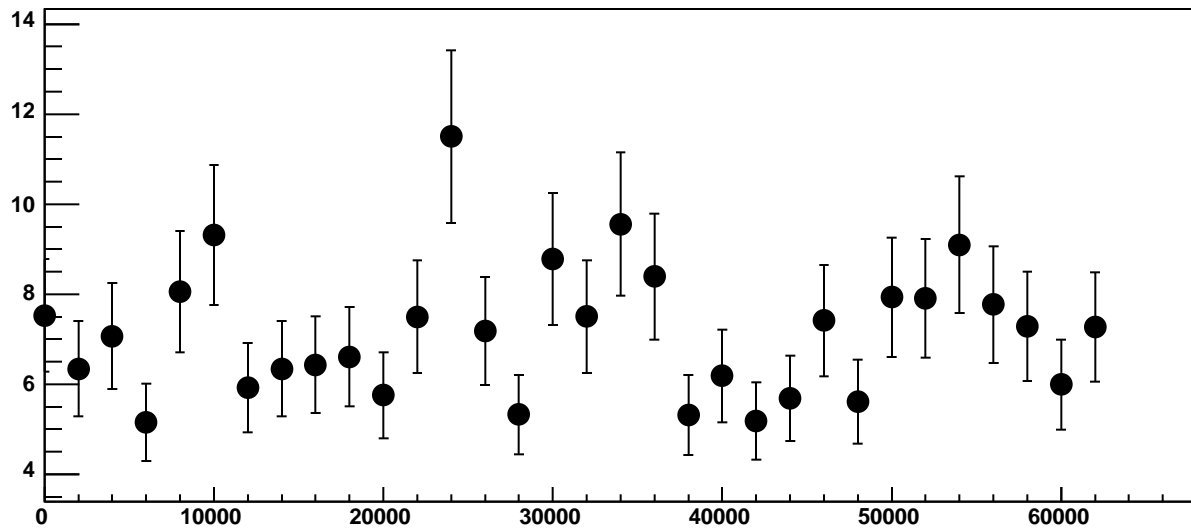
p0

744.6 ± 0.7037

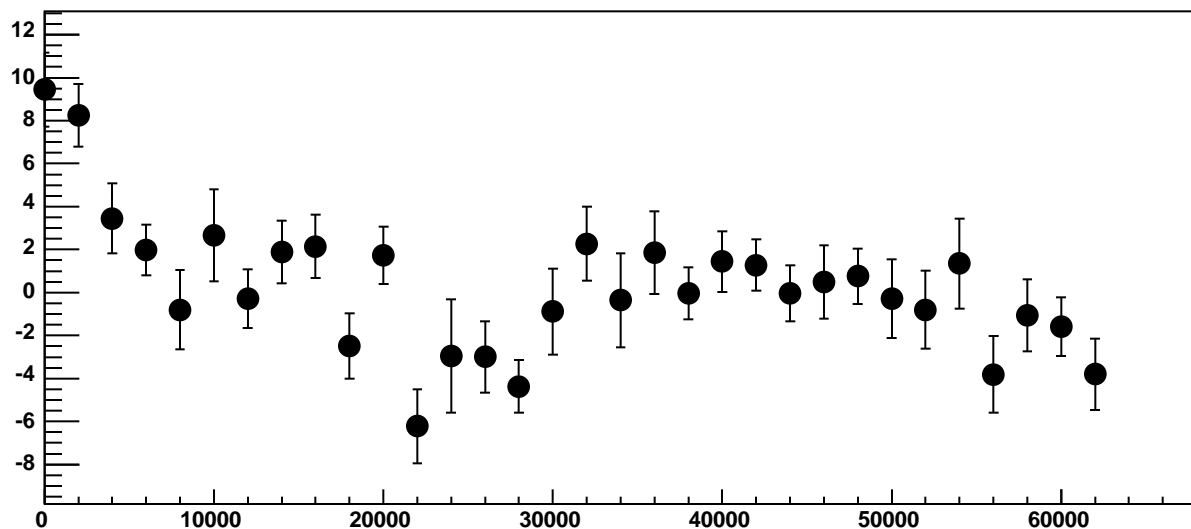
p1

$0.003052 \pm 2.138e-05$

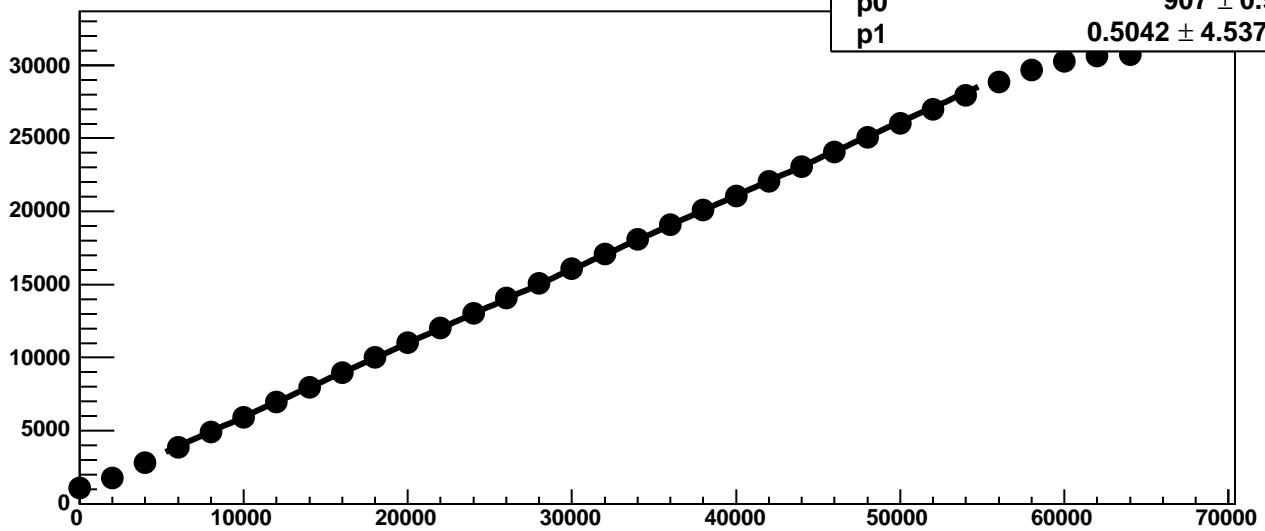
Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



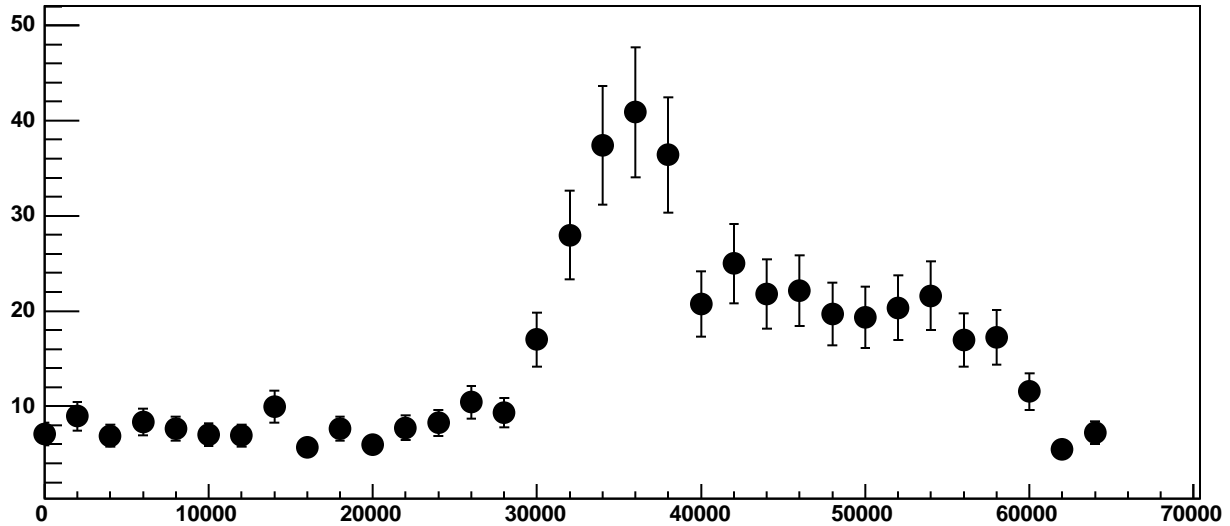
Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



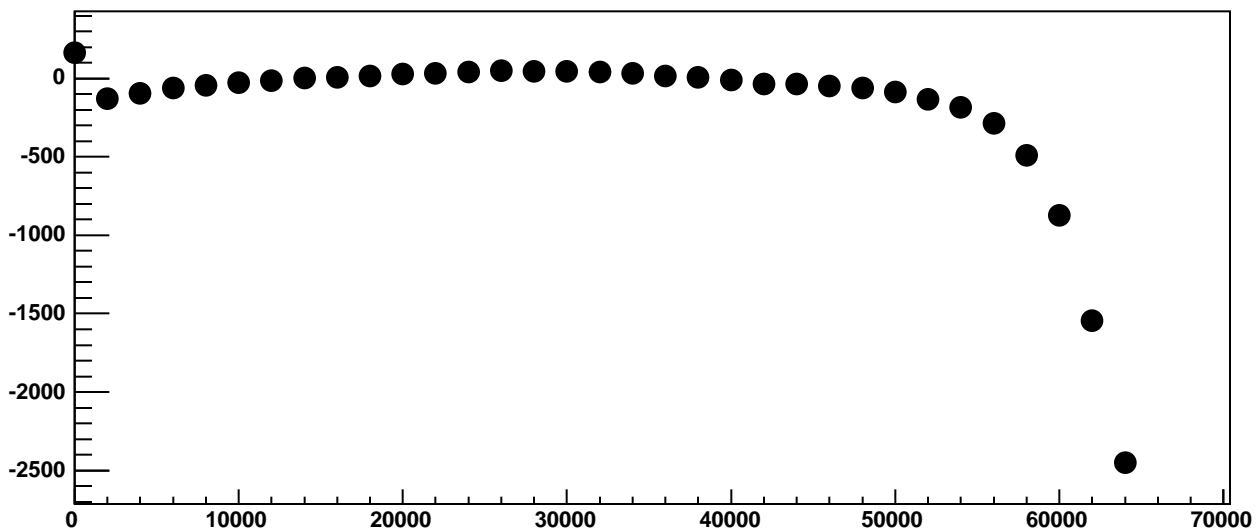
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



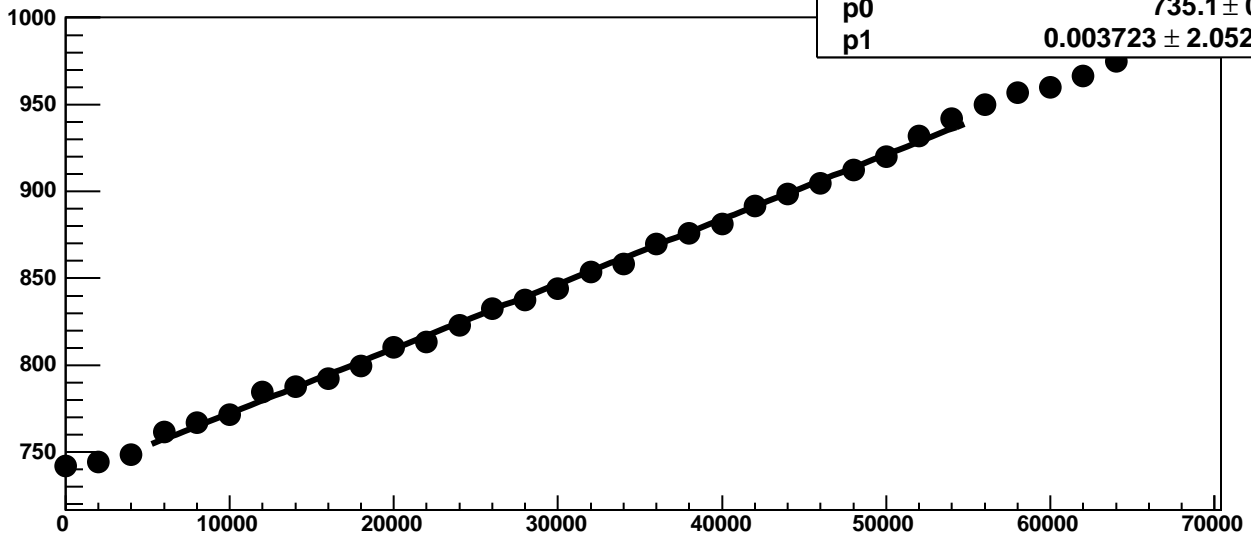
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

63.95 / 23

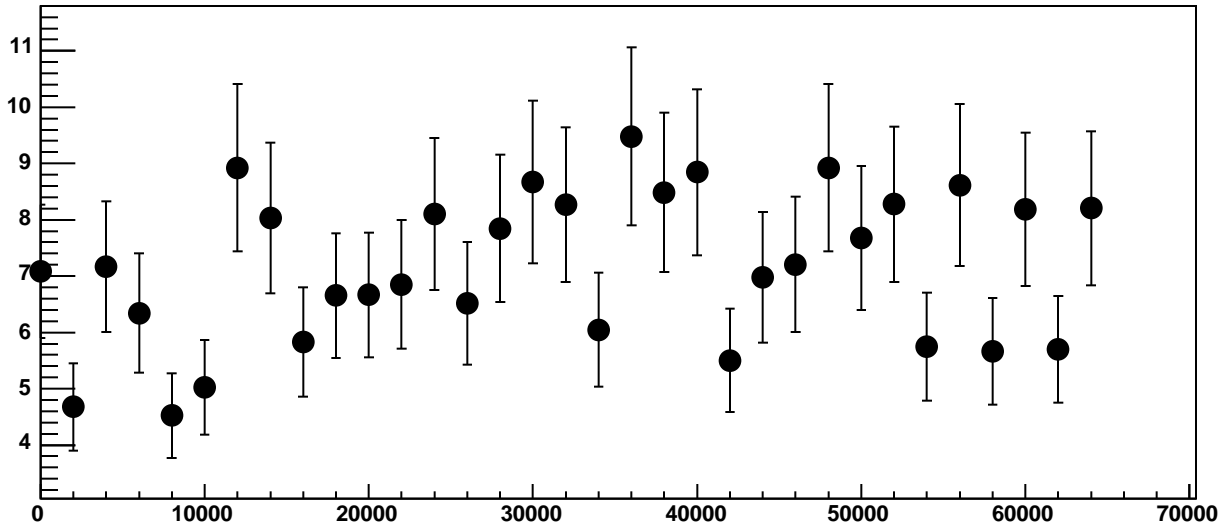
p0

735.1 ± 0.651

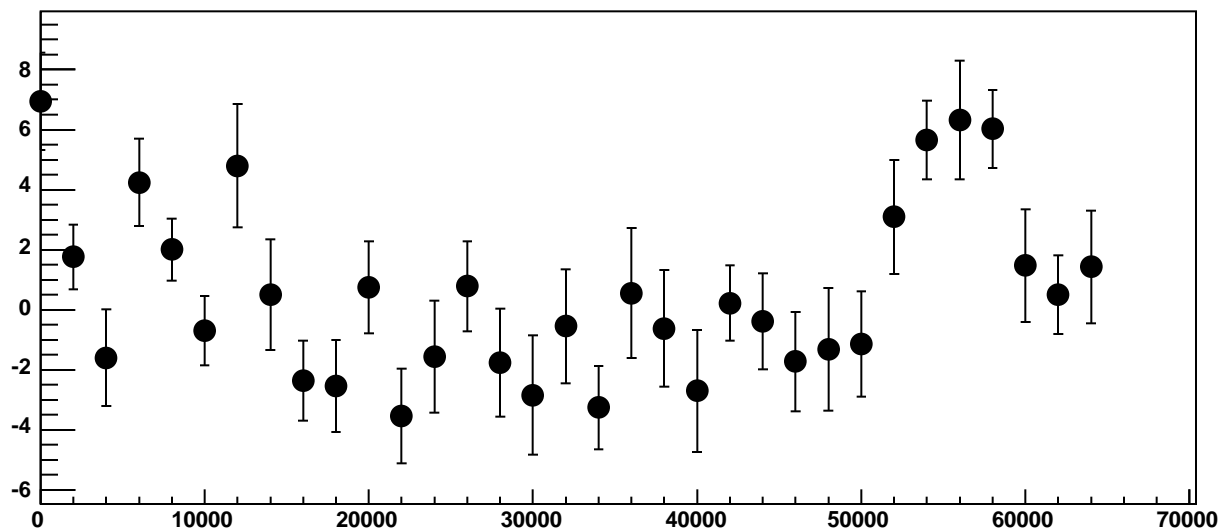
p1

$0.003723 \pm 2.052e-05$

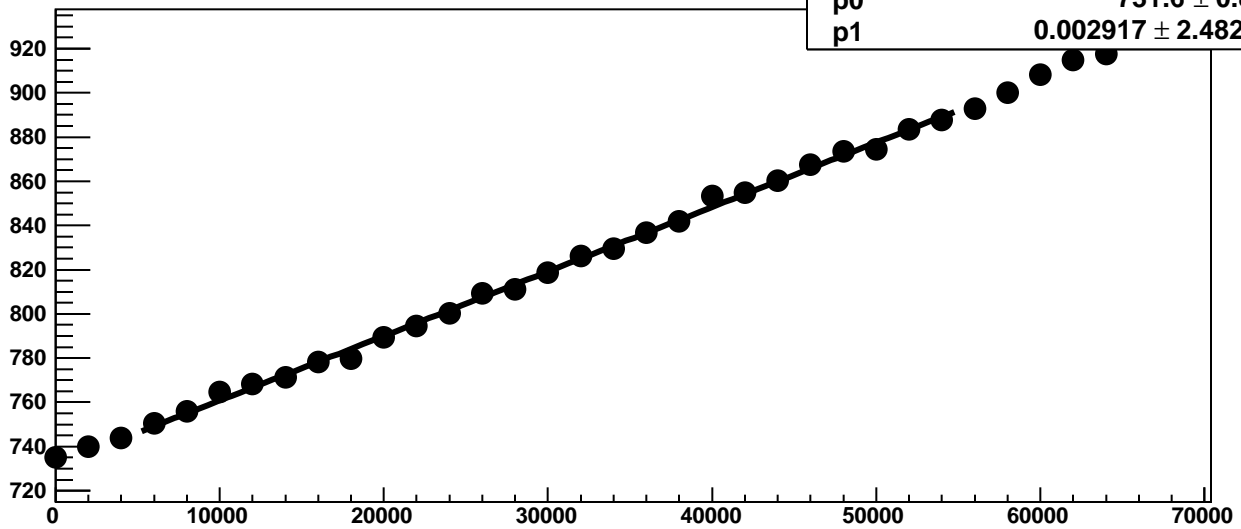
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

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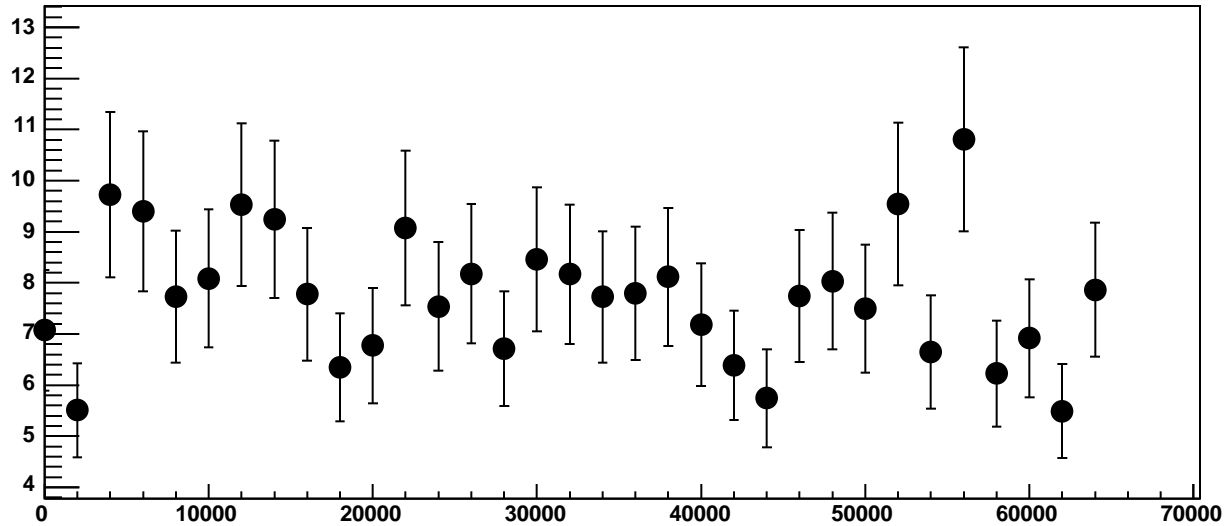
p0

731.6 ± 0.8504

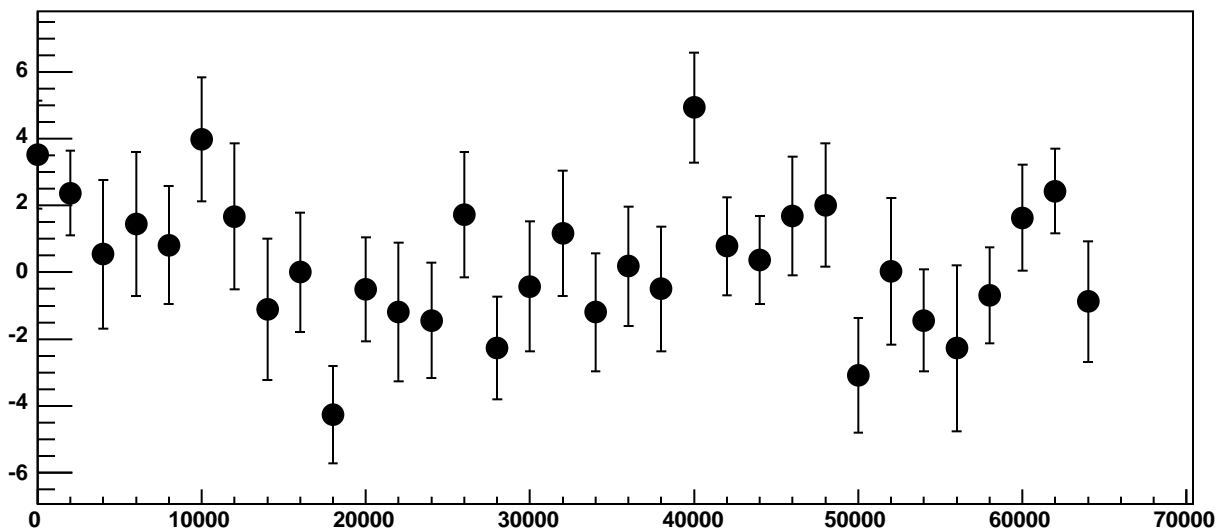
p1

$0.002917 \pm 2.482e-05$

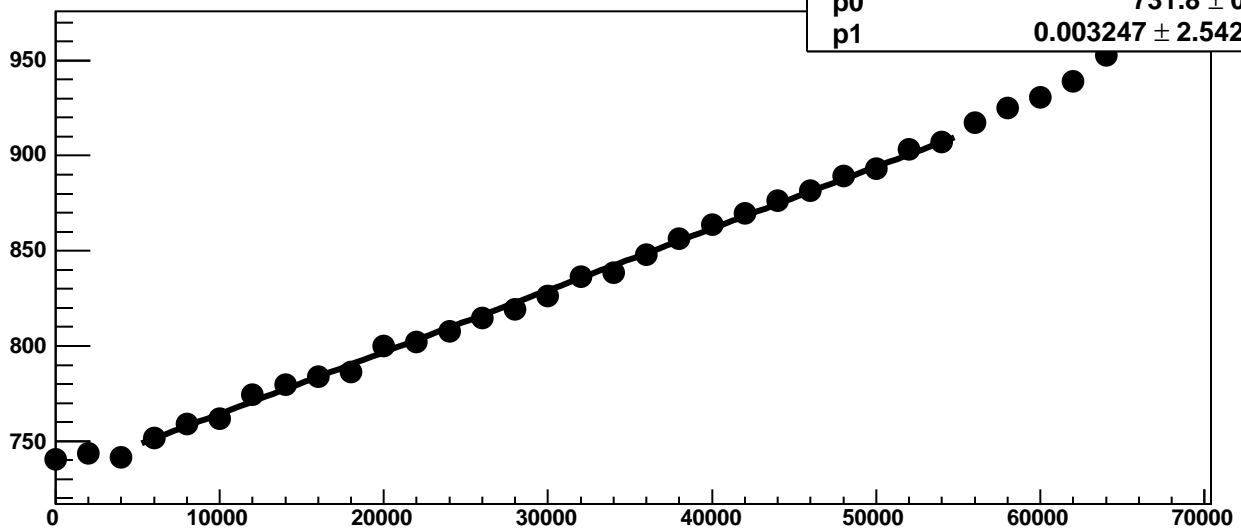
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

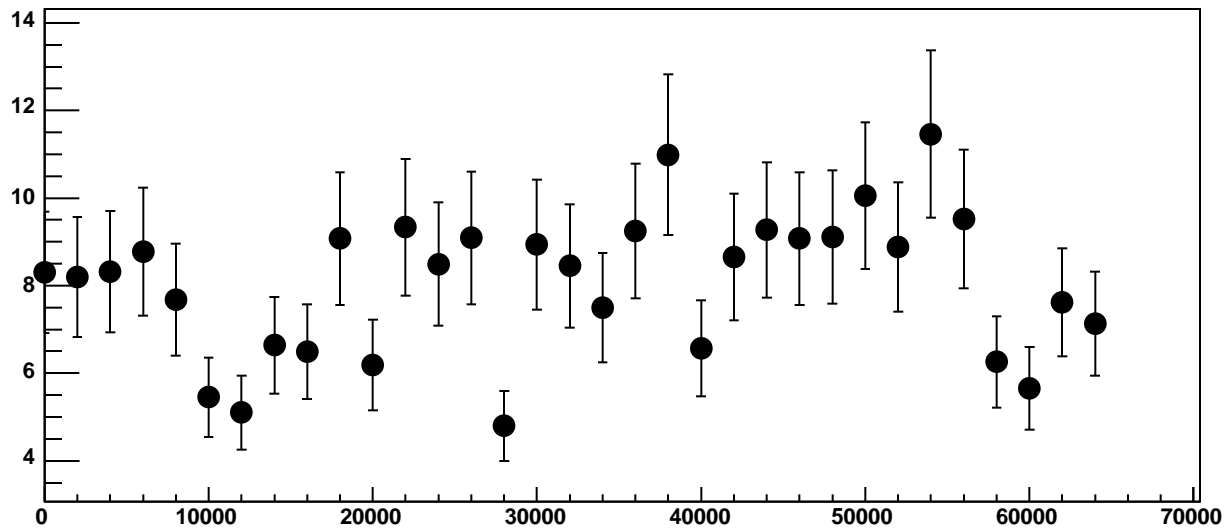


Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

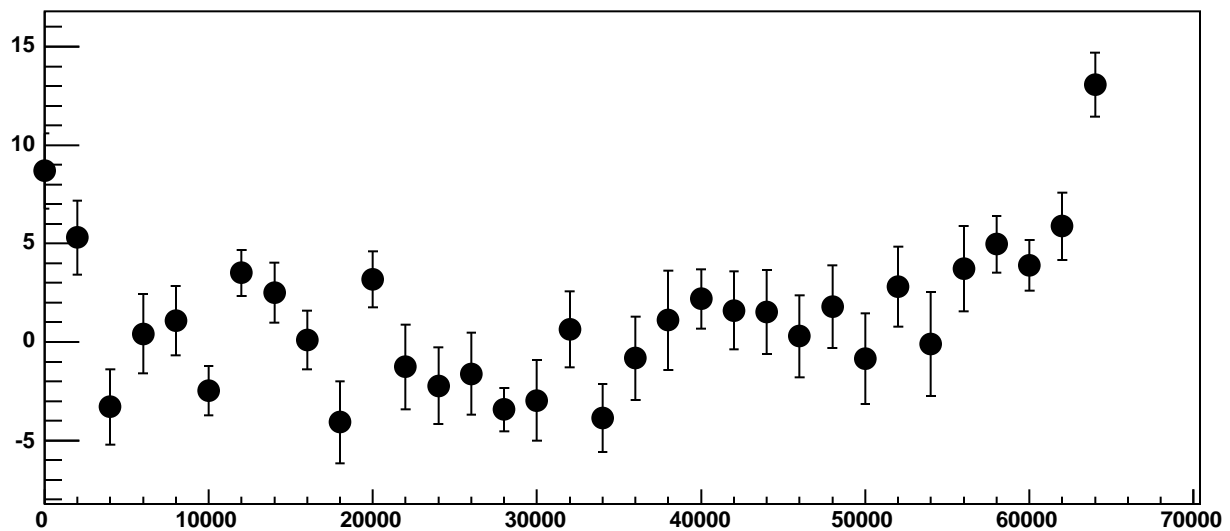


χ^2 / ndf 50.47 / 23
p0 731.8 ± 0.754
p1 $0.003247 \pm 2.542e-05$

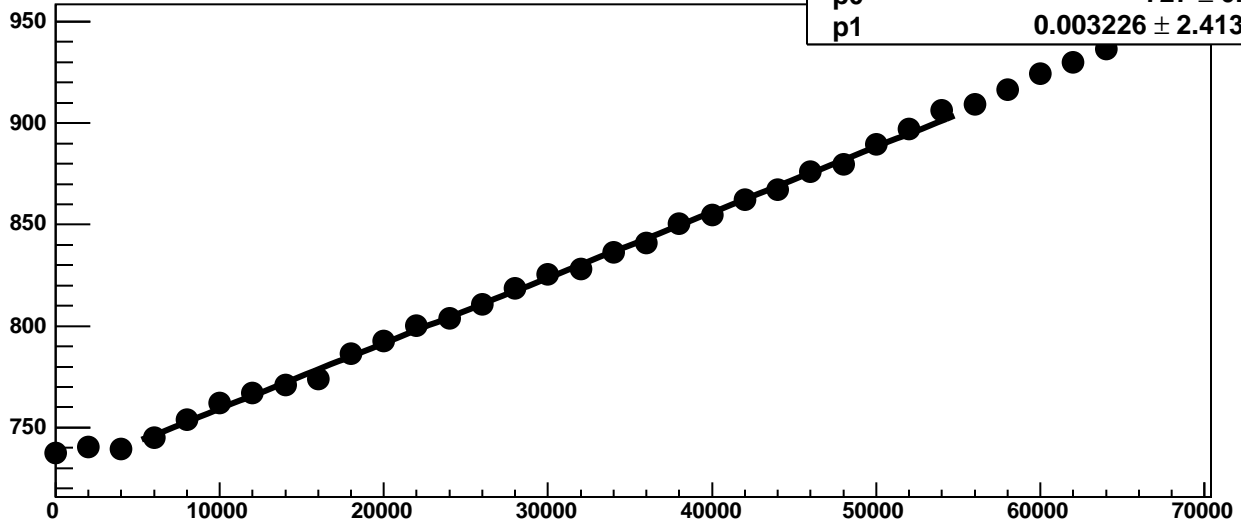
Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

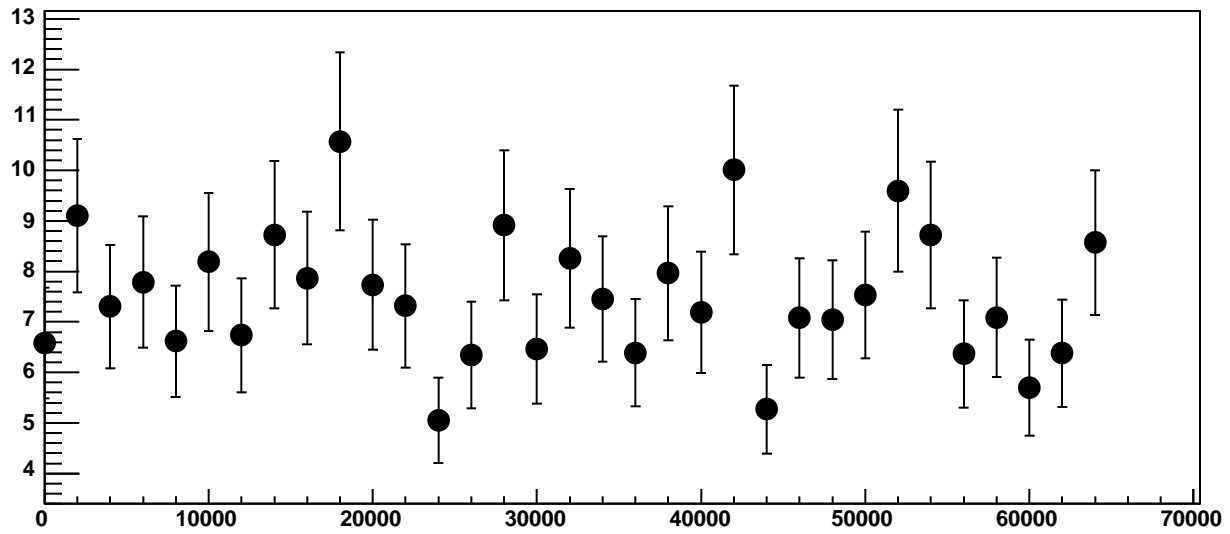


Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

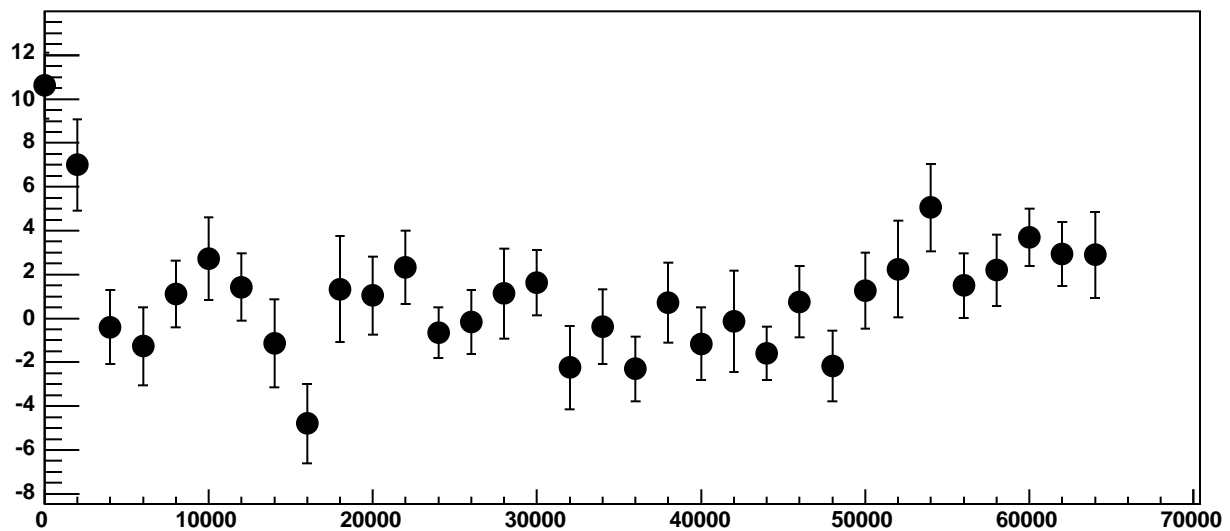


χ^2 / ndf 32.04 / 23
p0 727 ± 0.7961
p1 $0.003226 \pm 2.413e-05$

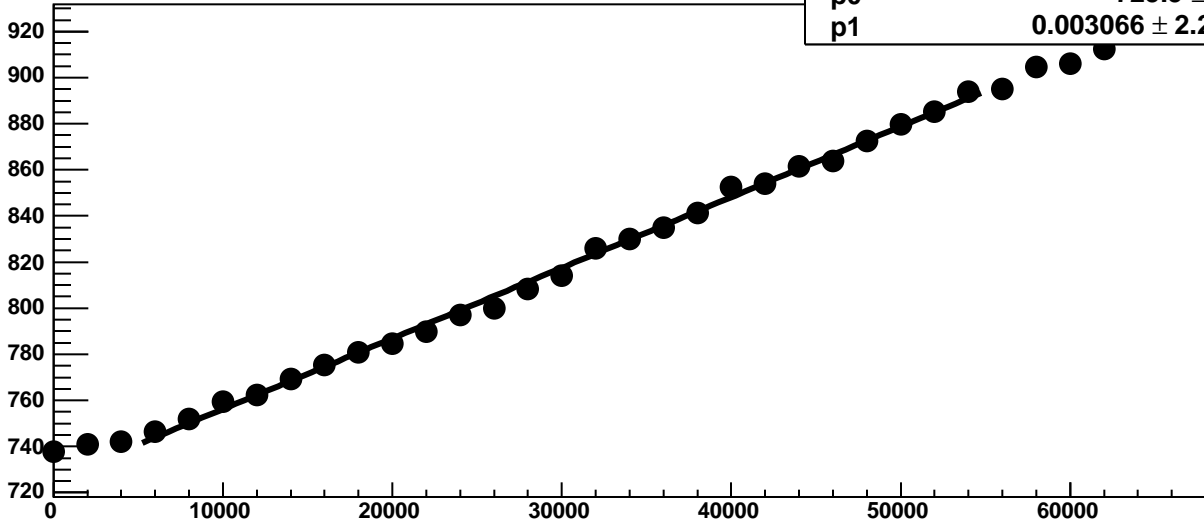
Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

53.15 / 23

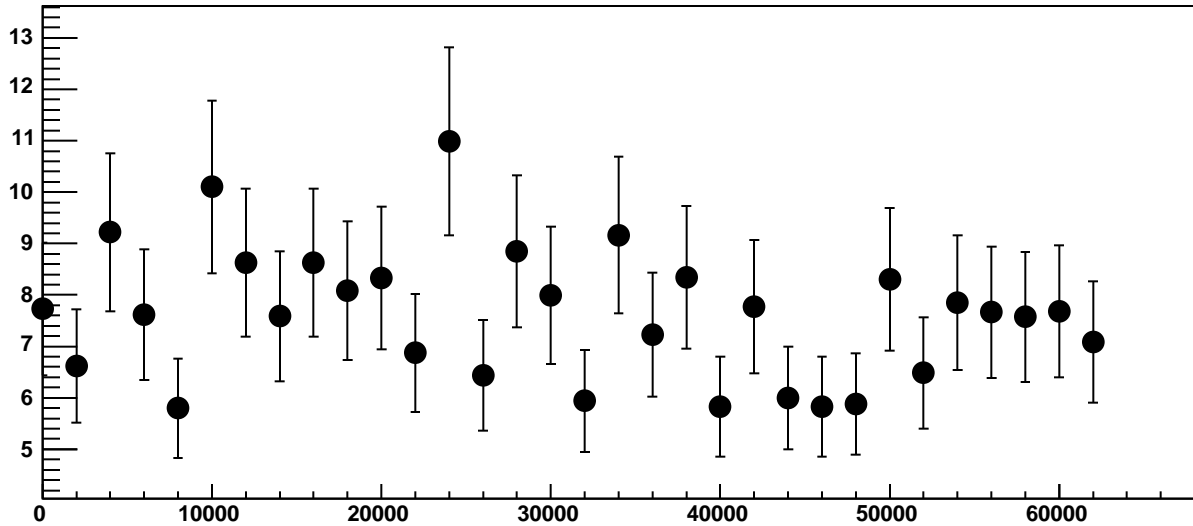
p0

725.5 ± 0.7972

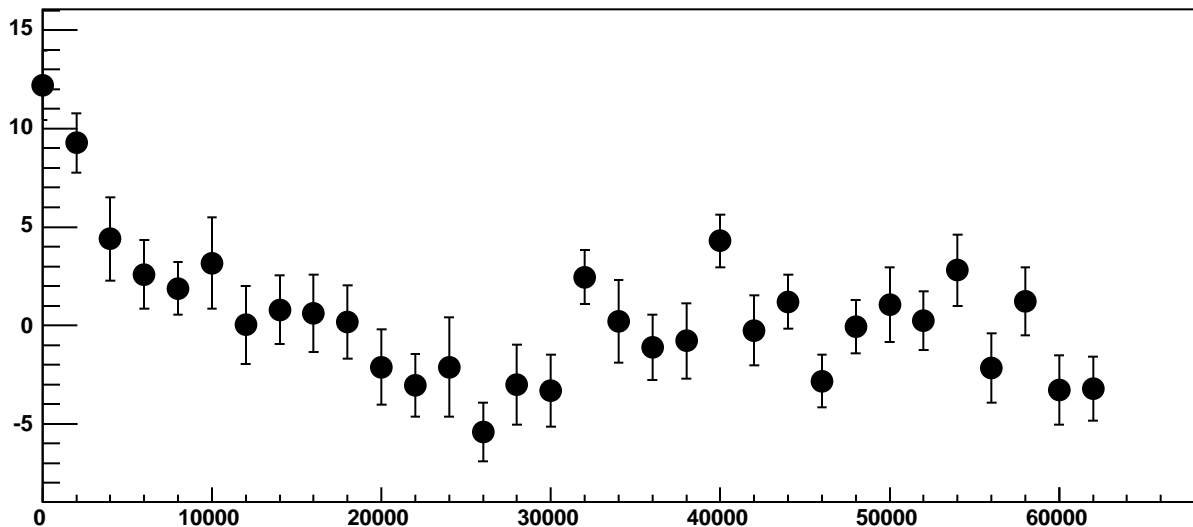
p1

$0.003066 \pm 2.289e-05$

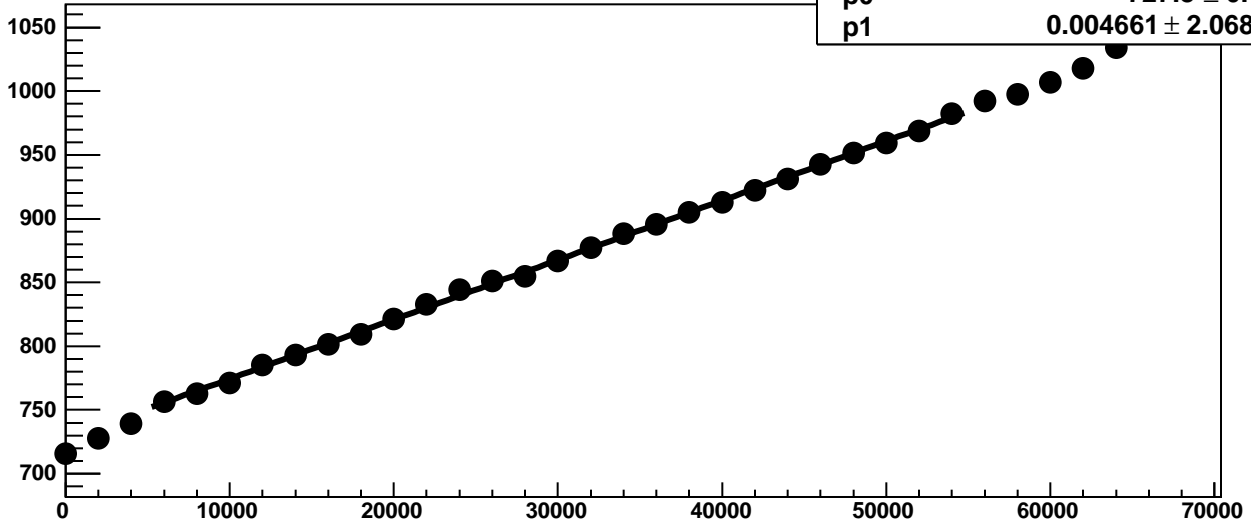
Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

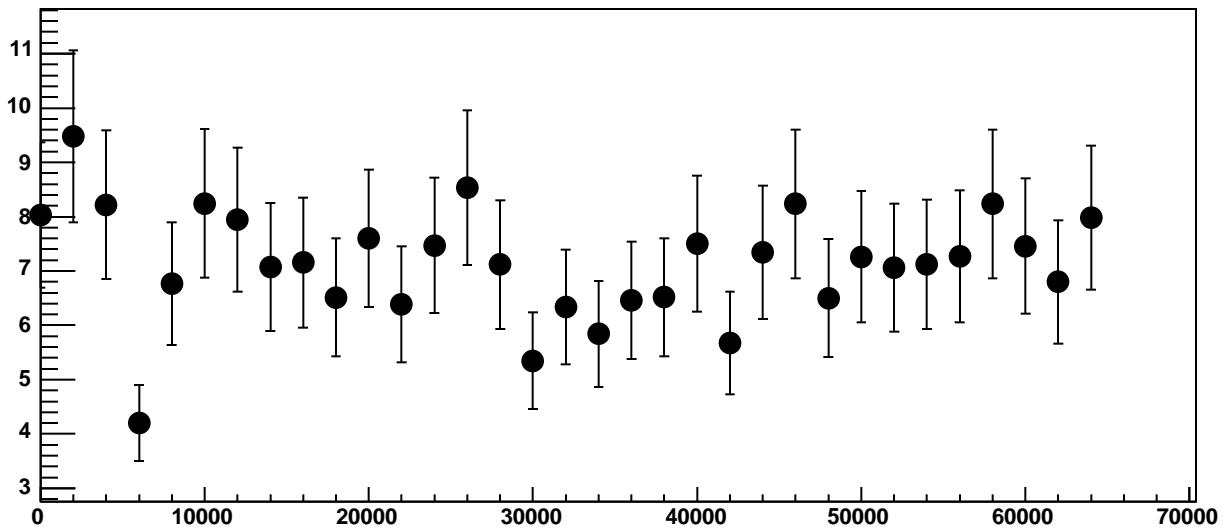


Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

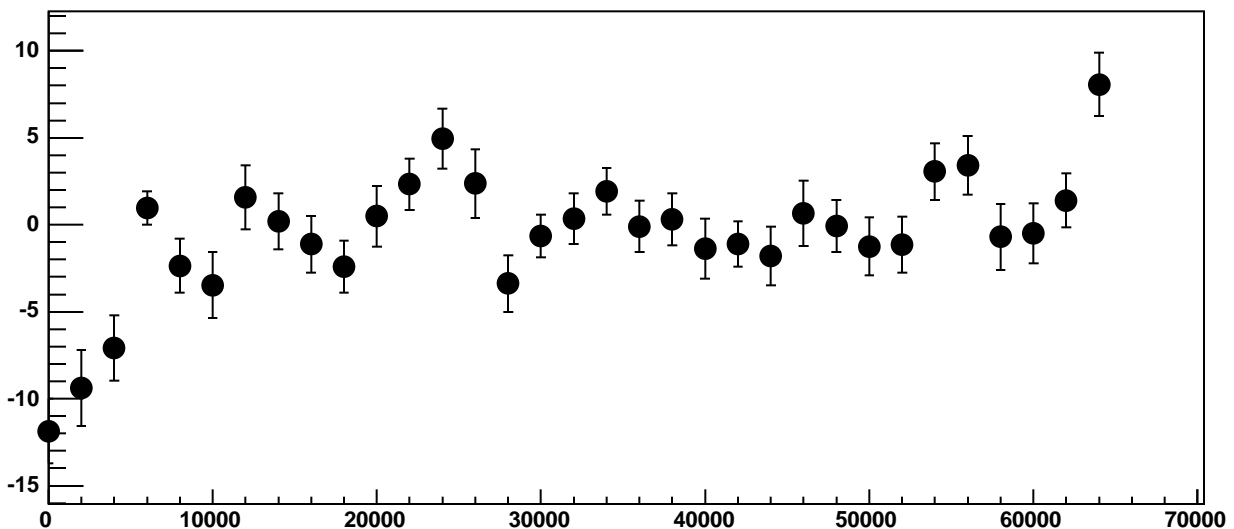


χ^2 / ndf 36.79 / 23
p0 727.8 ± 0.6717
p1 0.004661 ± 2.068e-05

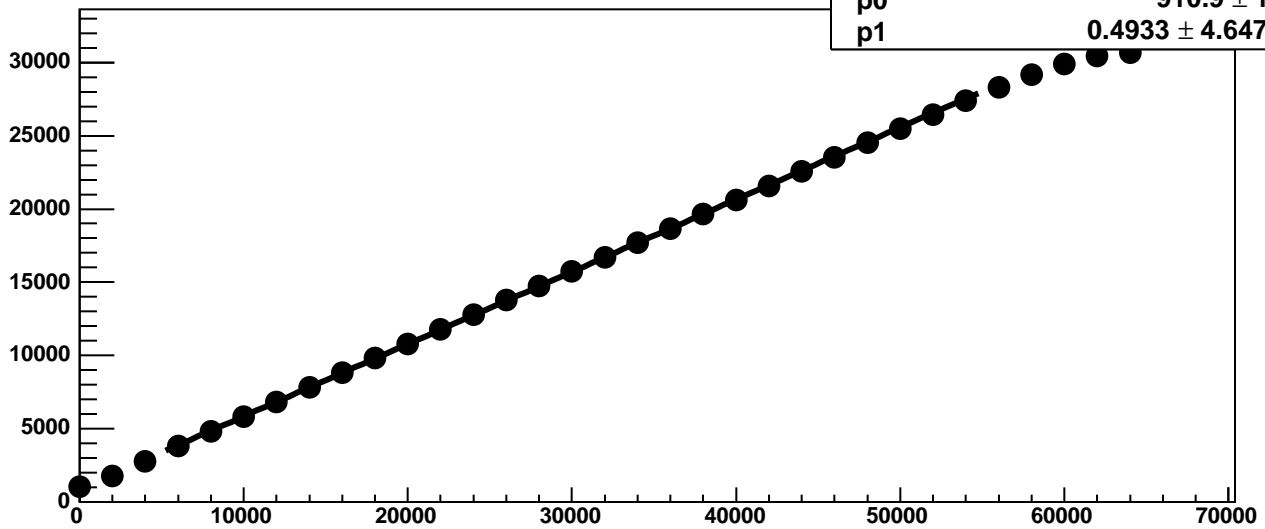
Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



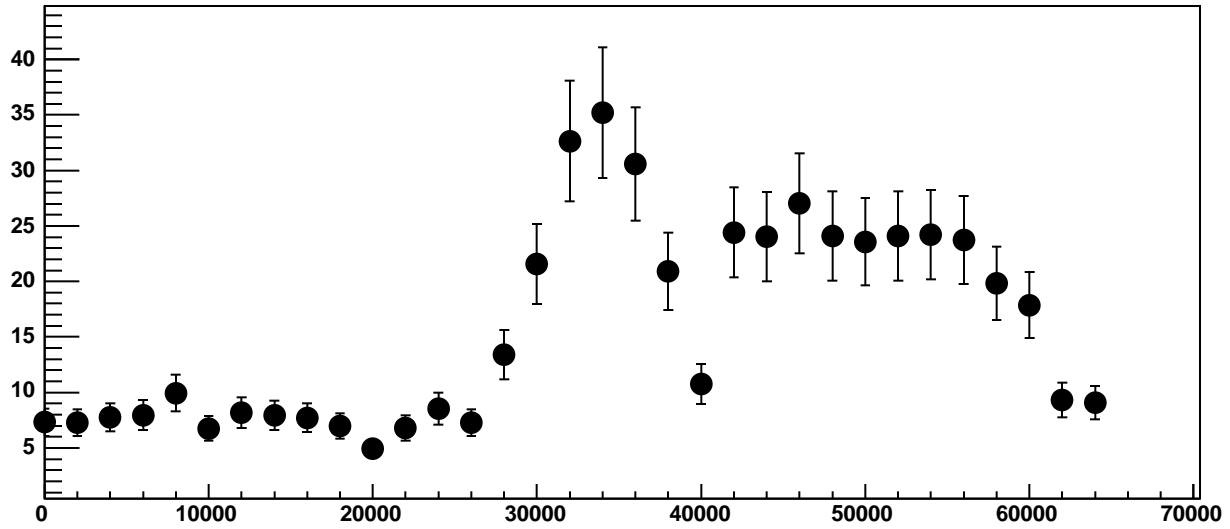
Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



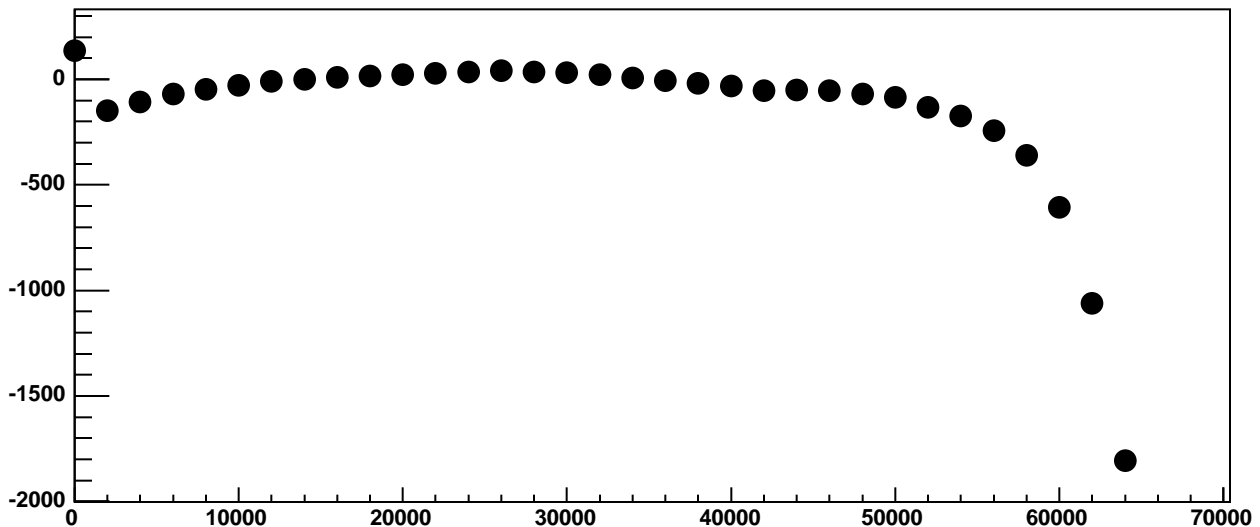
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



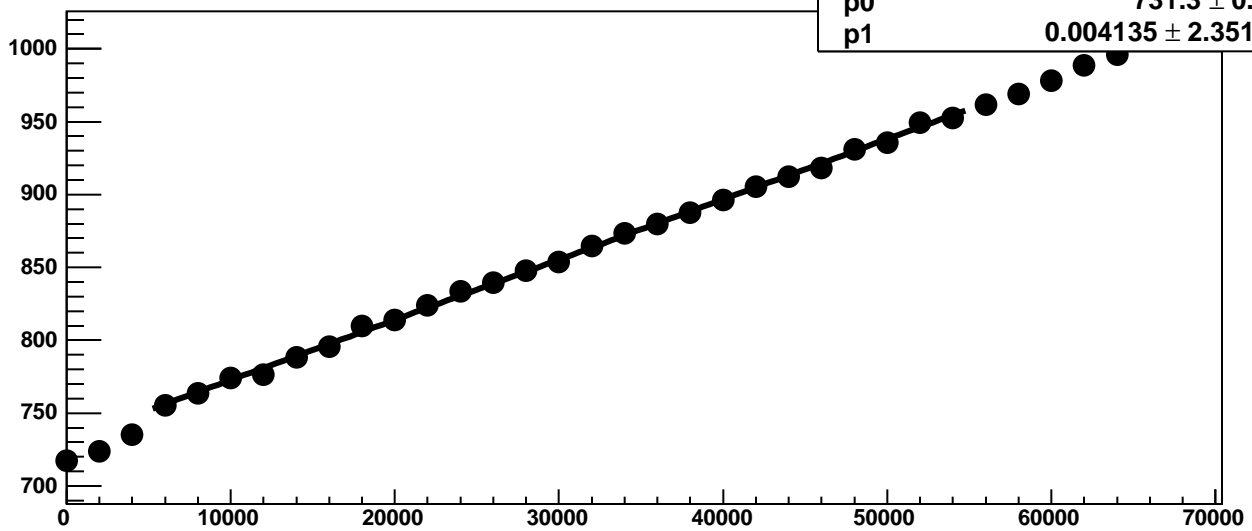
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

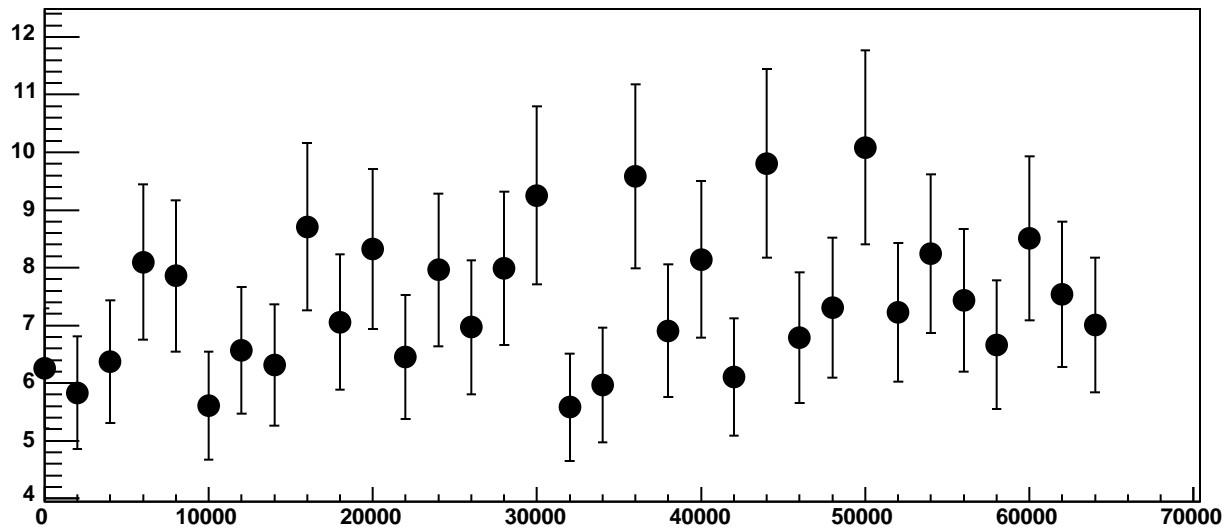


Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

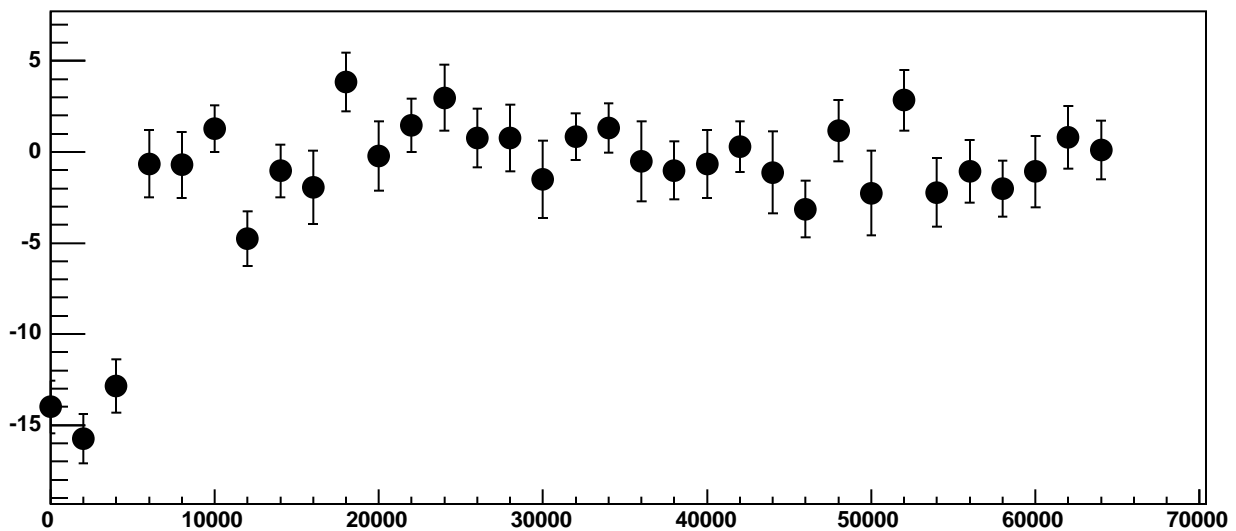


χ^2 / ndf 35.02 / 23
p0 731.3 ± 0.7591
p1 0.004135 ± 2.351e-05

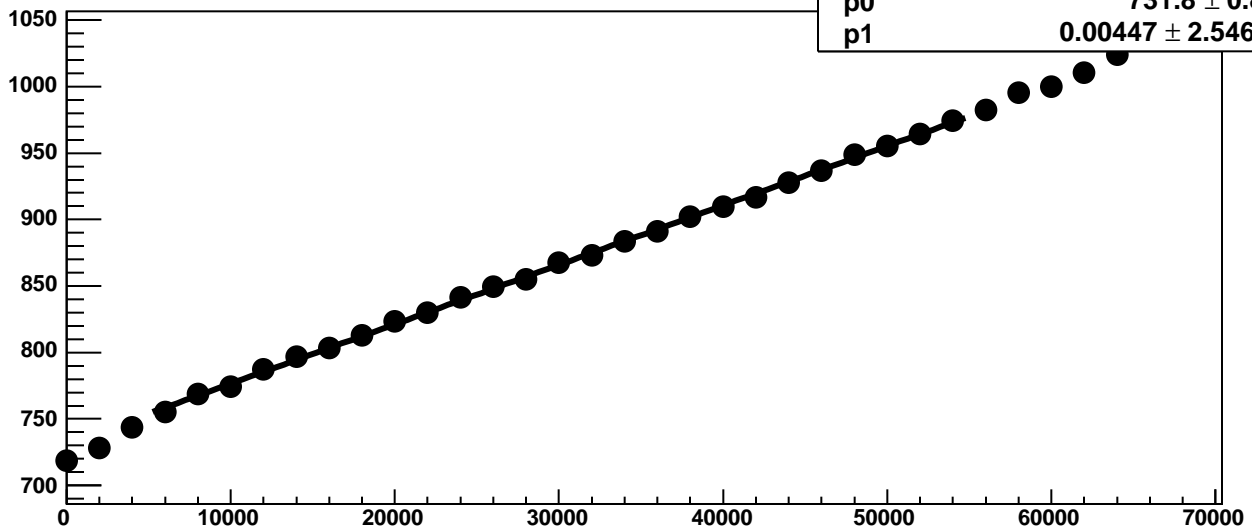
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

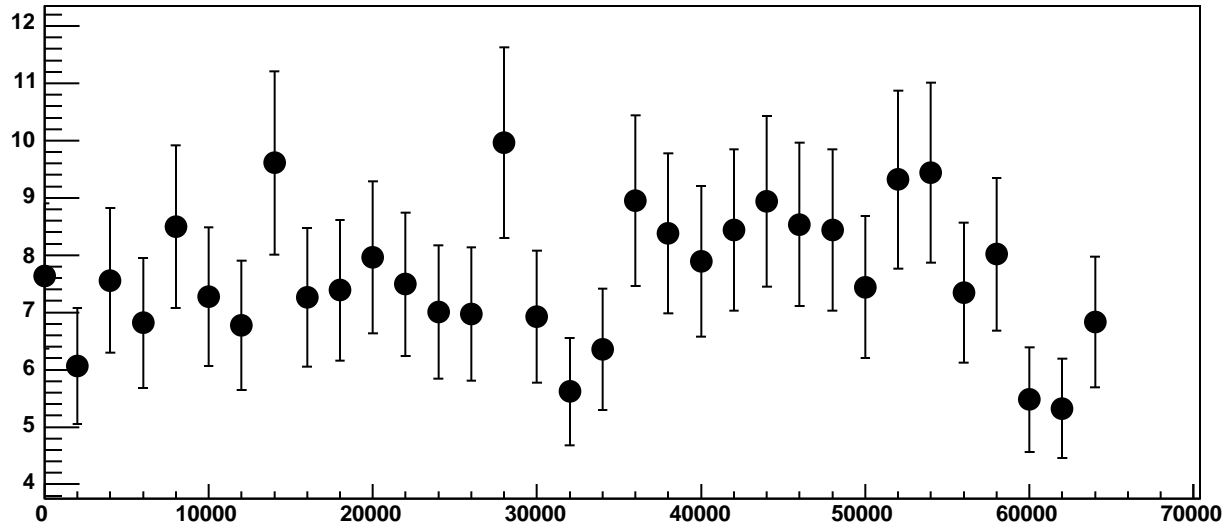


Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

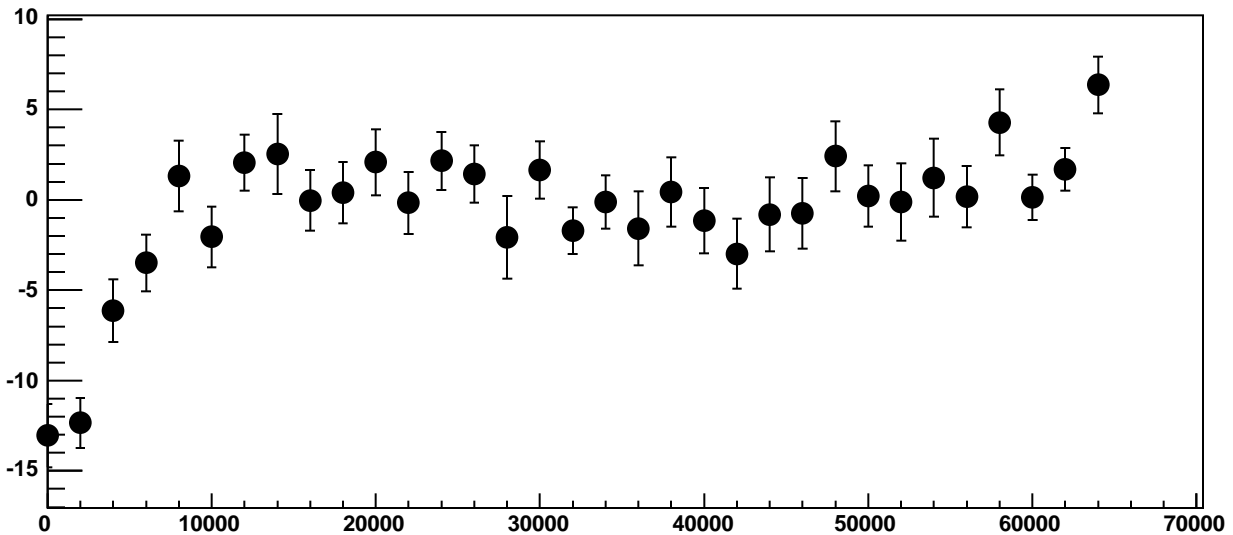


χ^2 / ndf 23.29 / 23
p0 731.8 ± 0.8106
p1 $0.00447 \pm 2.546e-05$

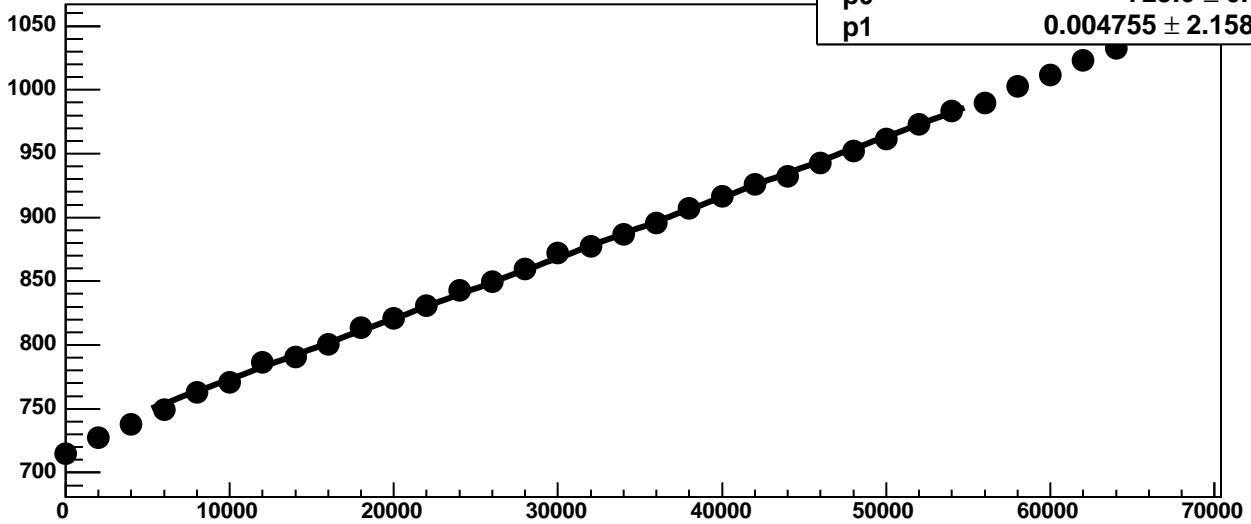
Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

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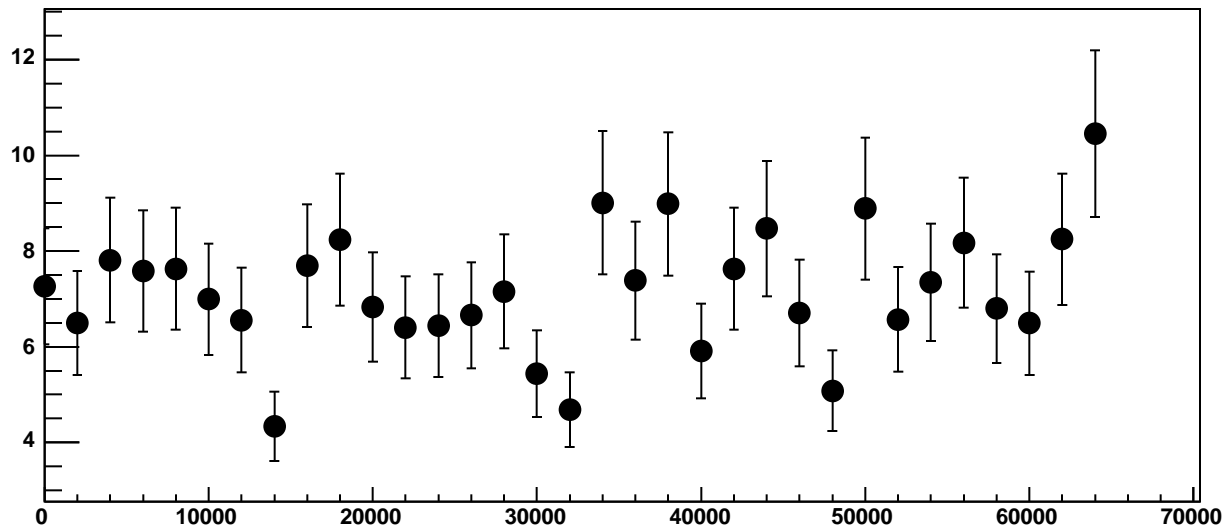
p0

725.6 ± 0.7056

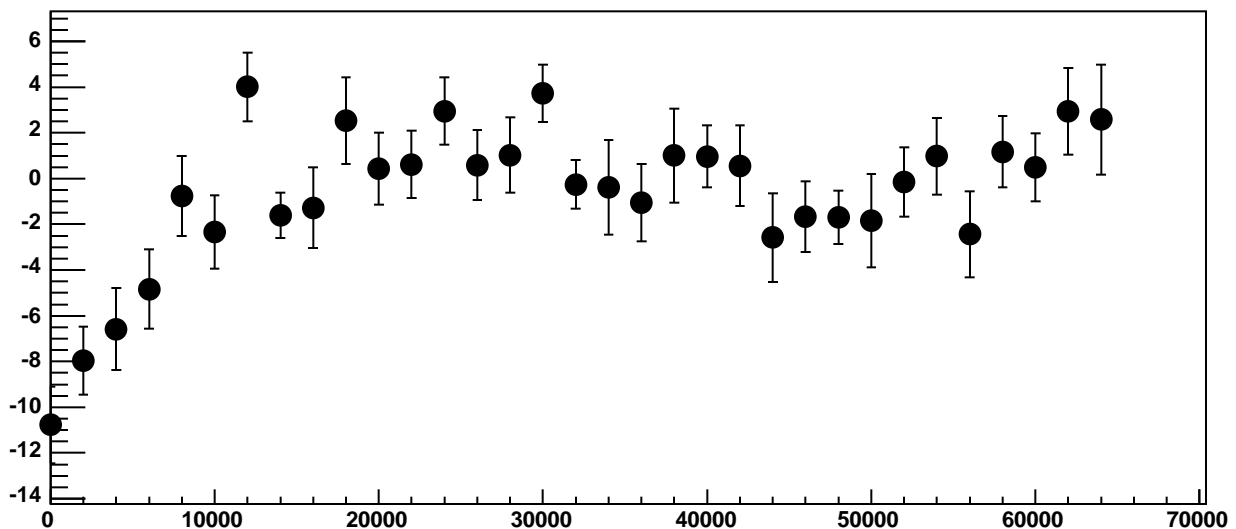
p1

$0.004755 \pm 2.158e-05$

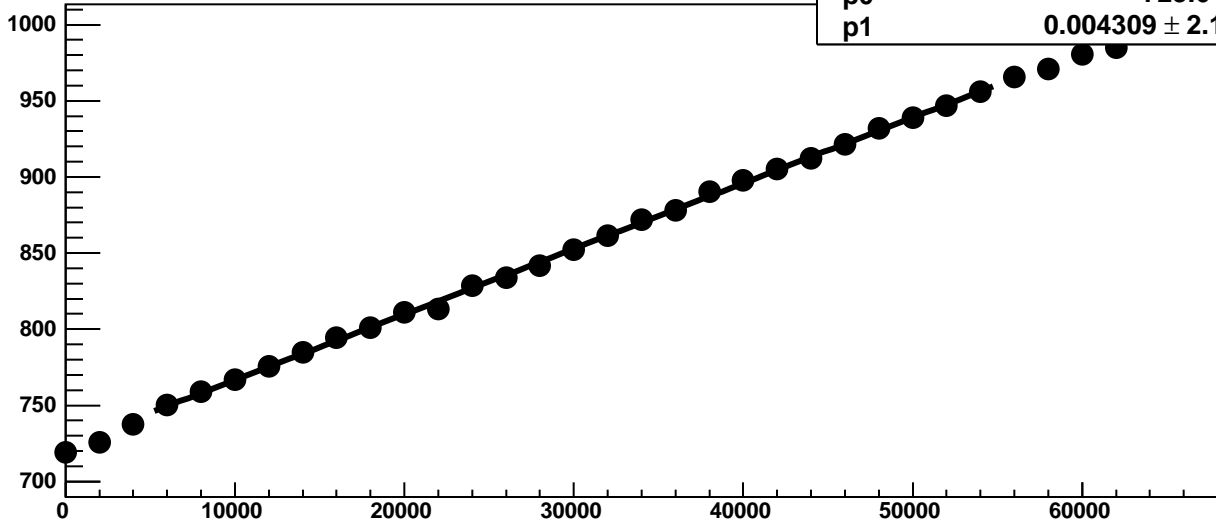
Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.31 / 23

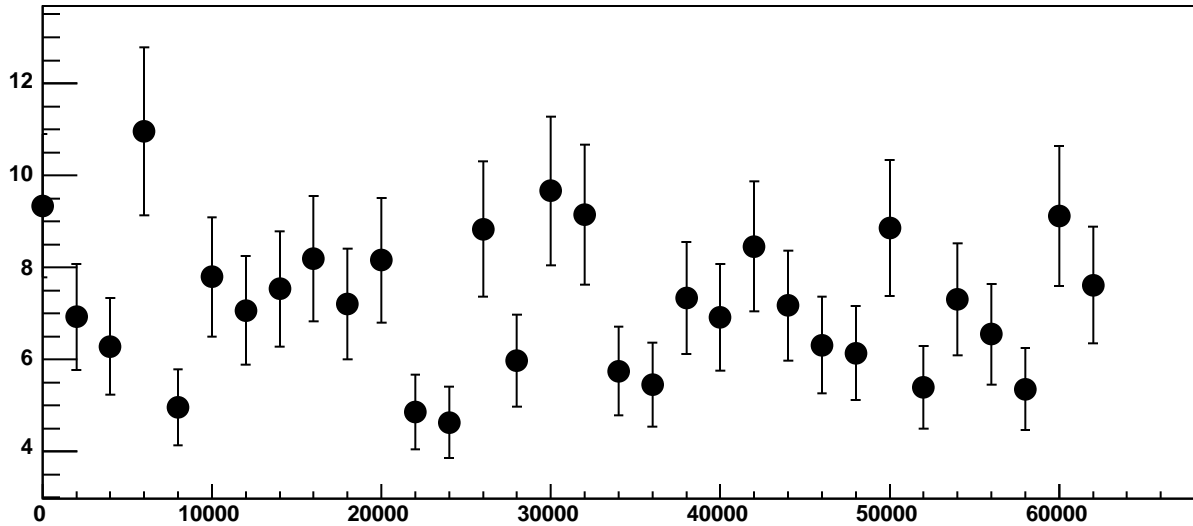
p0

723.6 ± 0.722

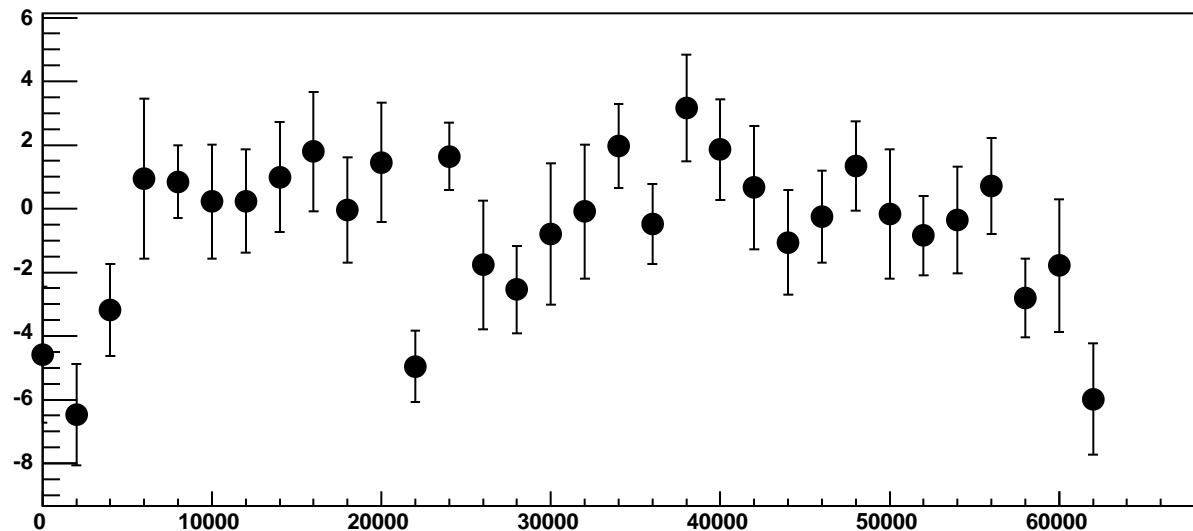
p1

$0.004309 \pm 2.165e-05$

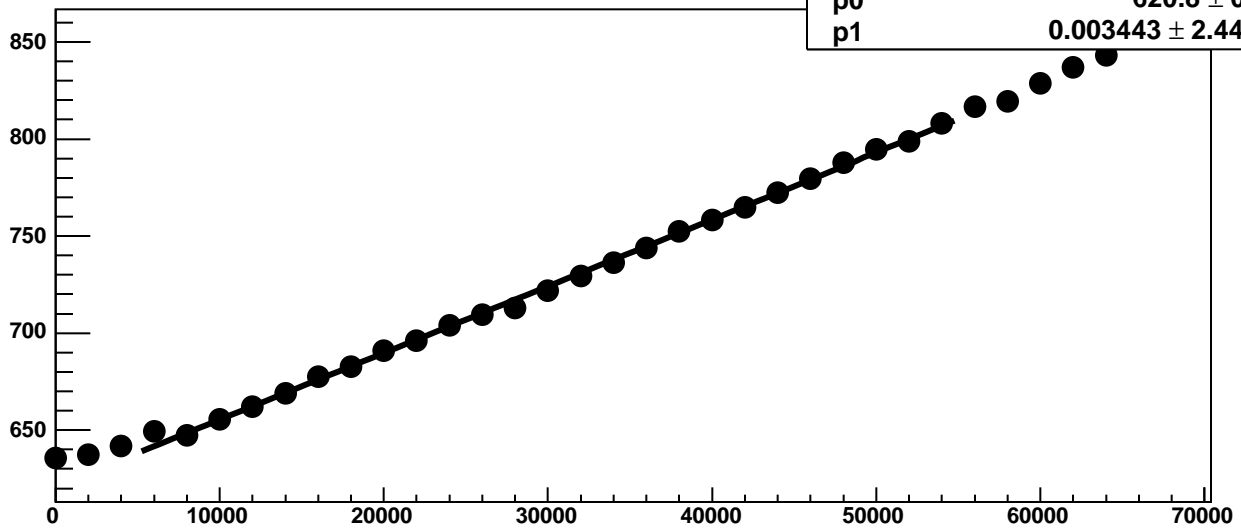
Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



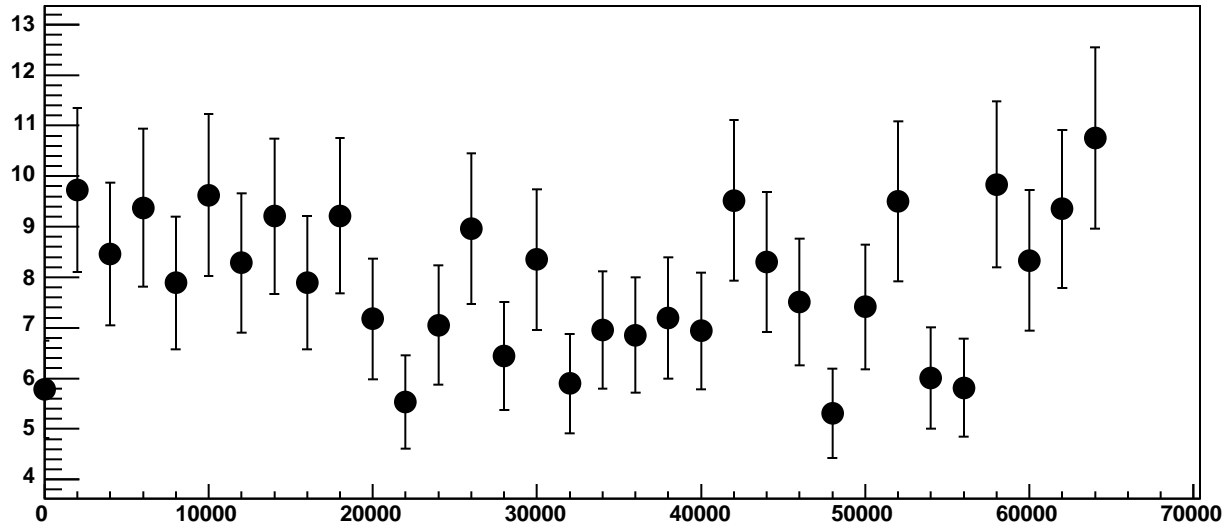
Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



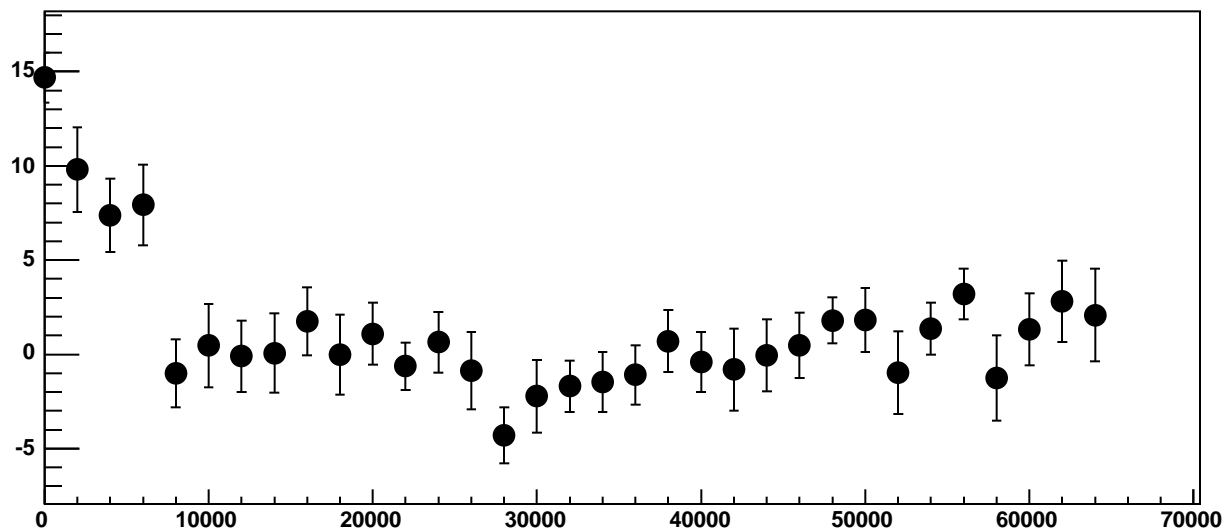
Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



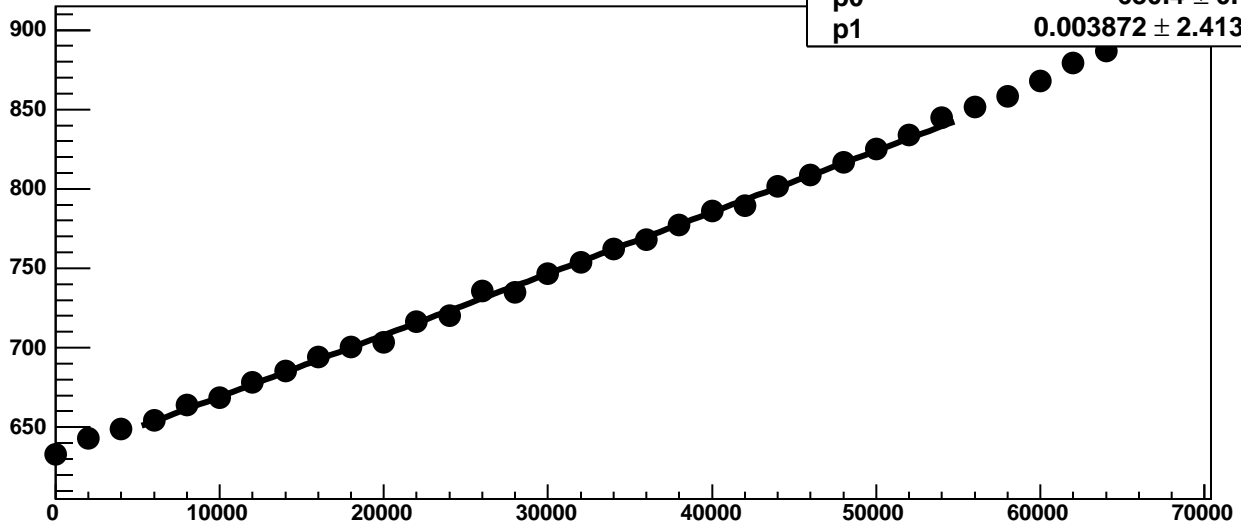
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



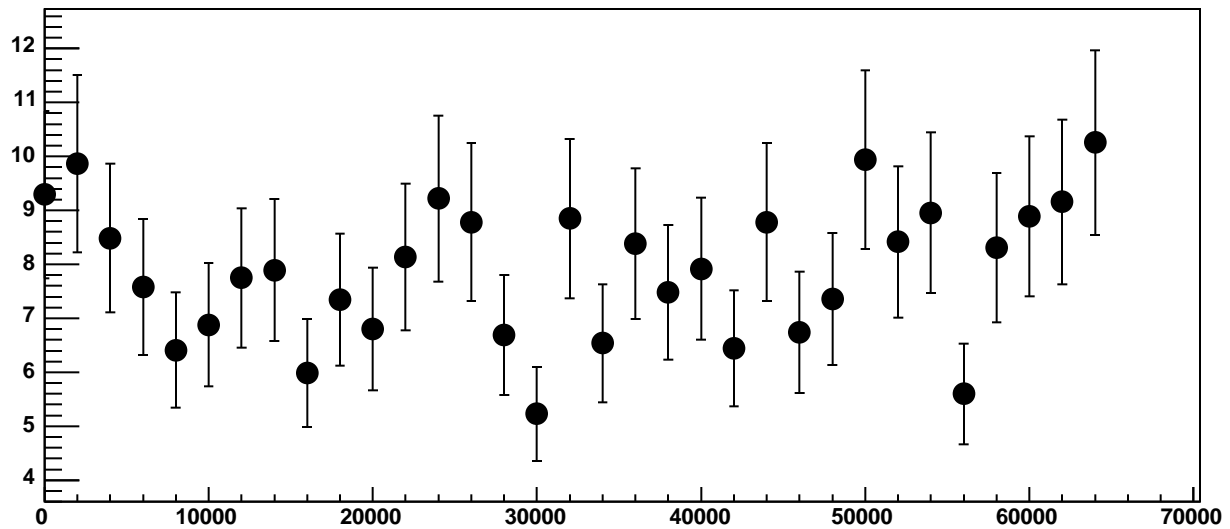
Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



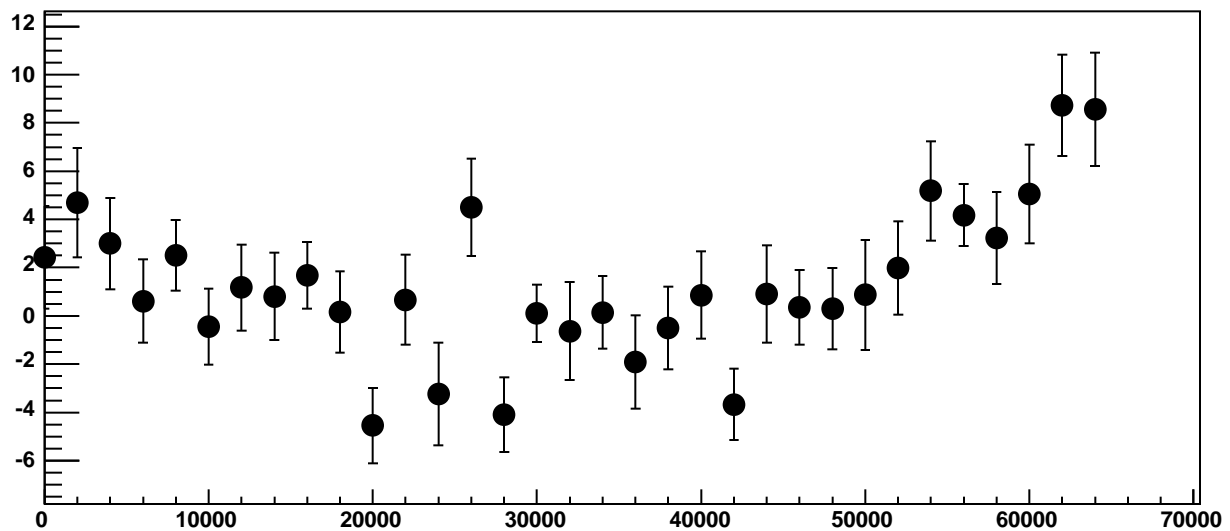
Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



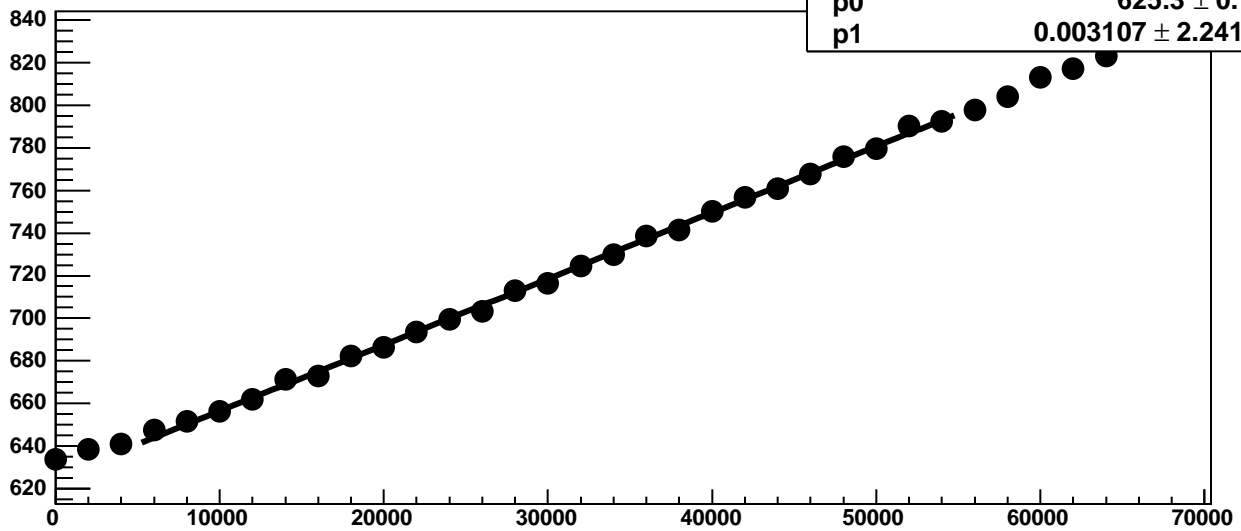
Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.91 / 23

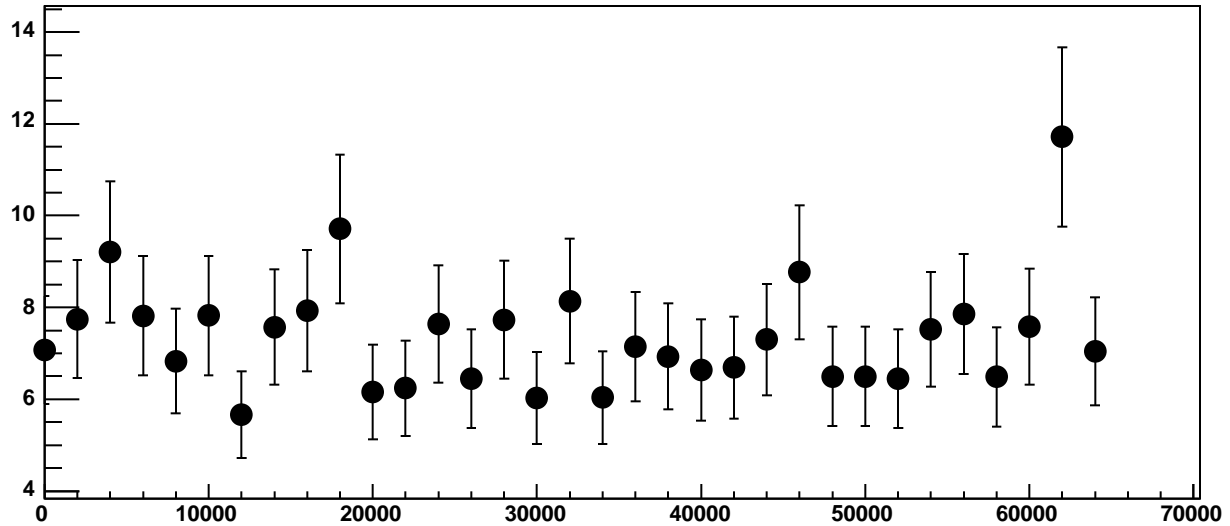
p0

625.3 ± 0.7506

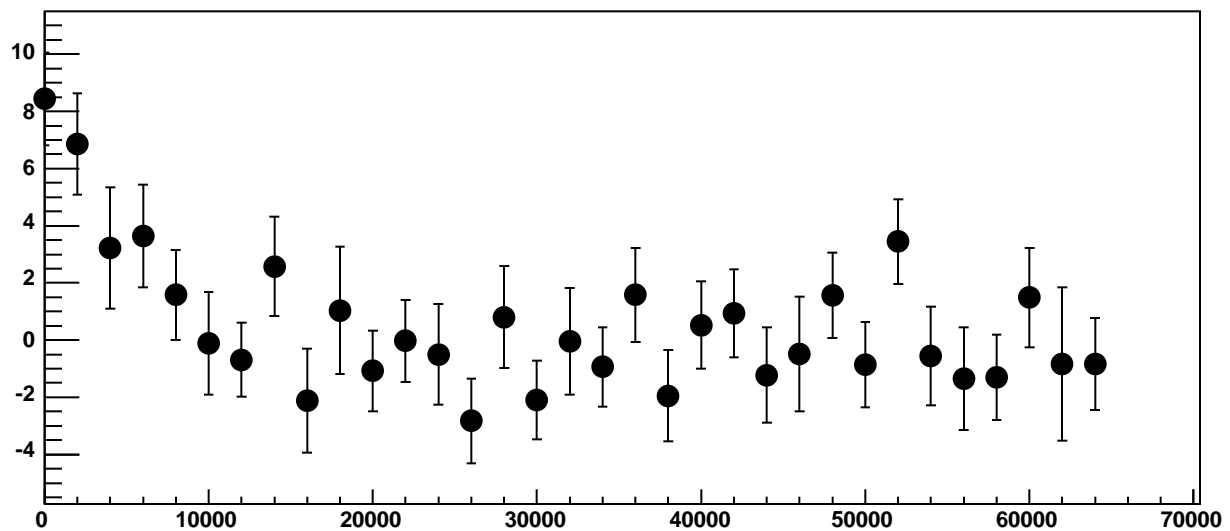
p1

$0.003107 \pm 2.241\text{e-}05$

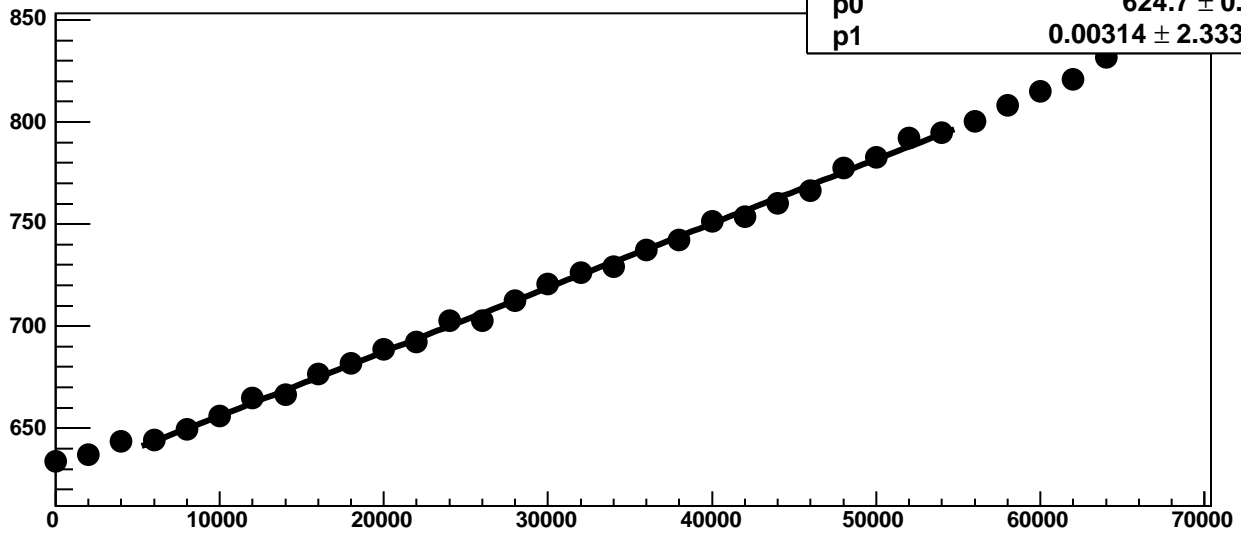
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.41 / 23

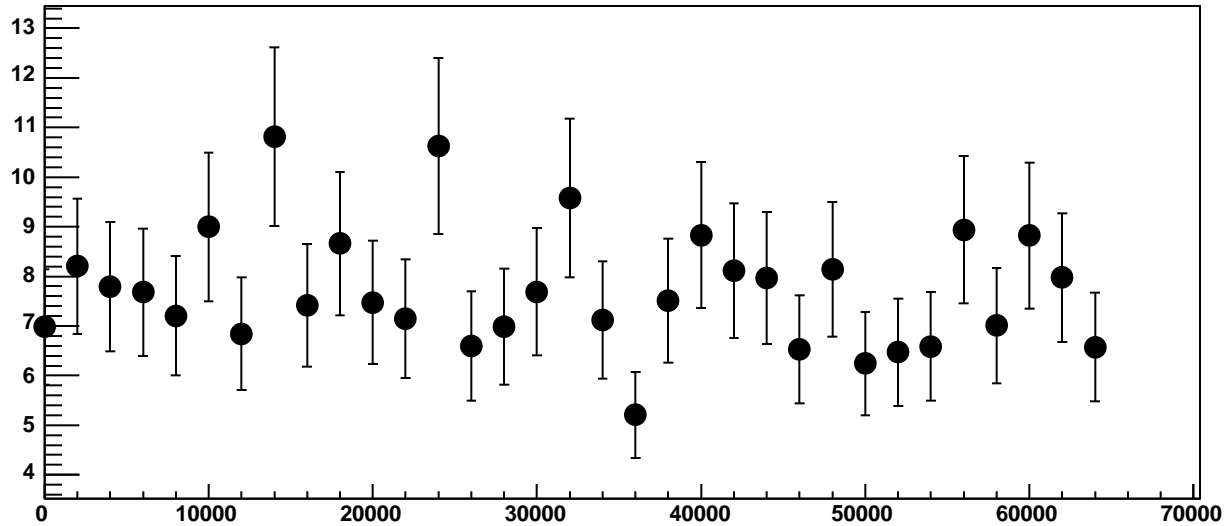
p0

624.7 ± 0.8101

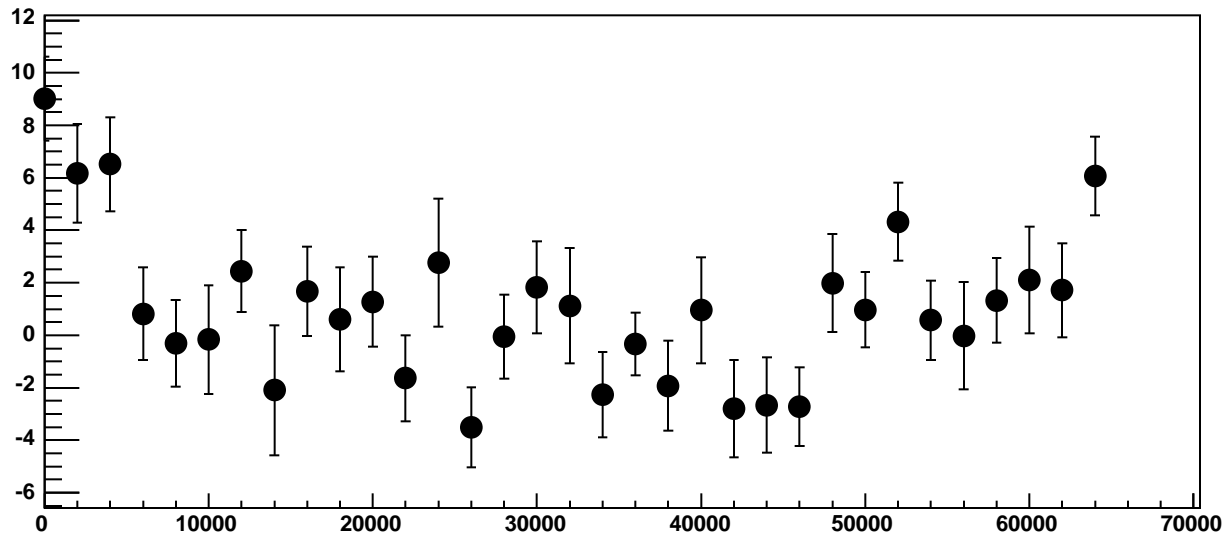
p1

$0.00314 \pm 2.333e-05$

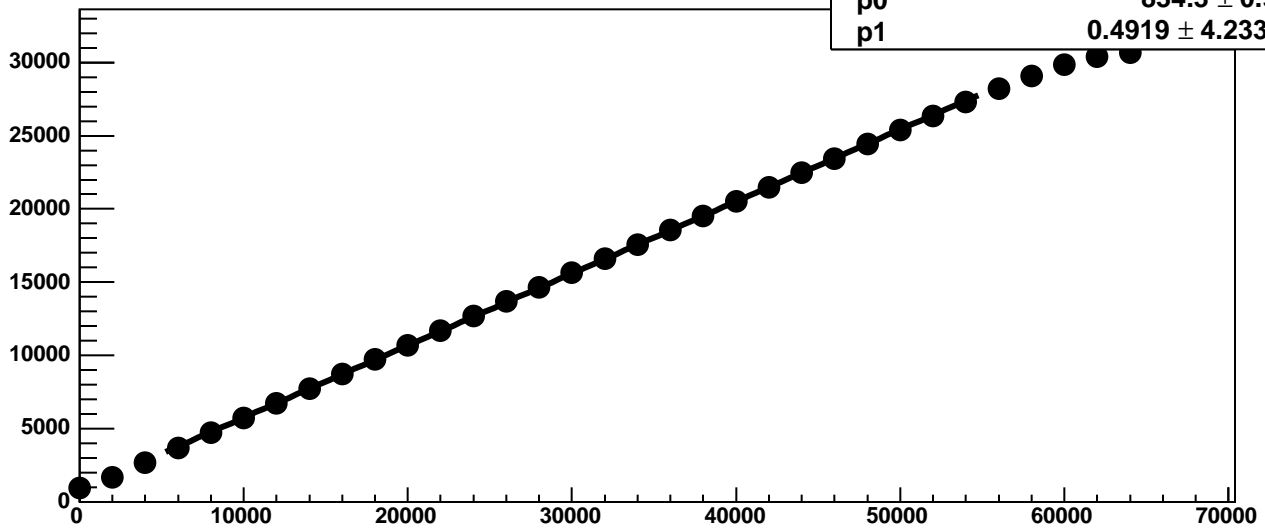
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



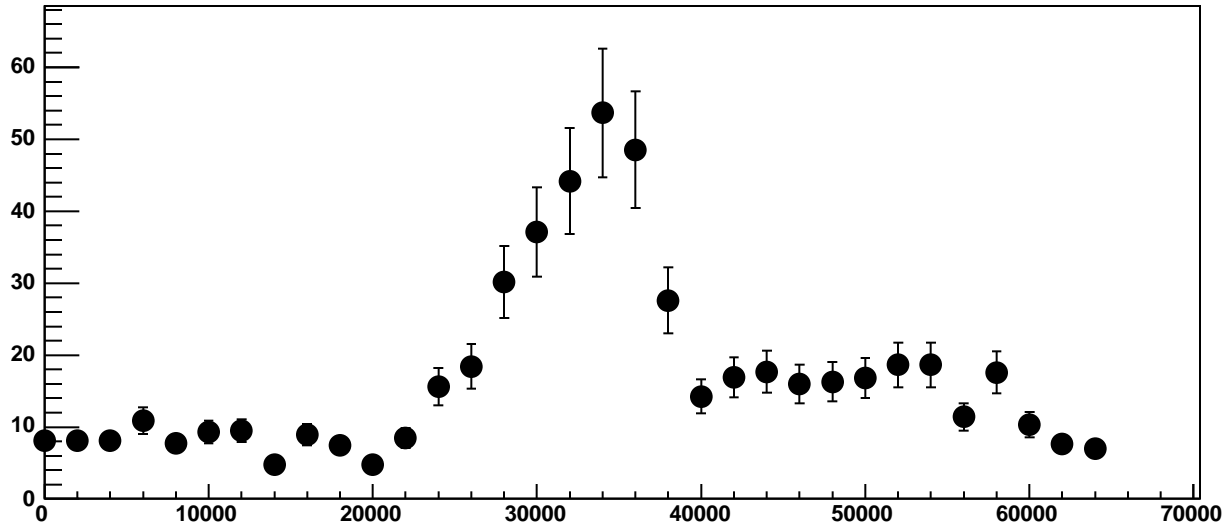
Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



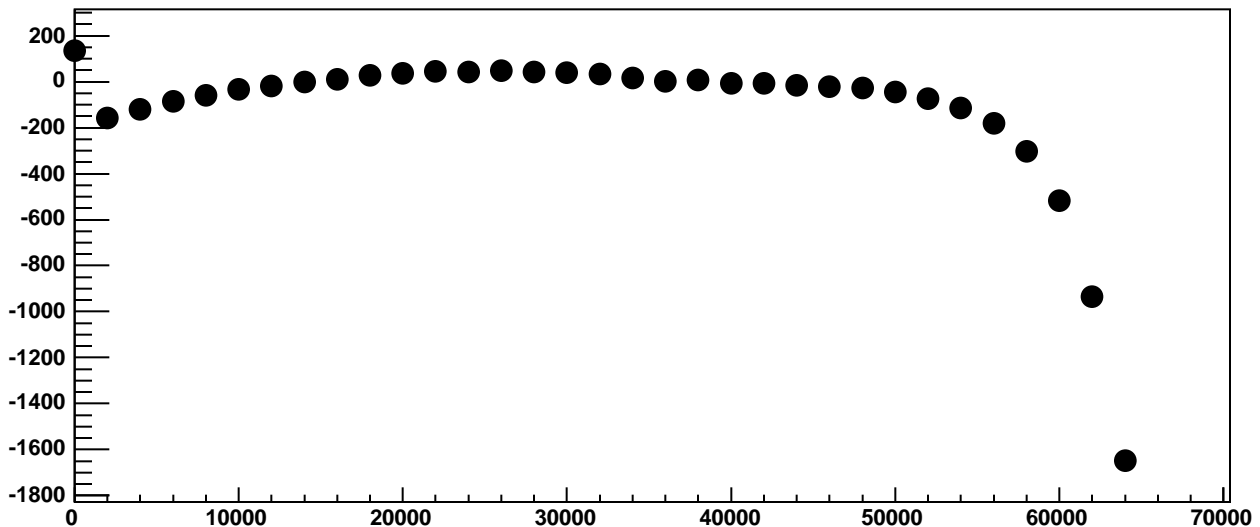
Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



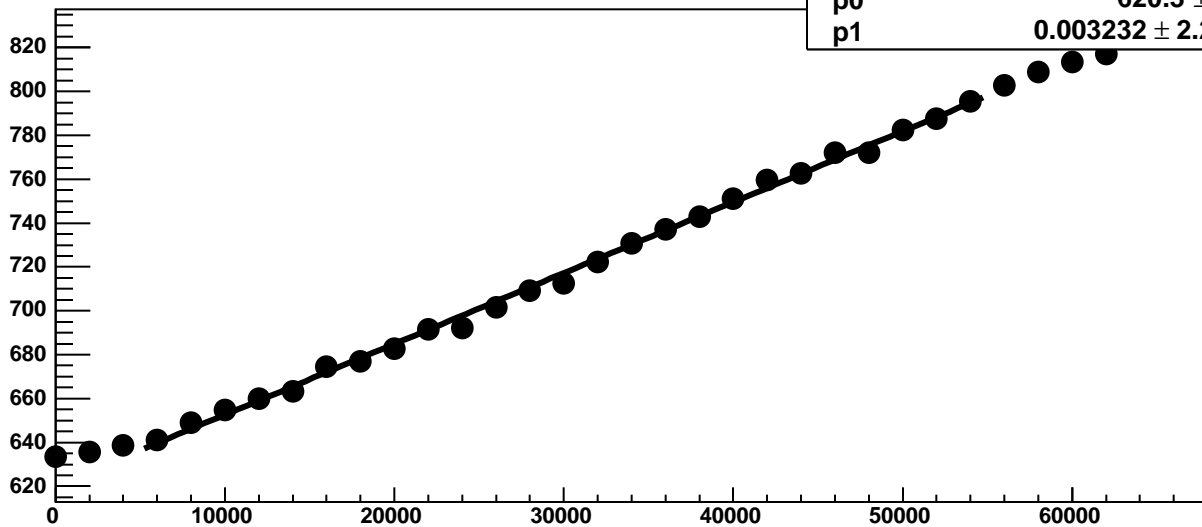
Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

50.43 / 23

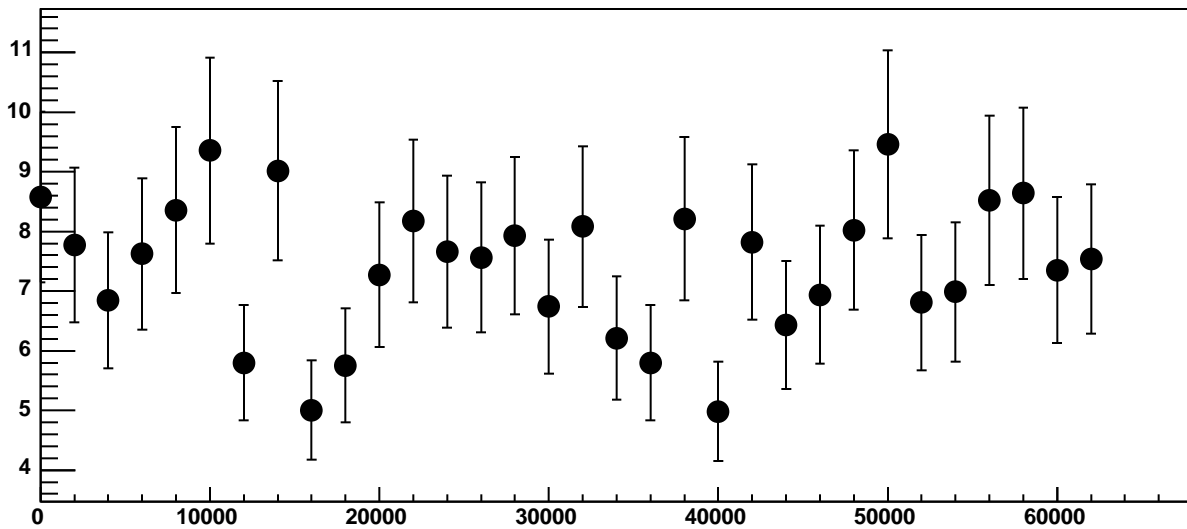
p0

620.3 ± 0.7578

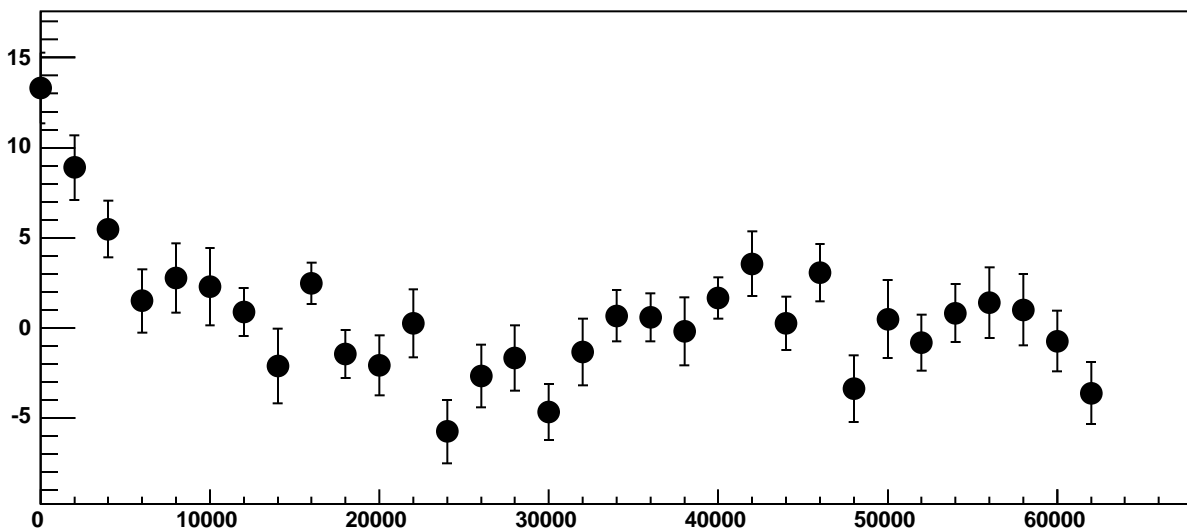
p1

$0.003232 \pm 2.285e-05$

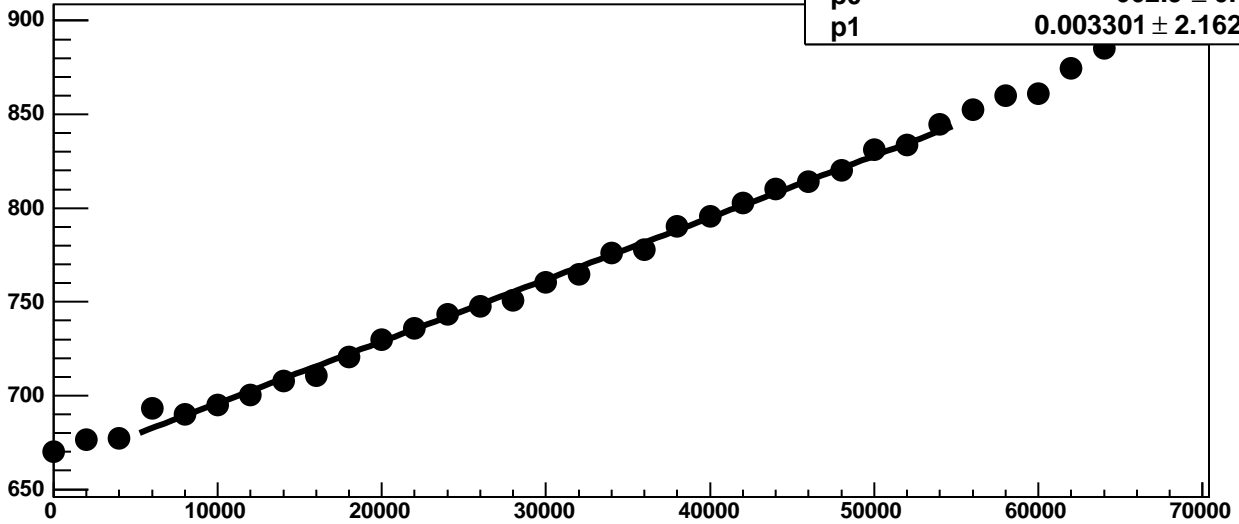
Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

92.28 / 23

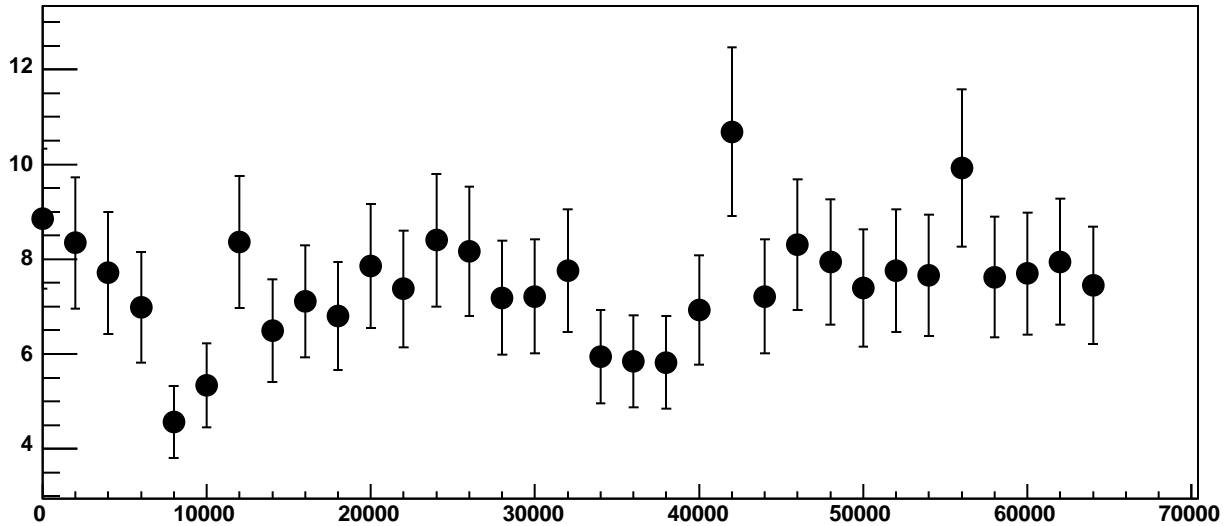
p0

662.9 ± 0.6812

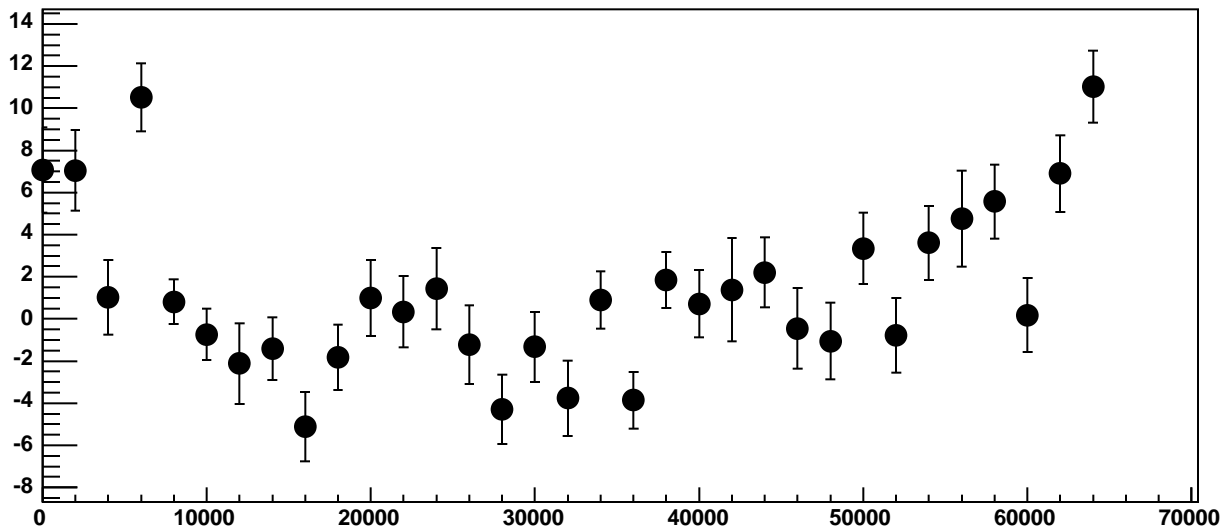
p1

$0.003301 \pm 2.162e-05$

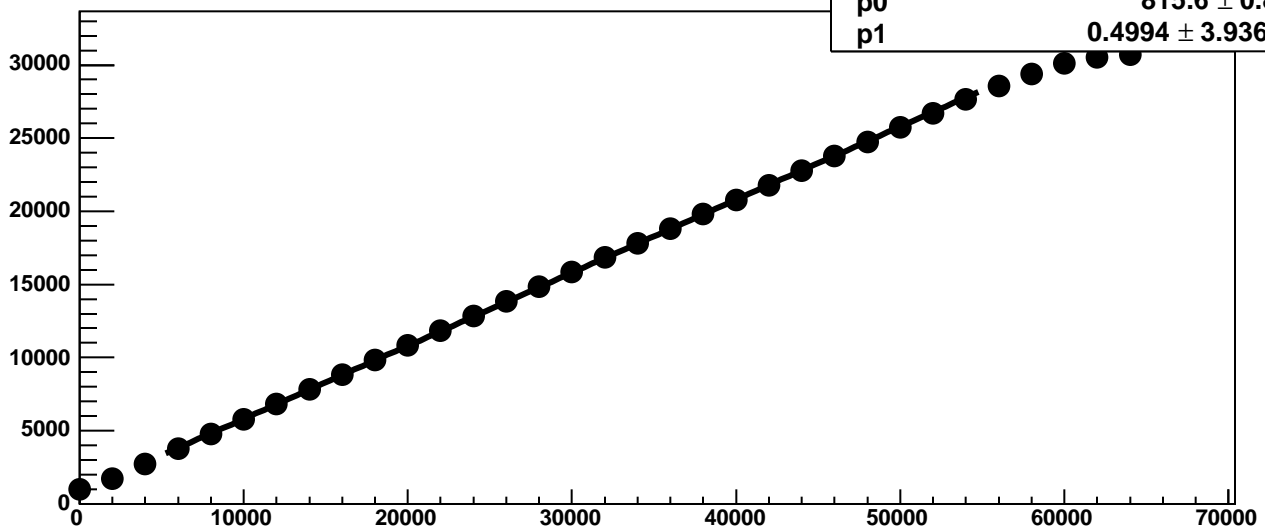
Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



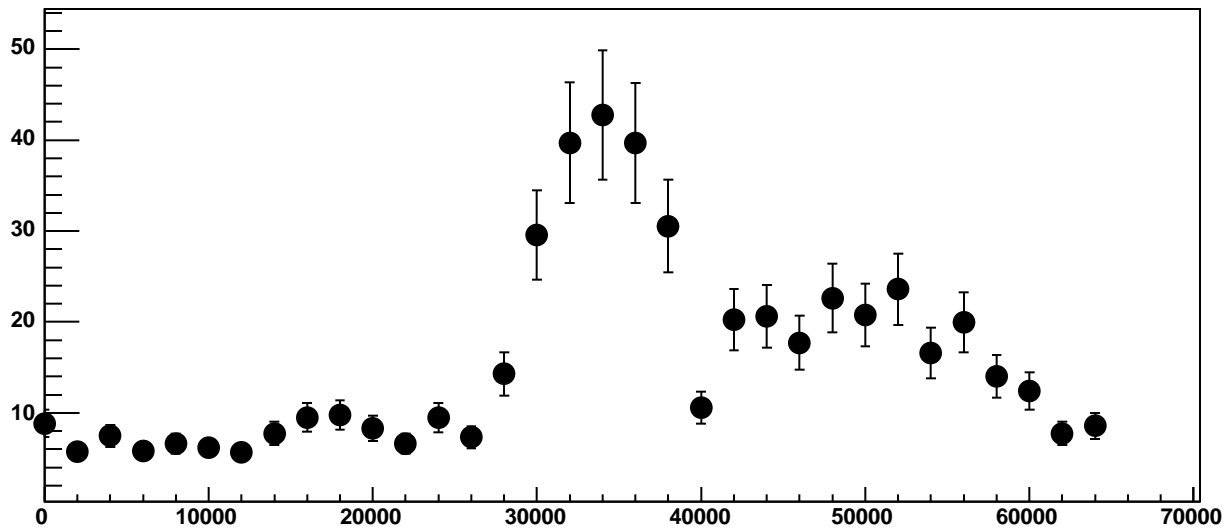
Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



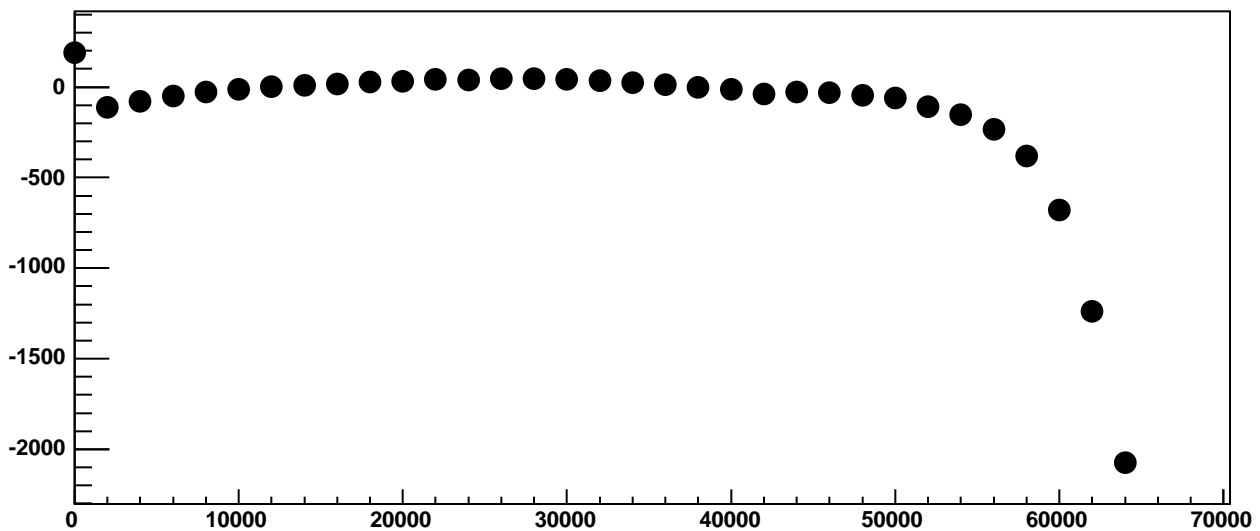
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



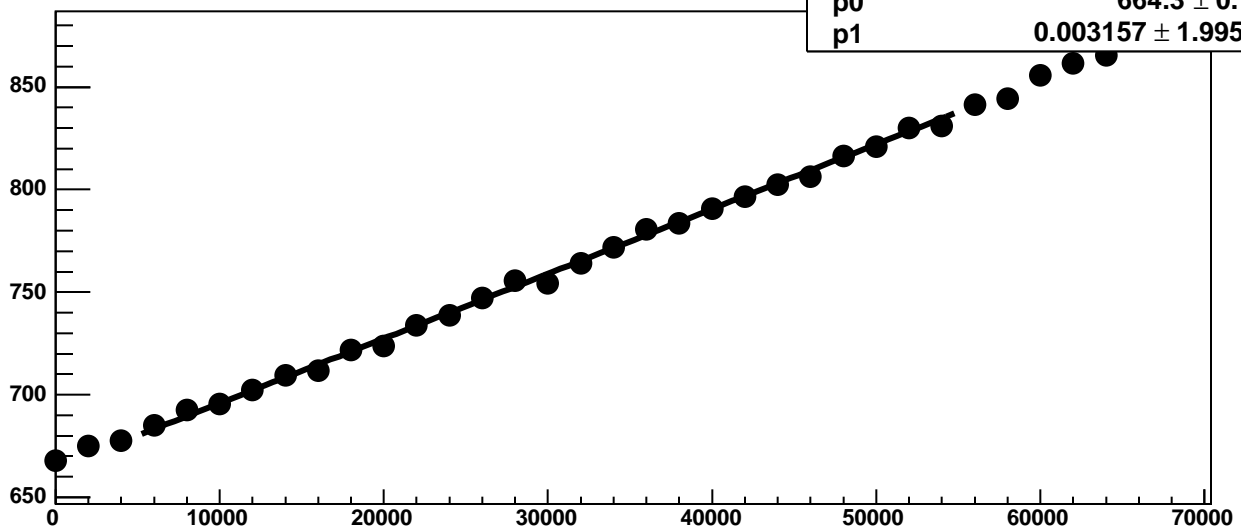
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



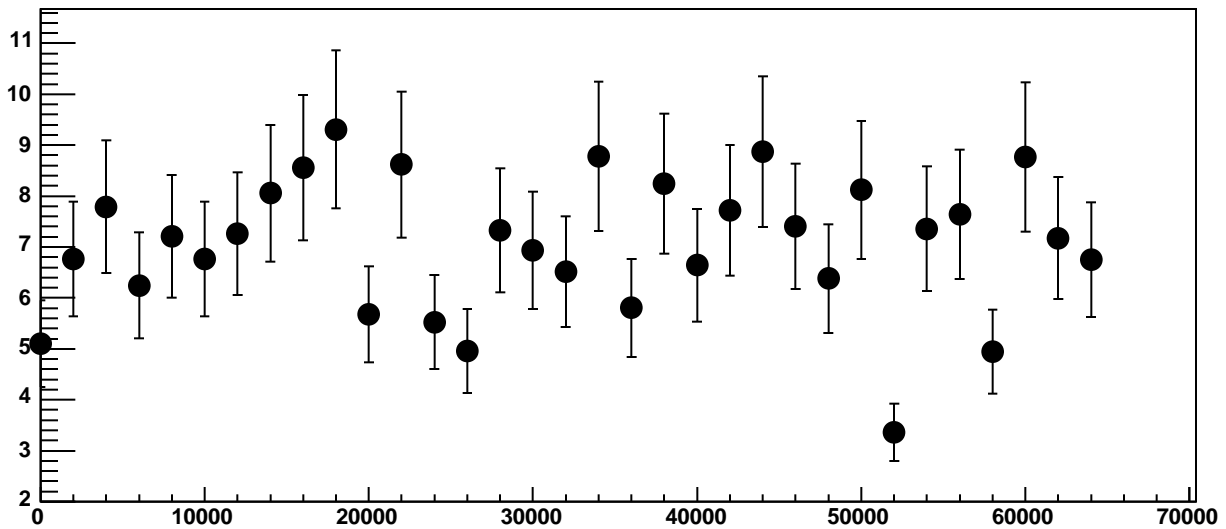
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



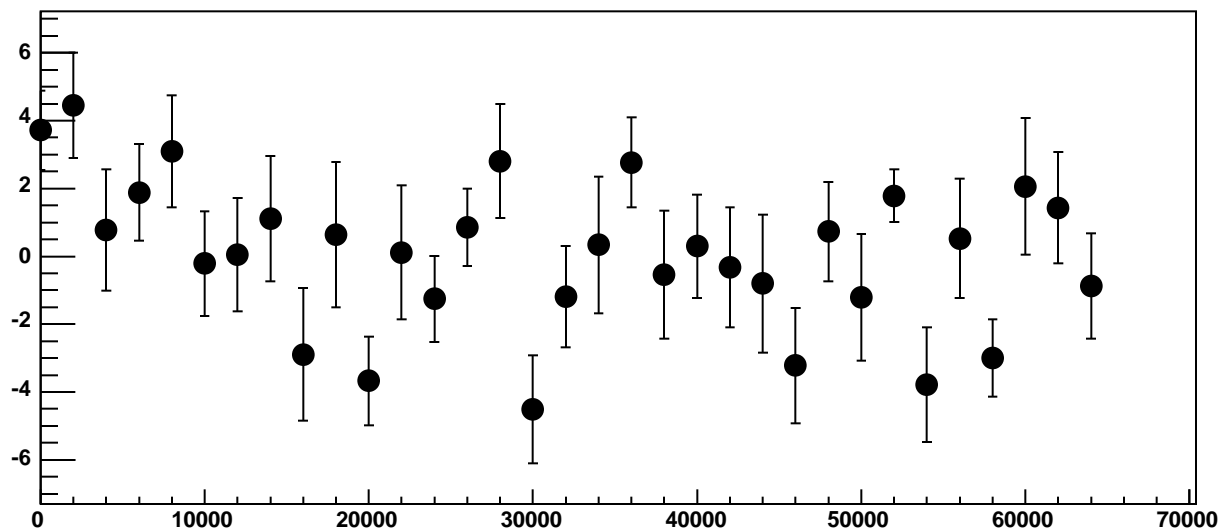
Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



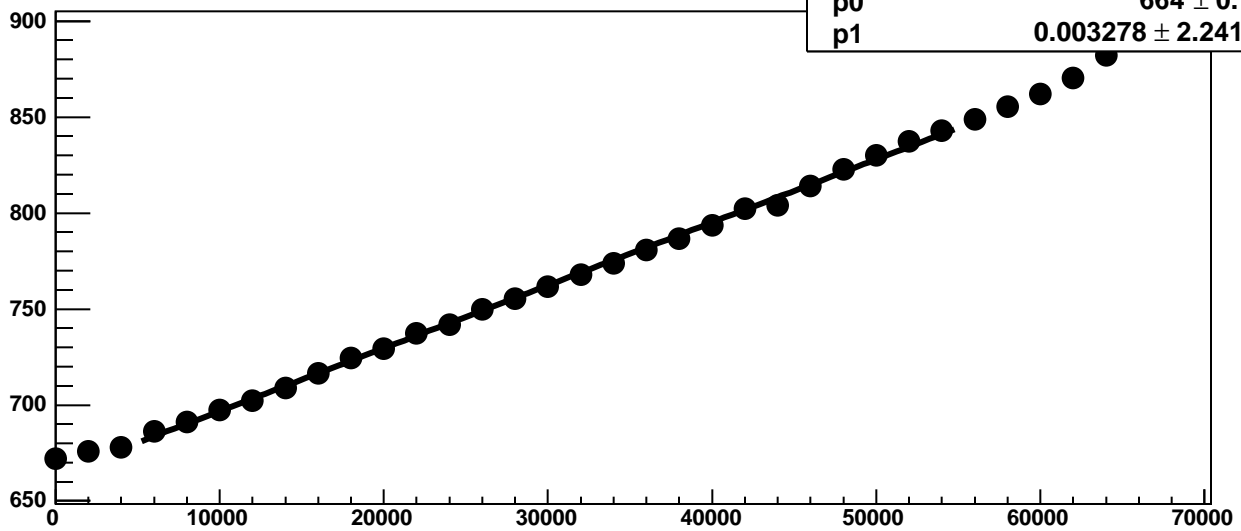
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.32 / 23

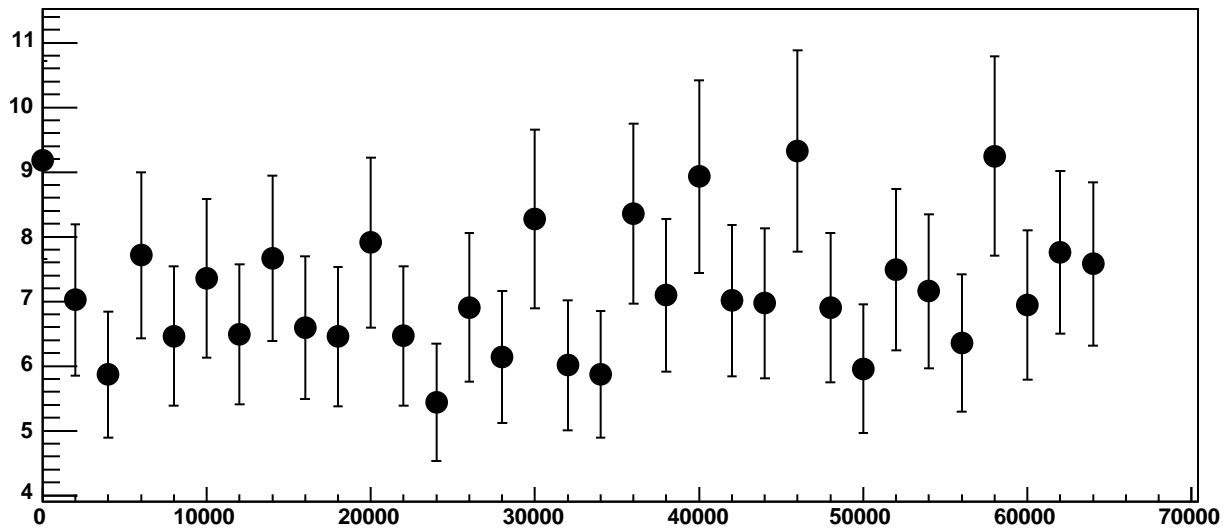
p0

664 ± 0.7338

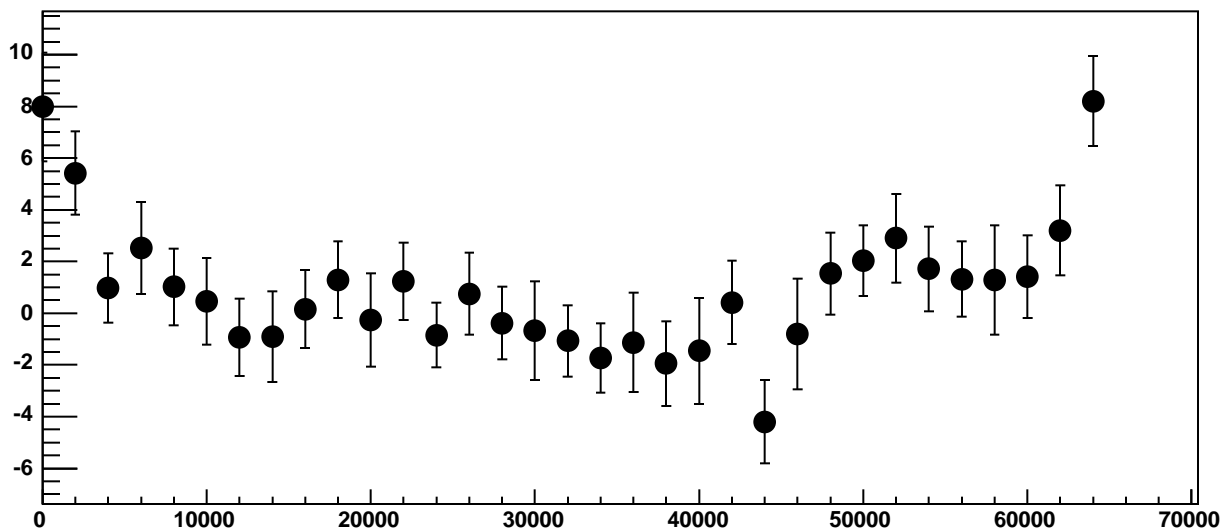
p1

$0.003278 \pm 2.241\text{e-}05$

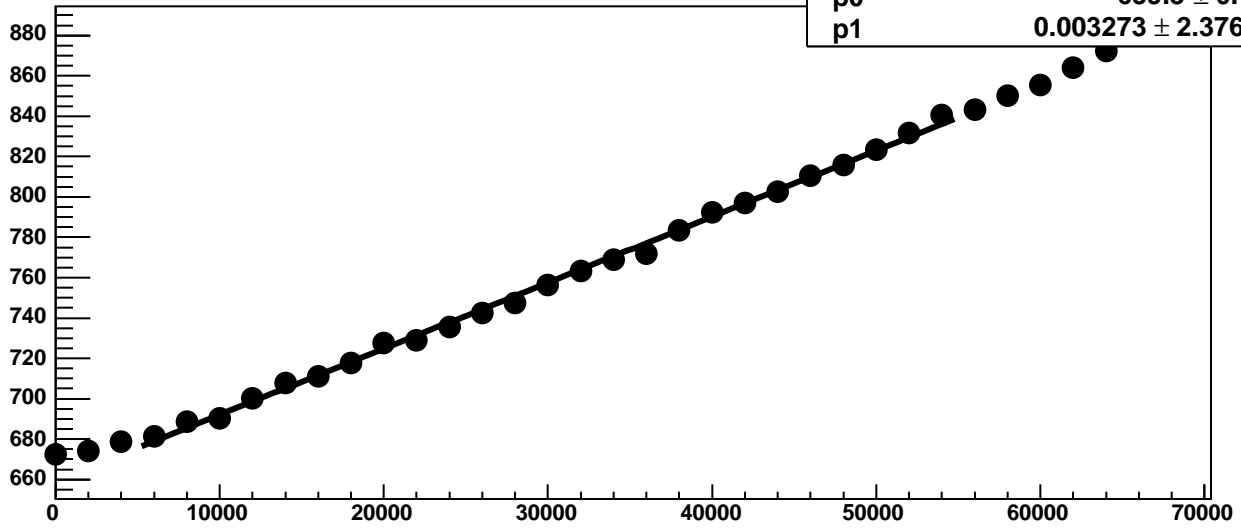
Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

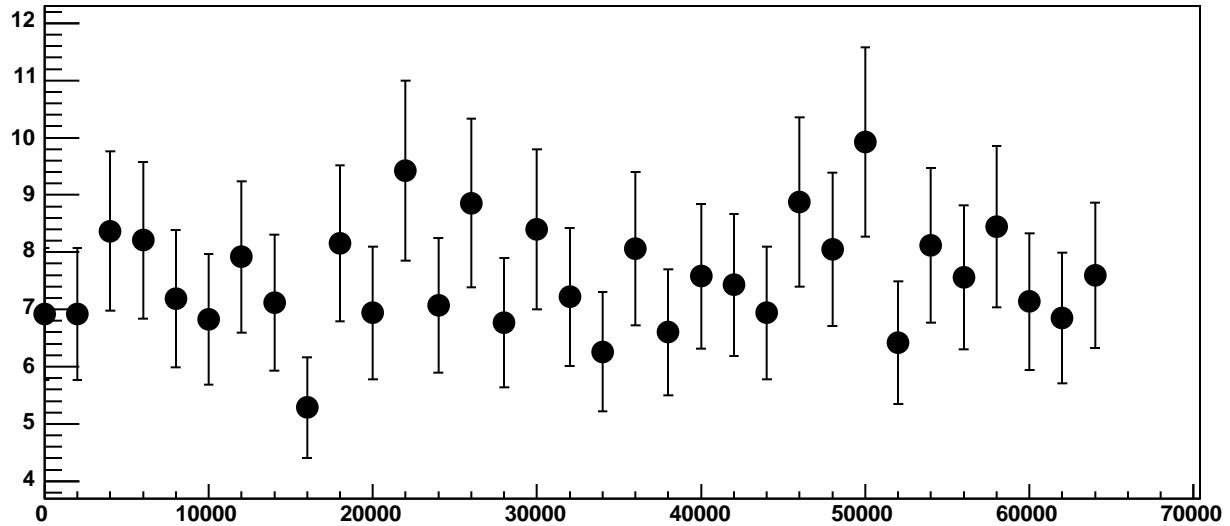


Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

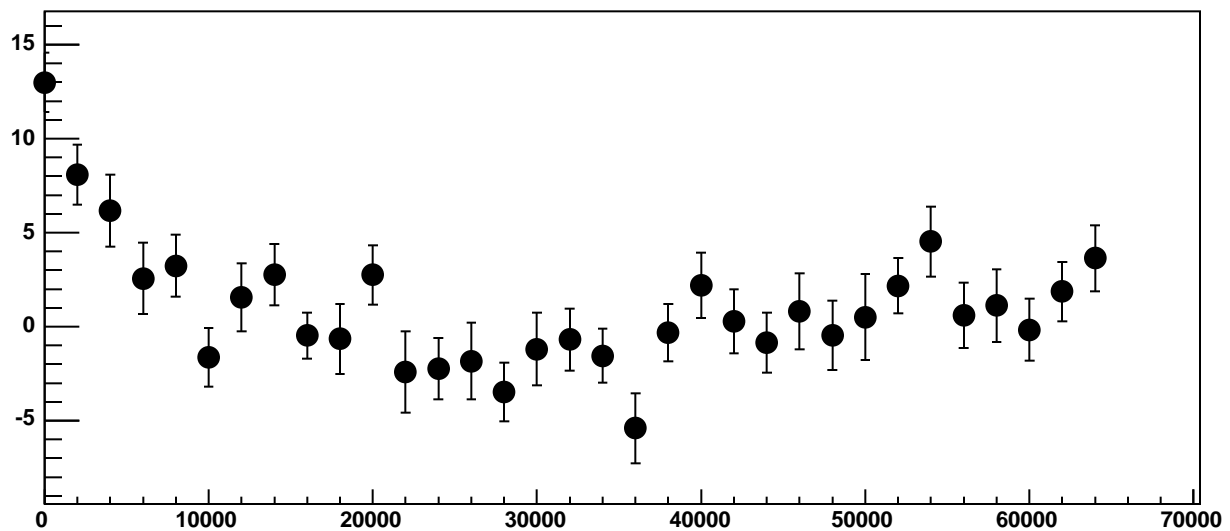


χ^2 / ndf 43.2 / 23
p0 659.3 ± 0.7738
p1 $0.003273 \pm 2.376e-05$

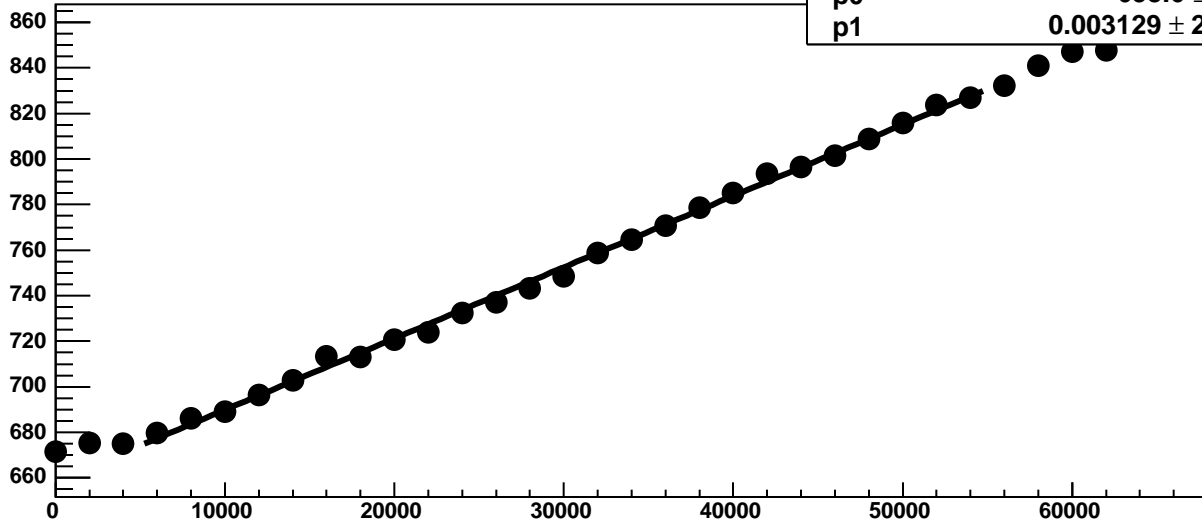
Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.49 / 23

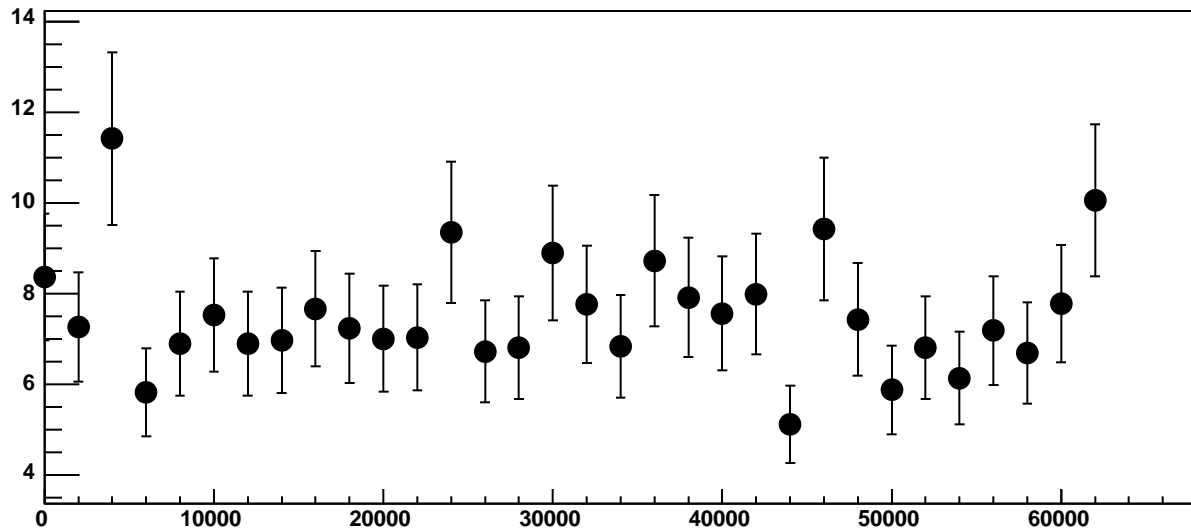
p0

658.6 ± 0.7226

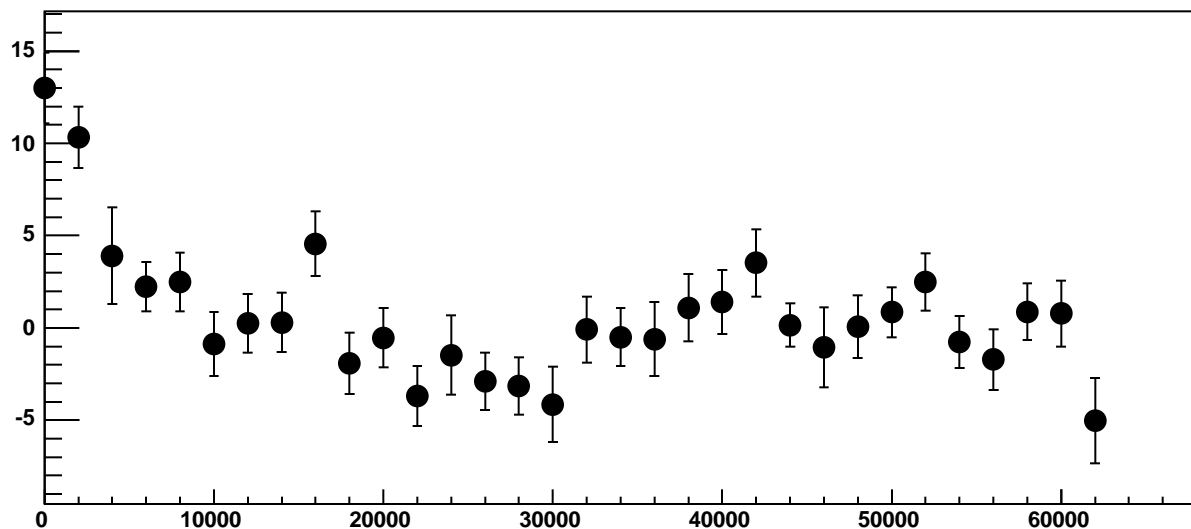
p1

$0.003129 \pm 2.13e-05$

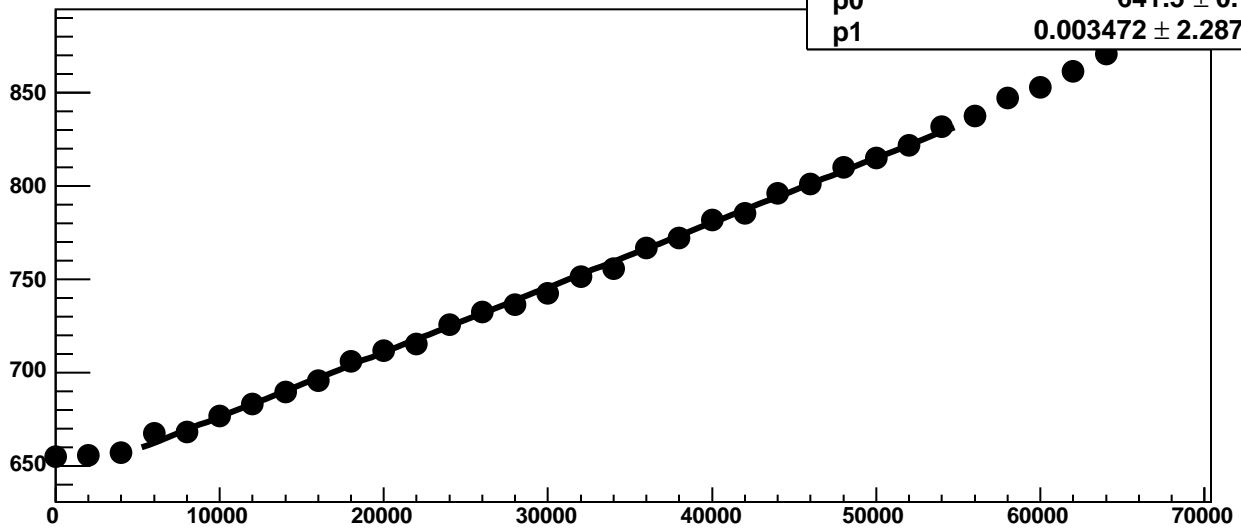
Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.08 / 23

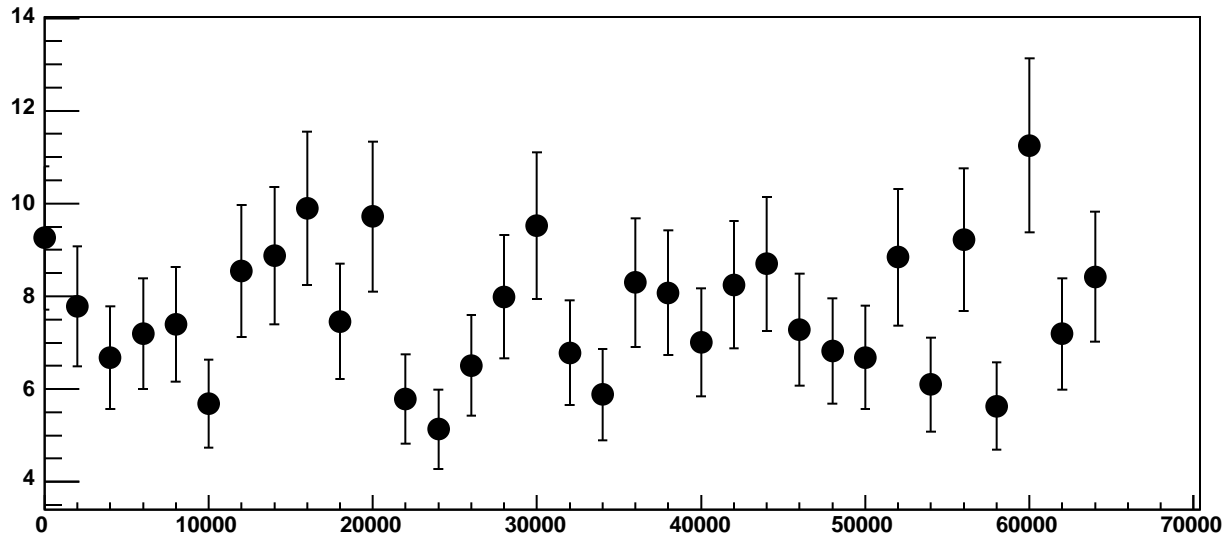
p0

641.5 ± 0.7617

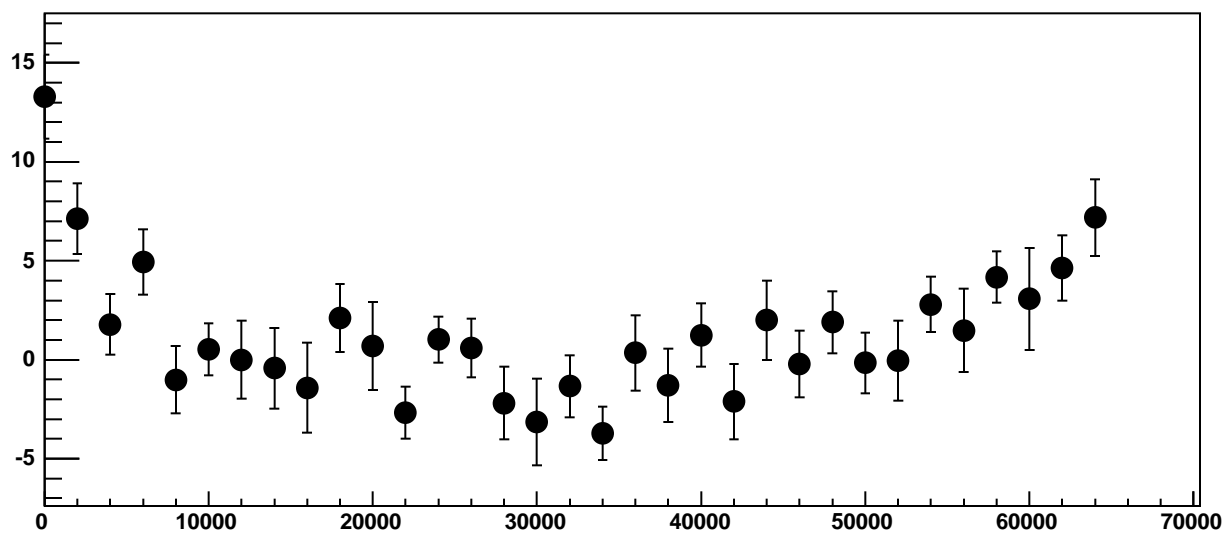
p1

$0.003472 \pm 2.287\text{e-}05$

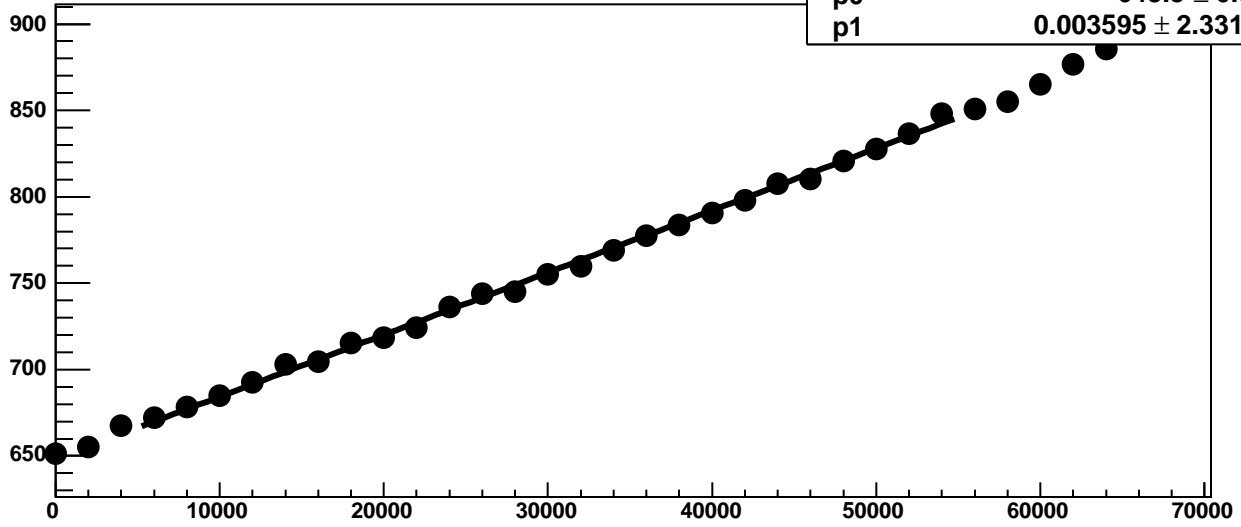
Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

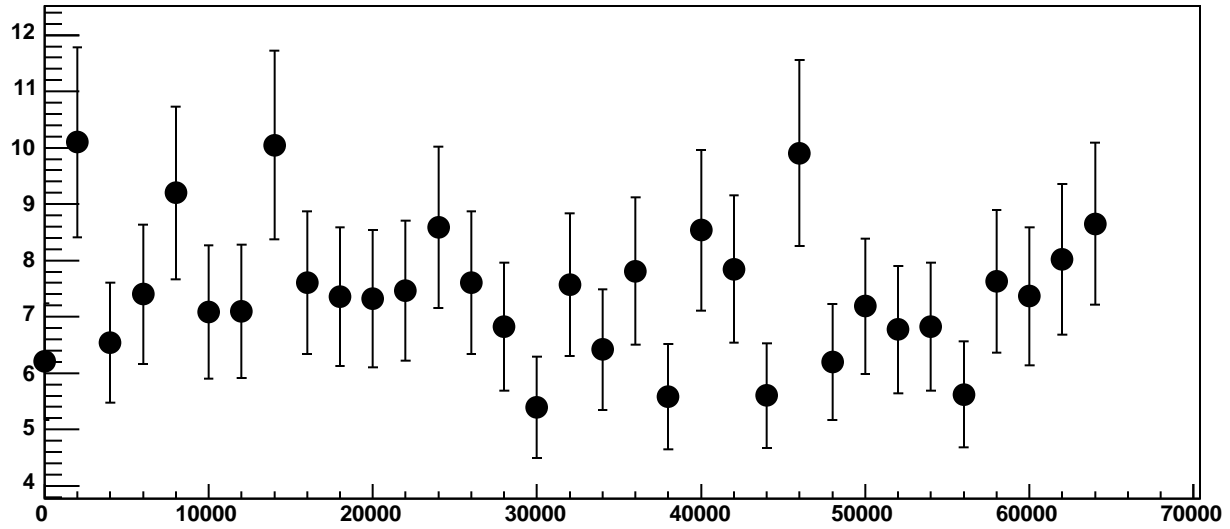


Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

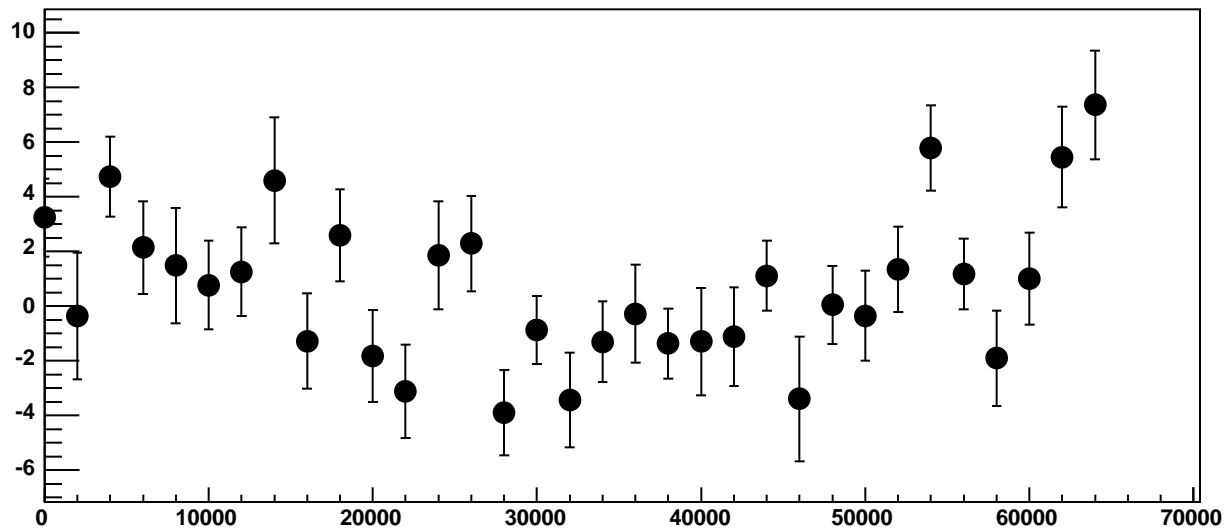


χ^2 / ndf 47.68 / 23
p0 648.3 ± 0.8016
p1 $0.003595 \pm 2.331e-05$

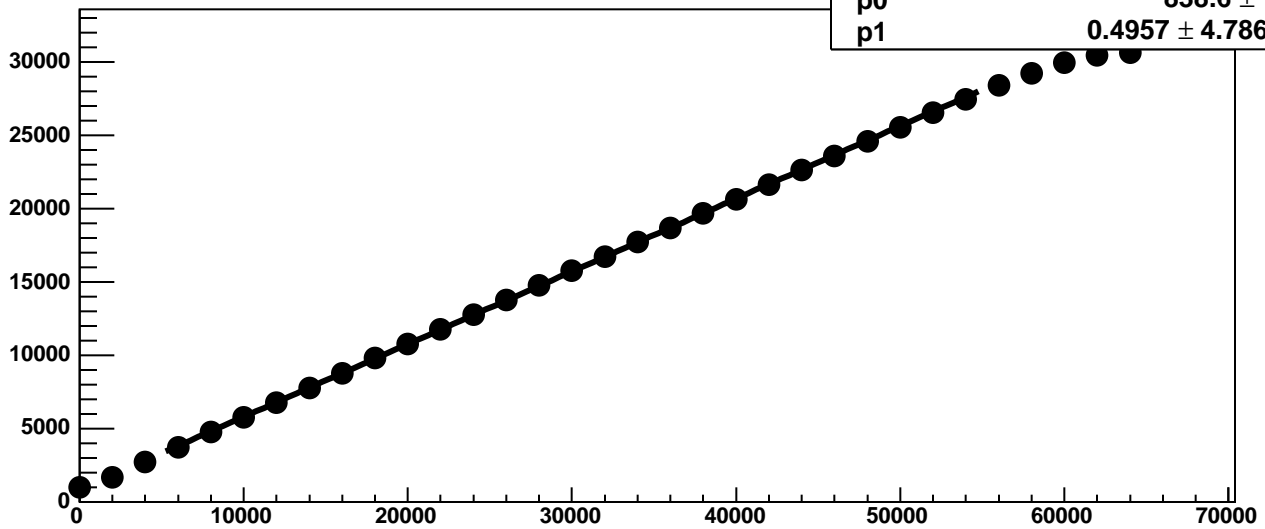
Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



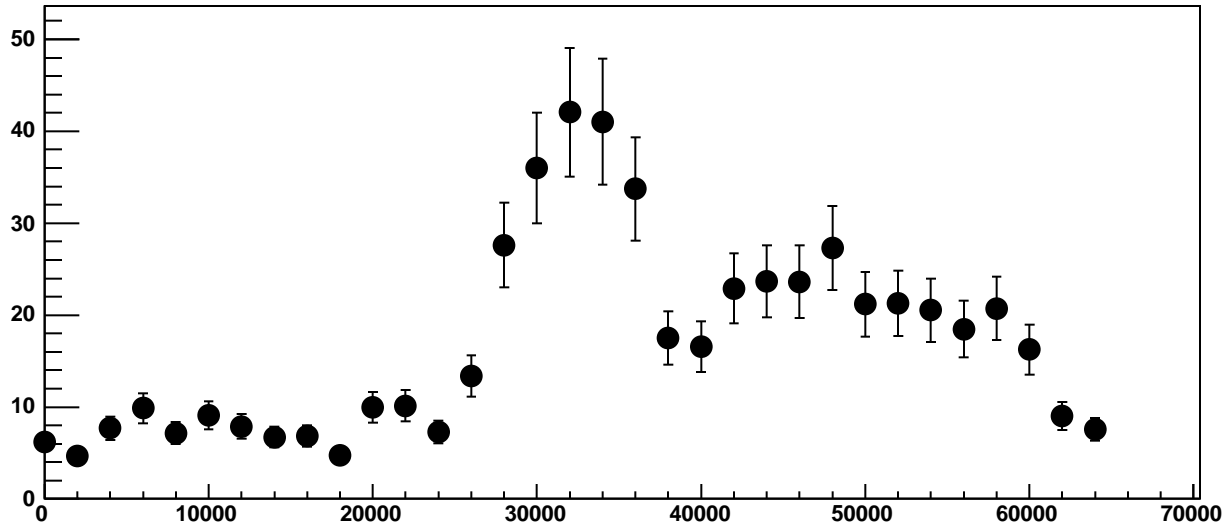
Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



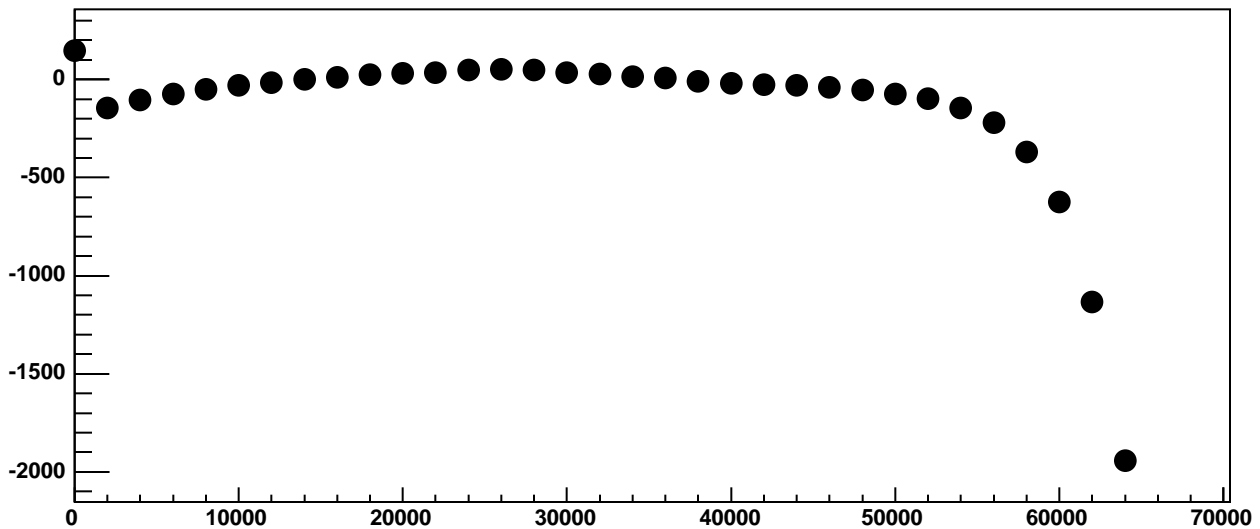
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



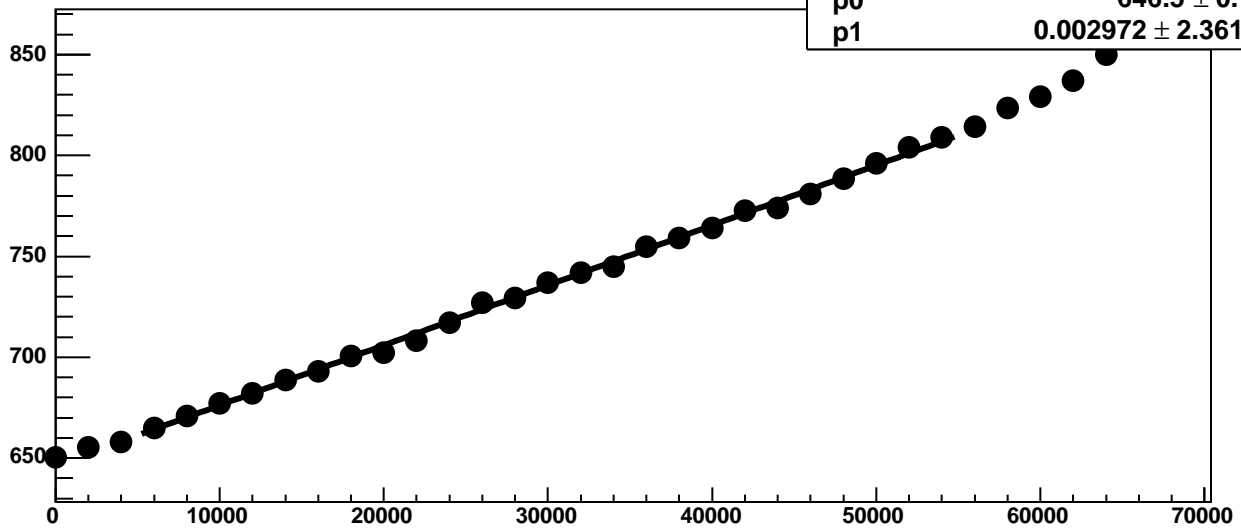
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

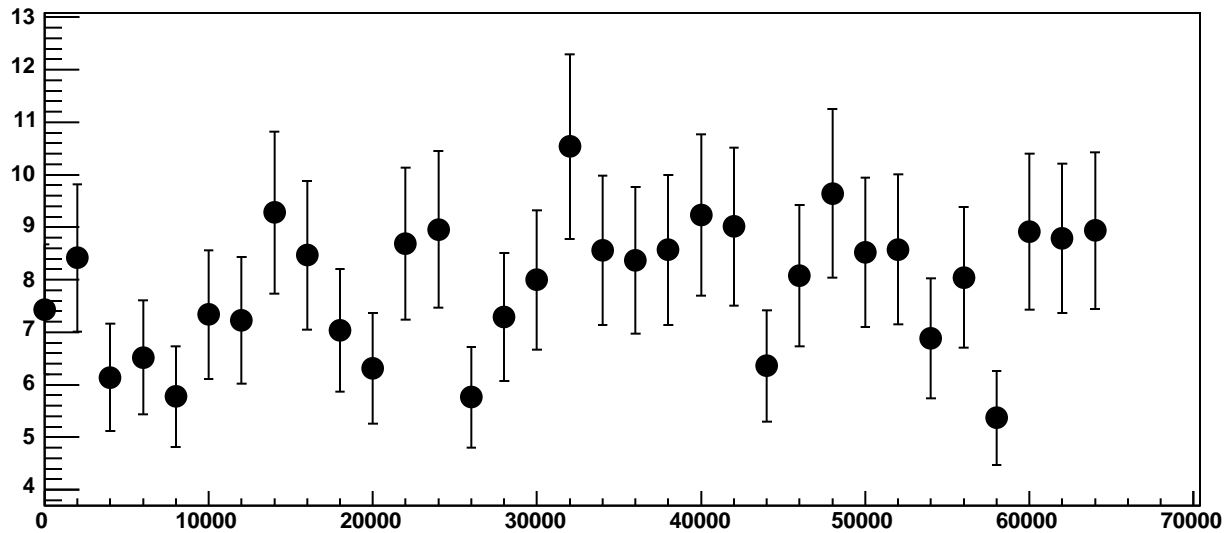


Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

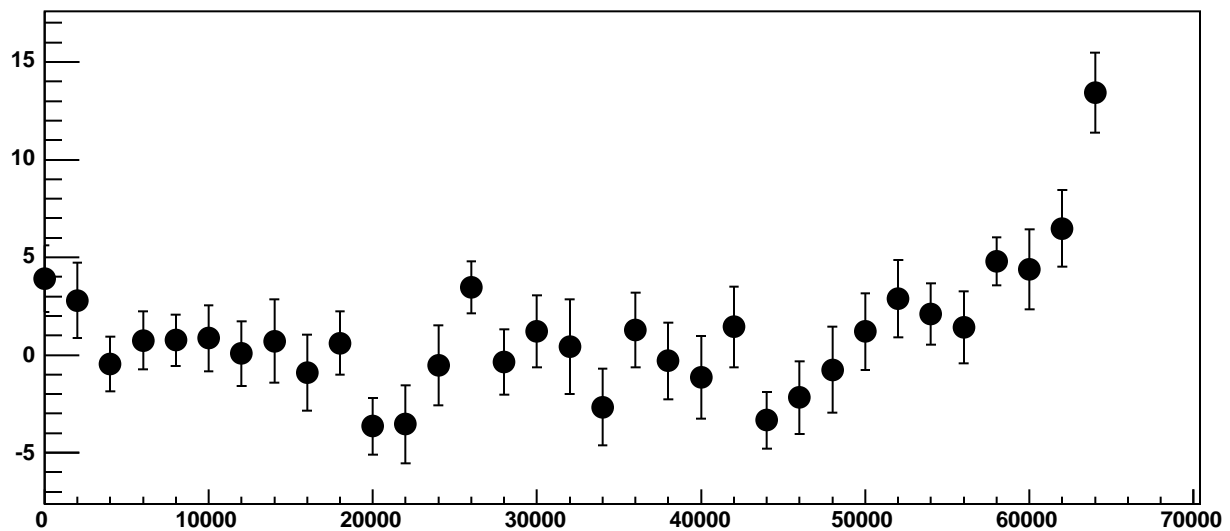


χ^2 / ndf 32.41 / 23
p0 646.5 ± 0.7533
p1 $0.002972 \pm 2.361e-05$

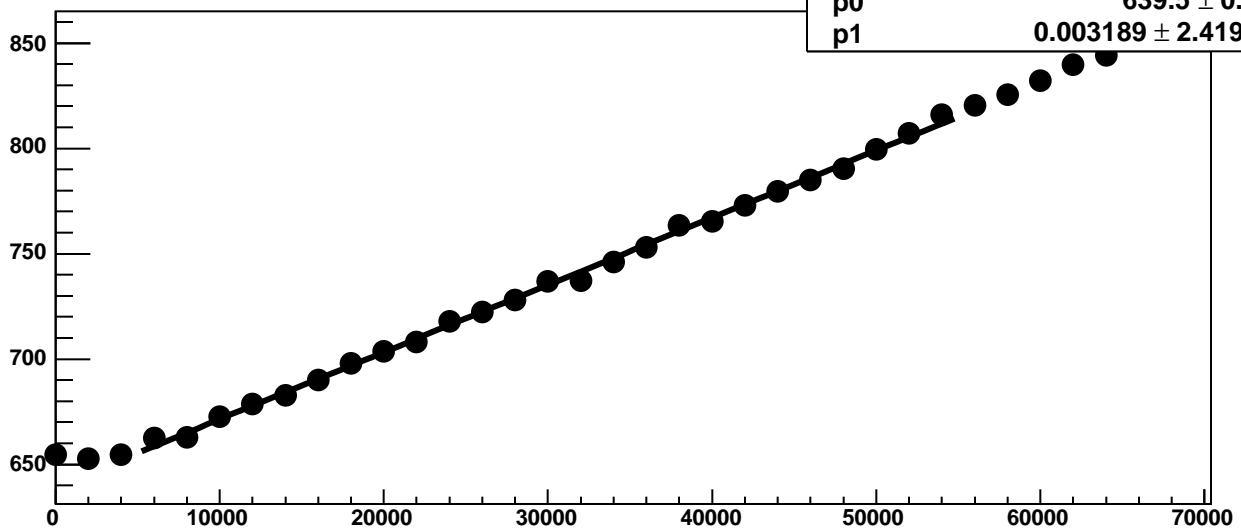
Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

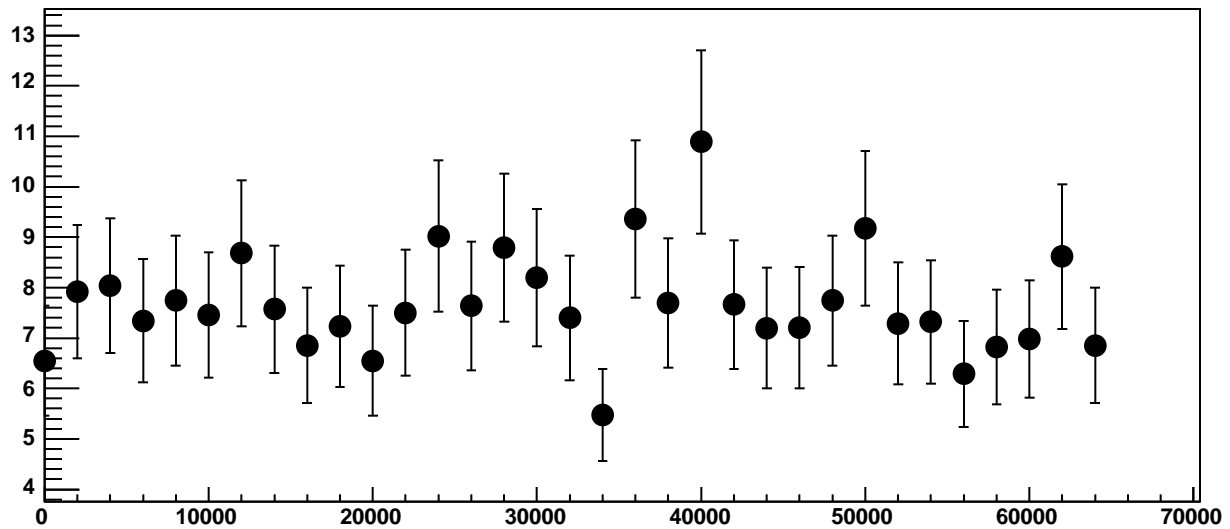


Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

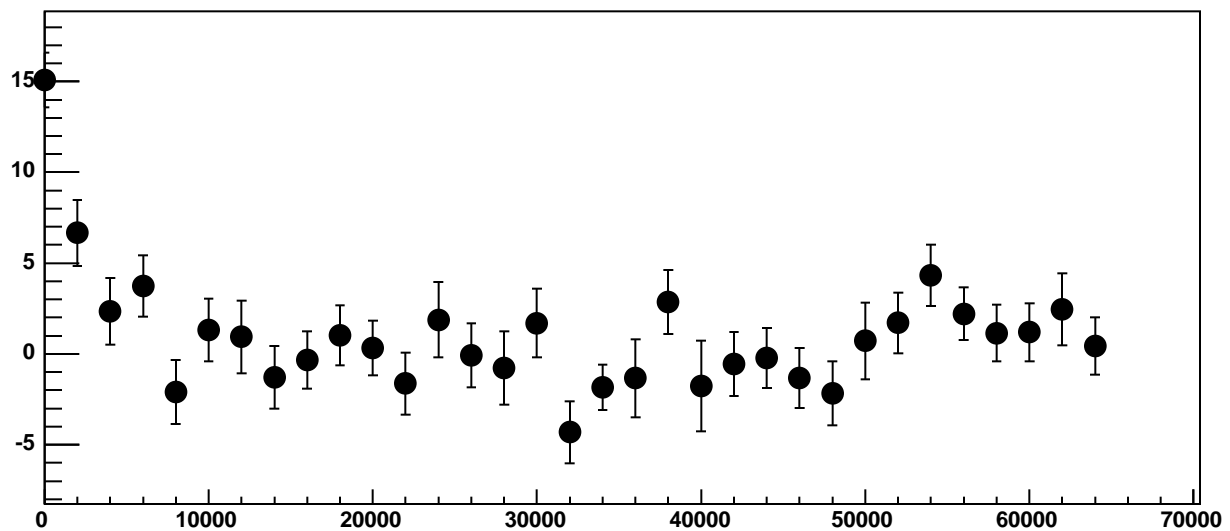


χ^2 / ndf 33.05 / 23
p0 639.5 ± 0.7991
p1 $0.003189 \pm 2.419\text{e-}05$

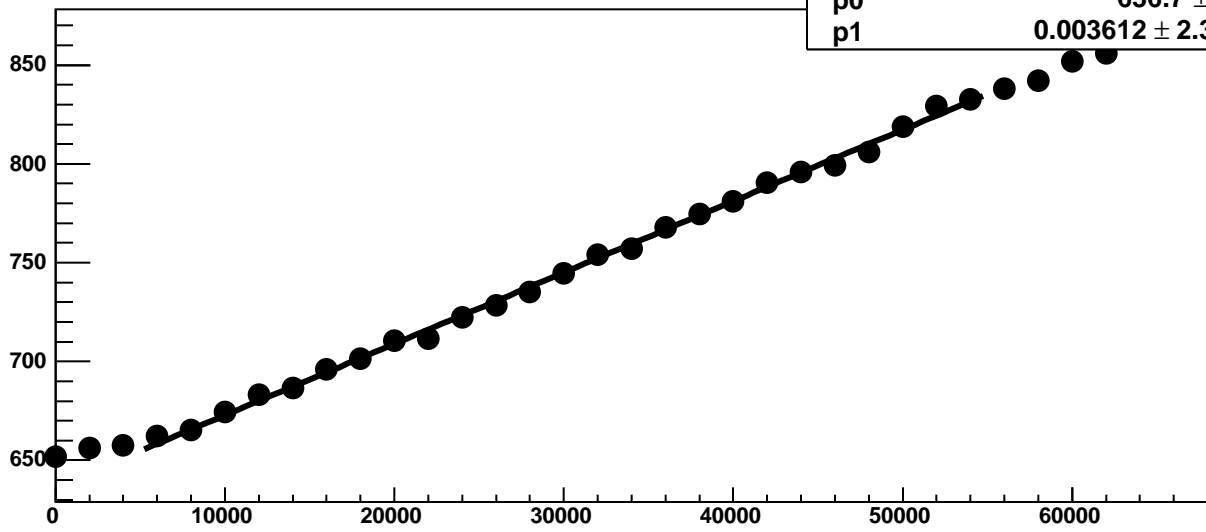
Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

53.88 / 23

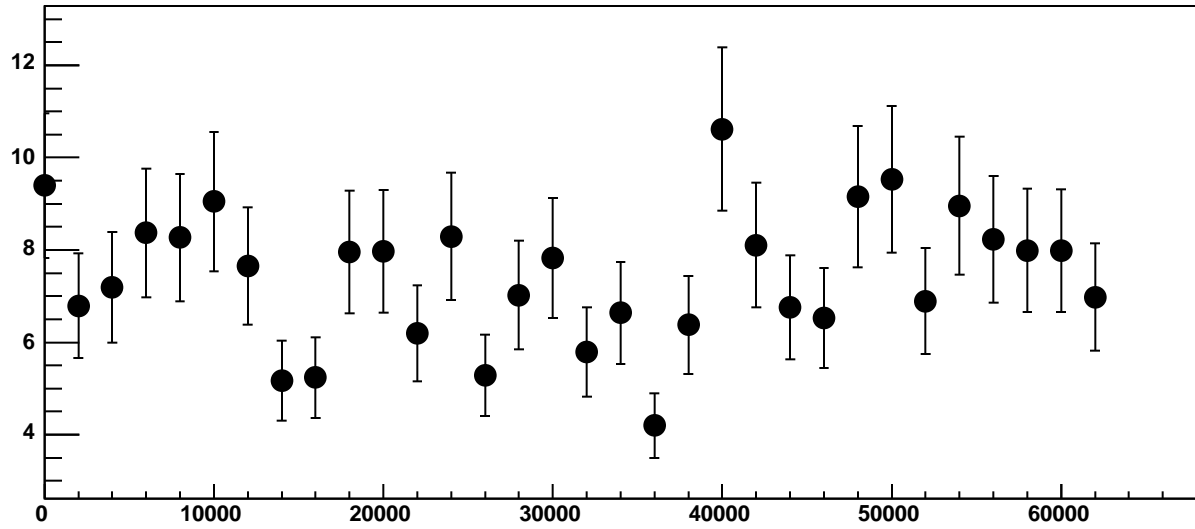
p0

636.7 ± 0.7699

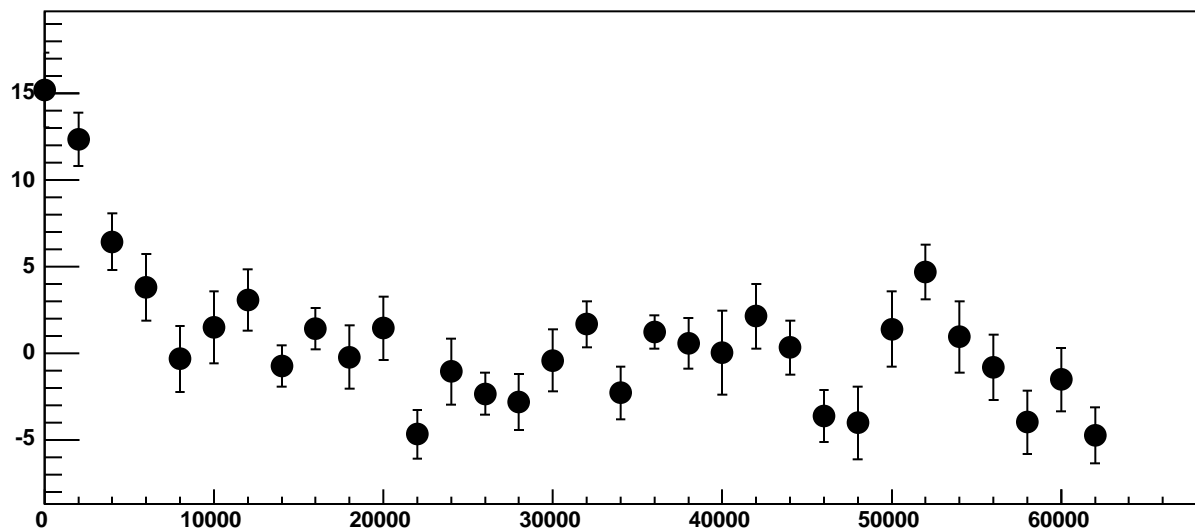
p1

$0.003612 \pm 2.391e-05$

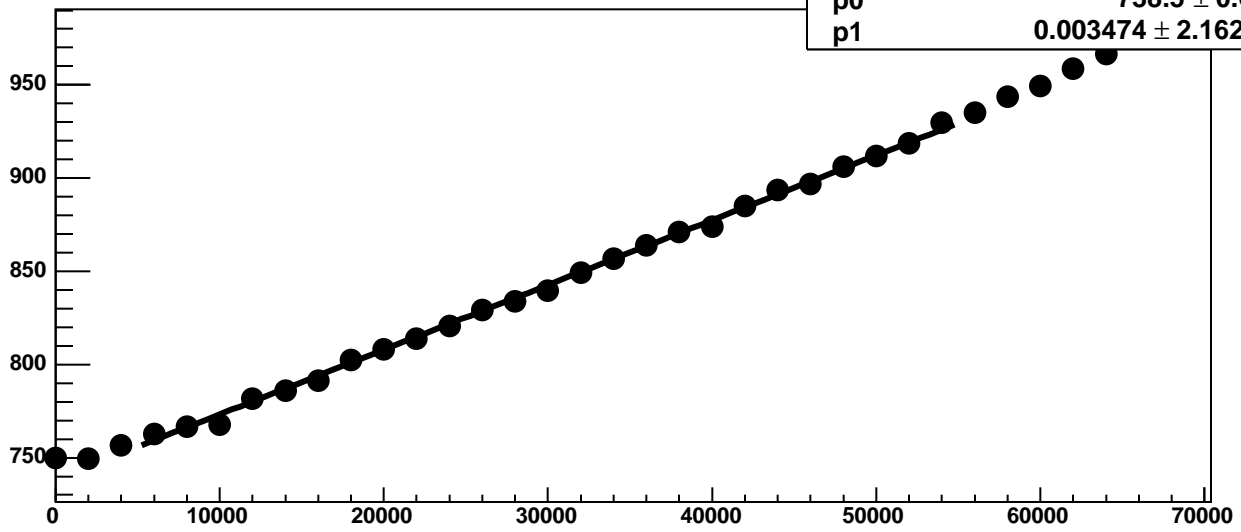
Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

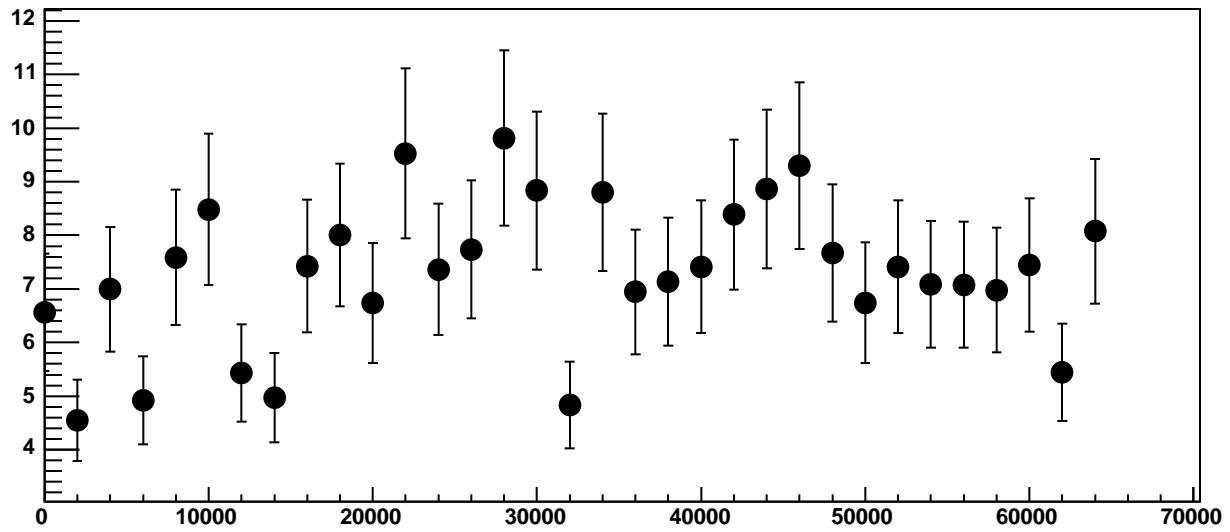


Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

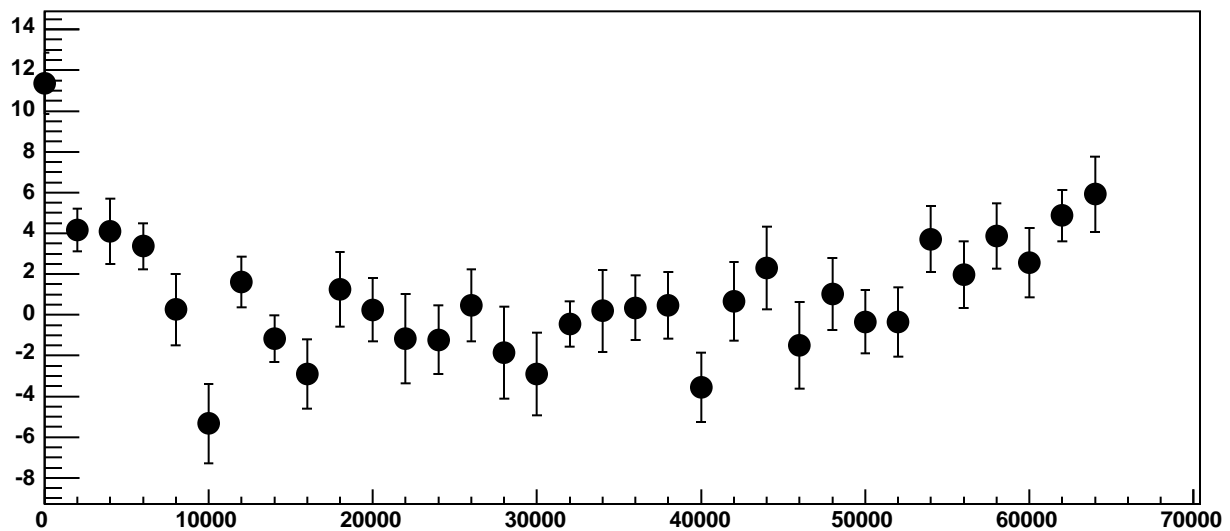


χ^2 / ndf 38.28 / 23
p0 738.5 ± 0.6829
p1 $0.003474 \pm 2.162e-05$

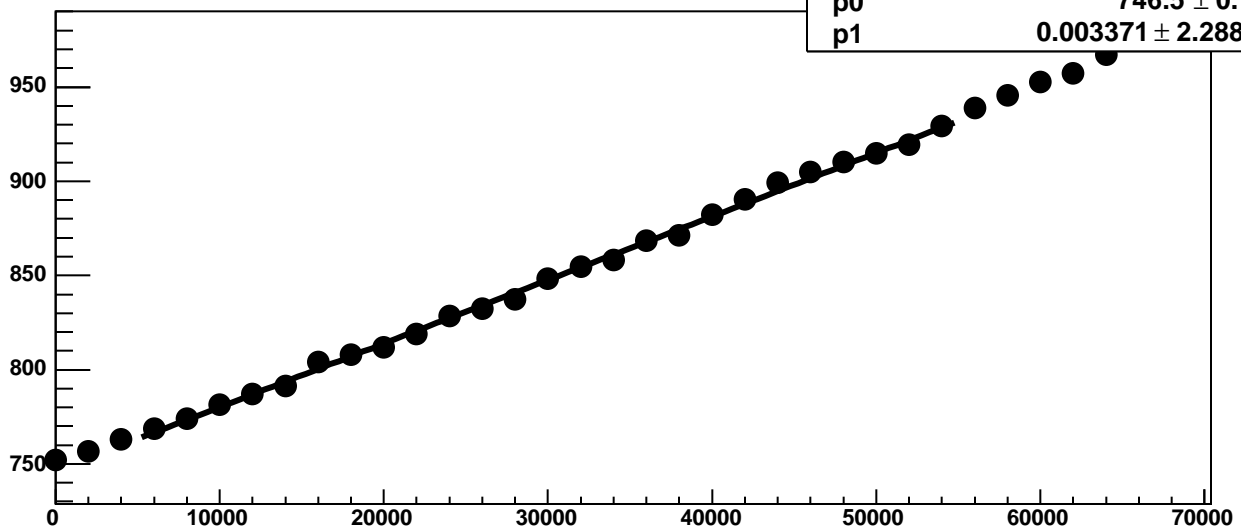
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

46.21 / 23

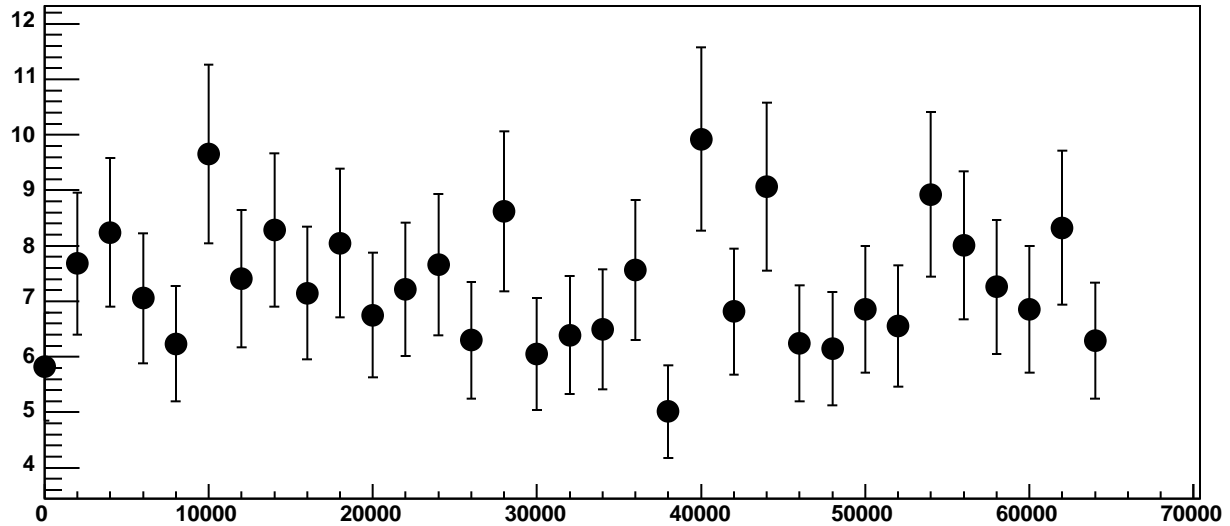
p0

746.5 ± 0.7719

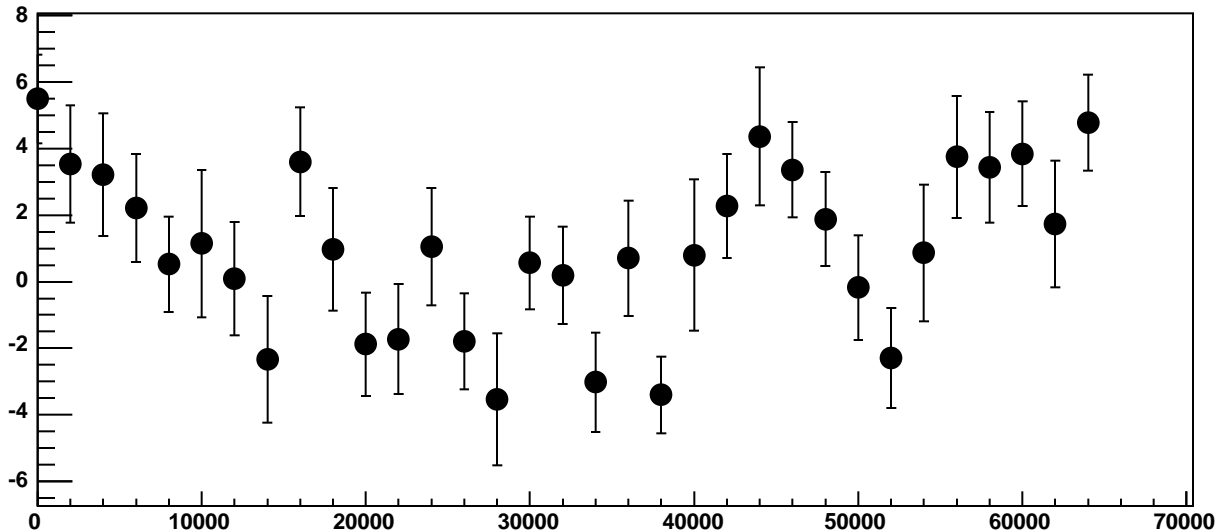
p1

$0.003371 \pm 2.288e-05$

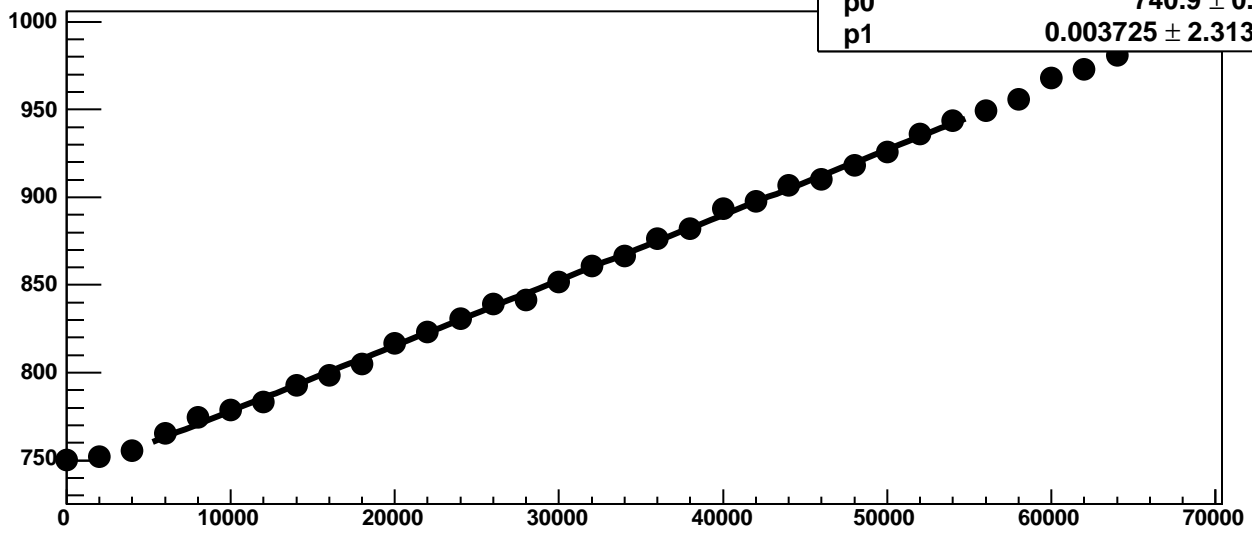
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

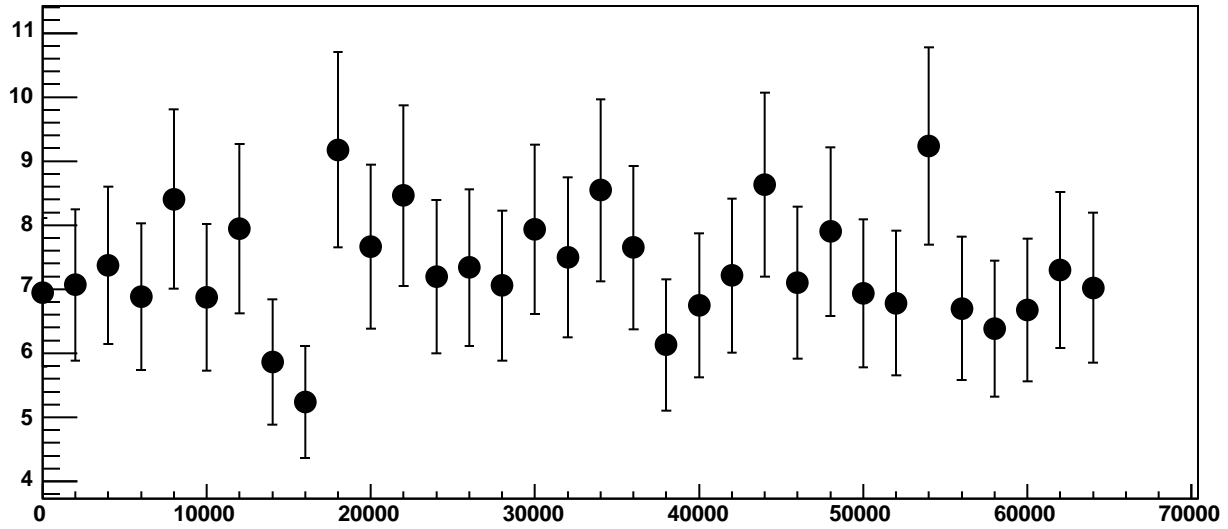


Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

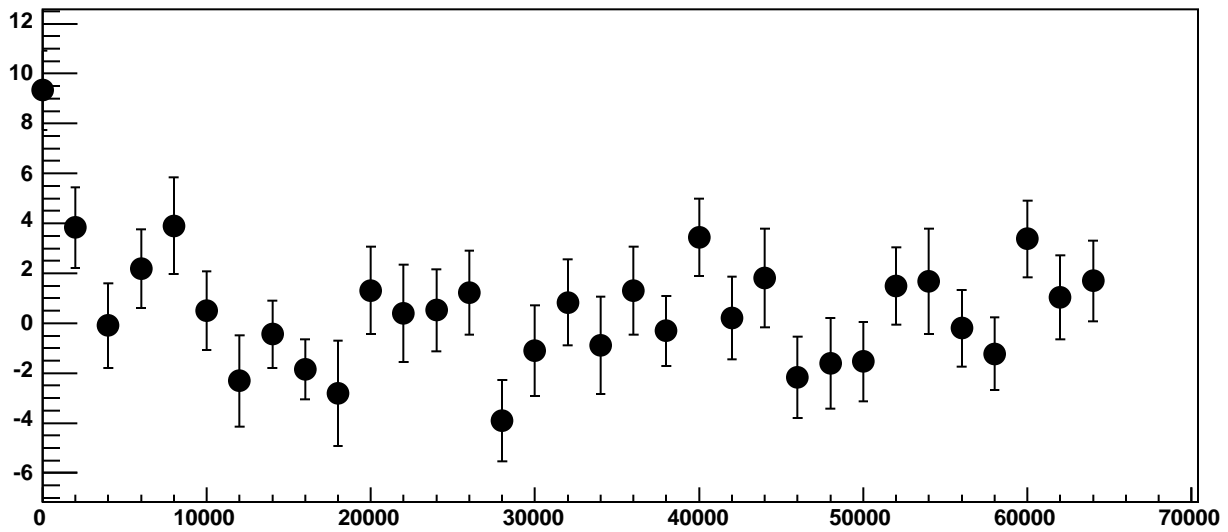


χ^2 / ndf 31.26 / 23
p0 740.9 ± 0.7551
p1 $0.003725 \pm 2.313e-05$

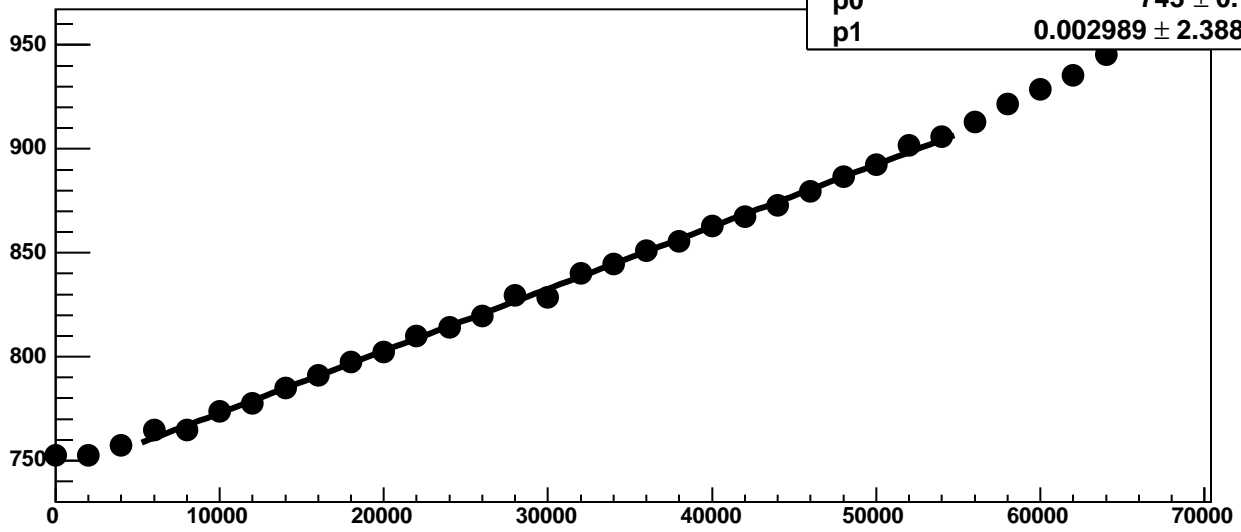
Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.7 / 23

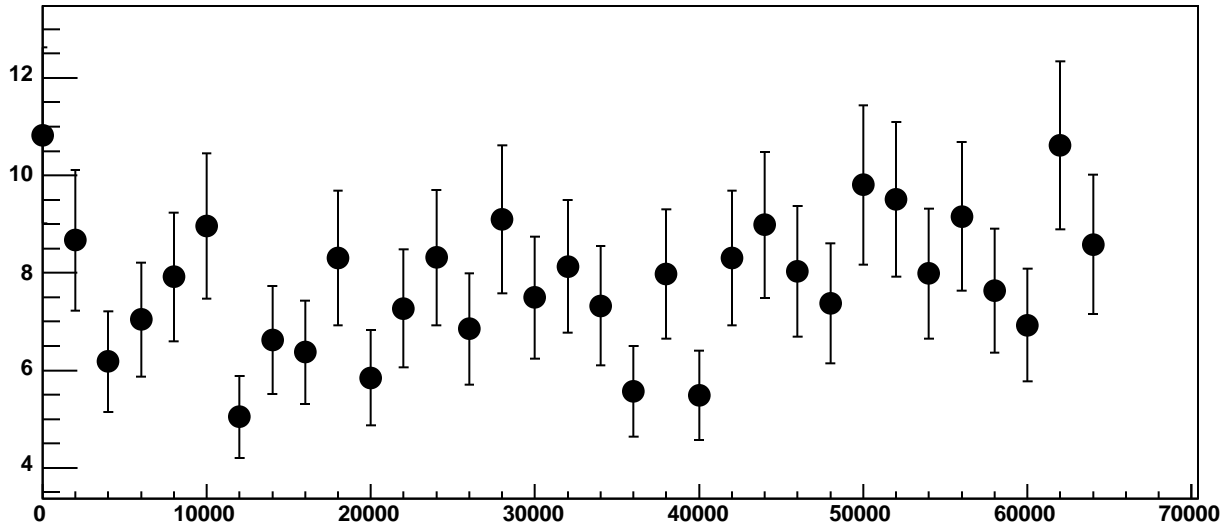
p0

743 ± 0.7533

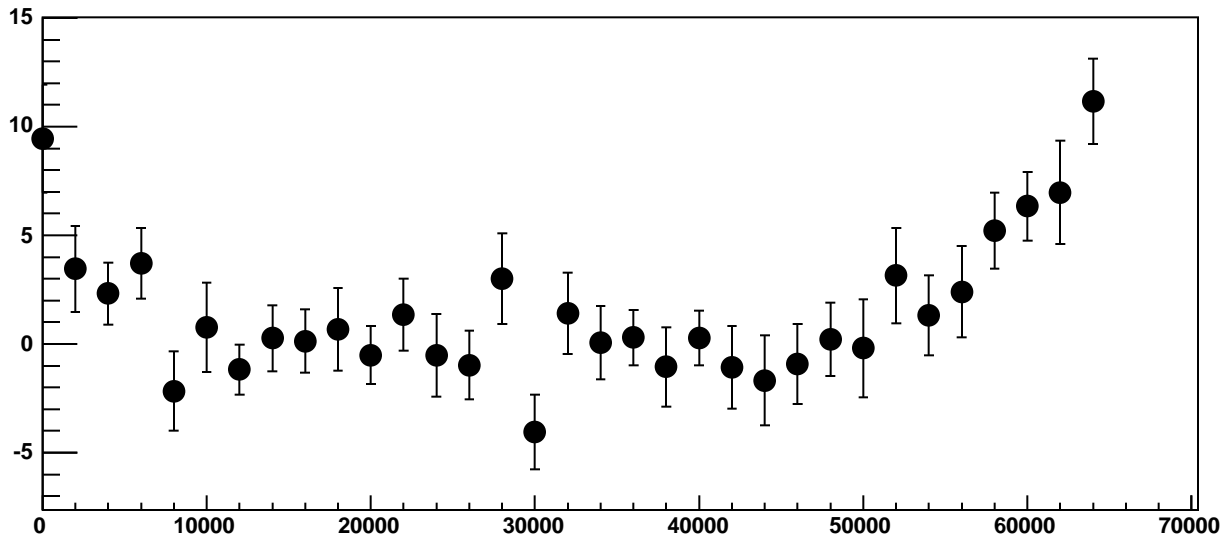
p1

$0.002989 \pm 2.388e-05$

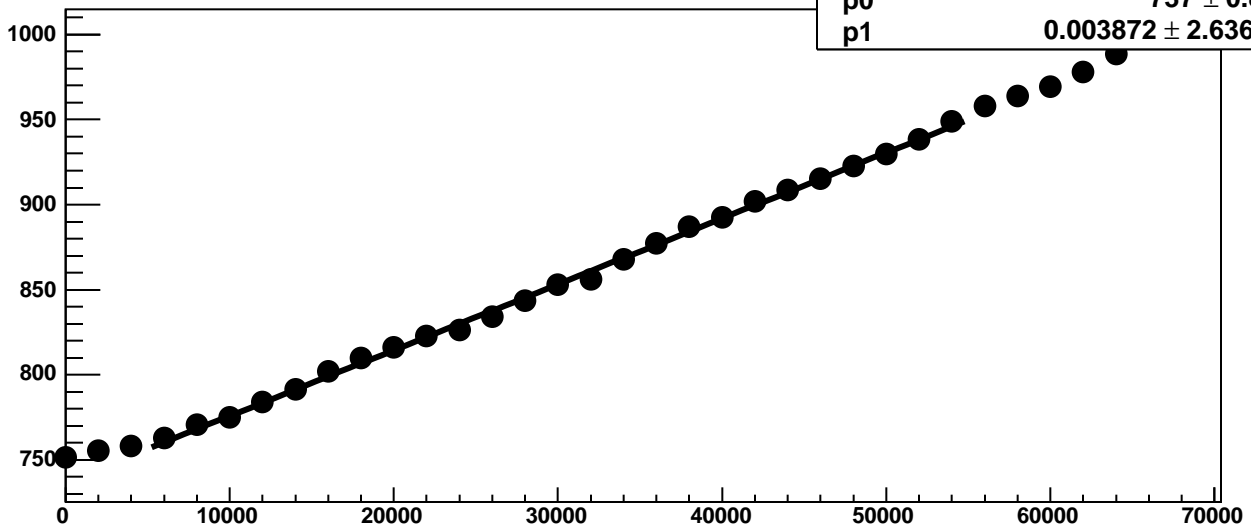
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

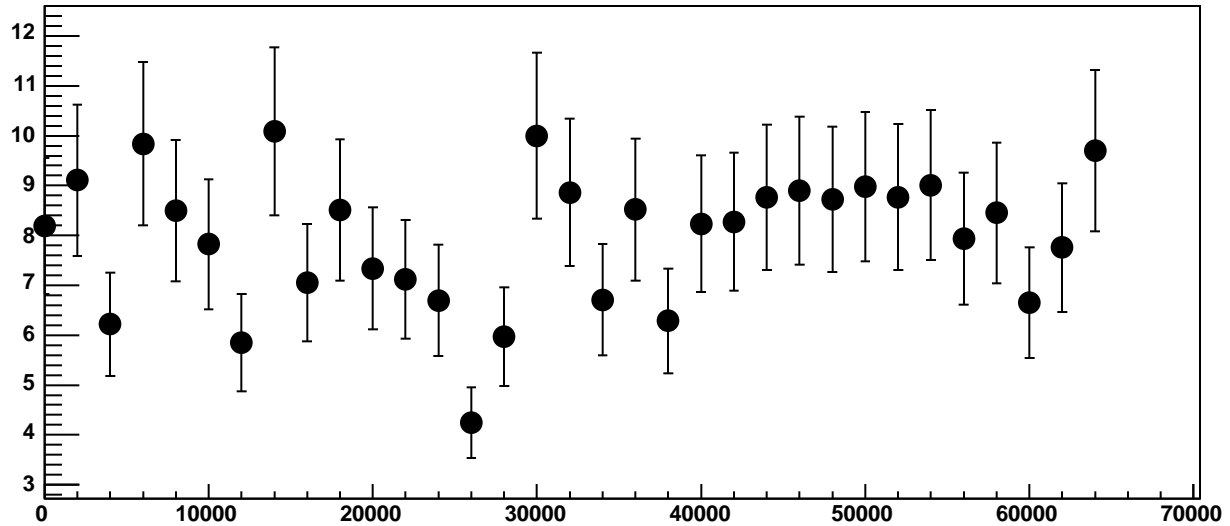


Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

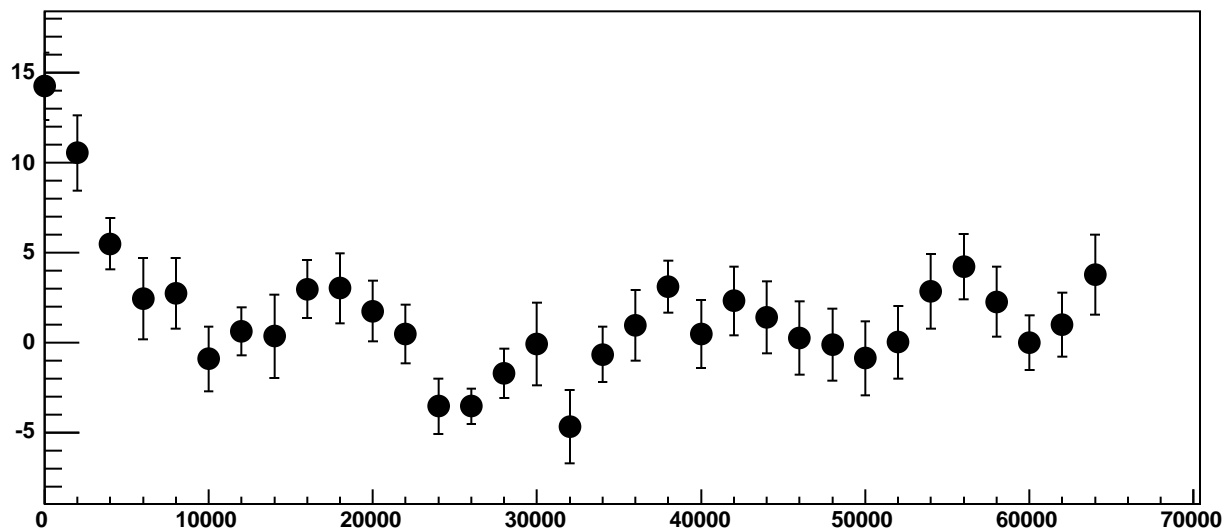


χ^2 / ndf 44.96 / 23
p0 737 ± 0.8302
p1 $0.003872 \pm 2.636e-05$

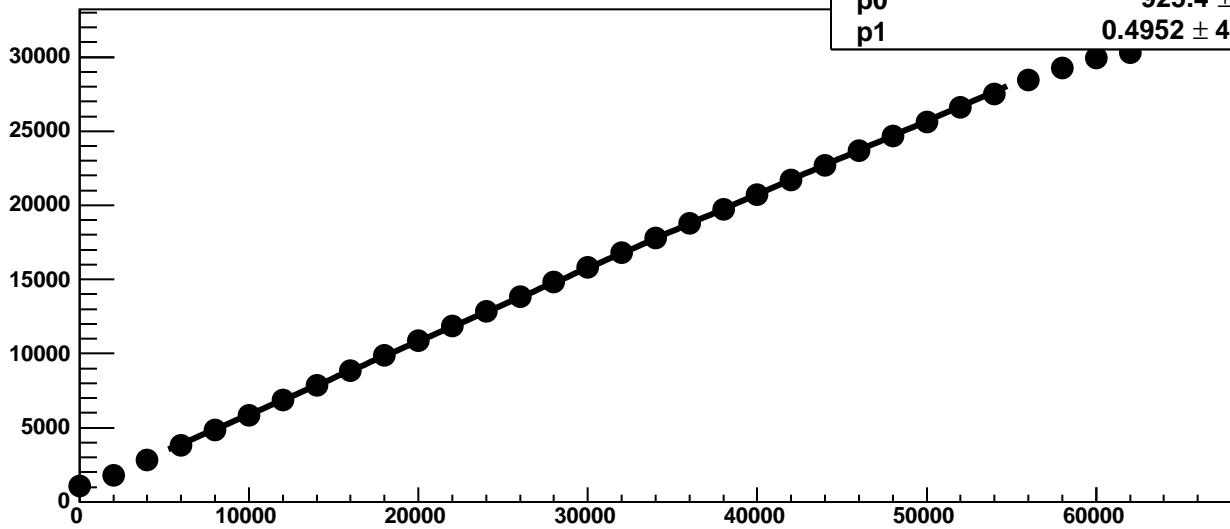
Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

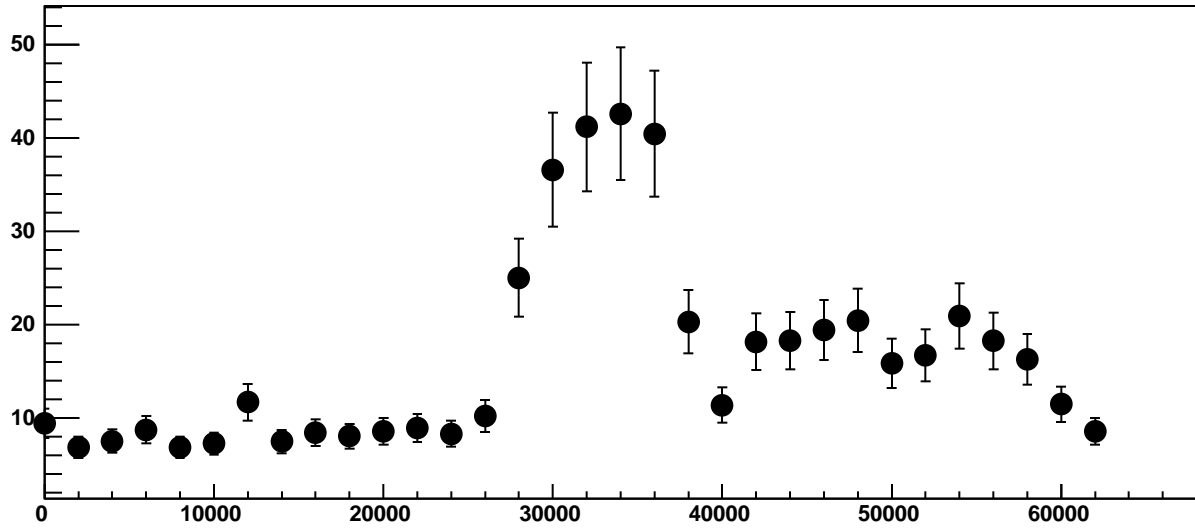


Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC

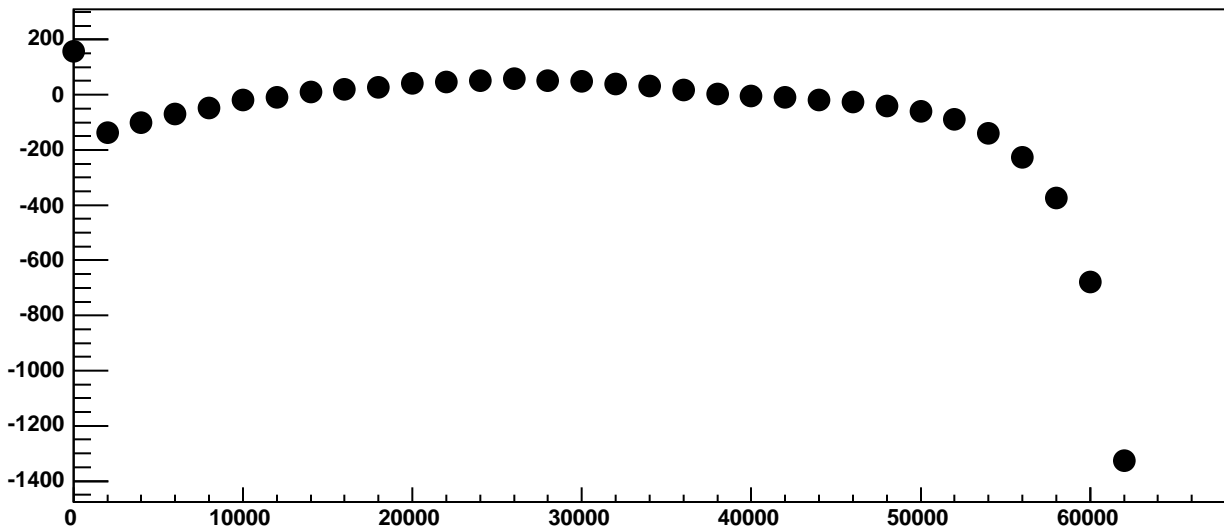


χ^2 / ndf 6877 / 23
p0 925.4 ± 0.9972
p1 0.4952 ± 4.15e-05

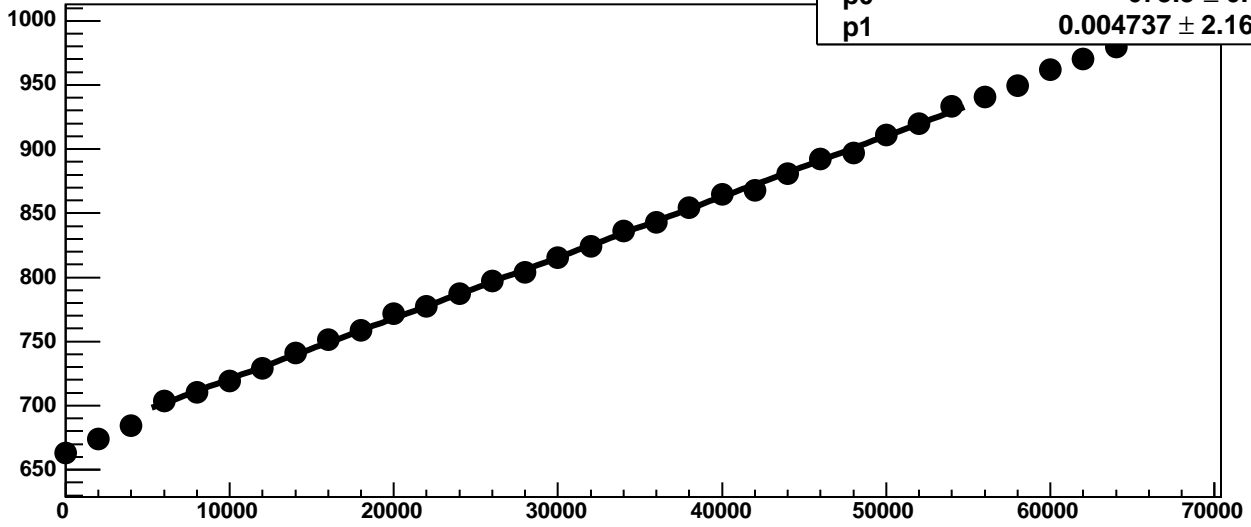
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.85 / 23

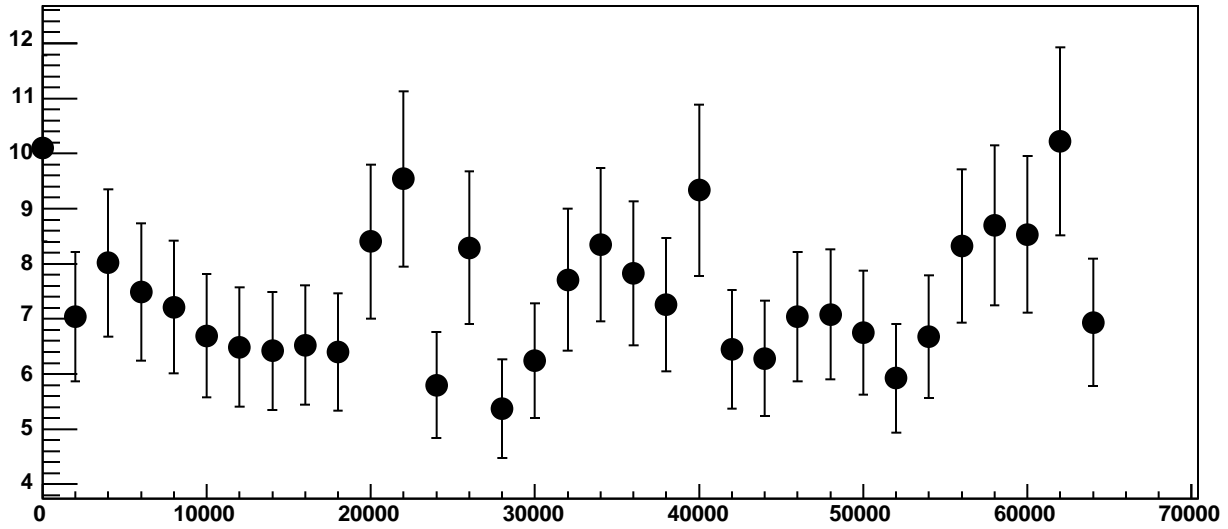
p0

673.3 ± 0.7253

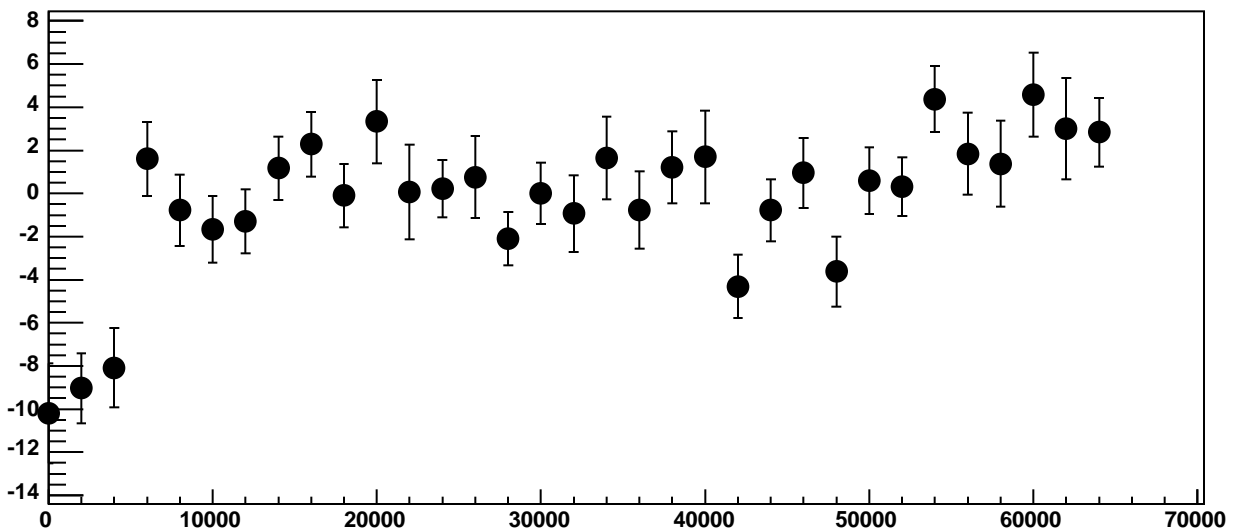
p1

$0.004737 \pm 2.16e-05$

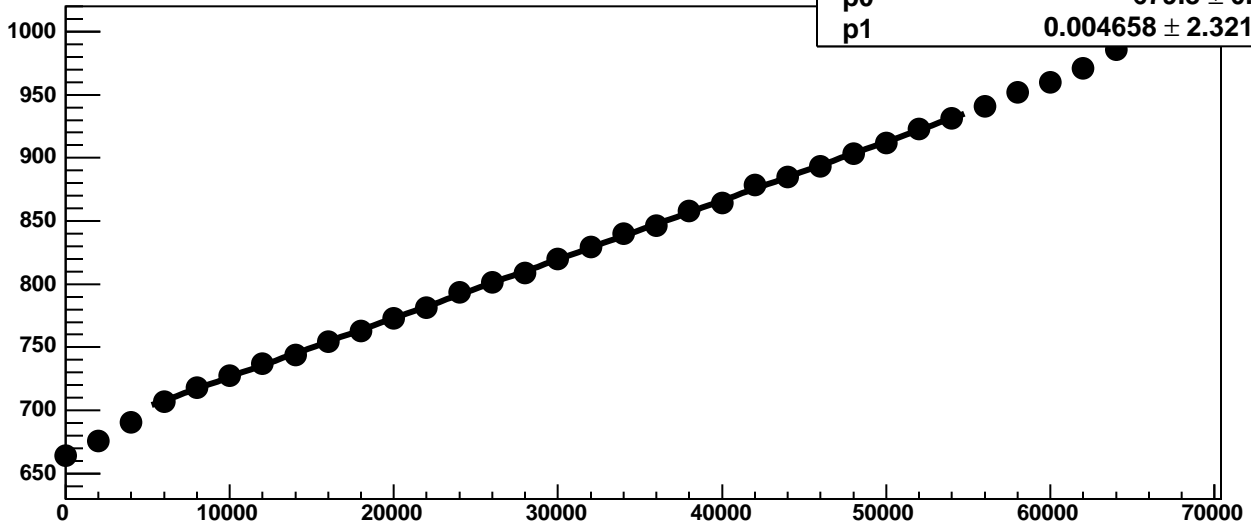
Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

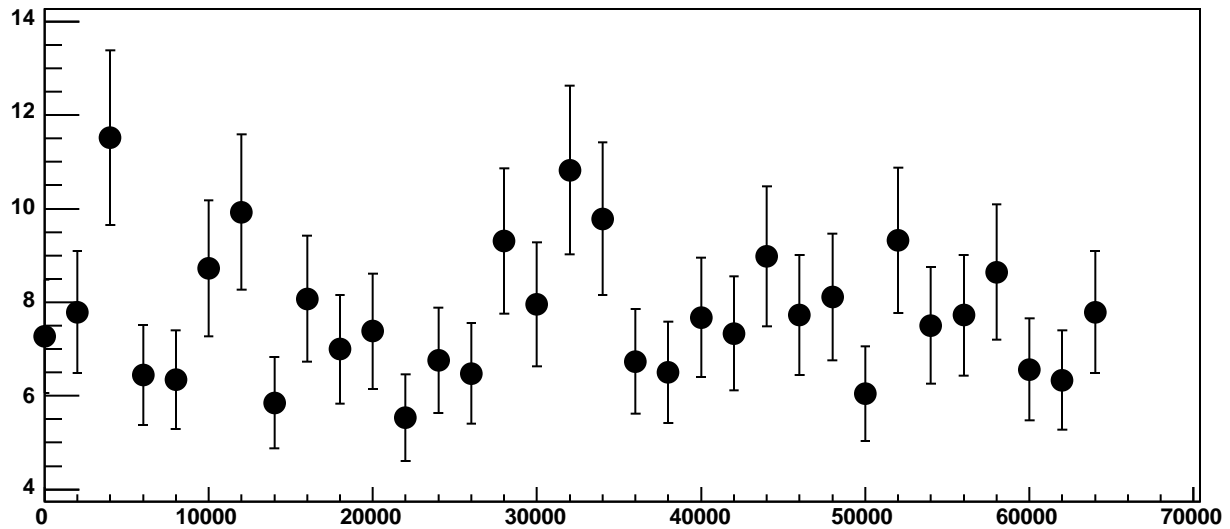


Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

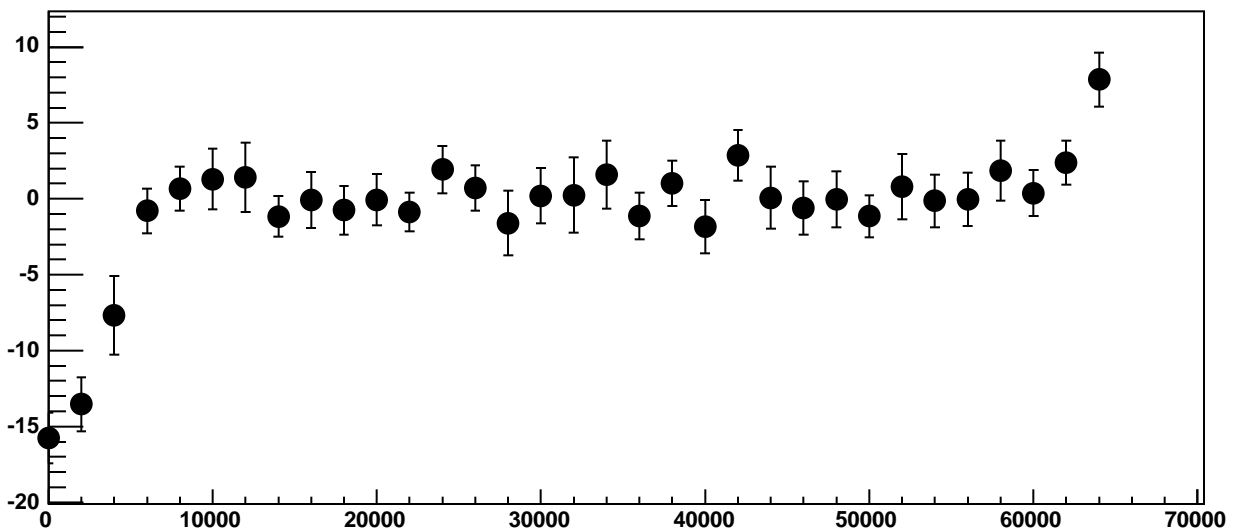


χ^2 / ndf 11.54 / 23
p0 679.8 ± 0.7521
p1 $0.004658 \pm 2.321e-05$

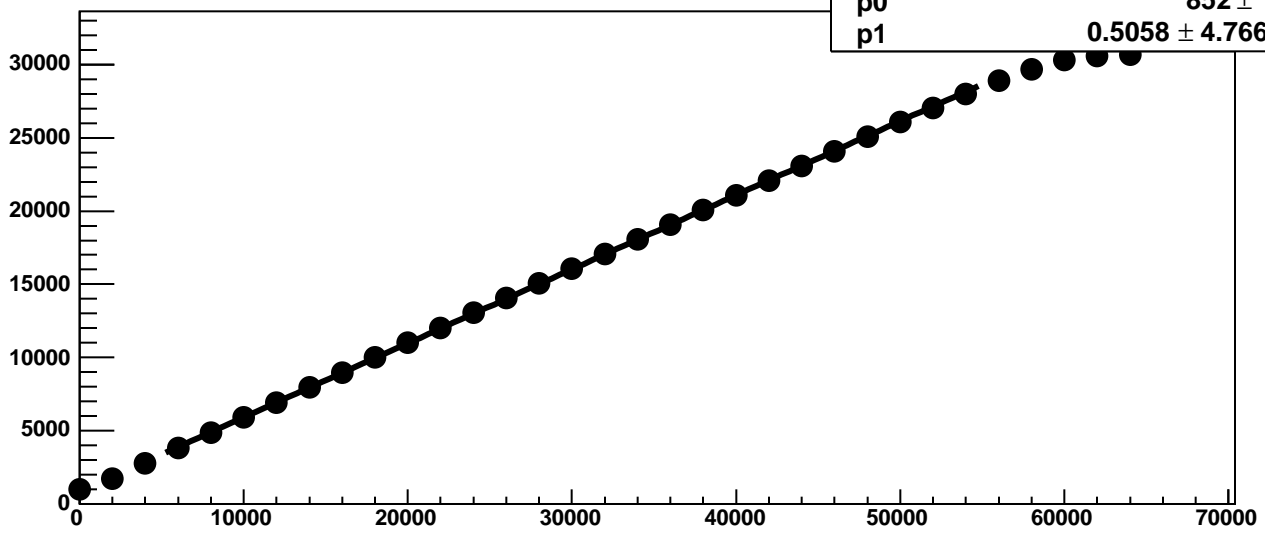
Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



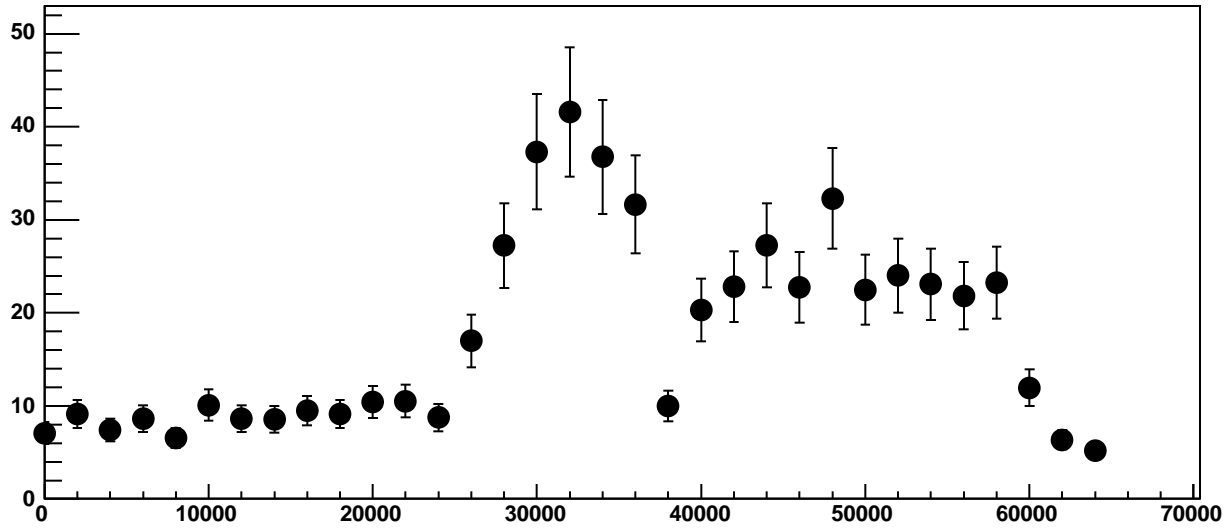
Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



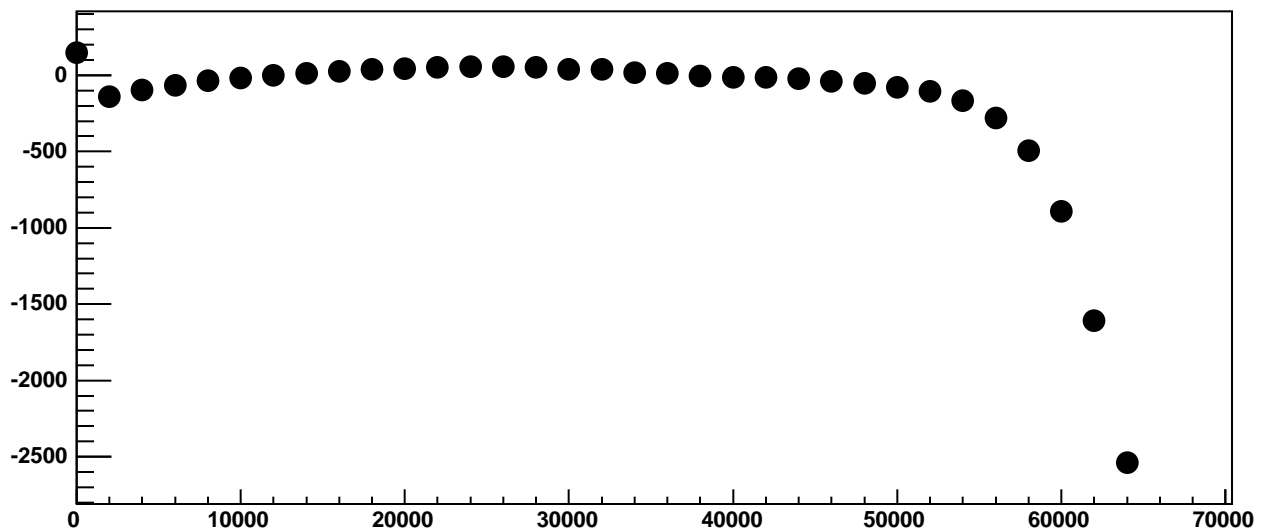
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



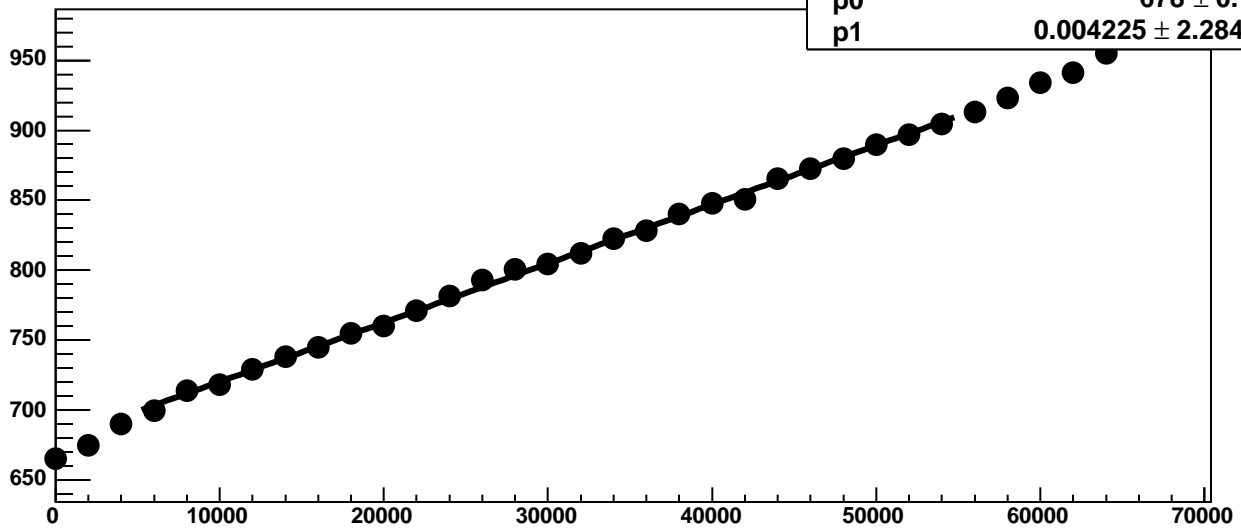
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.91 / 23

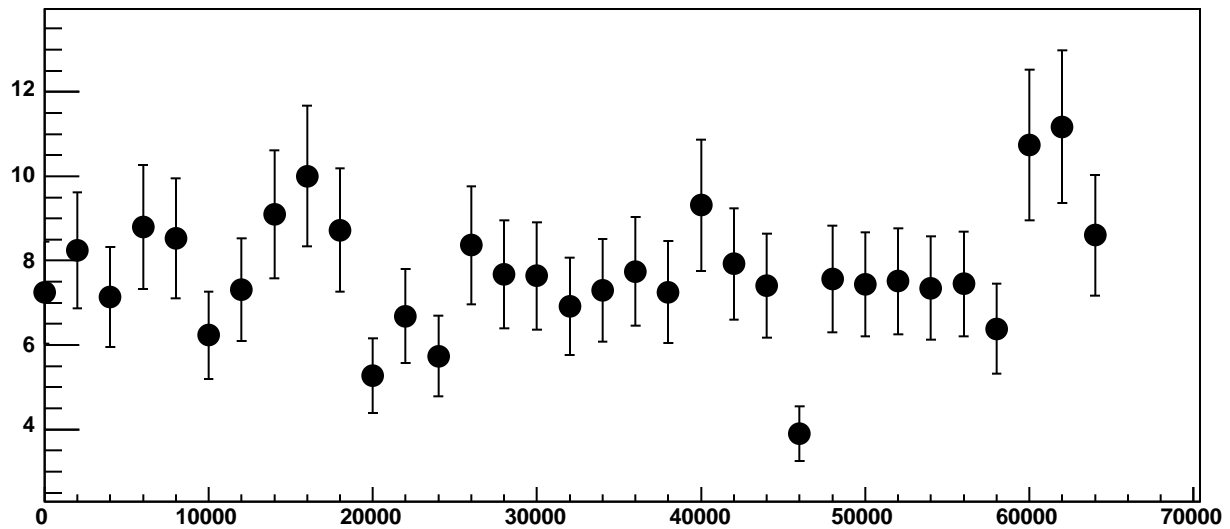
p0

678 ± 0.7877

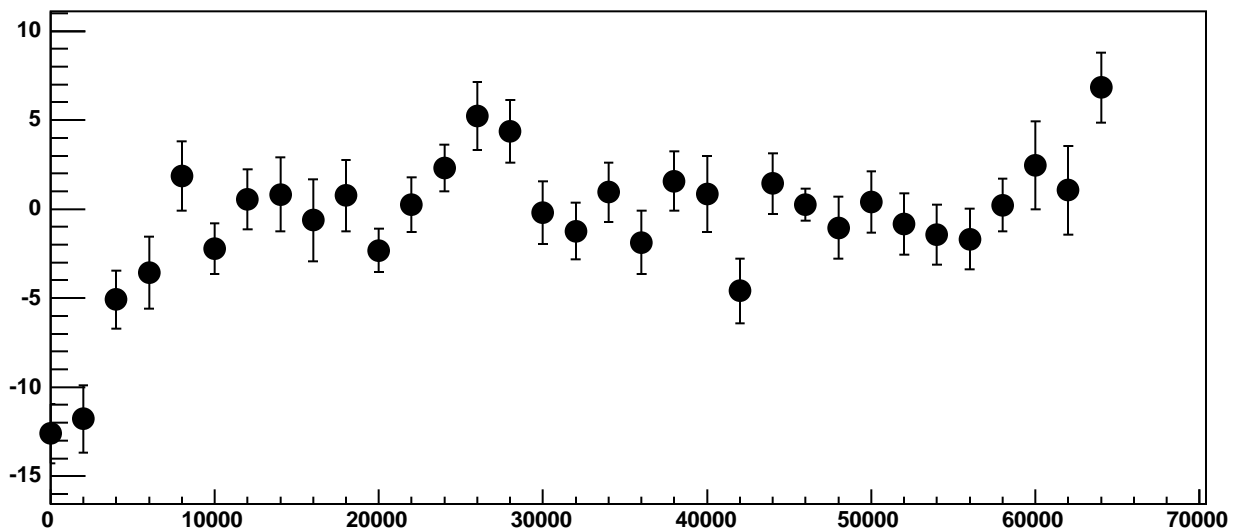
p1

$0.004225 \pm 2.284e-05$

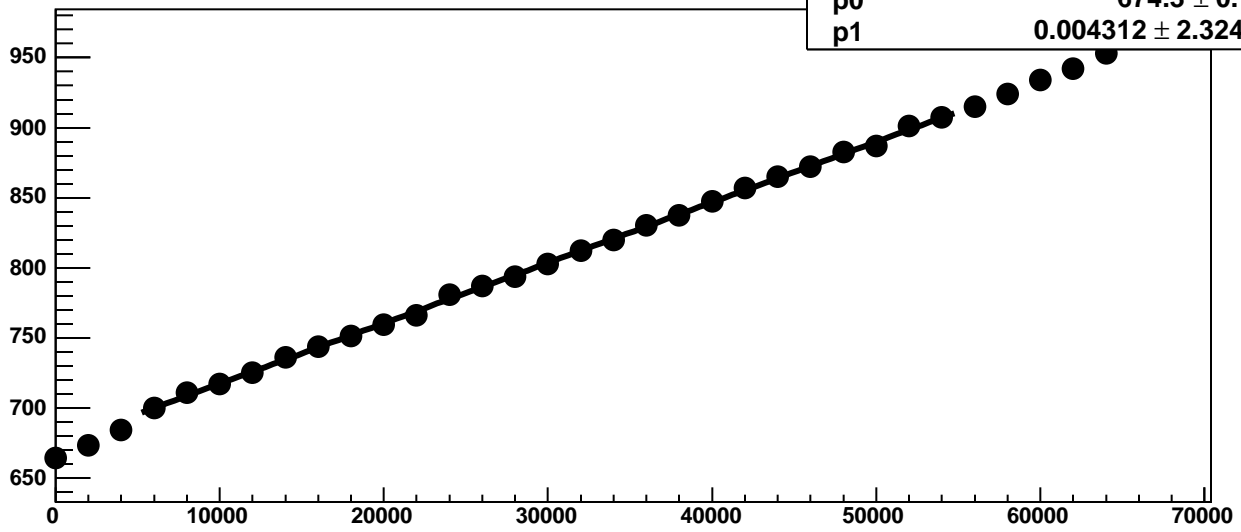
Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.5 / 23

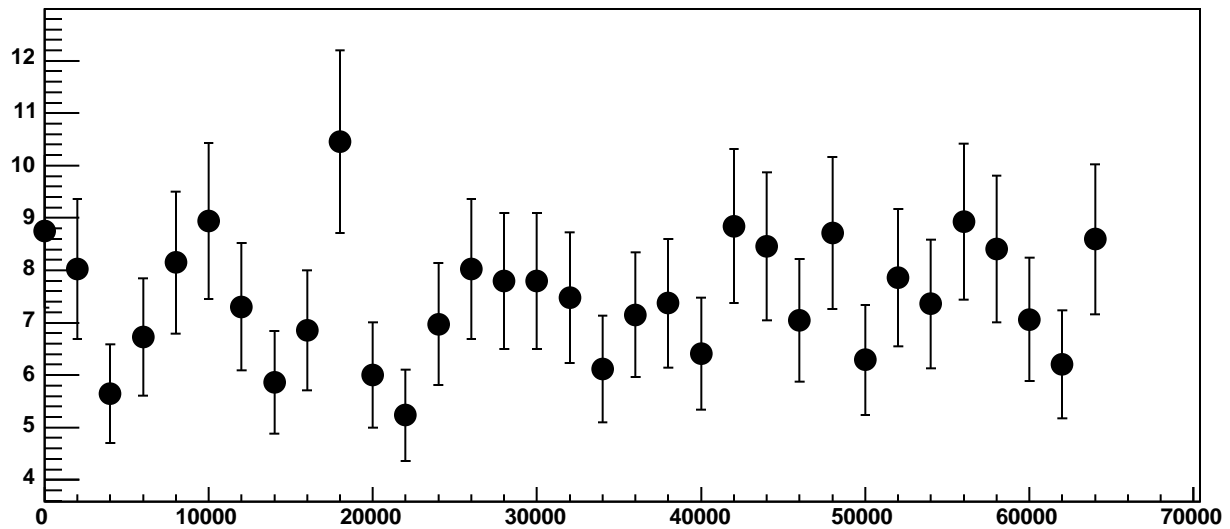
p0

674.3 ± 0.7599

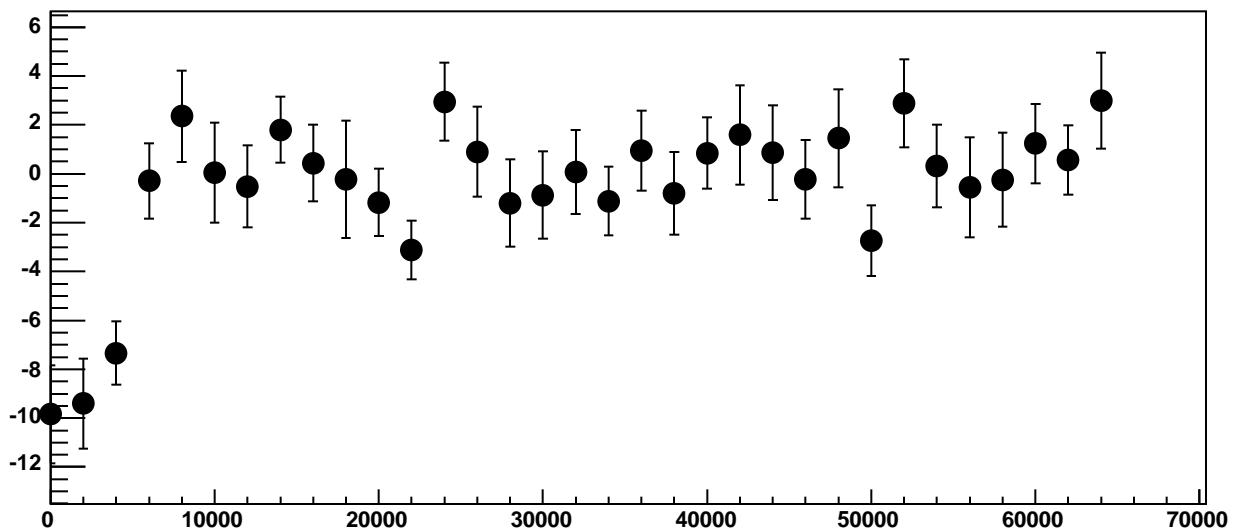
p1

$0.004312 \pm 2.324e-05$

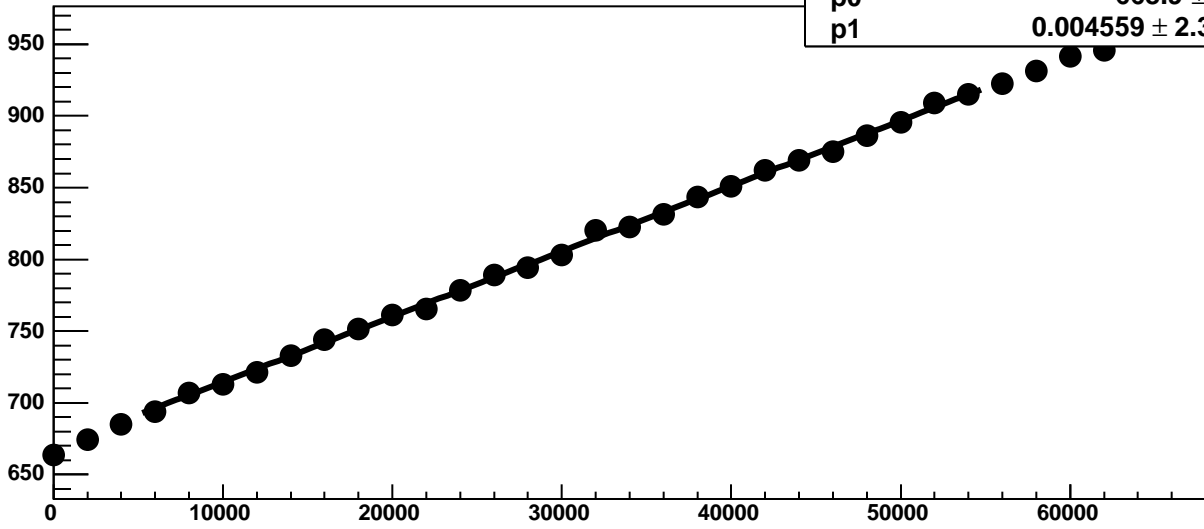
Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

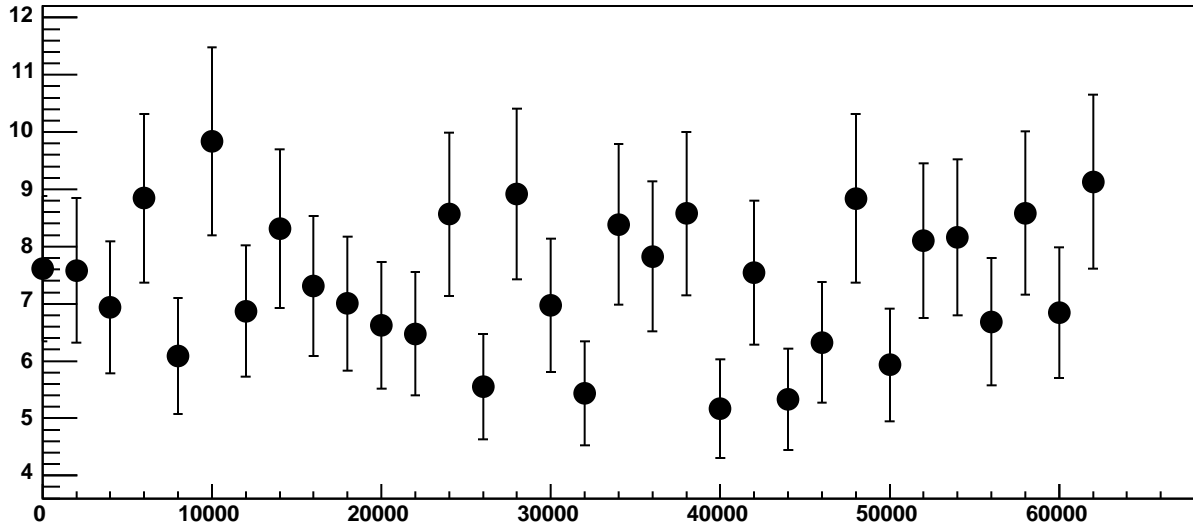


Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

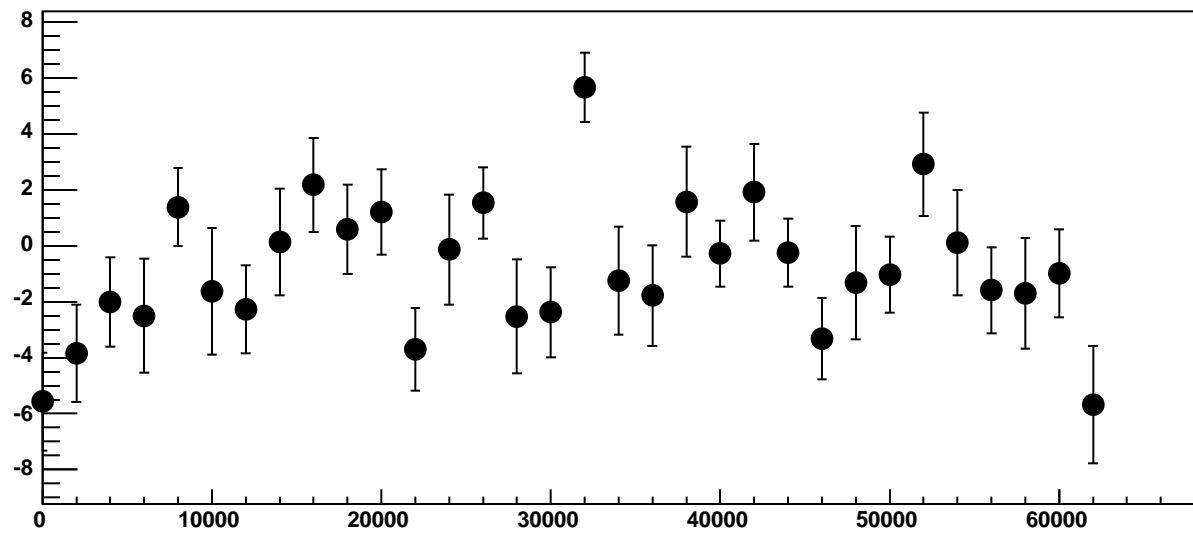


χ^2 / ndf 51.59 / 23
p0 668.9 ± 0.7786
p1 $0.004559 \pm 2.304e-05$

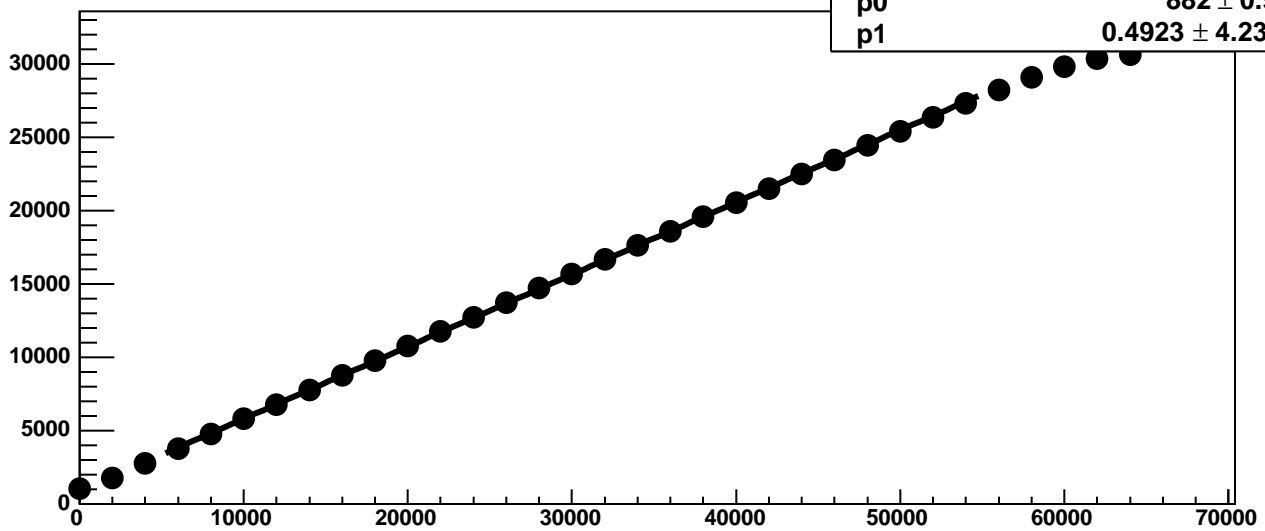
Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



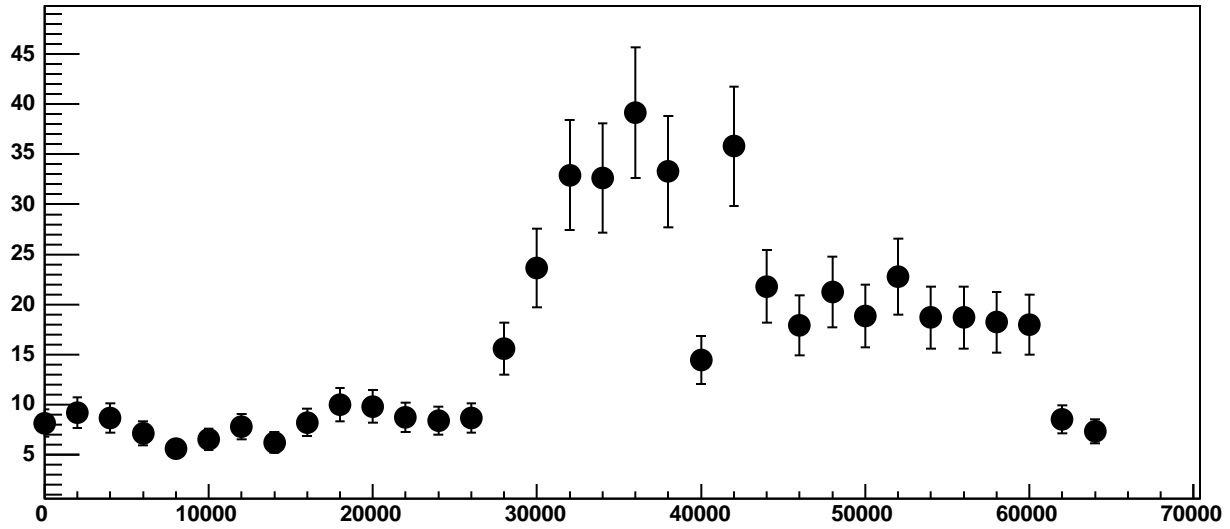
Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



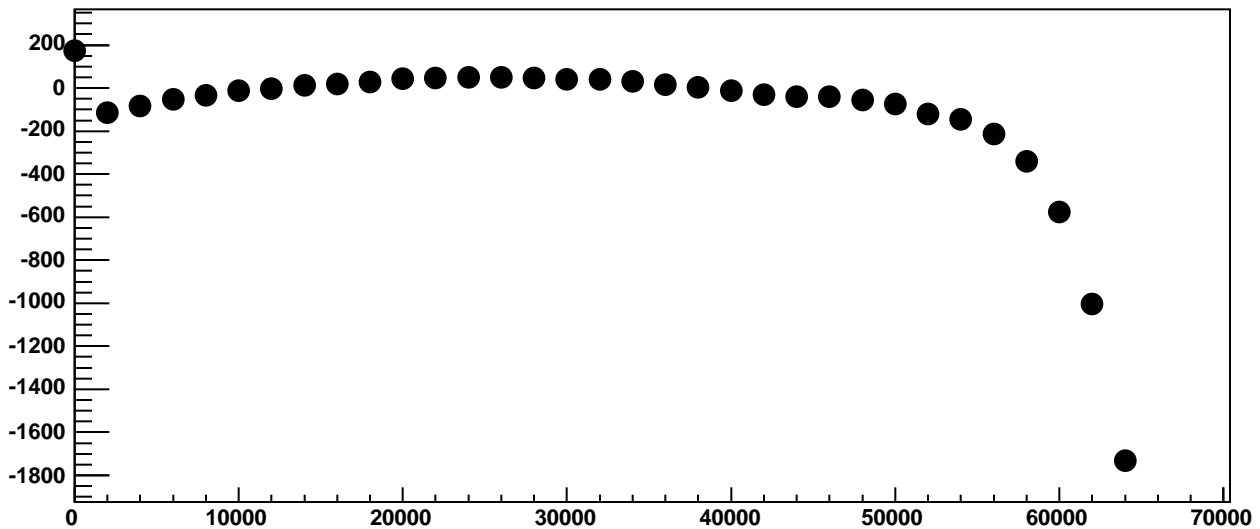
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



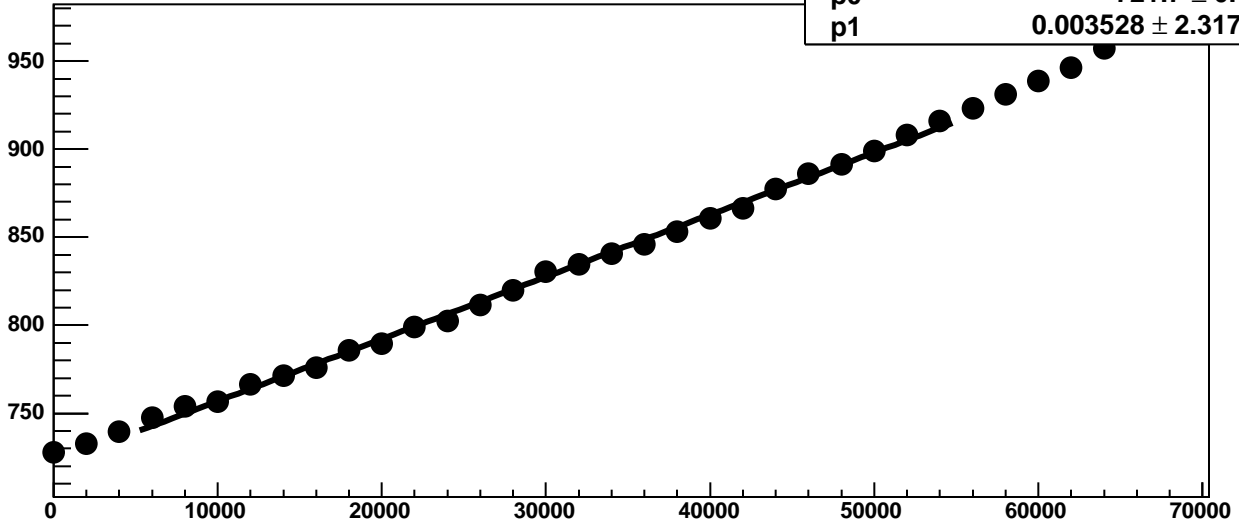
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

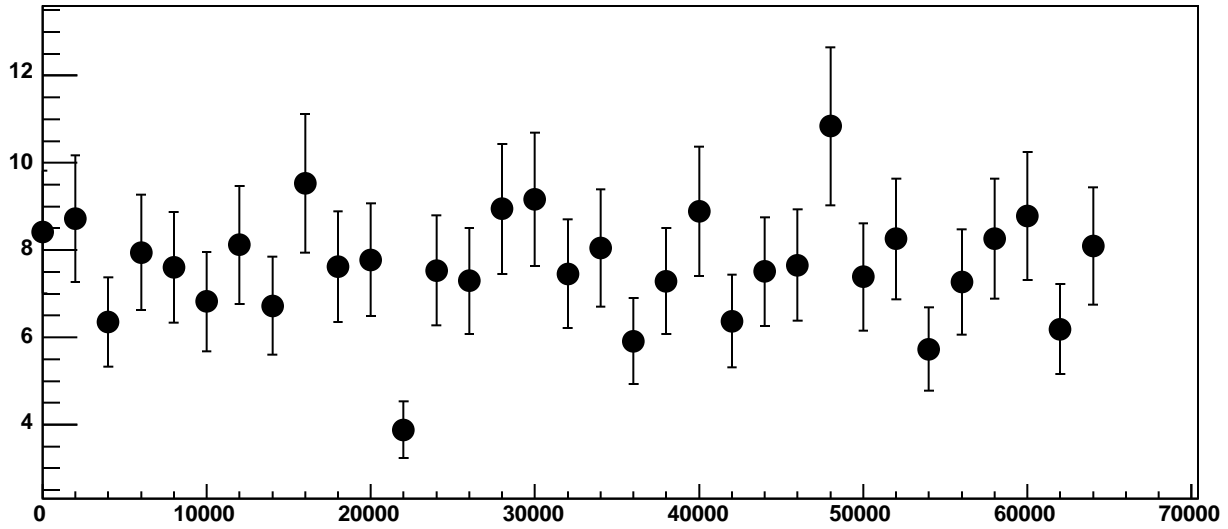


Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

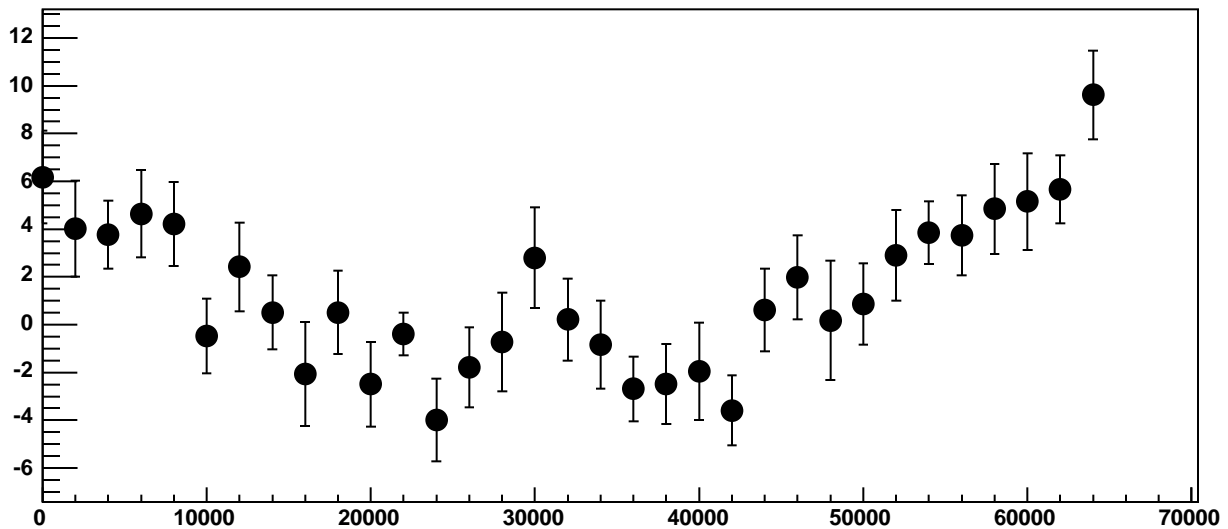


χ^2 / ndf 51.55 / 23
p0 721.7 ± 0.7626
p1 0.003528 ± 2.317e-05

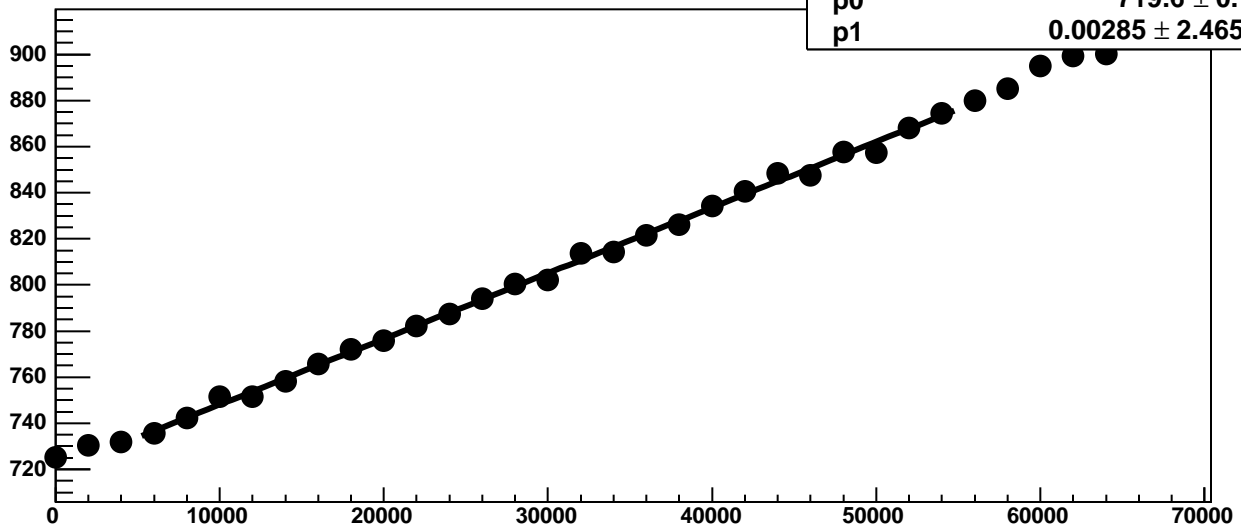
Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

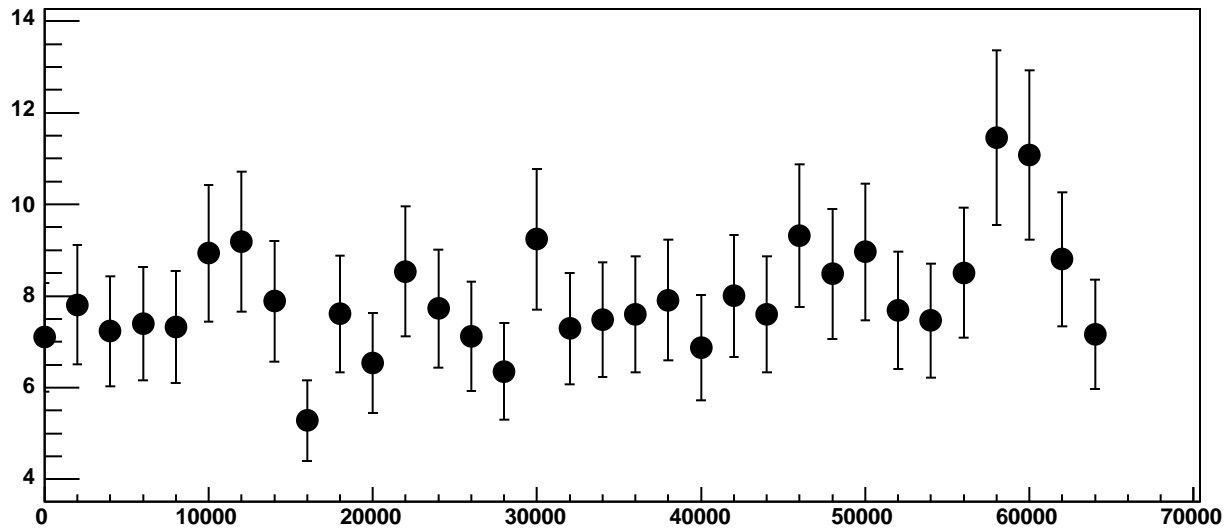


Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

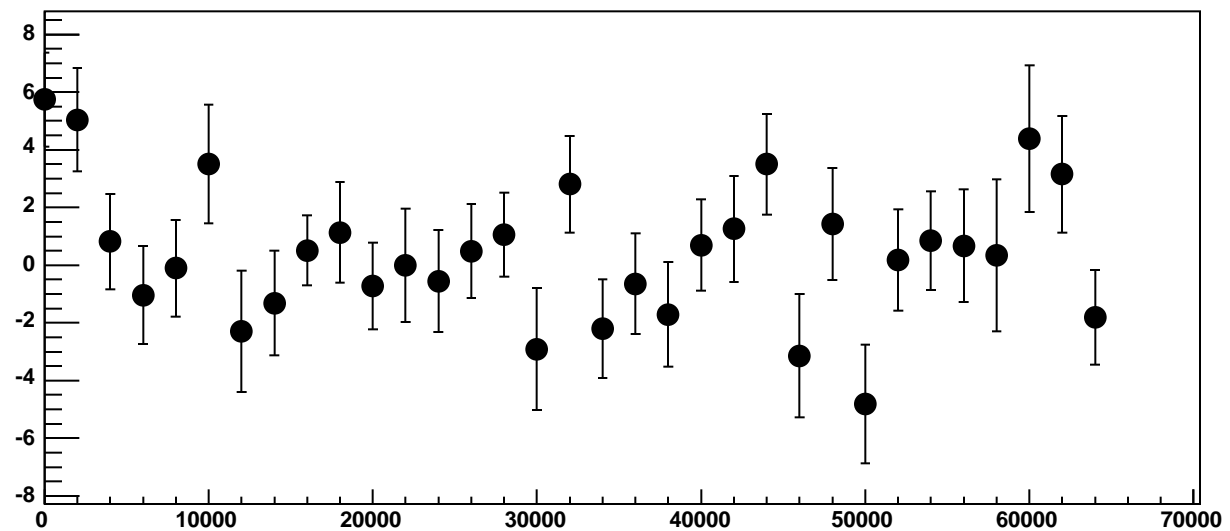


χ^2 / ndf 27.06 / 23
p0 719.6 ± 0.7973
p1 0.00285 ± 2.465e-05

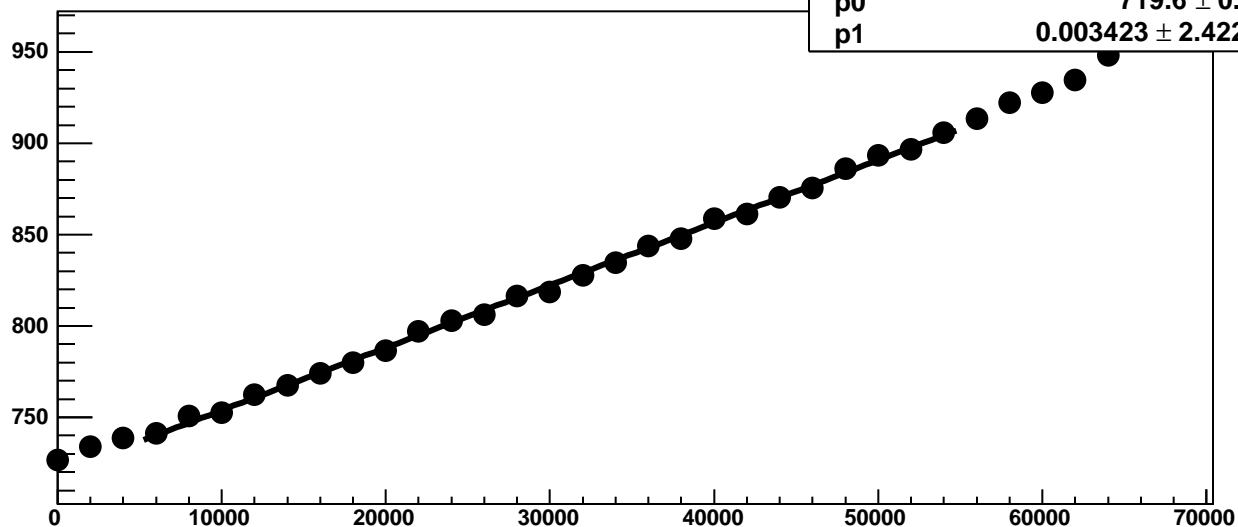
Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

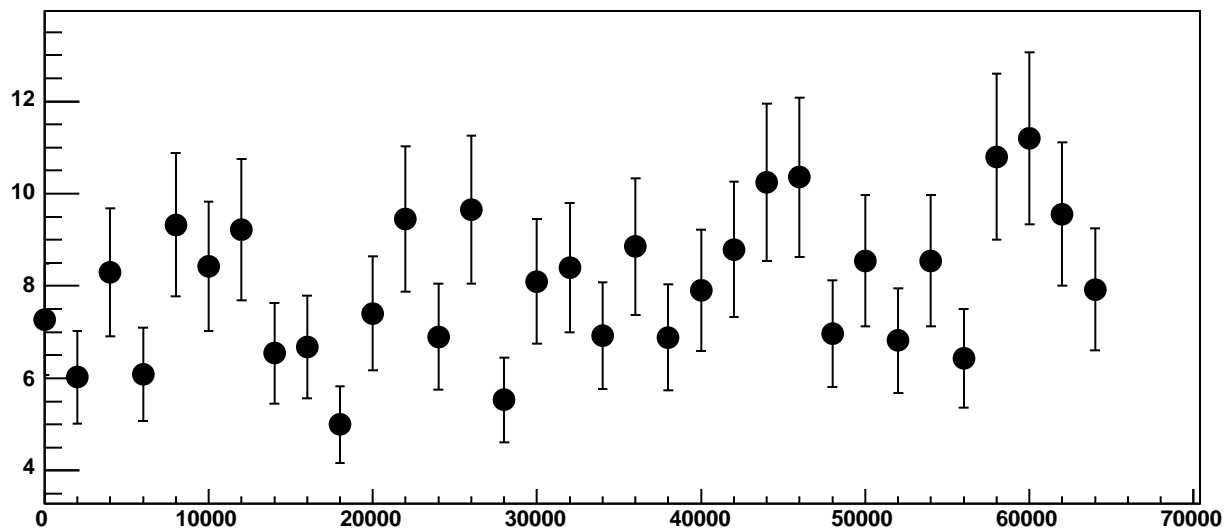


Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

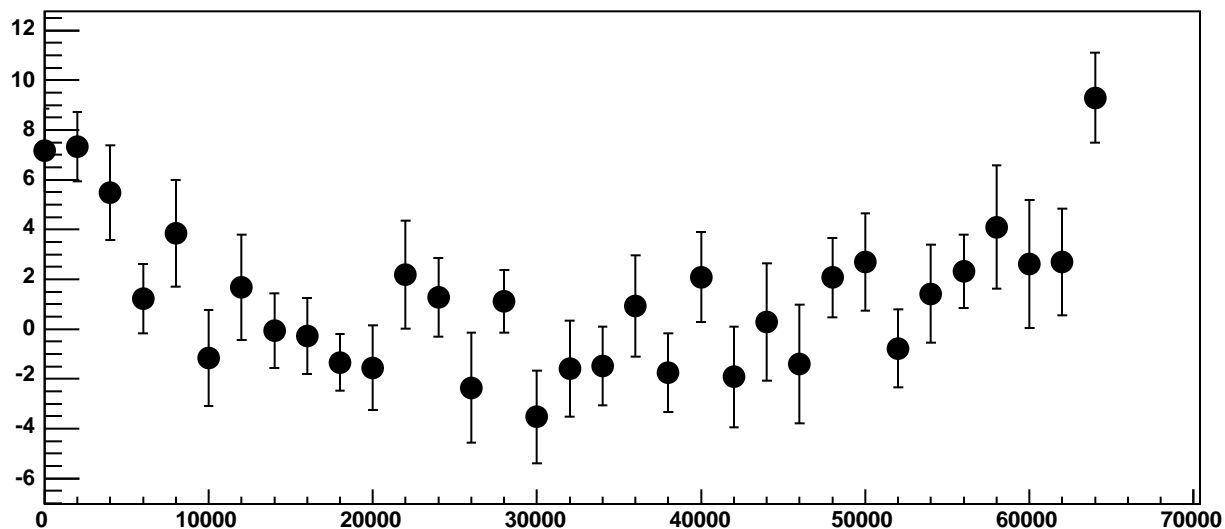


χ^2 / ndf 24.28 / 23
p0 719.6 ± 0.7713
p1 $0.003423 \pm 2.422e-05$

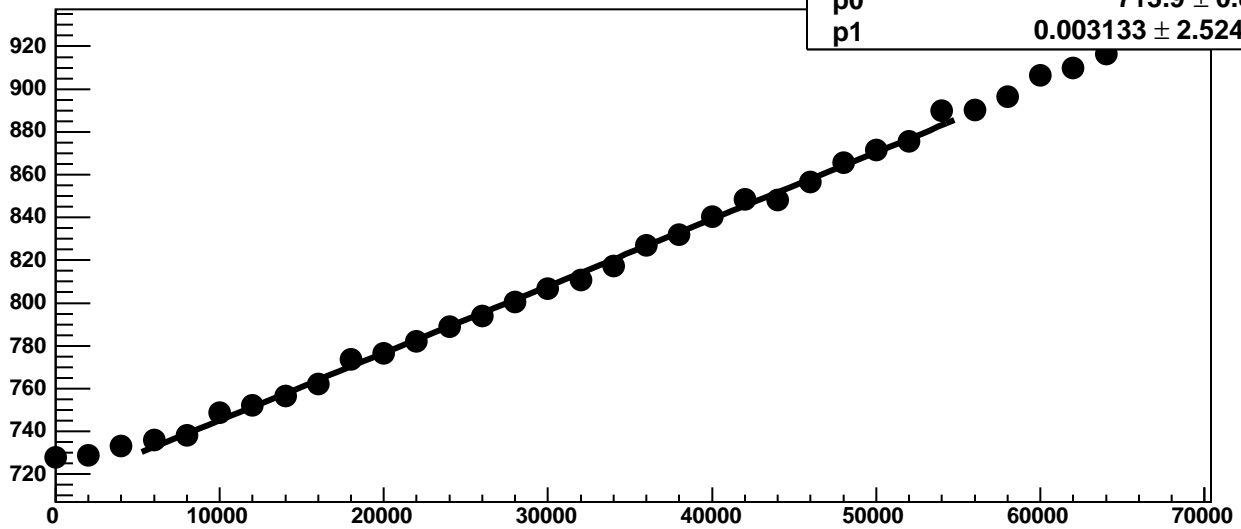
Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

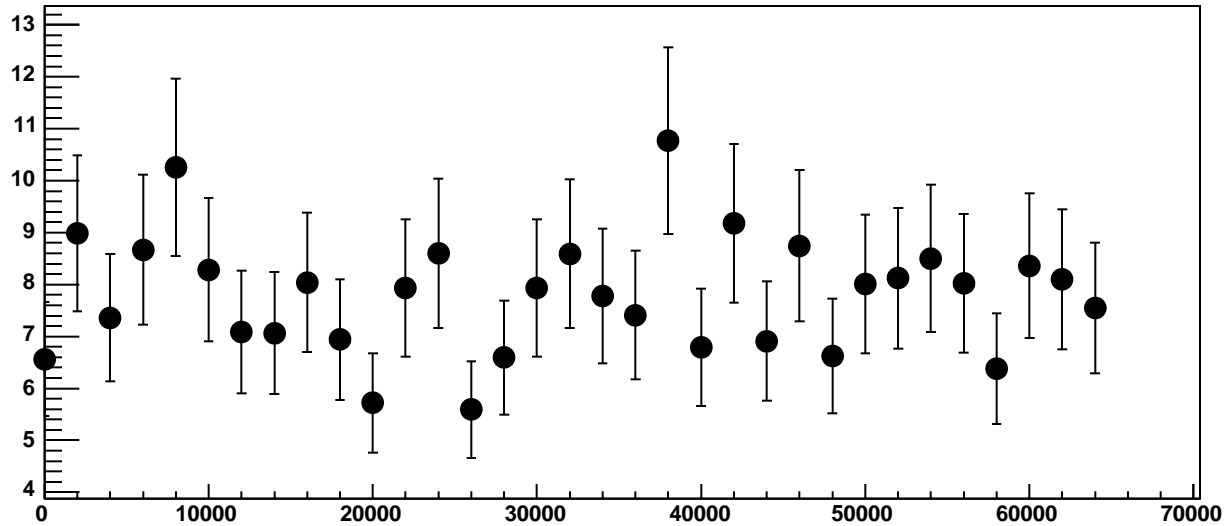


Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

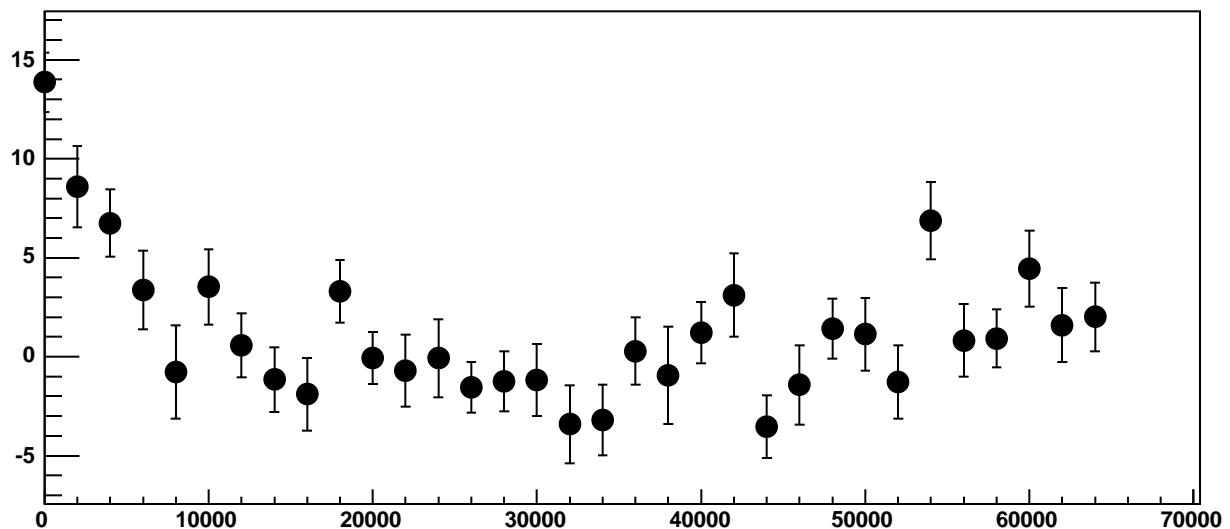


χ^2 / ndf 43.93 / 23
p0 713.9 ± 0.8262
p1 $0.003133 \pm 2.524e-05$

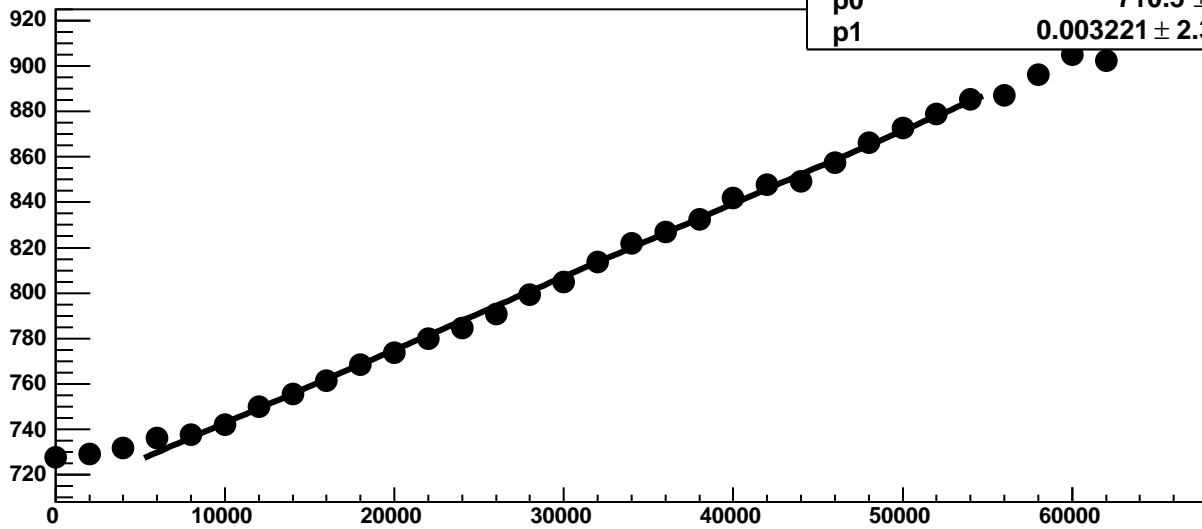
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.17 / 23

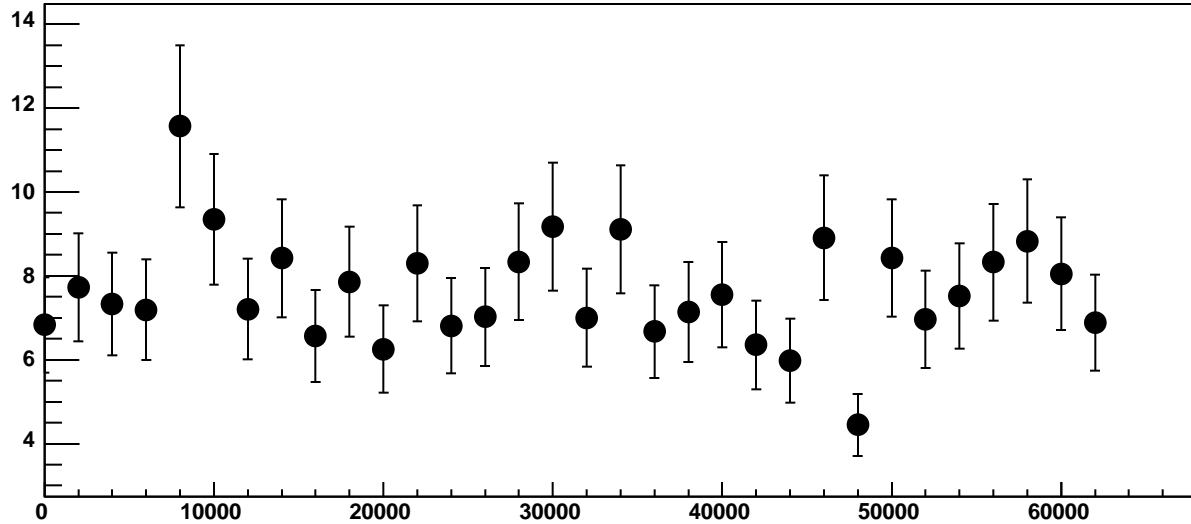
p0

710.5 ± 0.8145

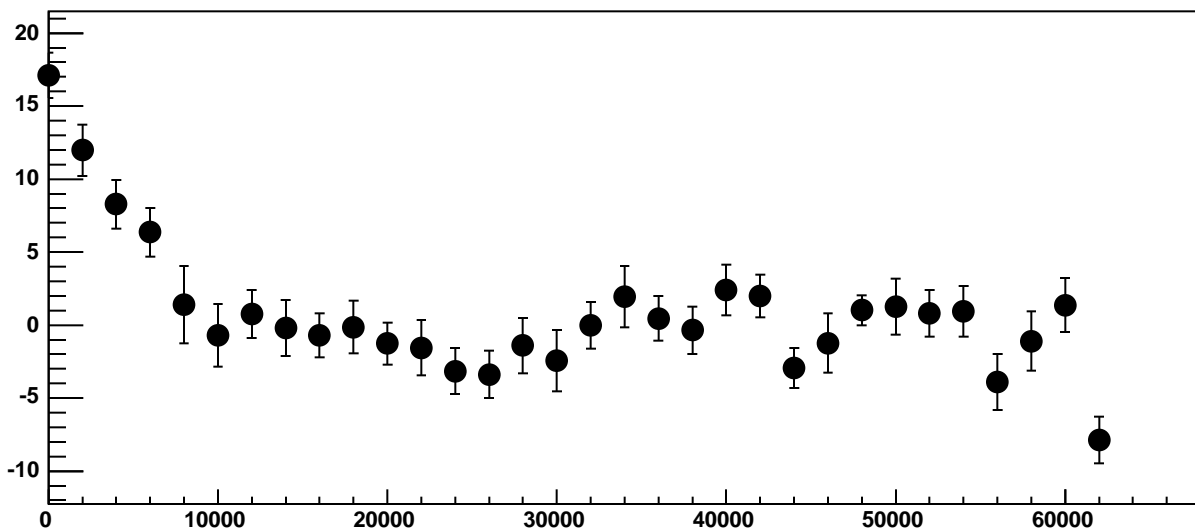
p1

$0.003221 \pm 2.326e-05$

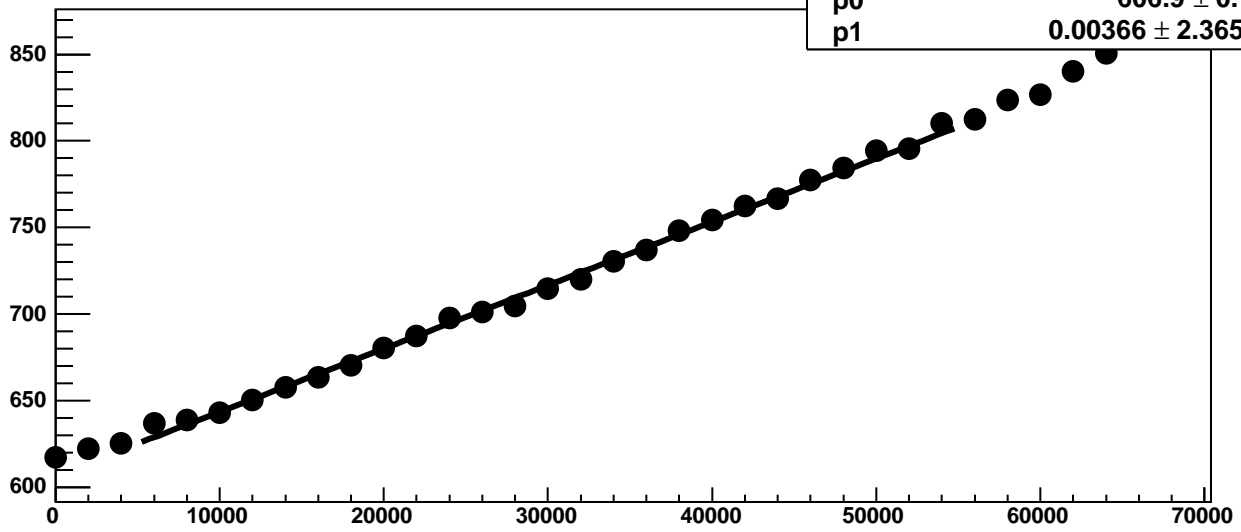
Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

72.68 / 23

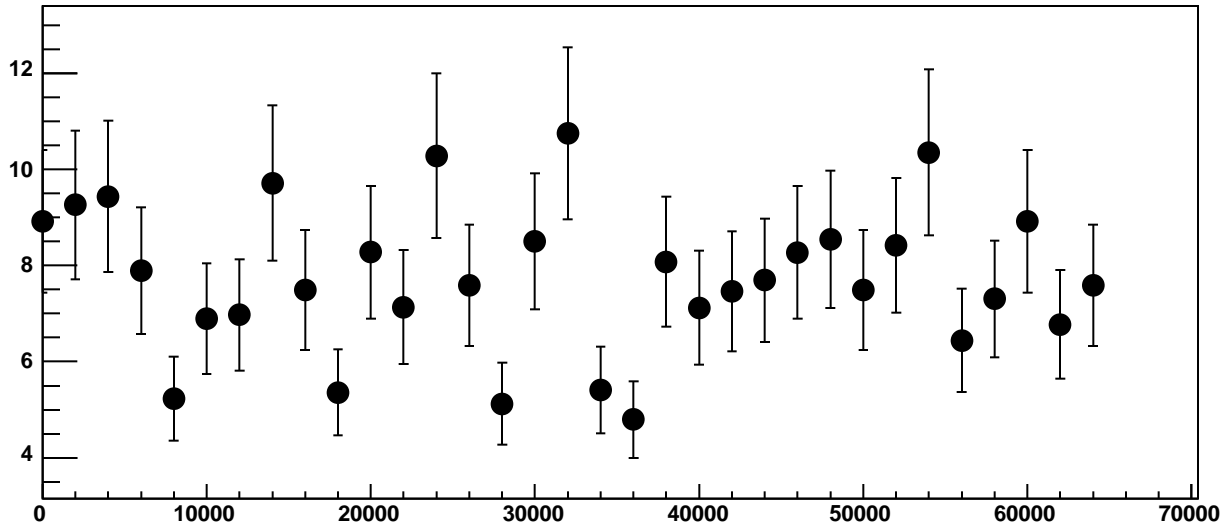
p0

606.9 ± 0.7476

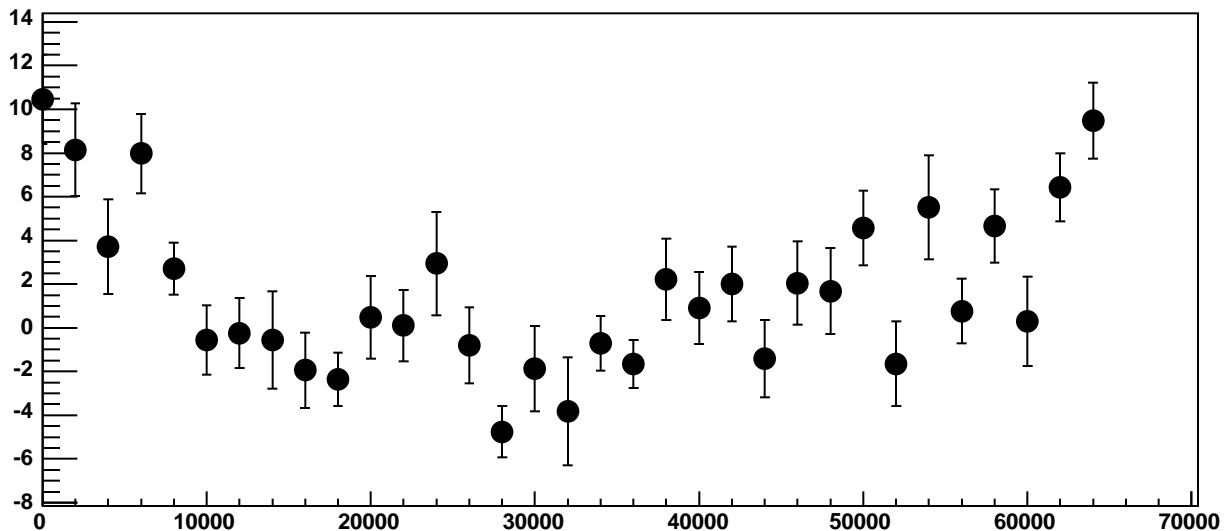
p1

$0.00366 \pm 2.365e-05$

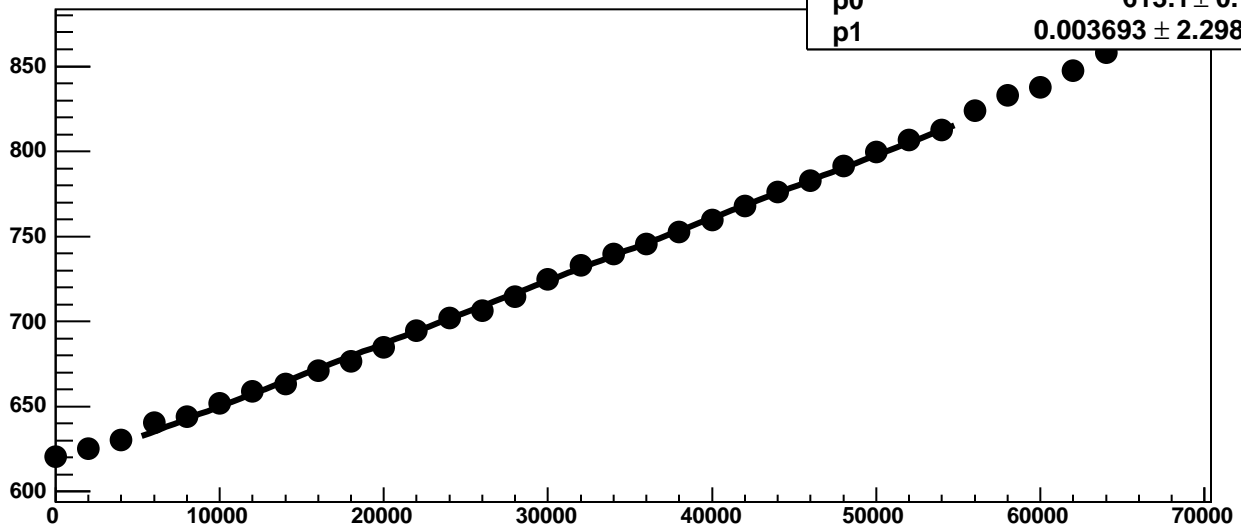
Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



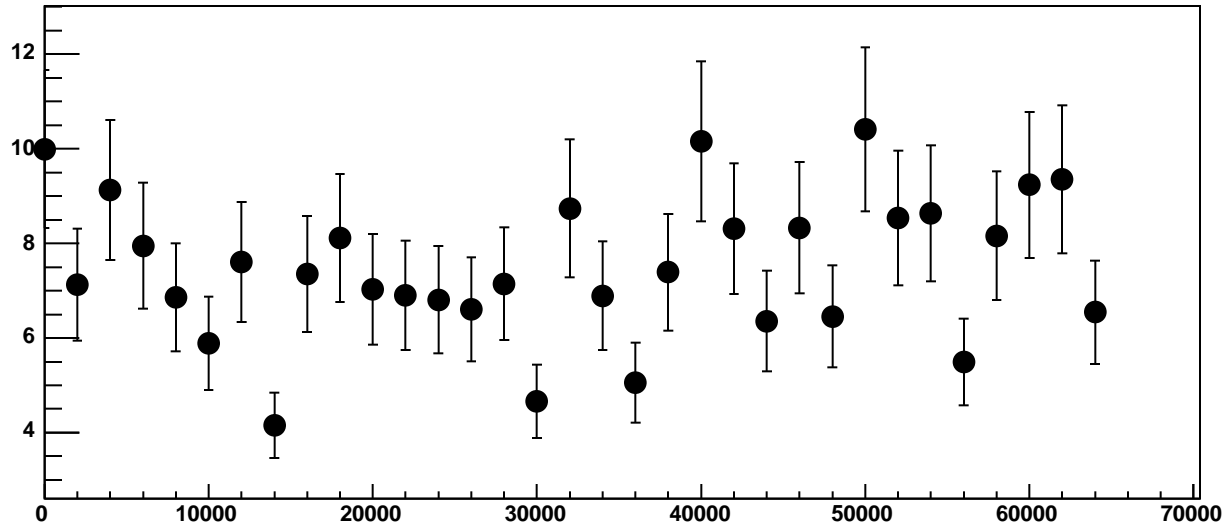
Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



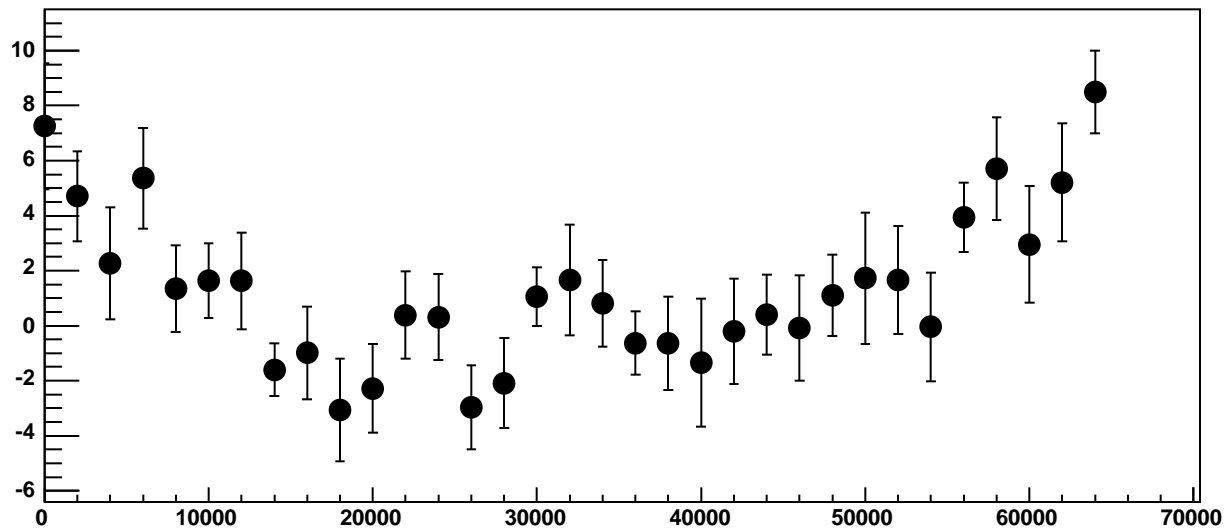
Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



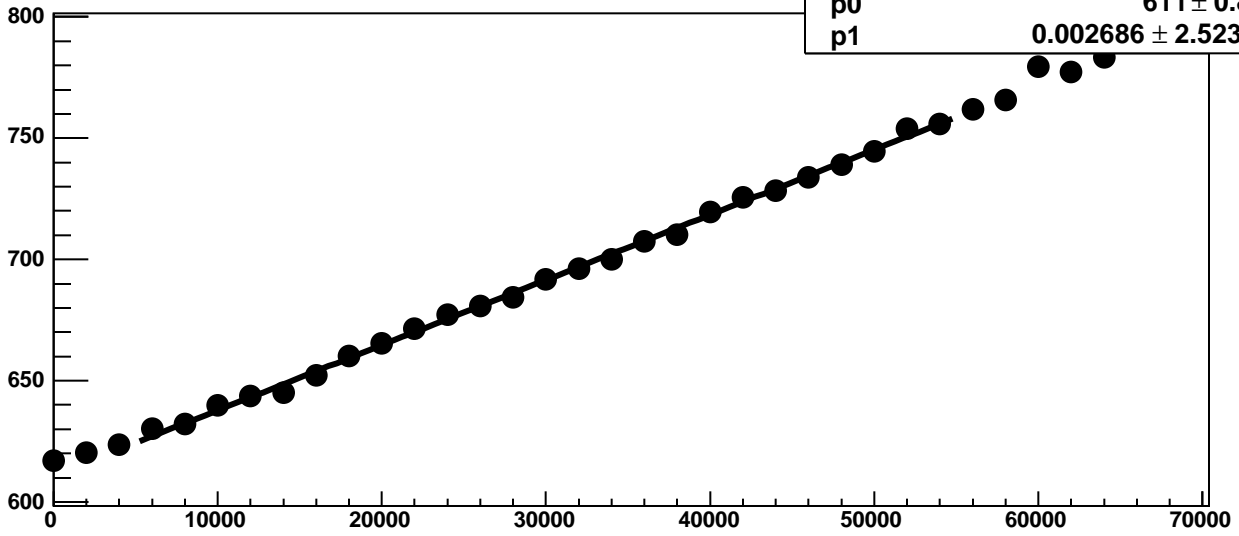
Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



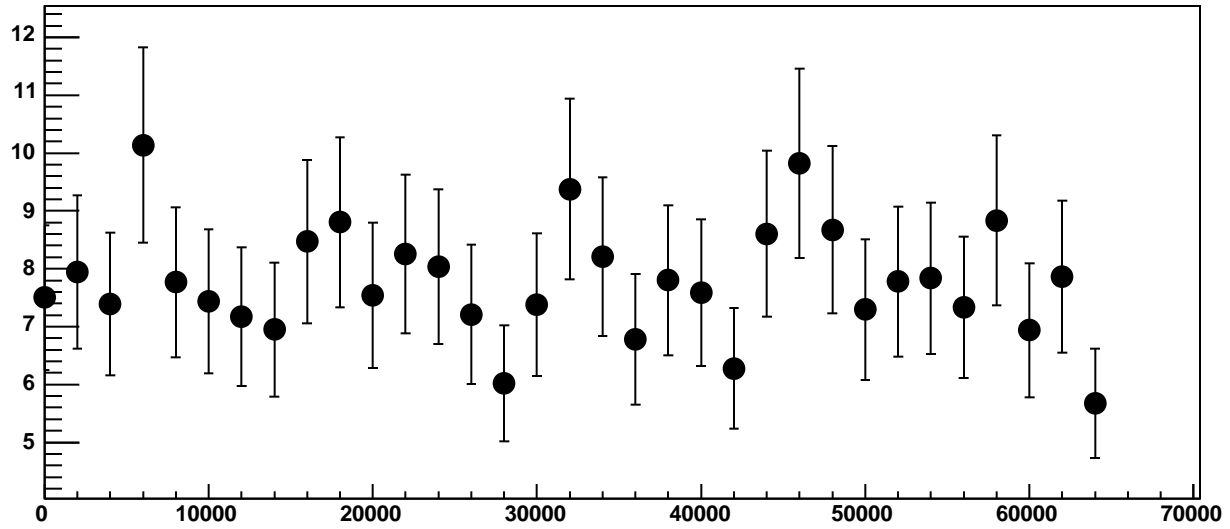
Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



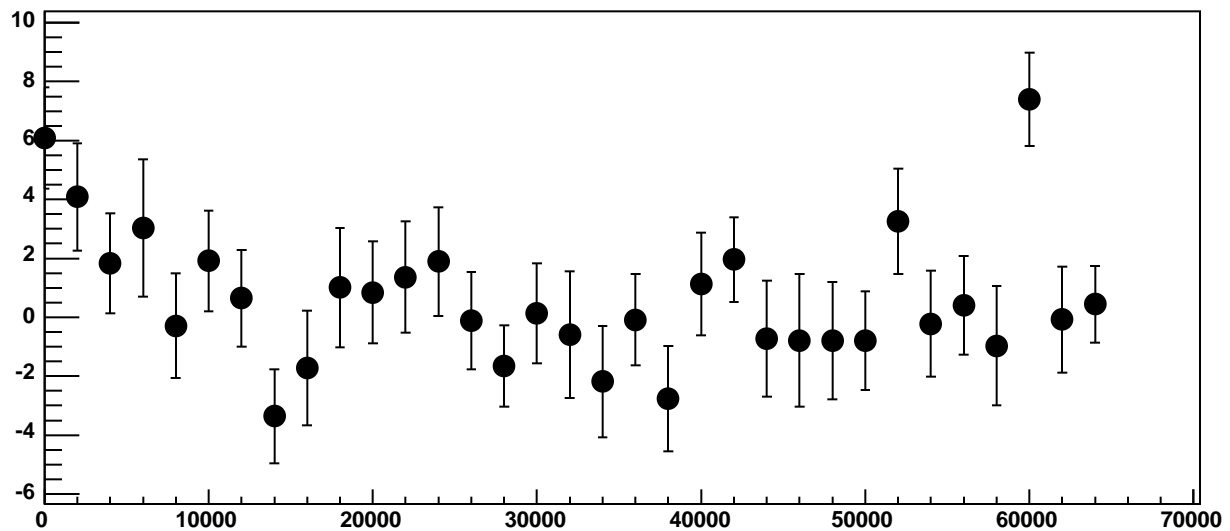
Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

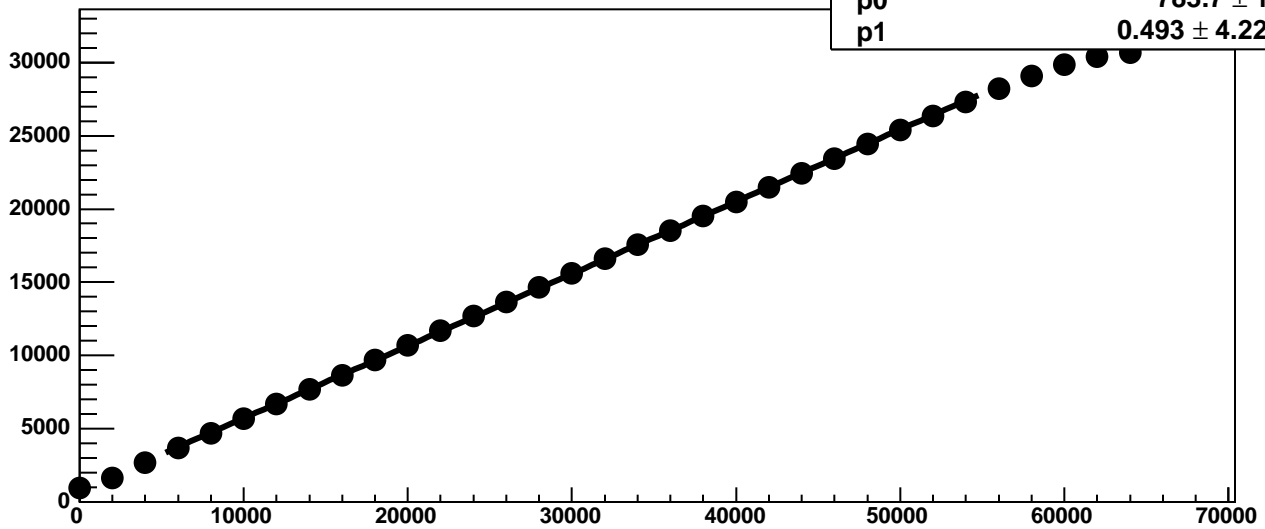


Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

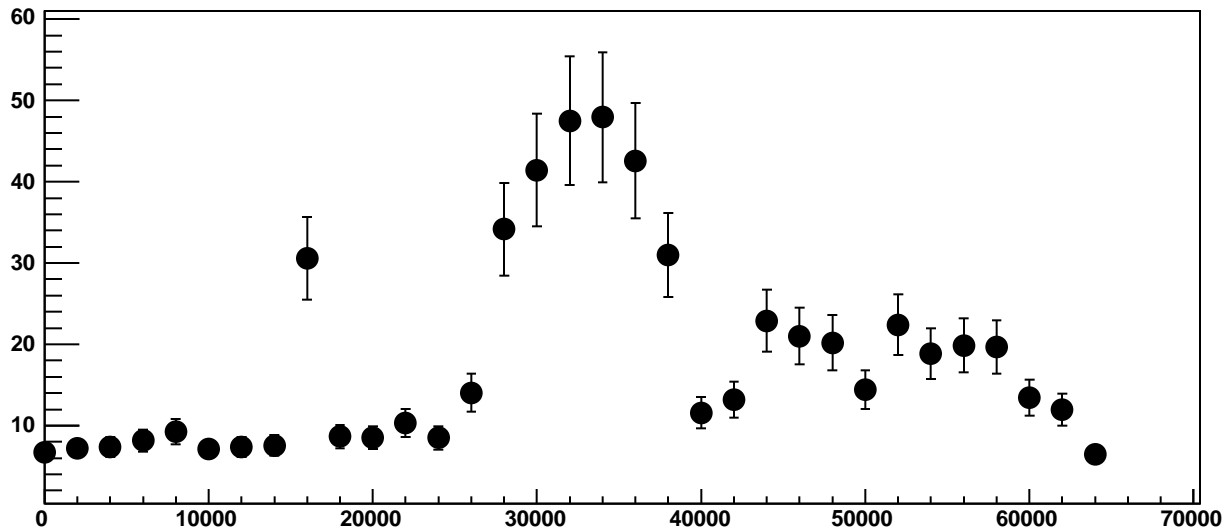


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

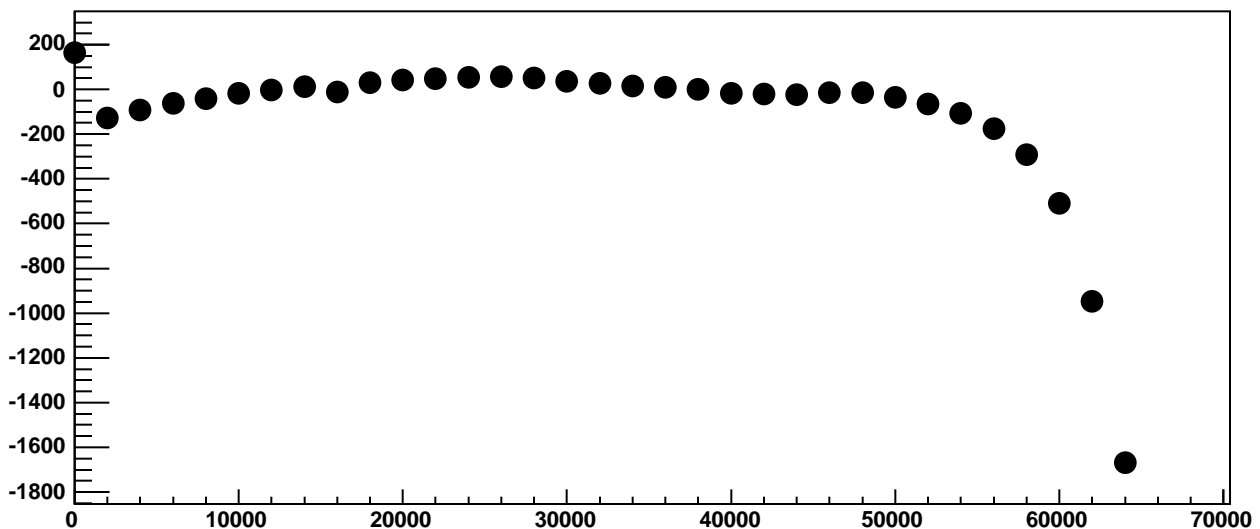
χ^2 / ndf 4959 / 23
p0 783.7 ± 1.025
p1 $0.493 \pm 4.22\text{e-}05$



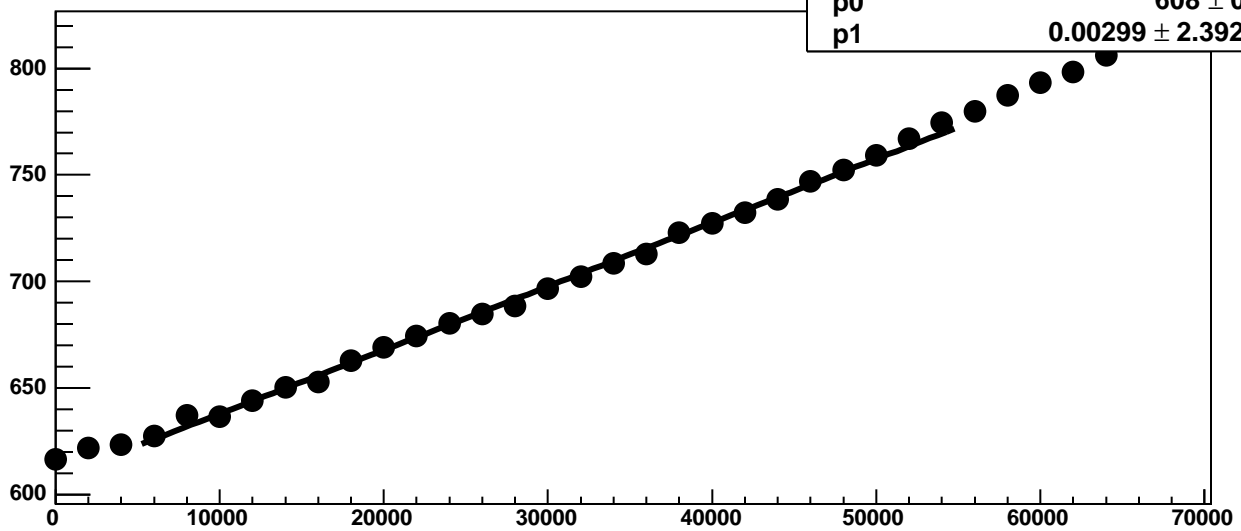
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

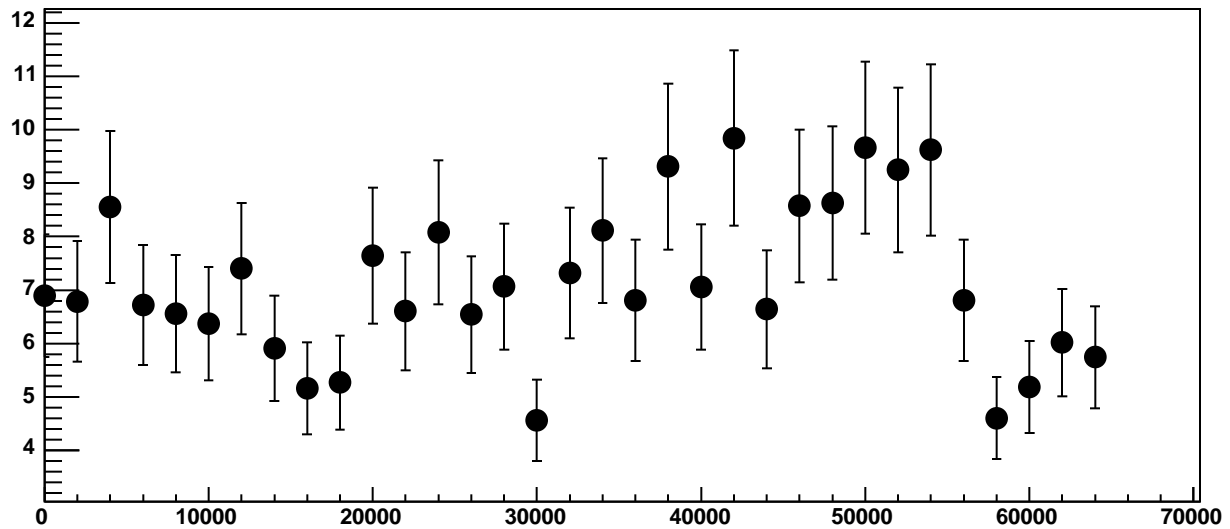


Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

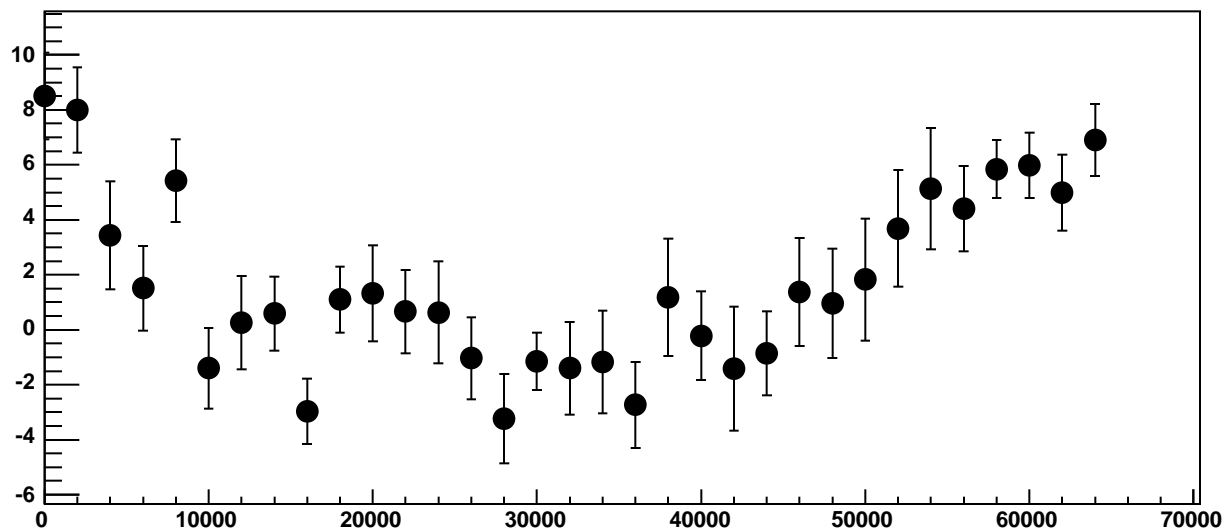


χ^2 / ndf 43.73 / 23
p0 608 ± 0.714
p1 $0.00299 \pm 2.392e-05$

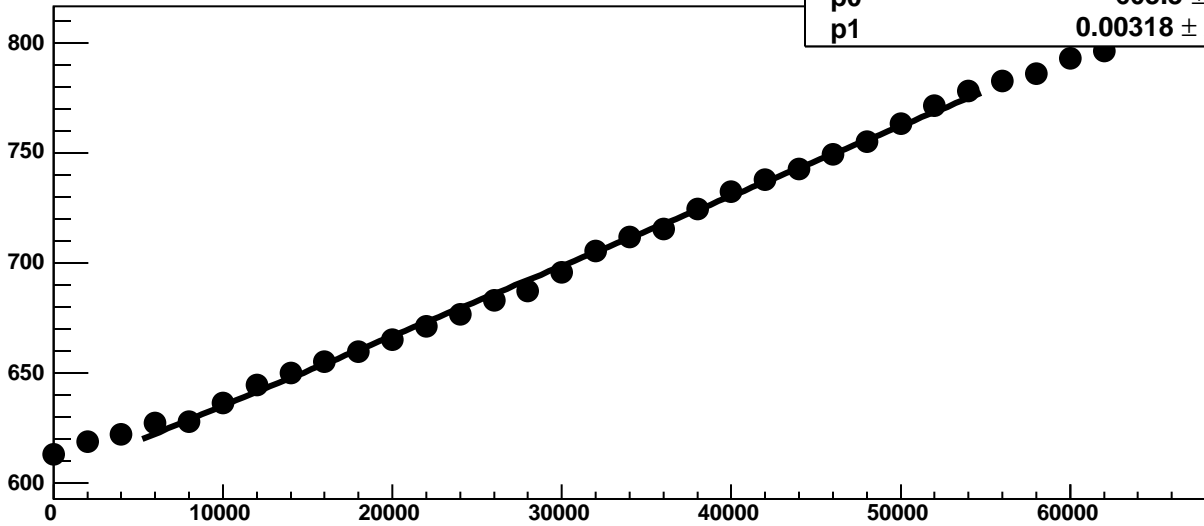
Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

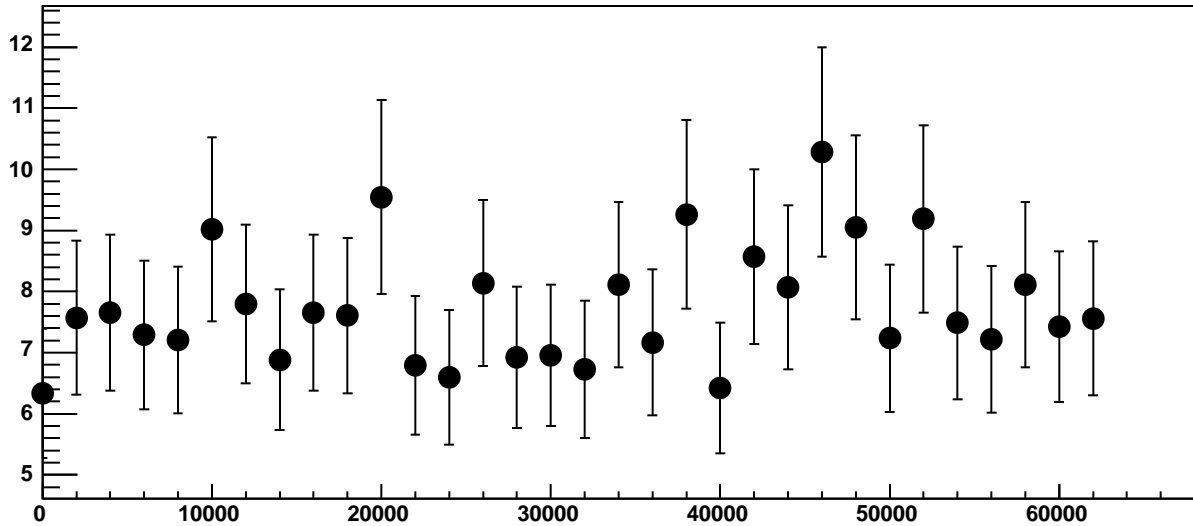


Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

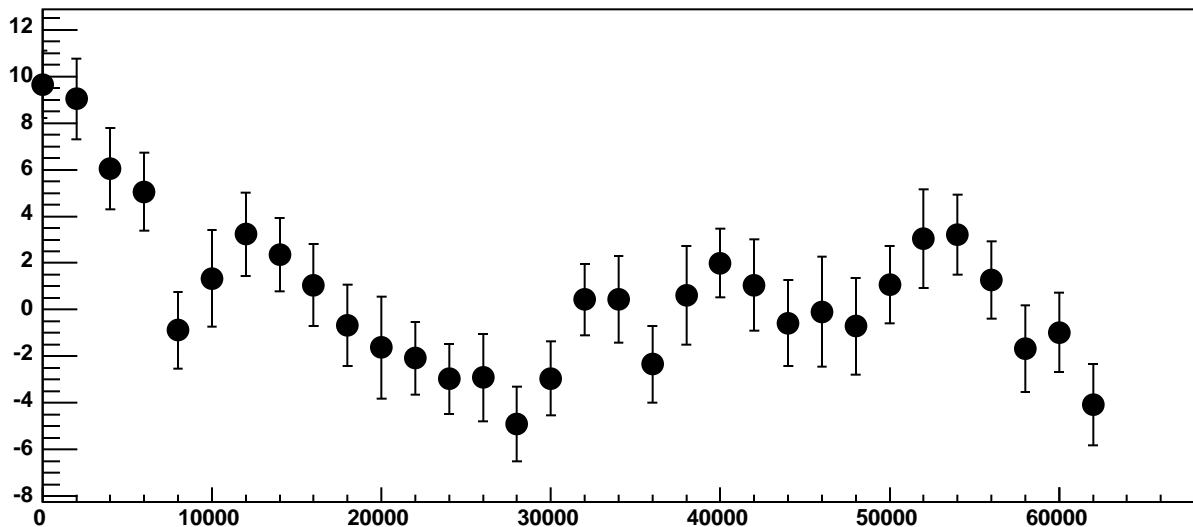


χ^2 / ndf 47.98 / 23
p0 603.3 ± 0.8099
p1 $0.00318 \pm 2.5e-05$

Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

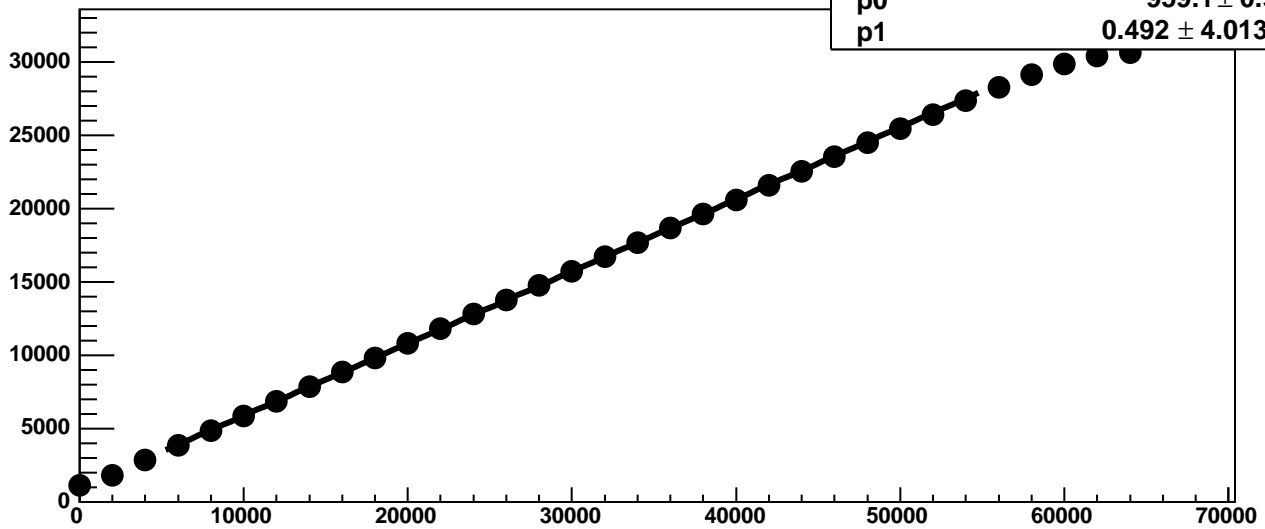


Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

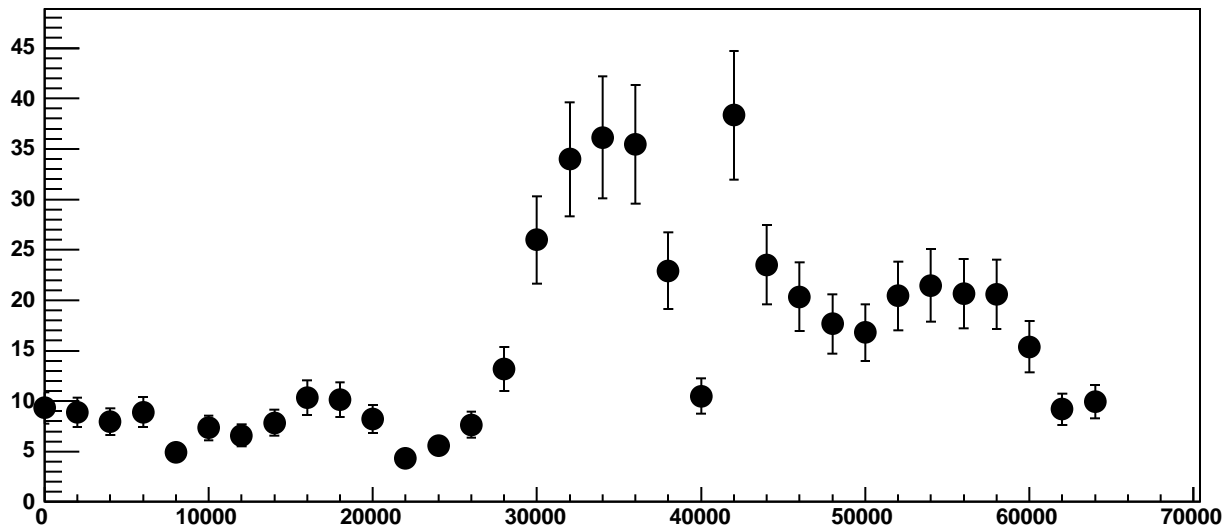


Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

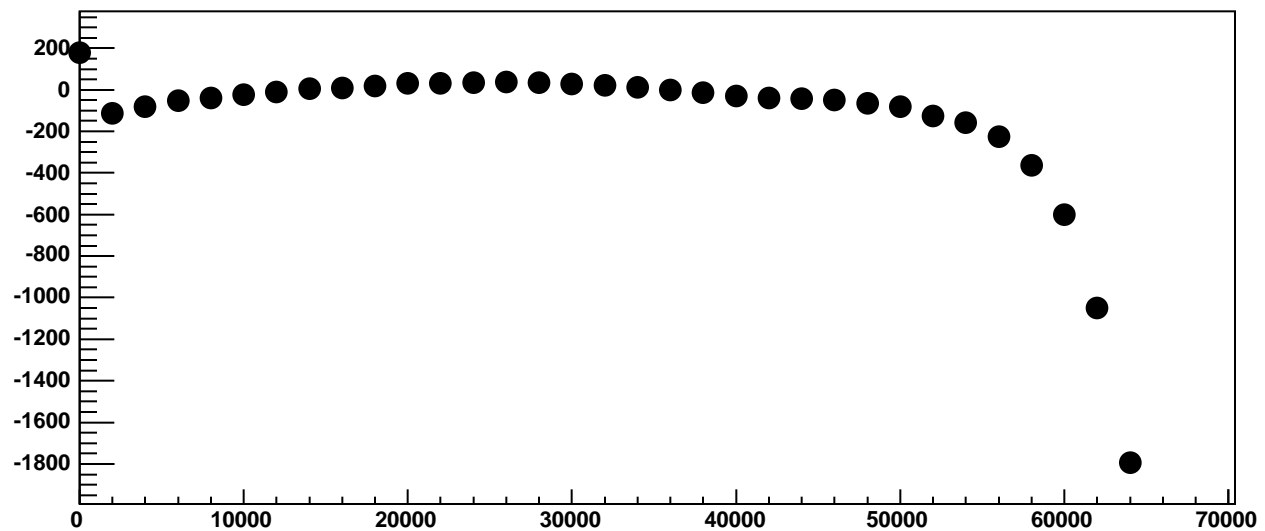
χ^2 / ndf 7592 / 23
p0 959.1 ± 0.9014
p1 0.492 ± 4.013e-05



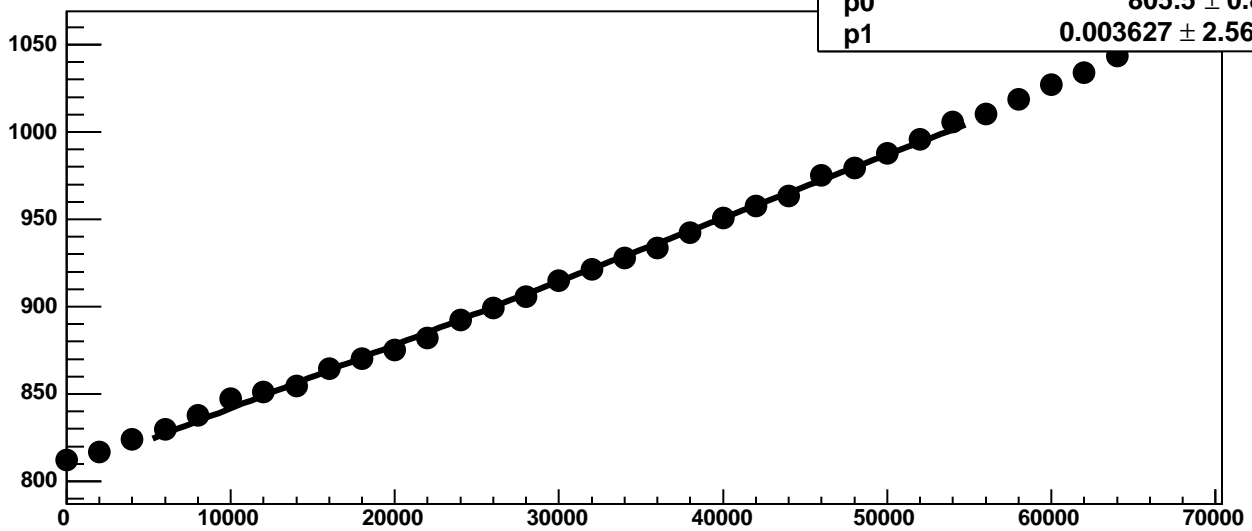
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.93 / 23

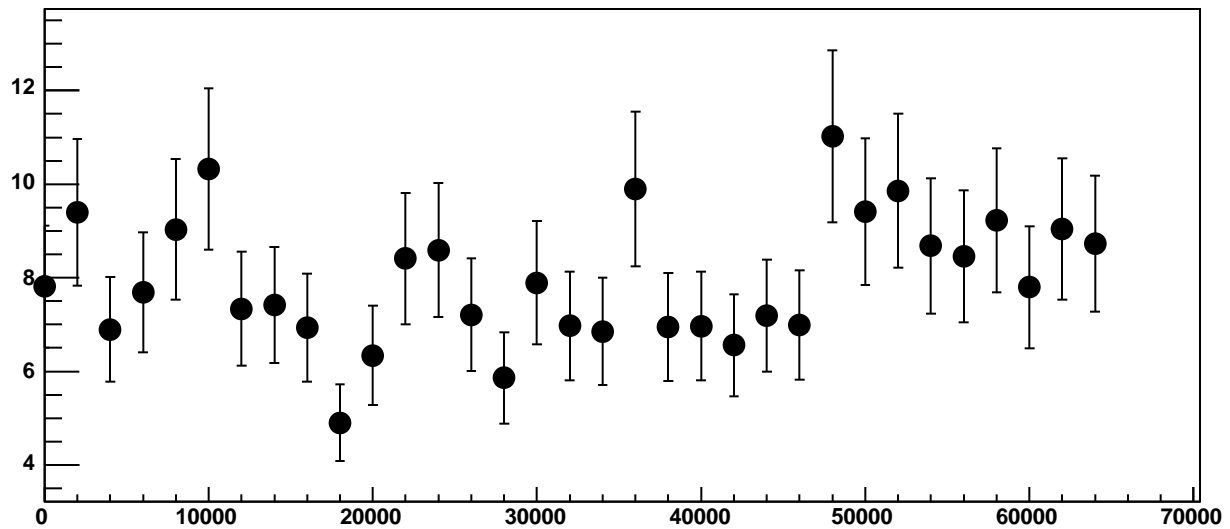
p0

805.5 ± 0.8144

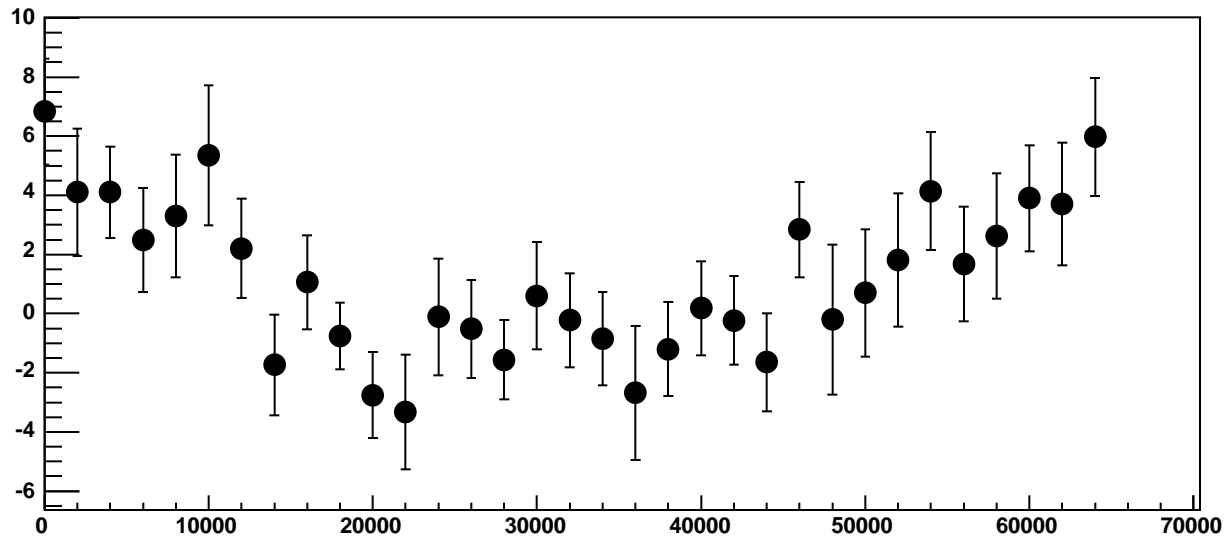
p1

$0.003627 \pm 2.56e-05$

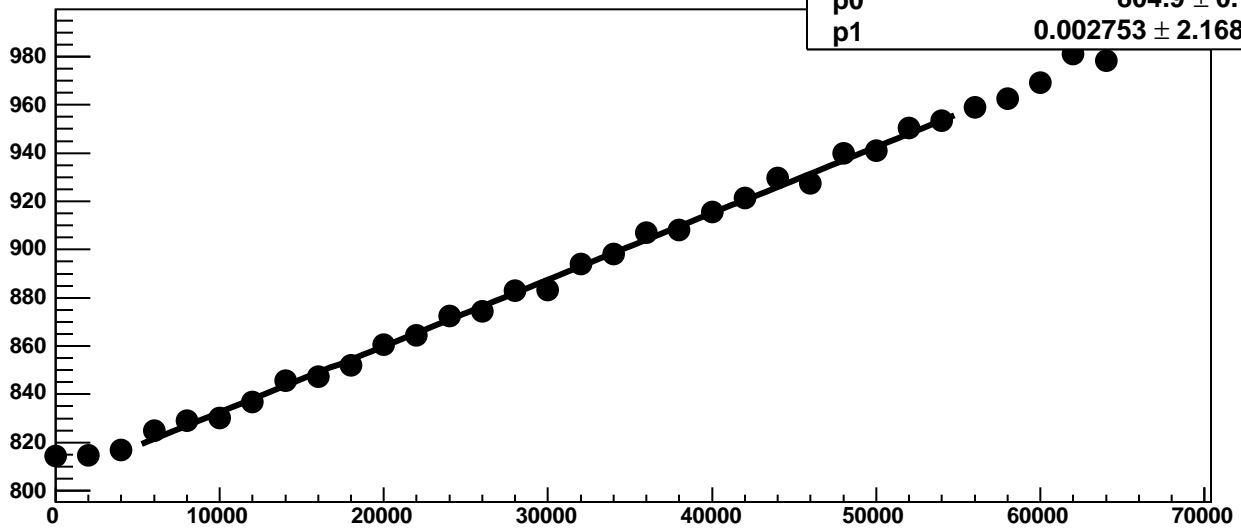
Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



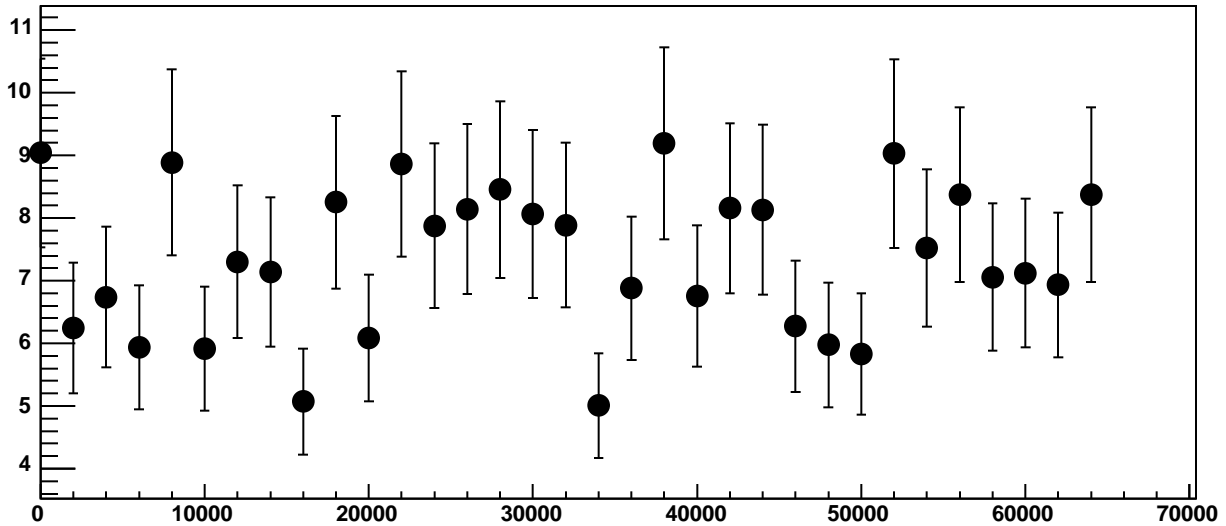
Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



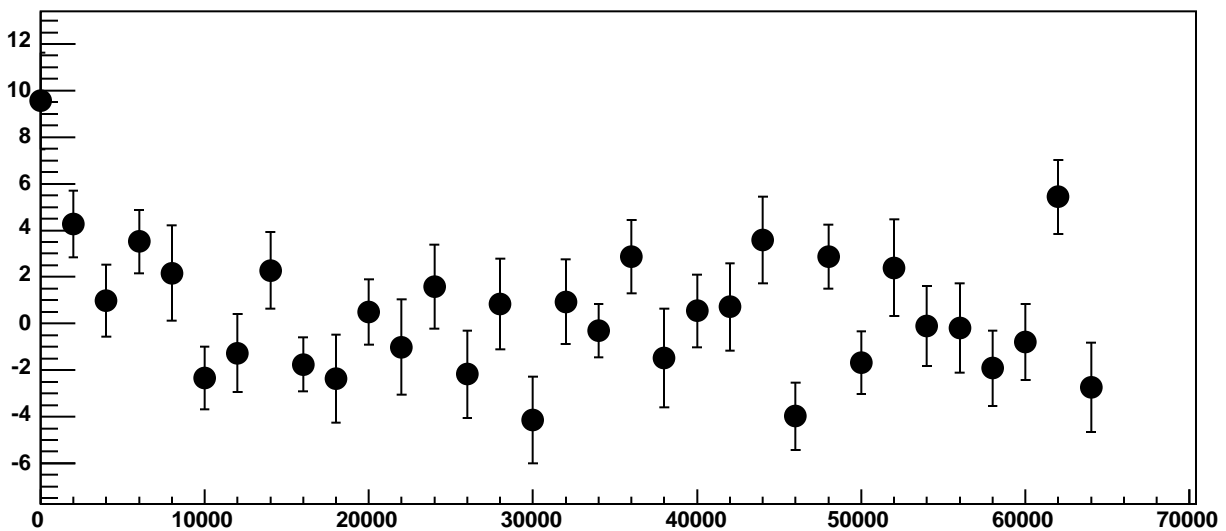
Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



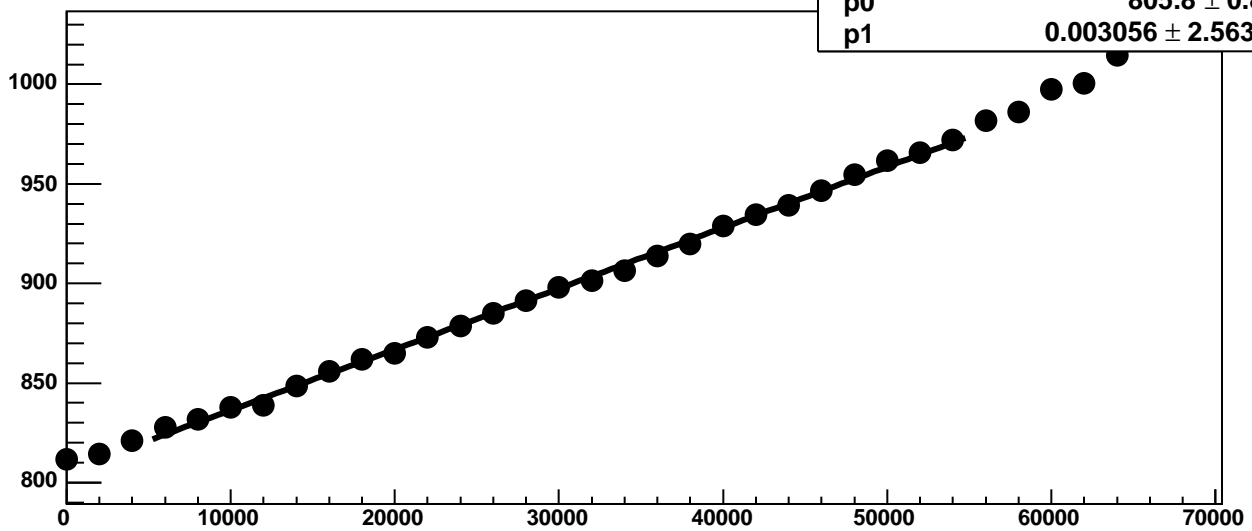
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

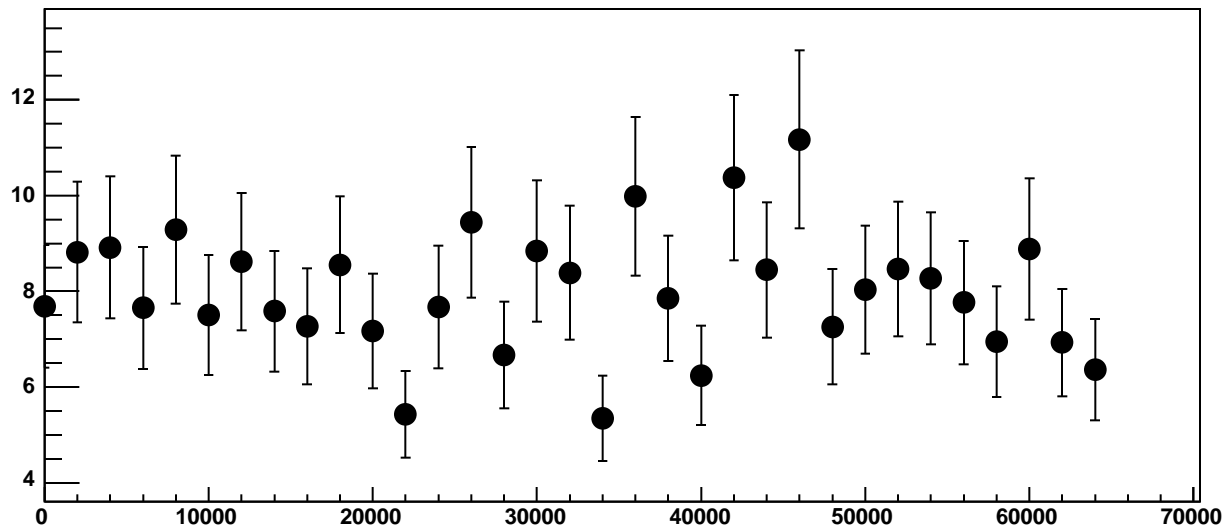


Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

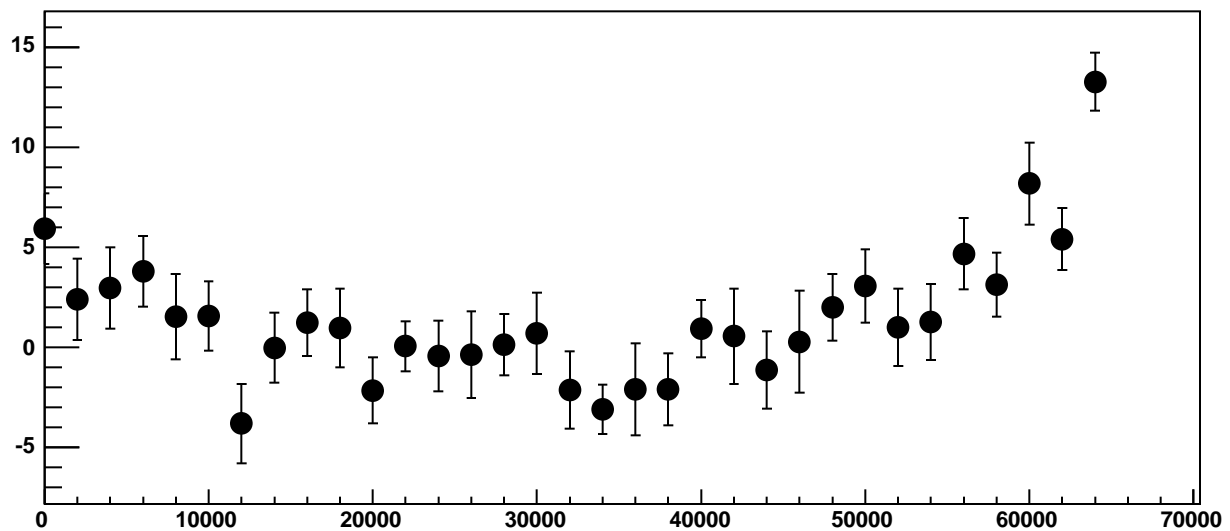


χ^2 / ndf 28.01 / 23
p0 805.8 ± 0.8315
p1 $0.003056 \pm 2.563e-05$

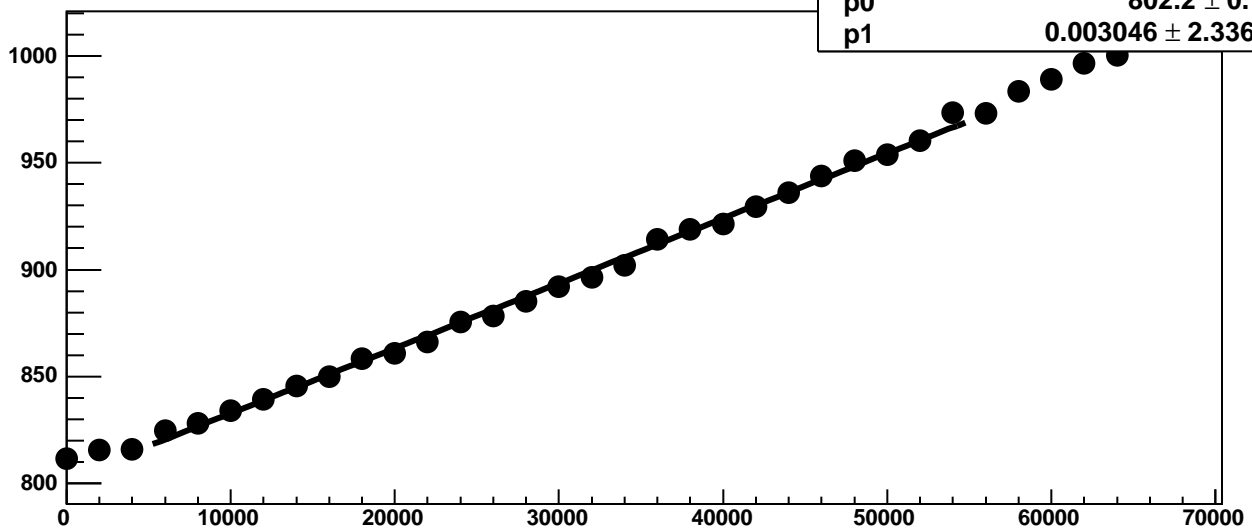
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

53.51 / 23

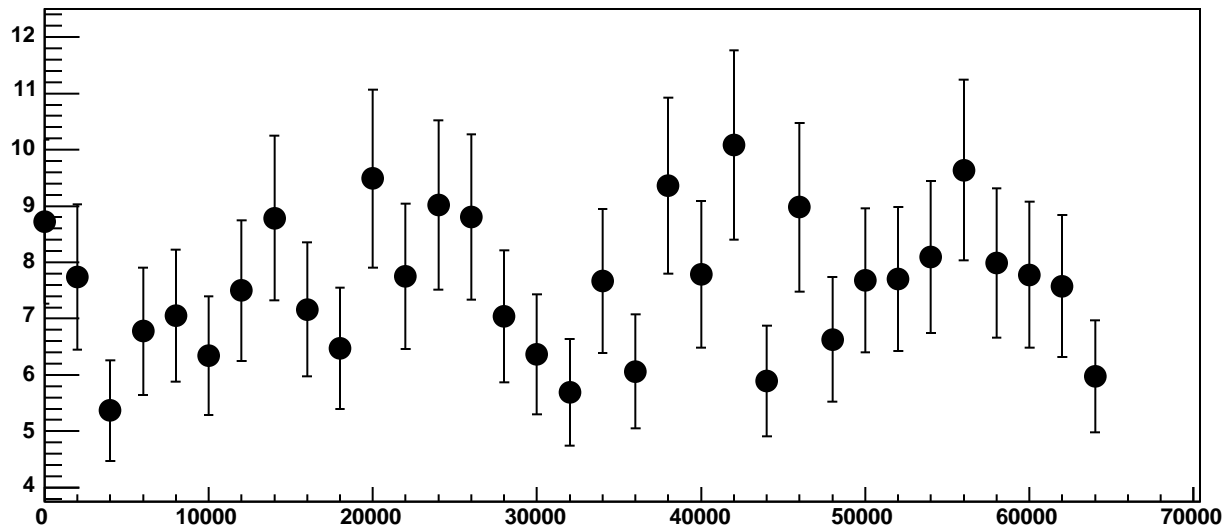
p0

802.2 ± 0.7682

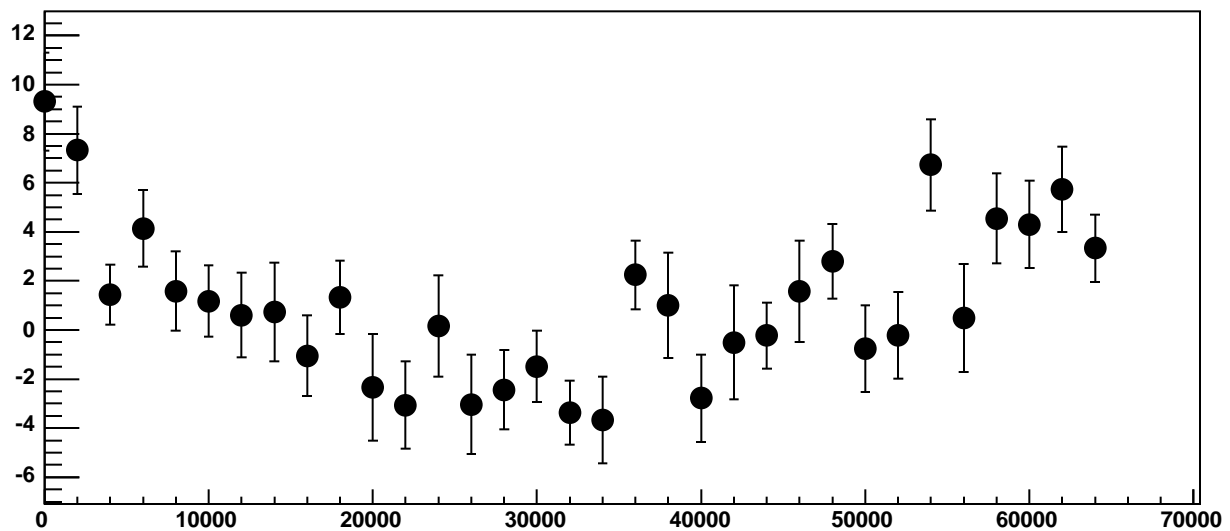
p1

$0.003046 \pm 2.336e-05$

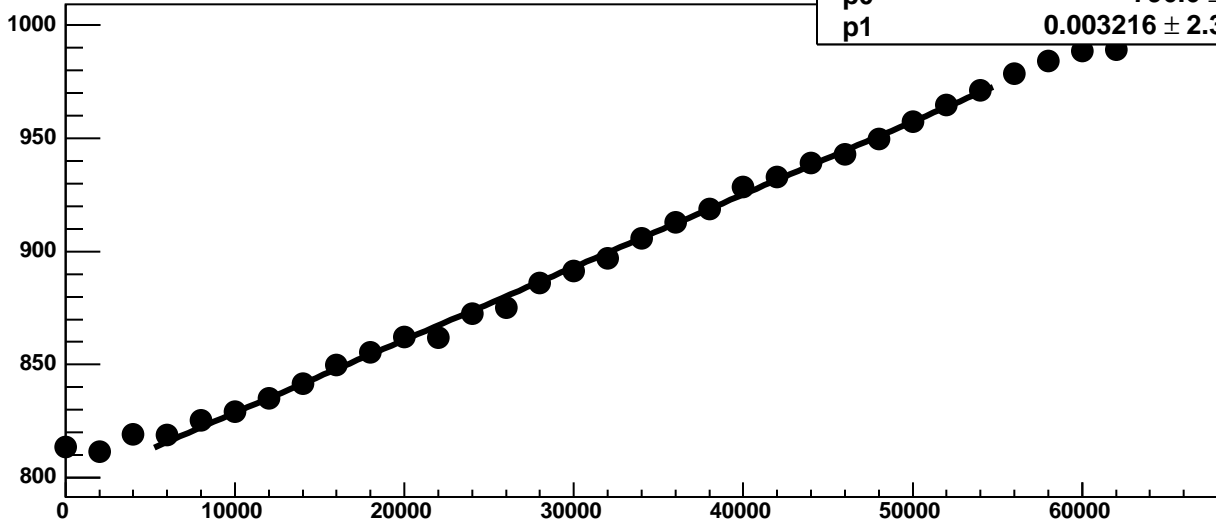
Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.79 / 23

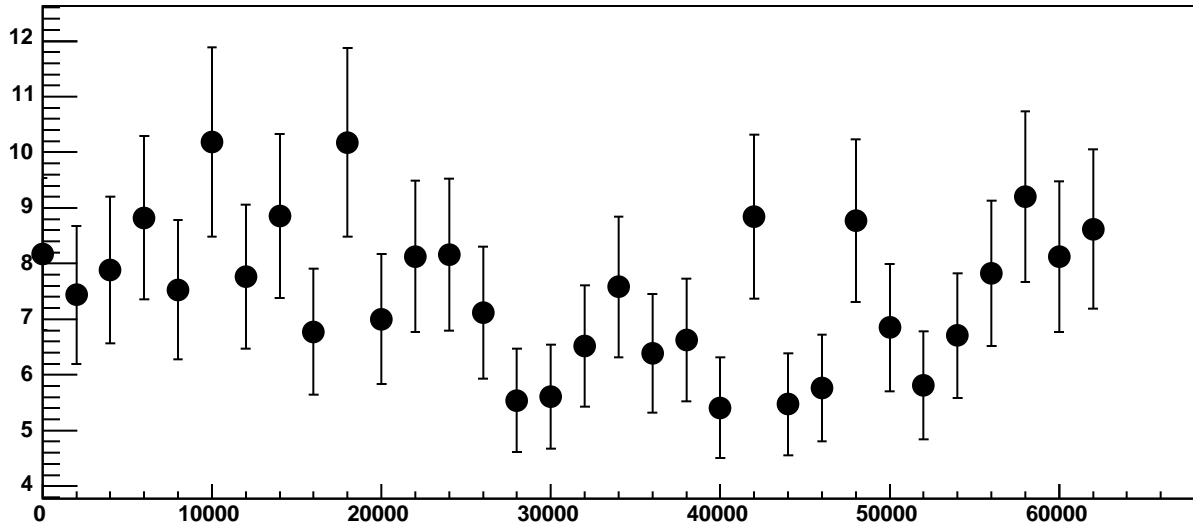
p0

796.6 ± 0.8251

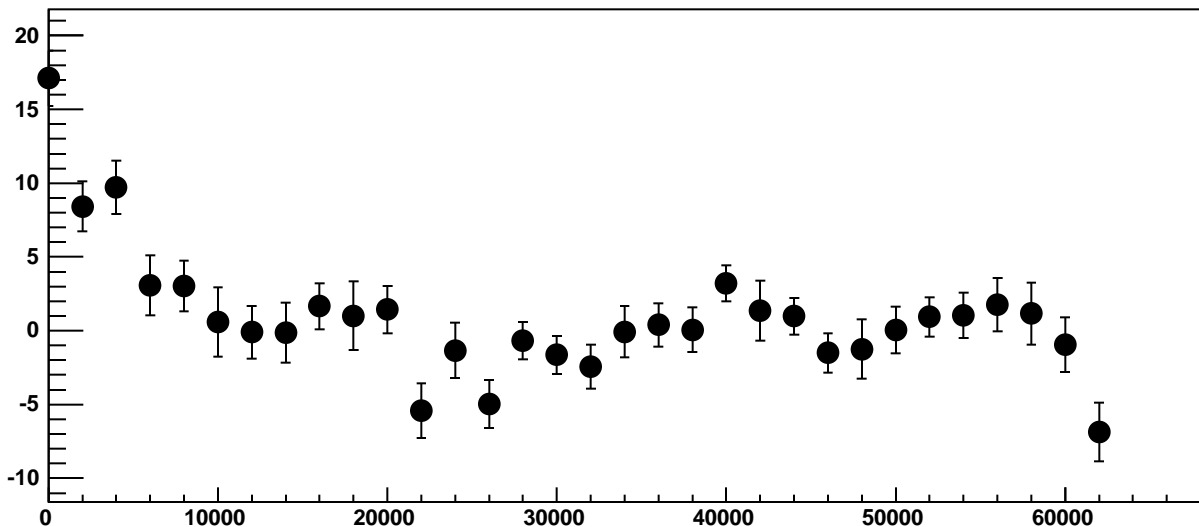
p1

$0.003216 \pm 2.334e-05$

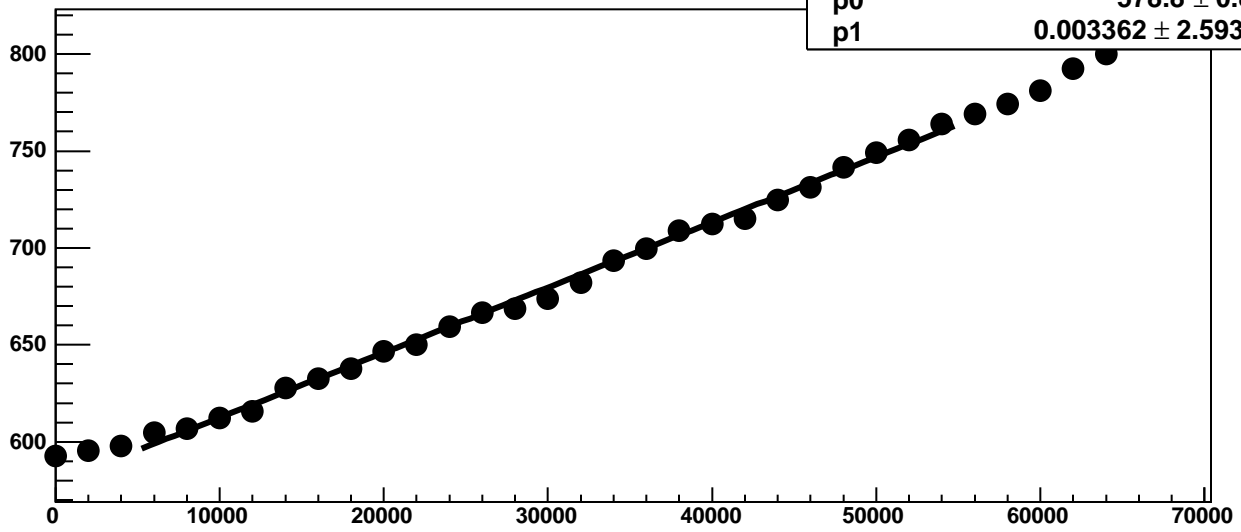
Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

50.48 / 23

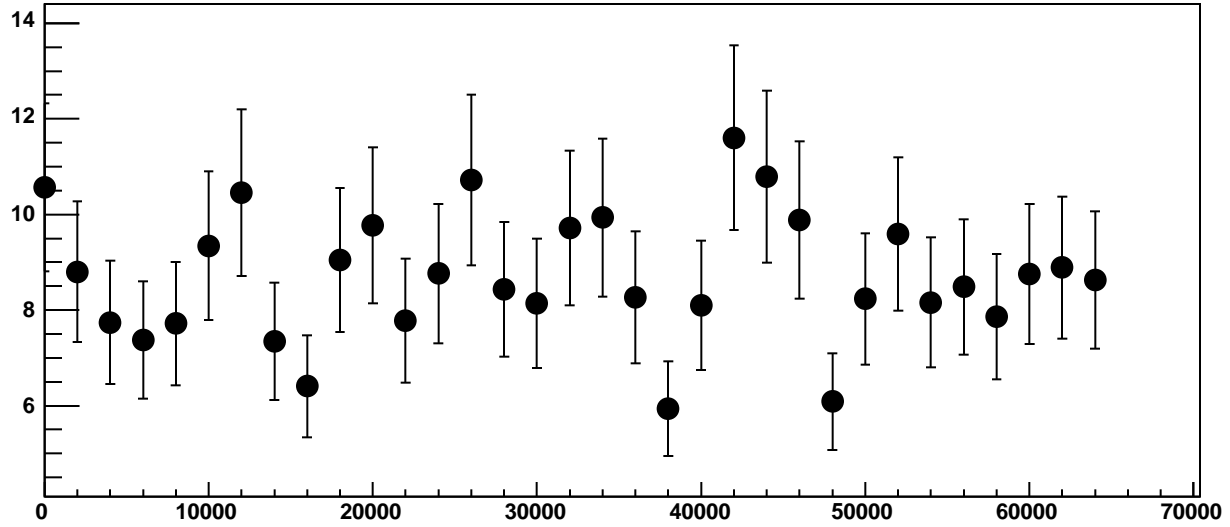
p0

578.8 ± 0.8609

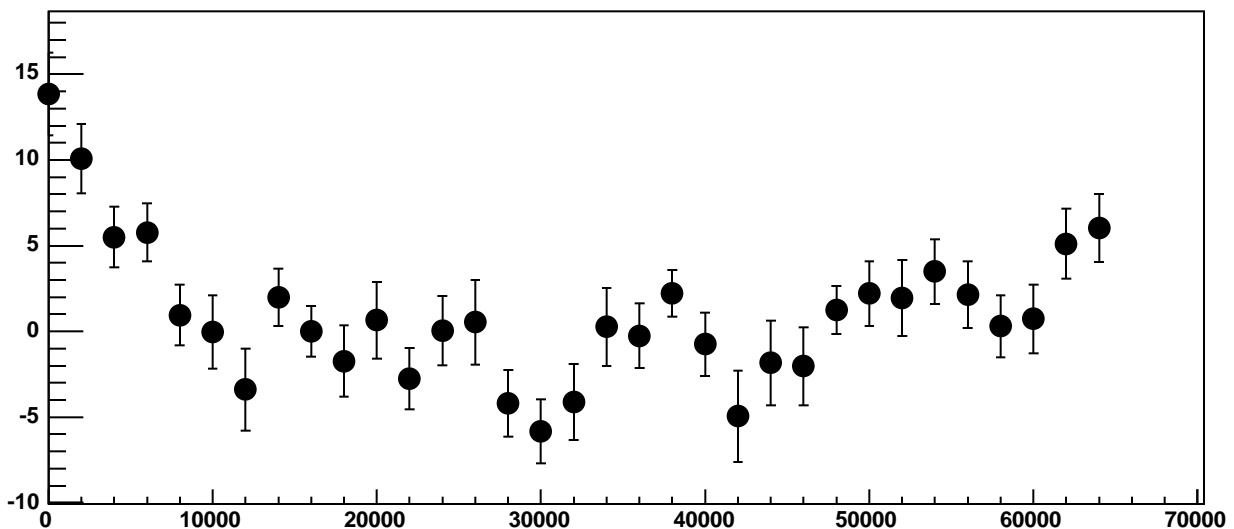
p1

$0.003362 \pm 2.593e-05$

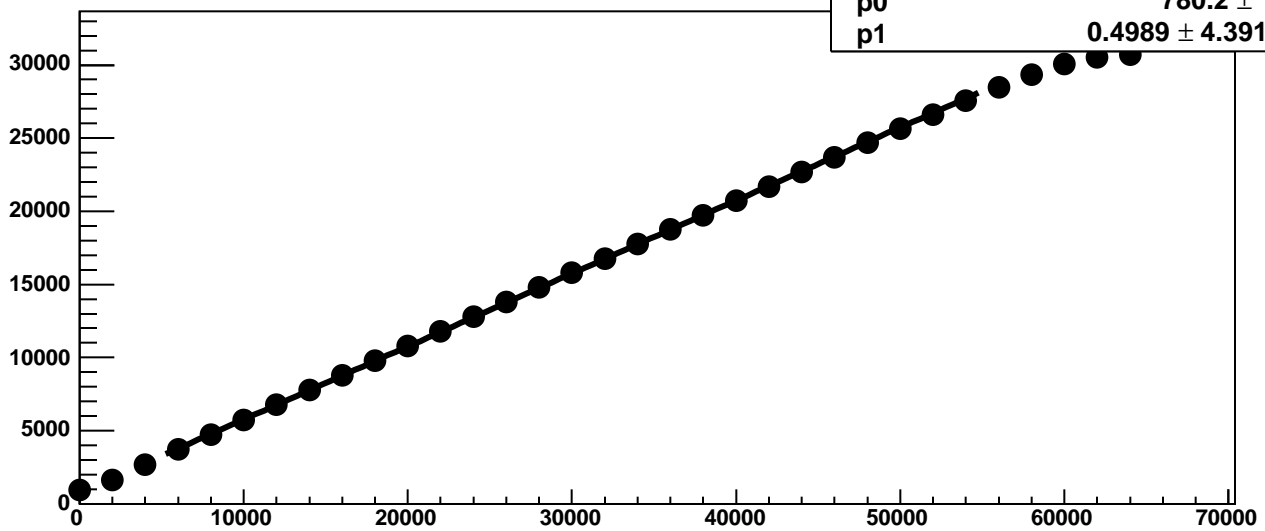
Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



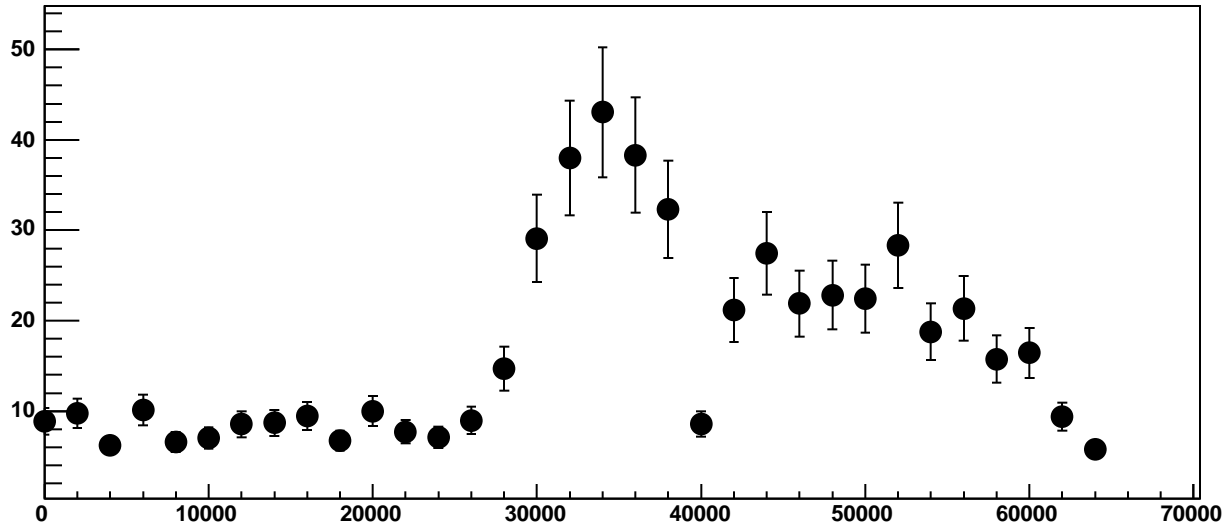
Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



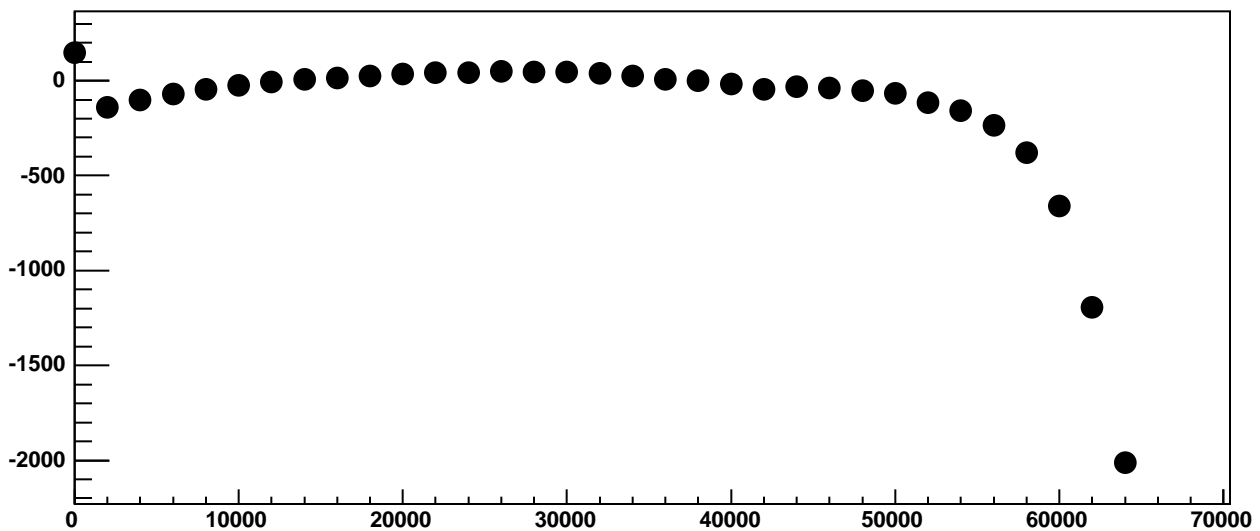
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



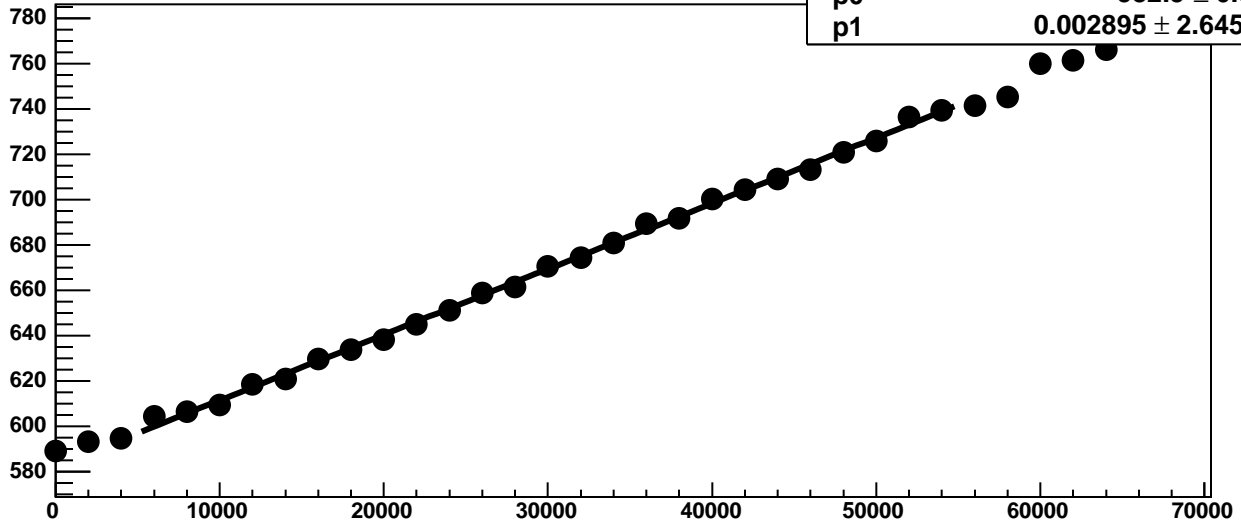
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

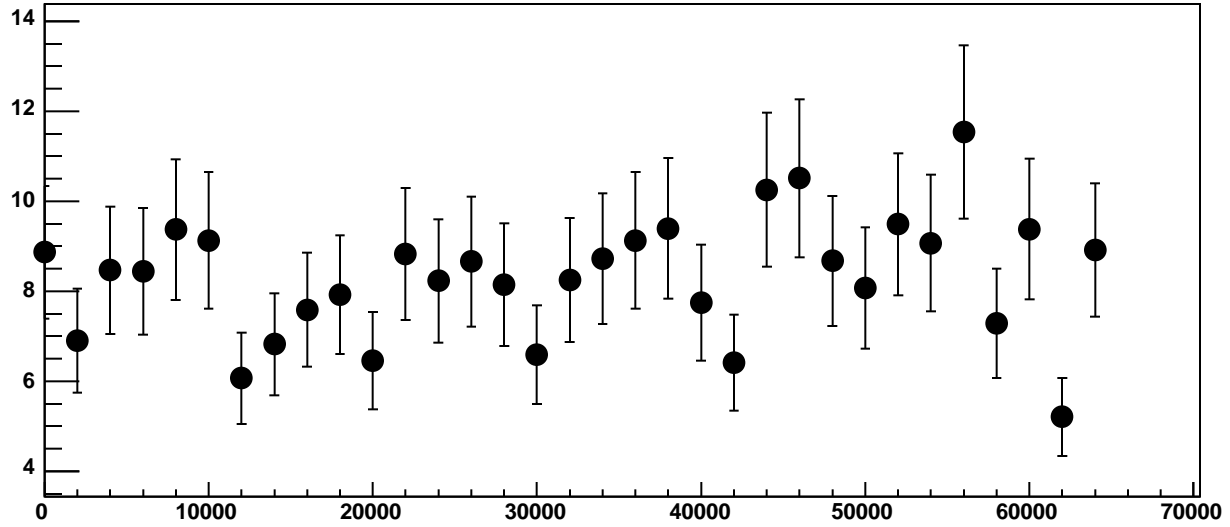


Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

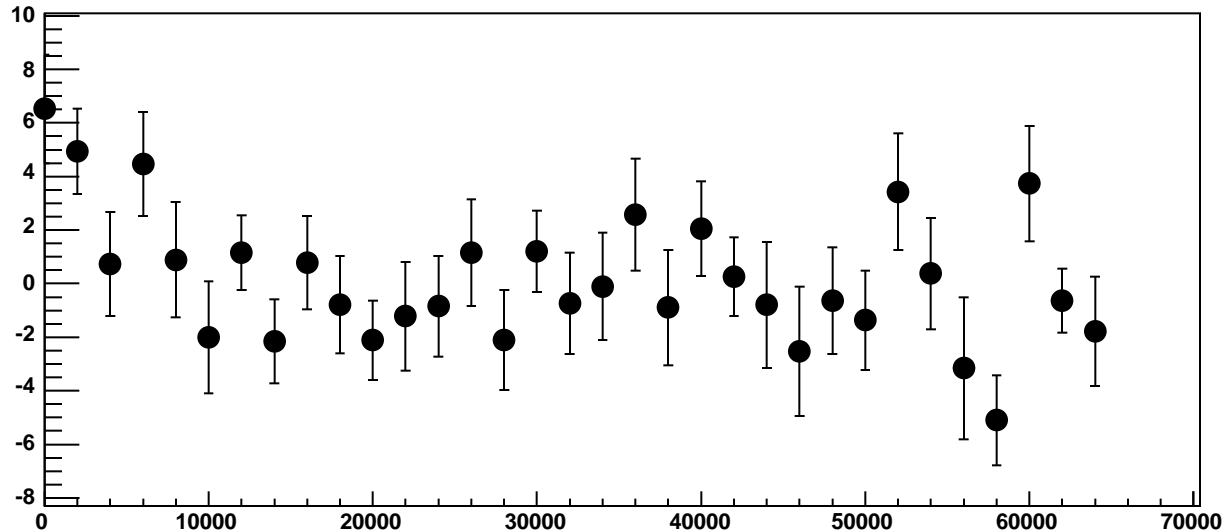


χ^2 / ndf 21.8 / 23
p0 582.5 ± 0.8415
p1 $0.002895 \pm 2.645e-05$

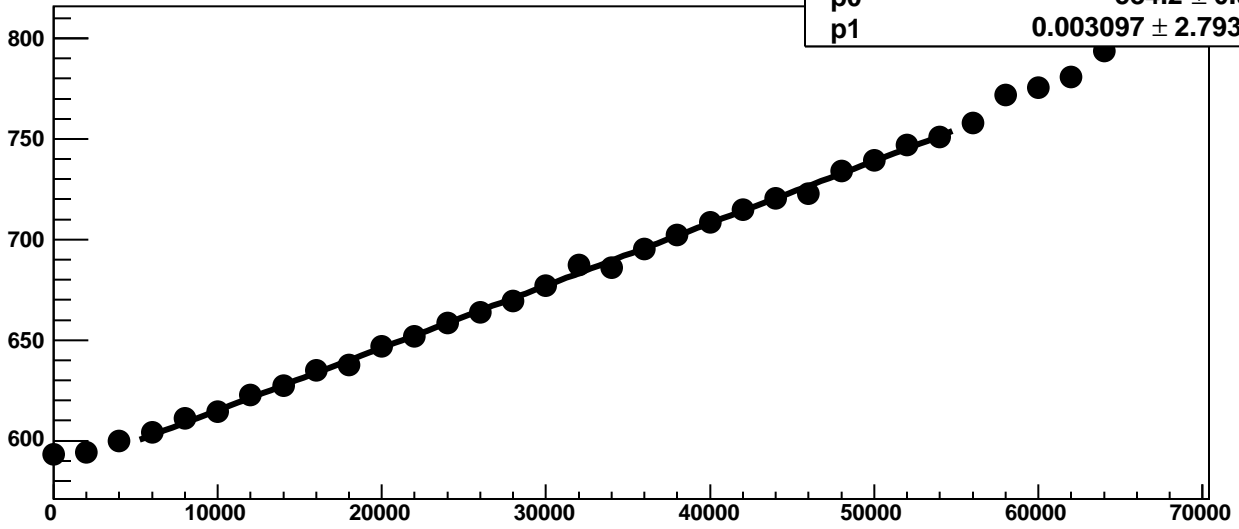
Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

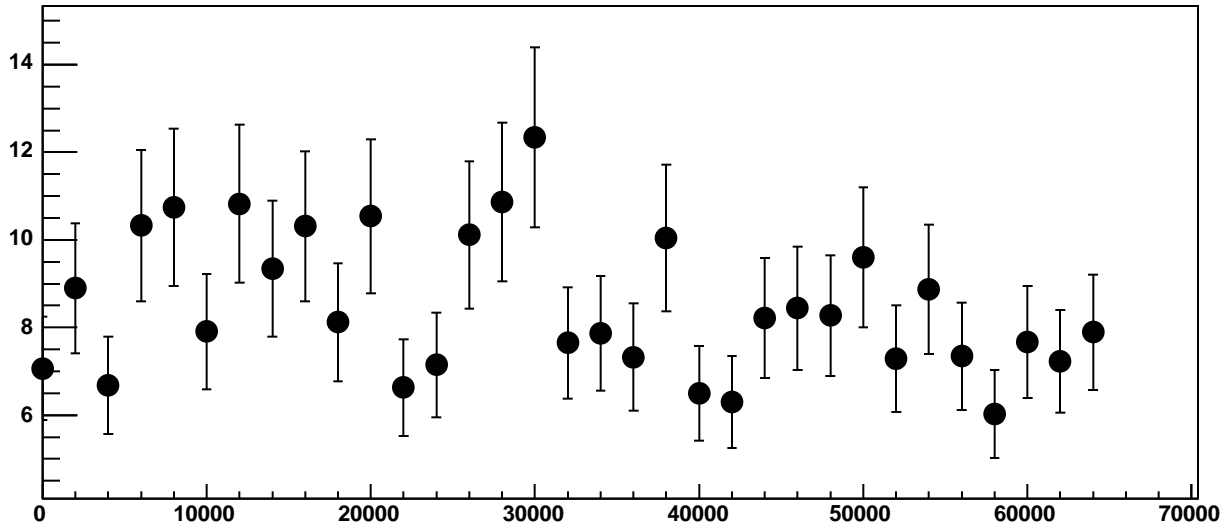


Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

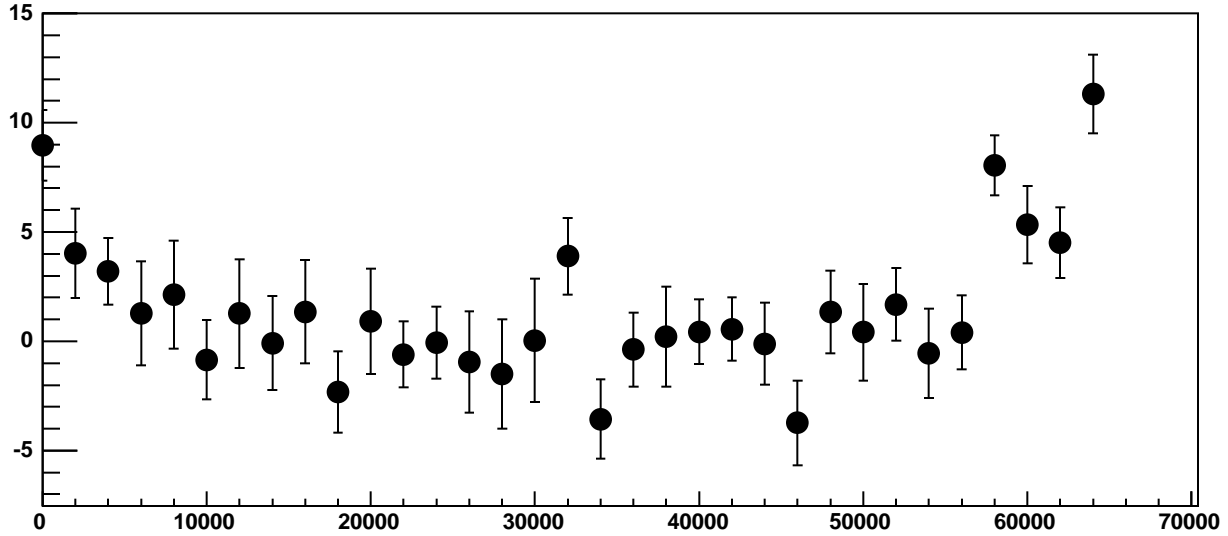


χ^2 / ndf 18.68 / 23
p0 584.2 ± 0.9686
p1 $0.003097 \pm 2.793e-05$

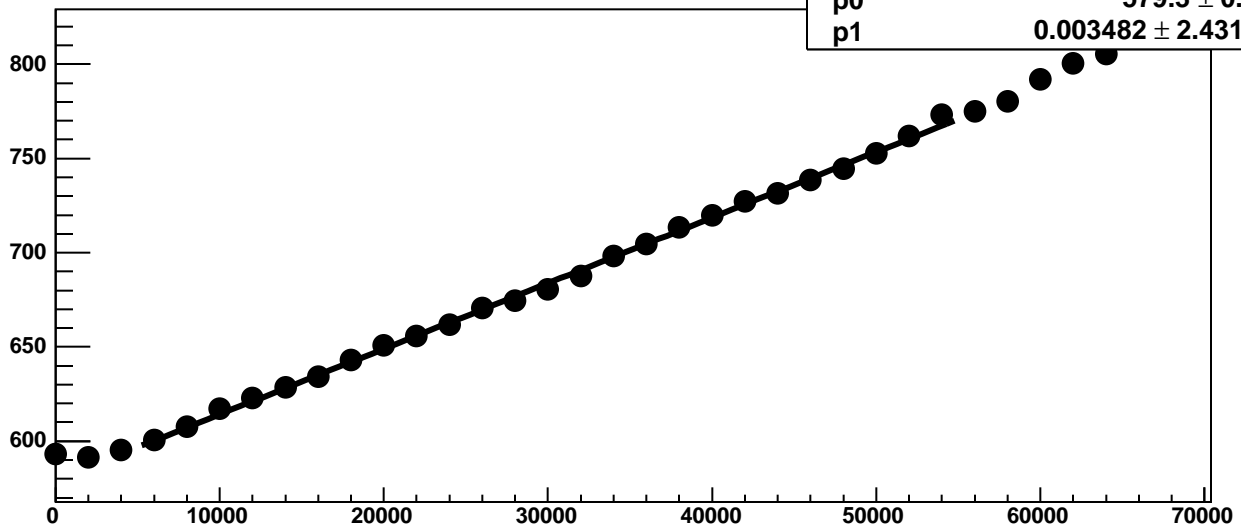
Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.84 / 23

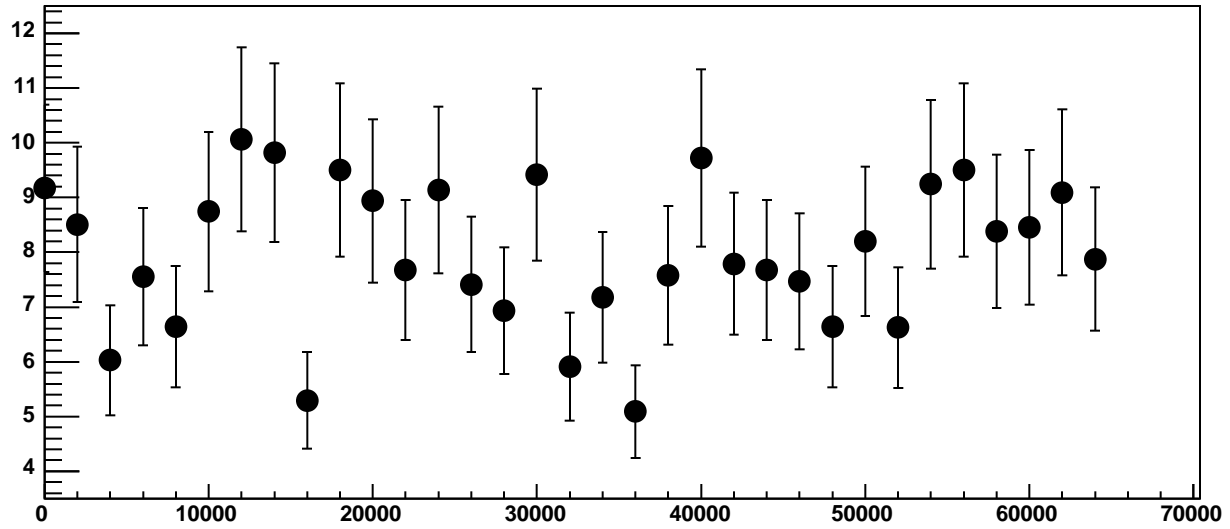
p0

579.3 ± 0.8161

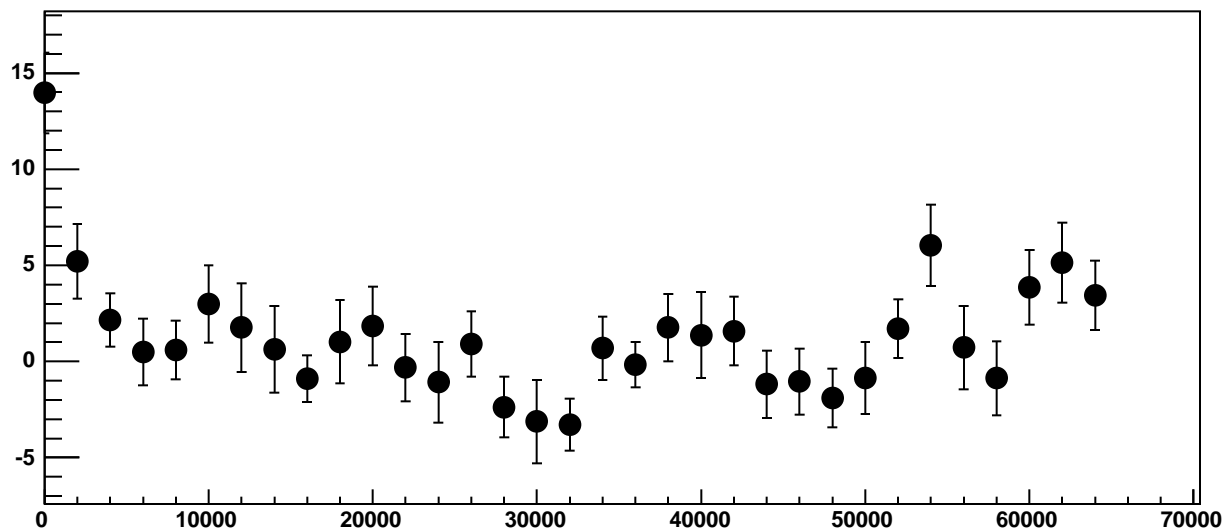
p1

$0.003482 \pm 2.431e-05$

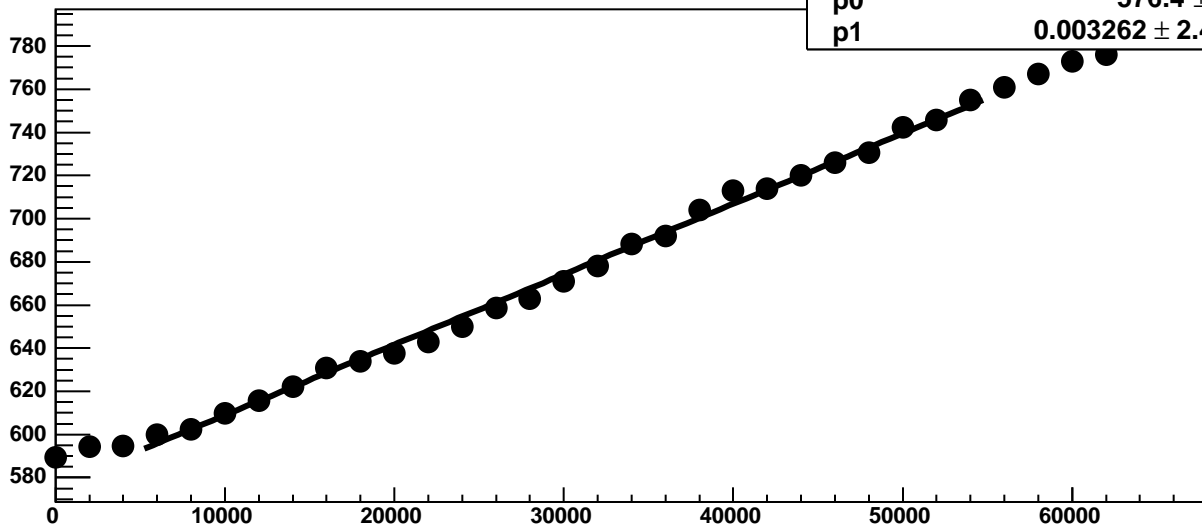
Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

64.09 / 23

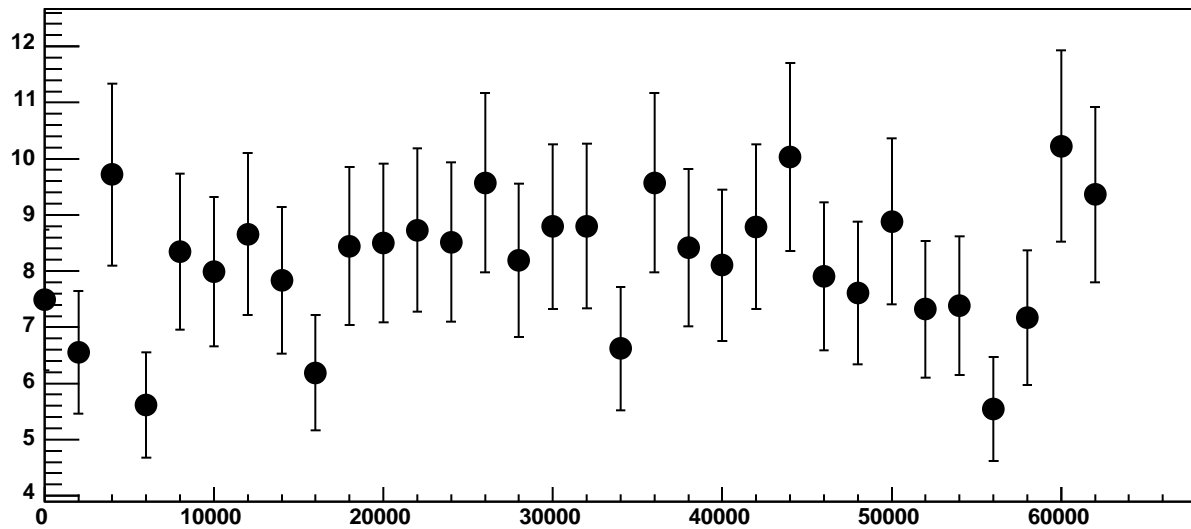
p0

576.4 ± 0.7865

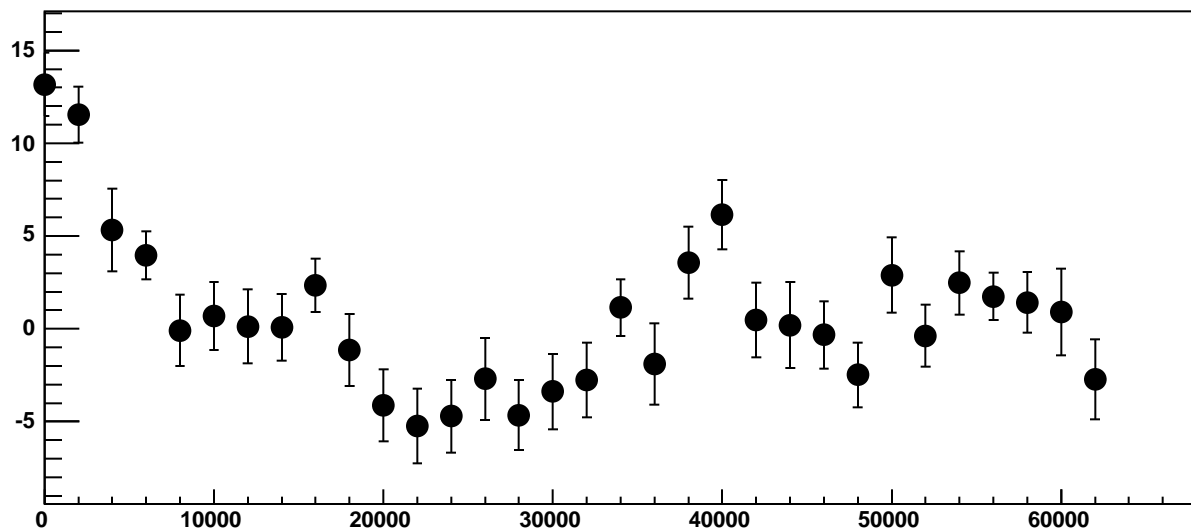
p1

$0.003262 \pm 2.404e-05$

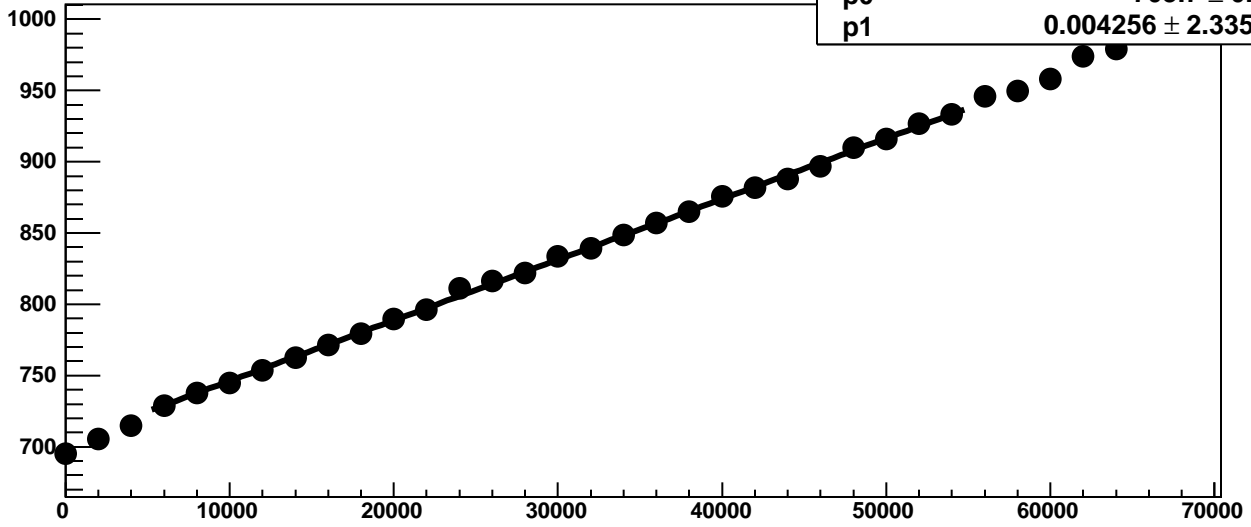
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.01 / 23

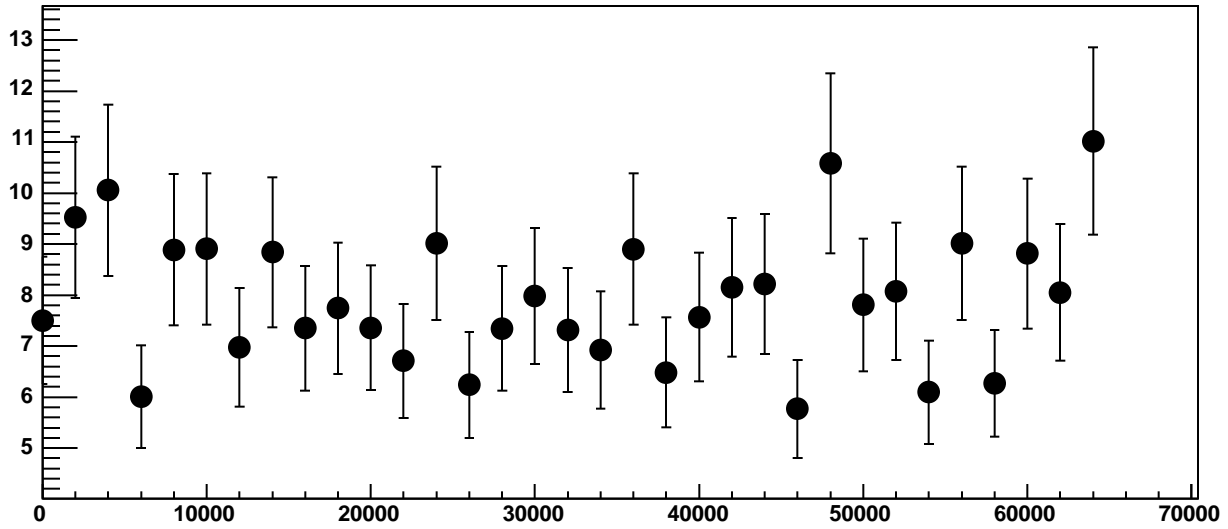
p0

703.7 ± 0.7831

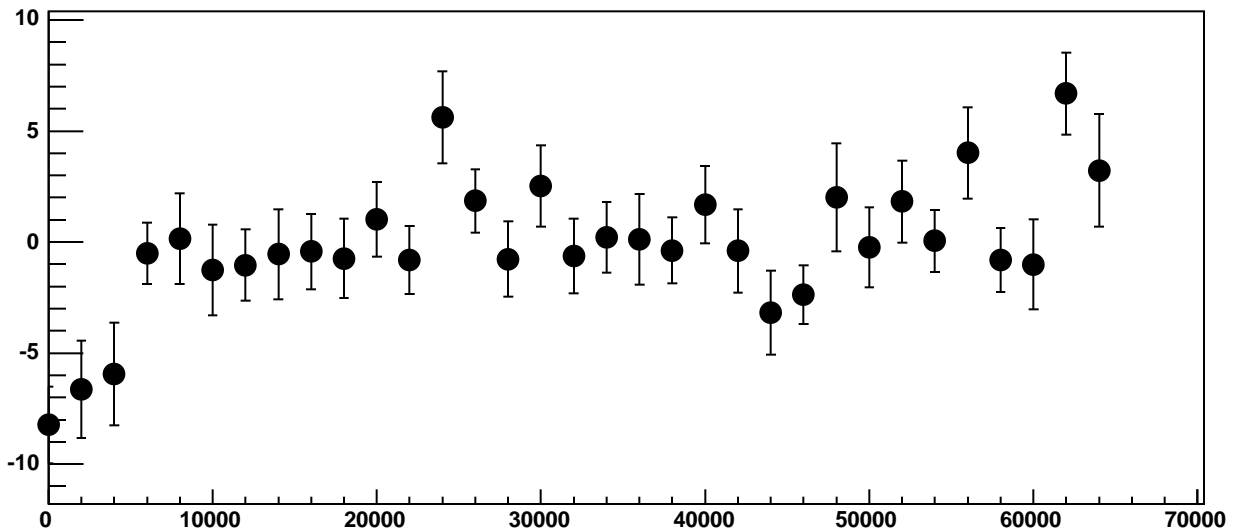
p1

$0.004256 \pm 2.335e-05$

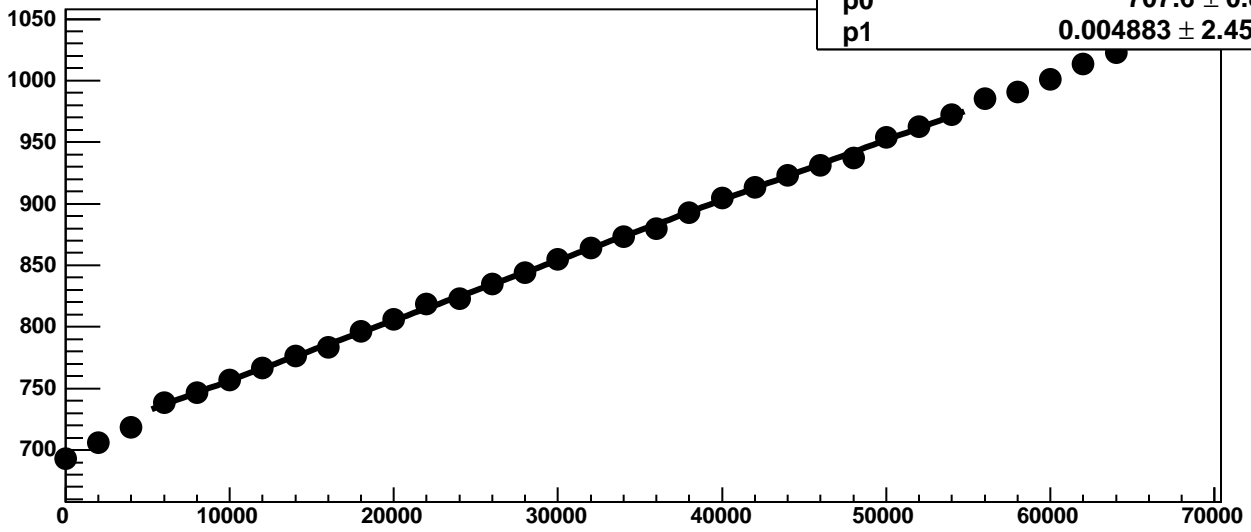
Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



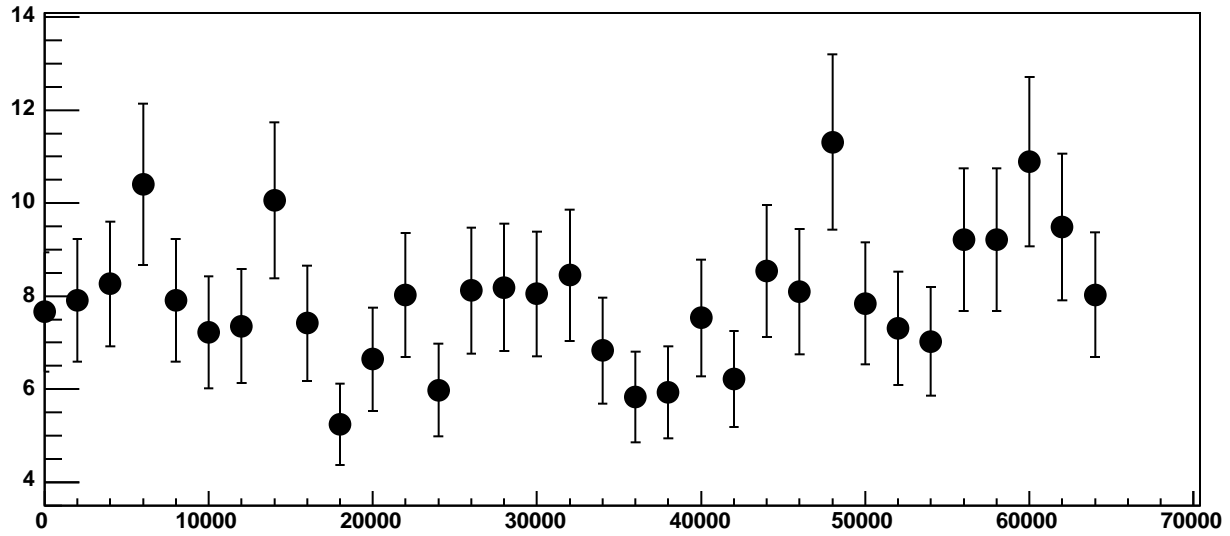
Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



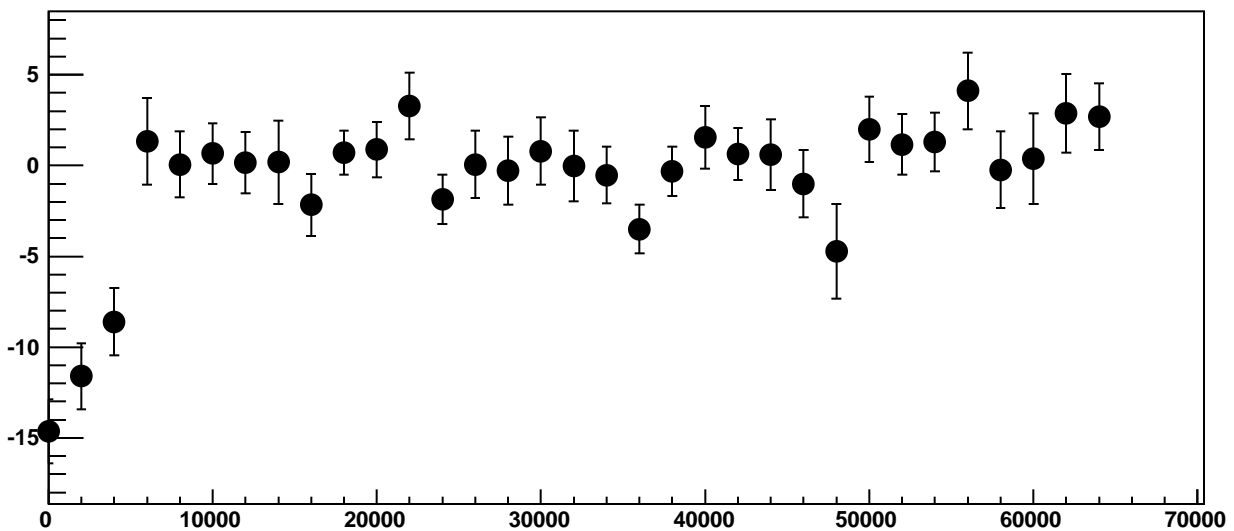
Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



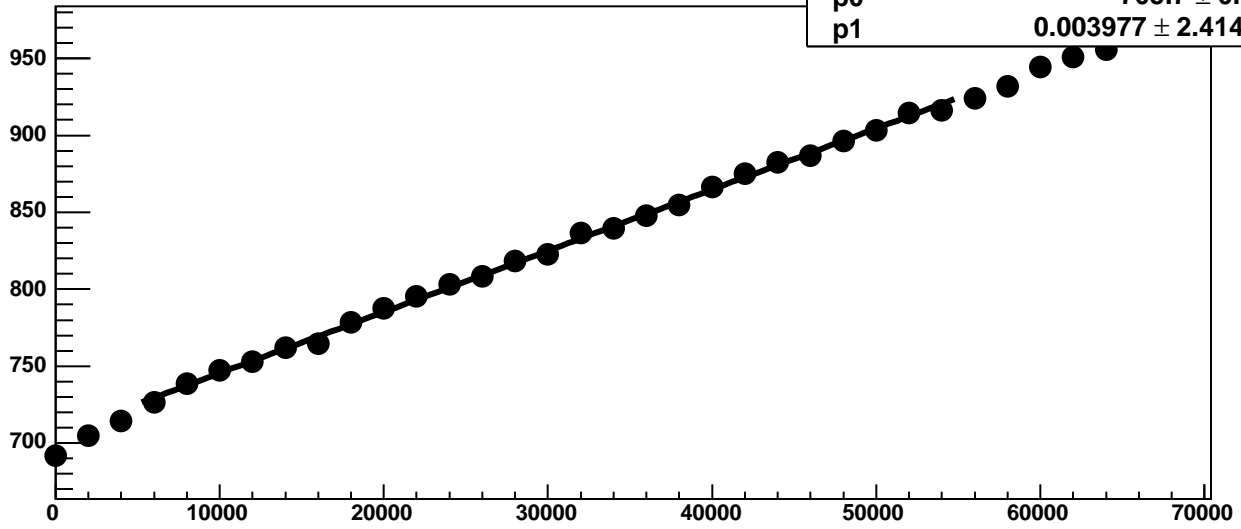
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

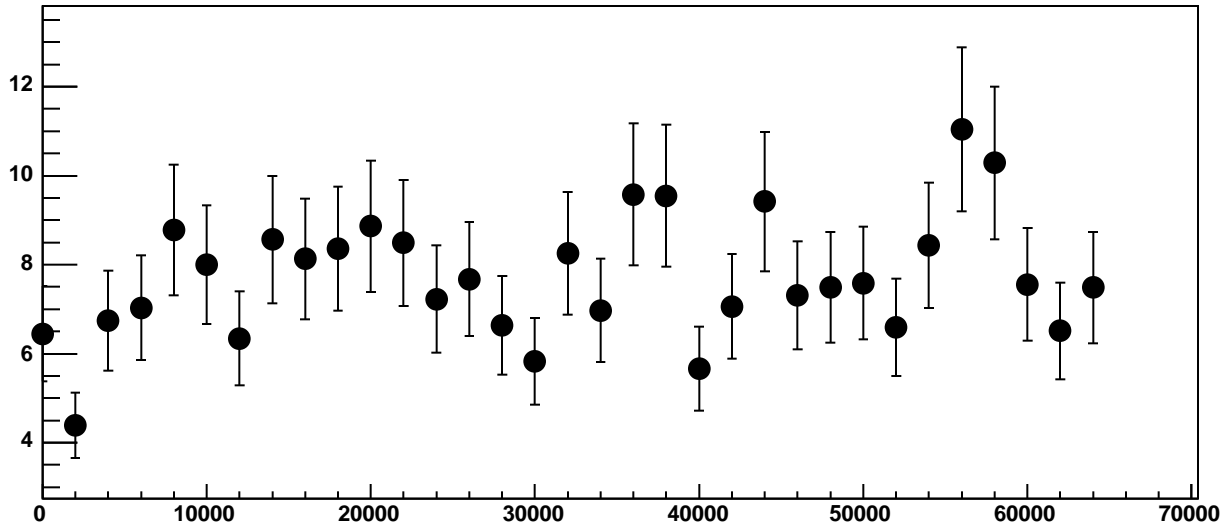


Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

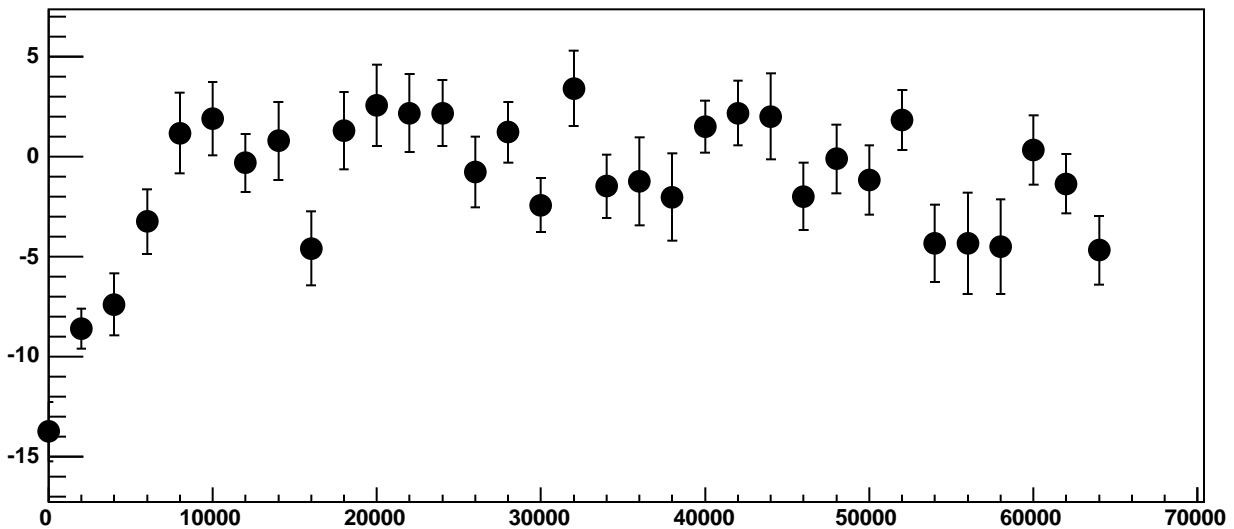


χ^2 / ndf 38.46 / 23
p0 705.7 \pm 0.8114
p1 0.003977 \pm 2.414e-05

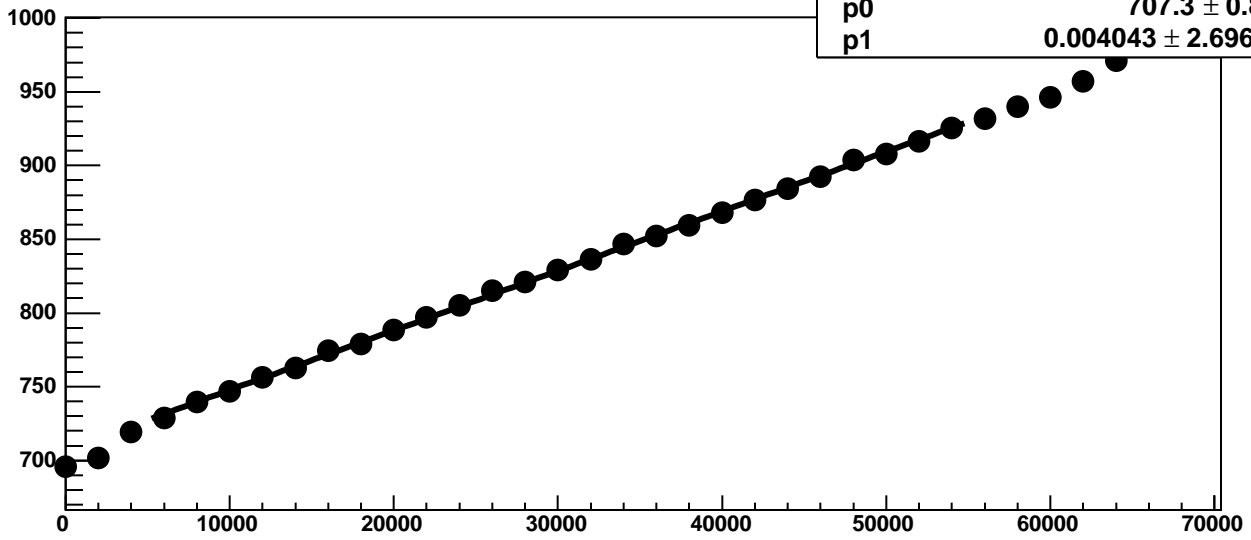
Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

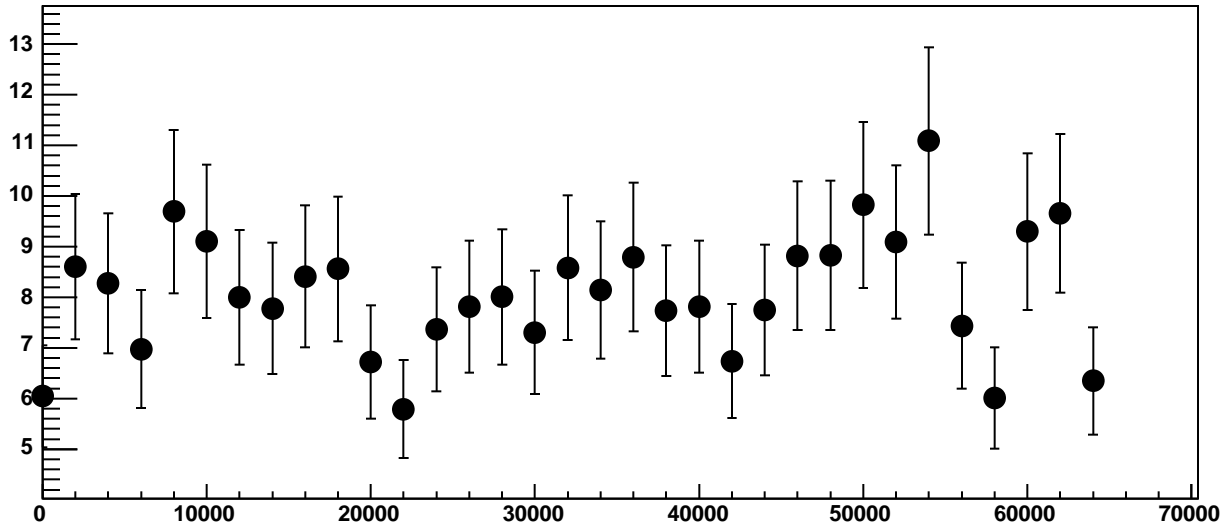


Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

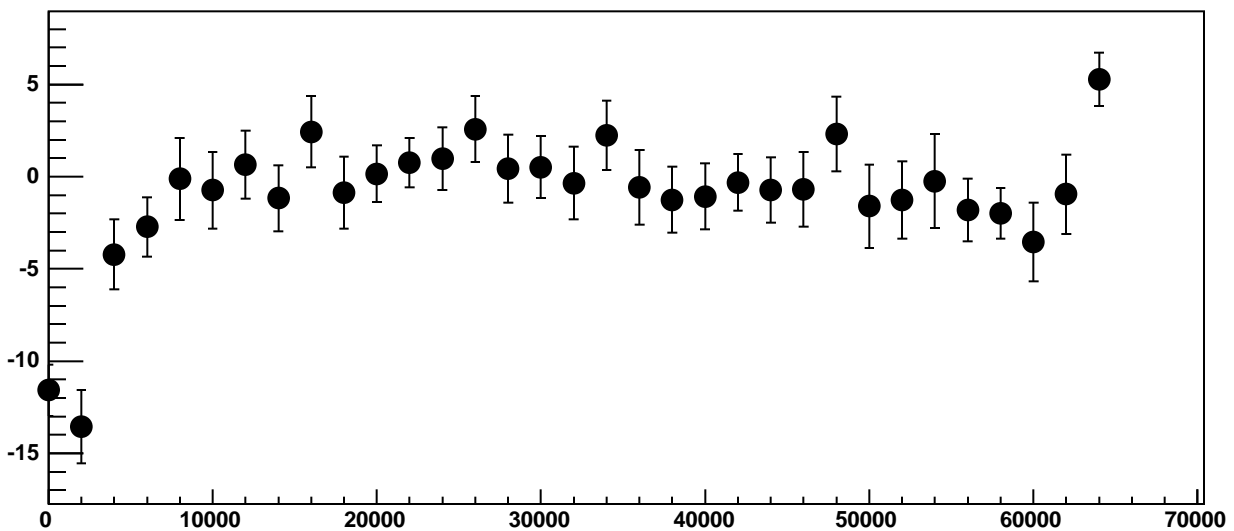


χ^2 / ndf 13.18 / 23
p0 707.3 ± 0.8578
p1 $0.004043 \pm 2.696e-05$

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

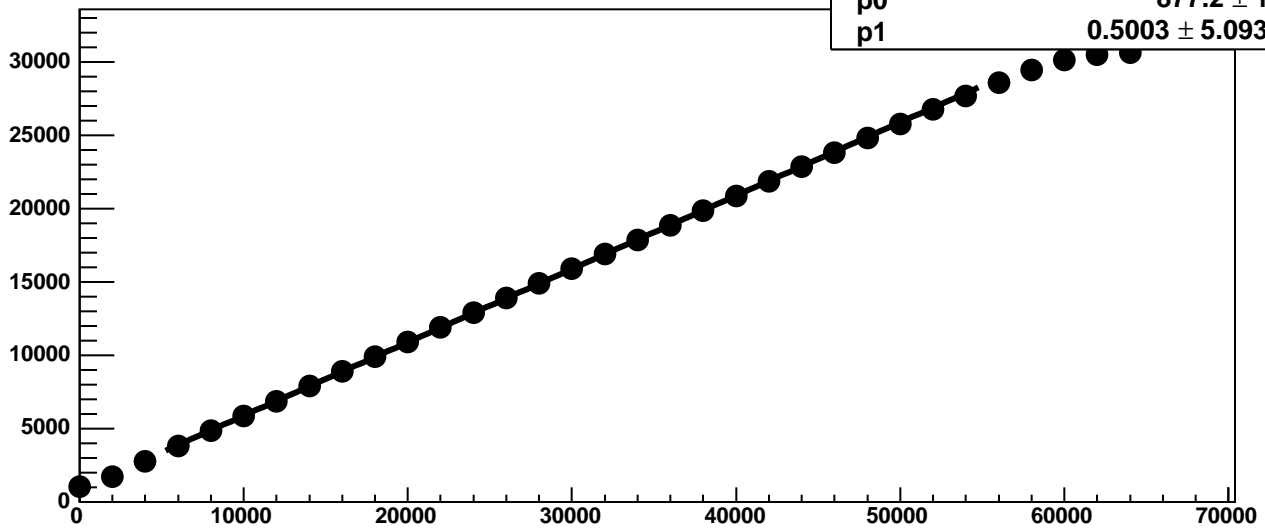


Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

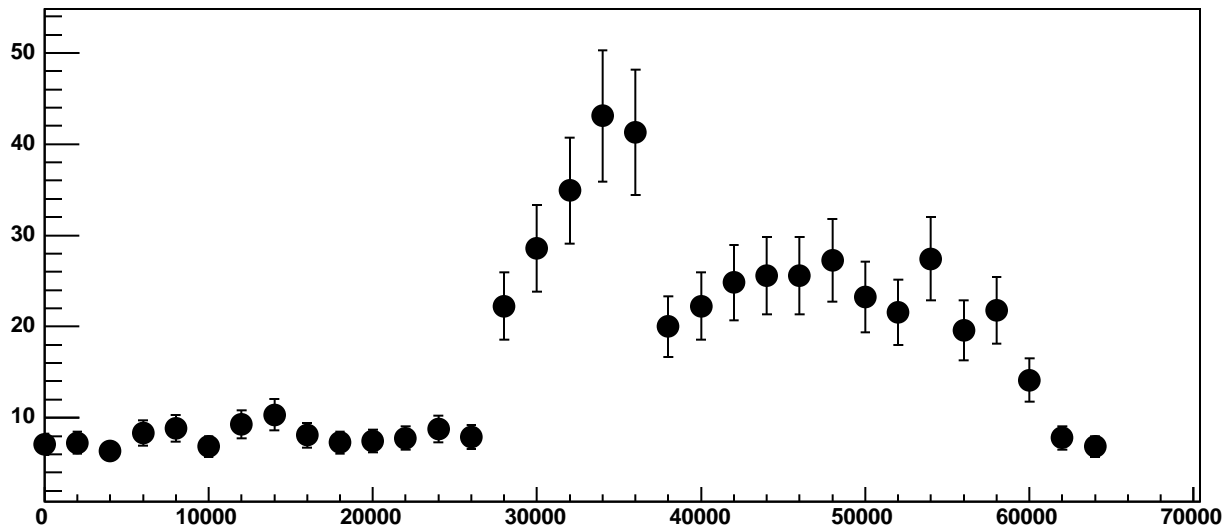


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

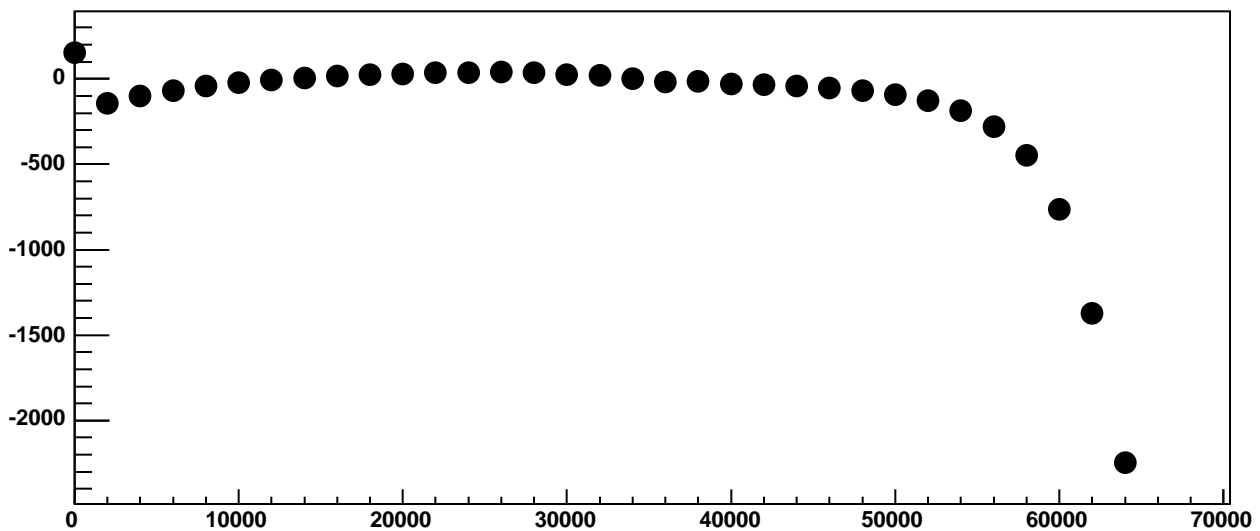
χ^2 / ndf 6123 / 23
p0 877.2 ± 1.106
p1 $0.5003 \pm 5.093e-05$



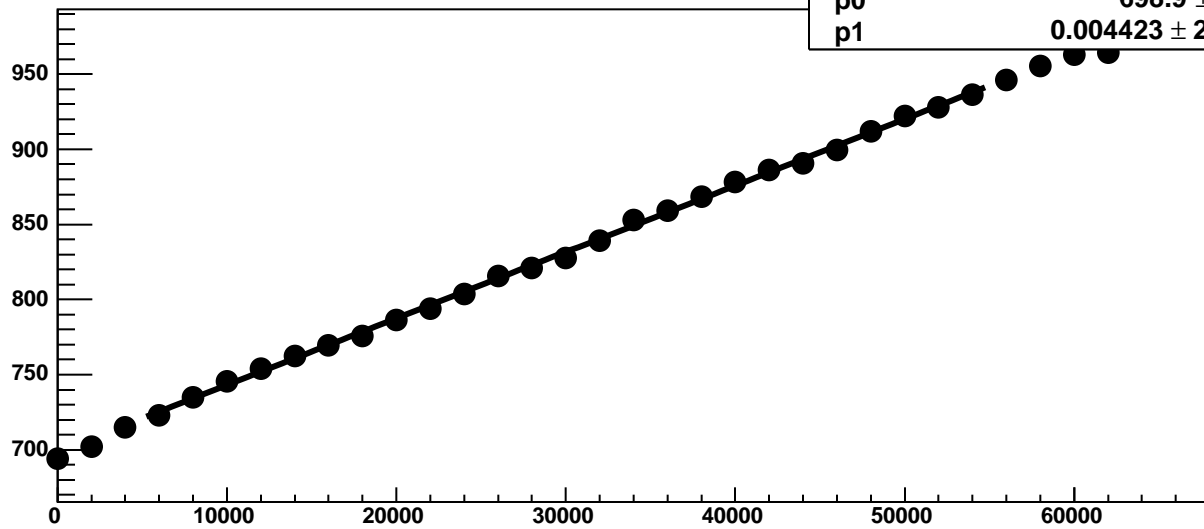
Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

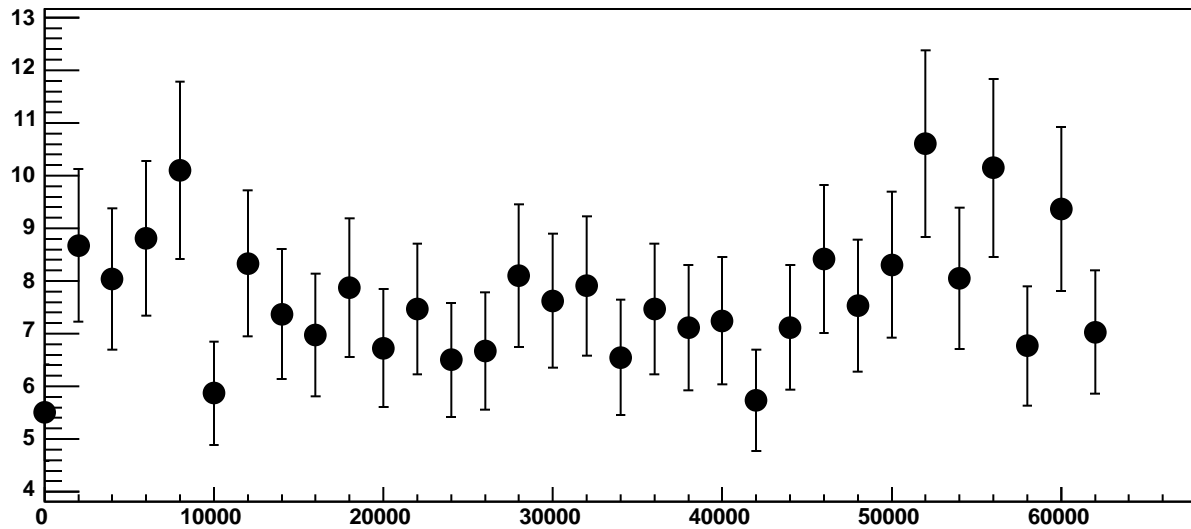


Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

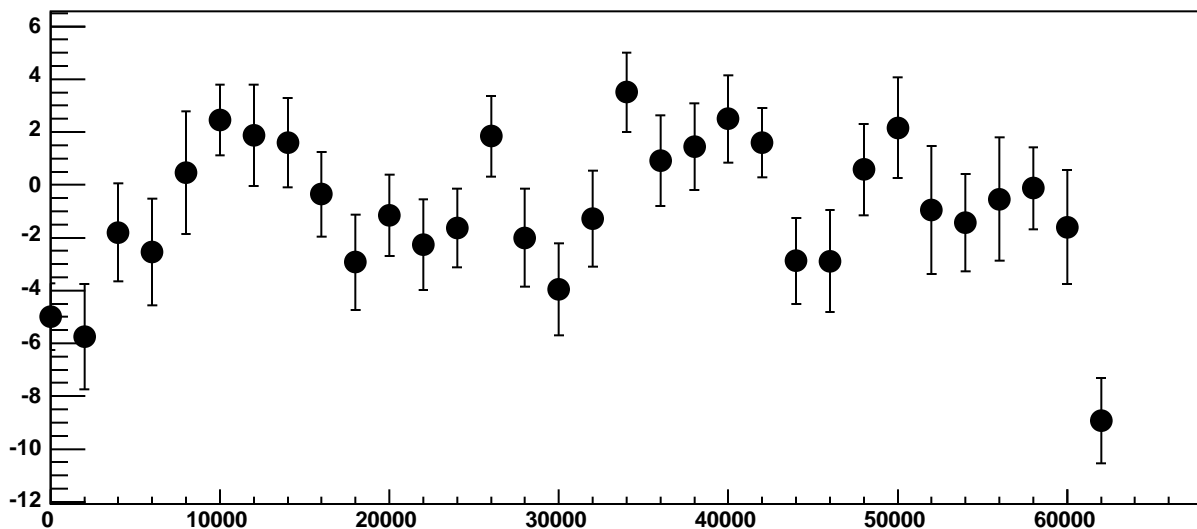


χ^2 / ndf 38.92 / 23
p0 698.9 ± 0.8134
p1 $0.004423 \pm 2.49\text{e-}05$

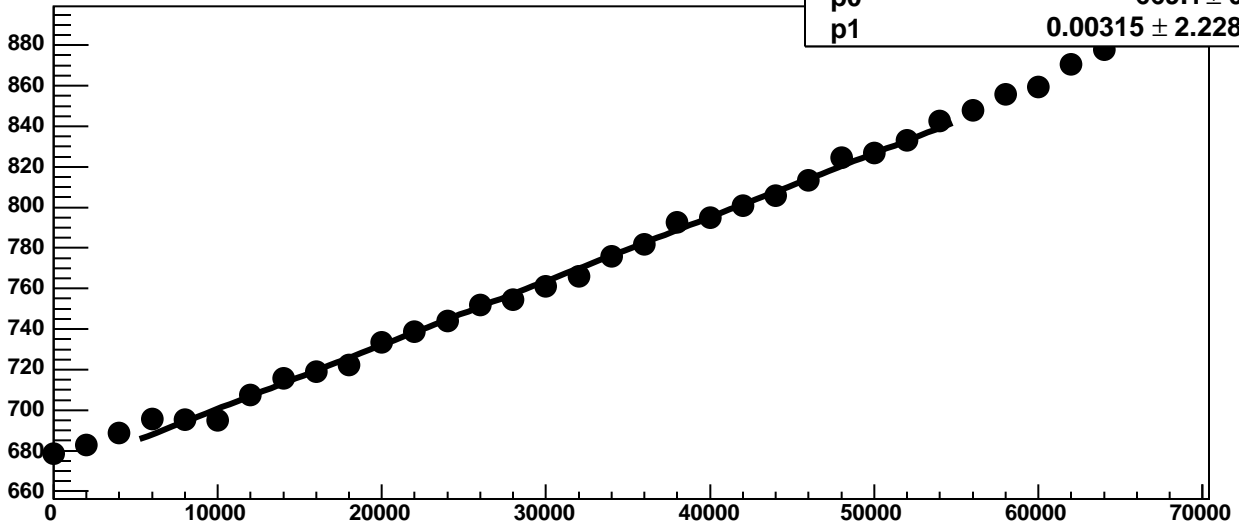
Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

69.7 / 23

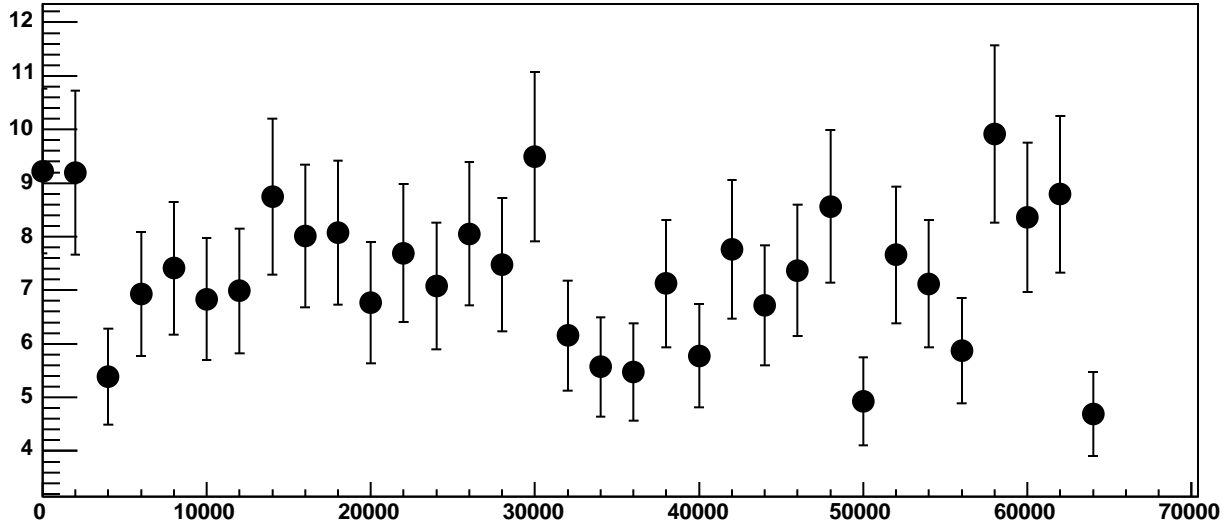
p0

669.1 ± 0.767

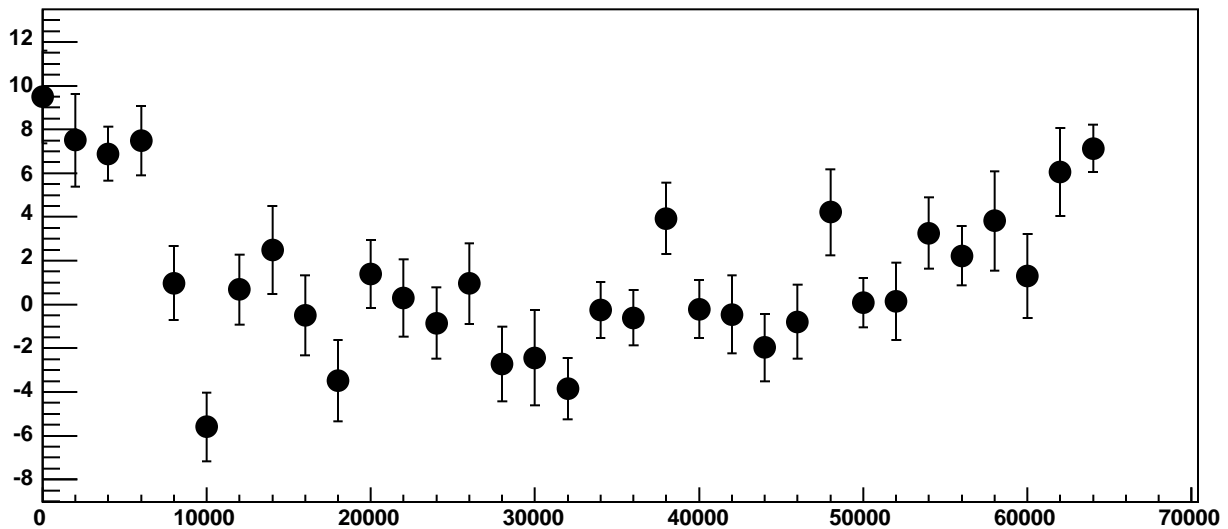
p1

$0.00315 \pm 2.228e-05$

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

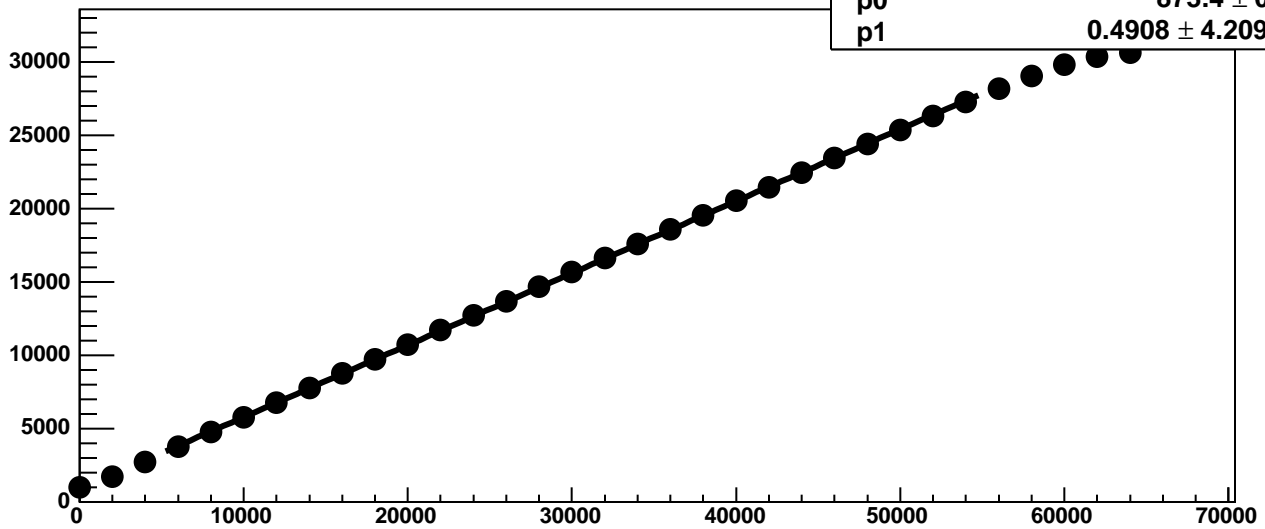


Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

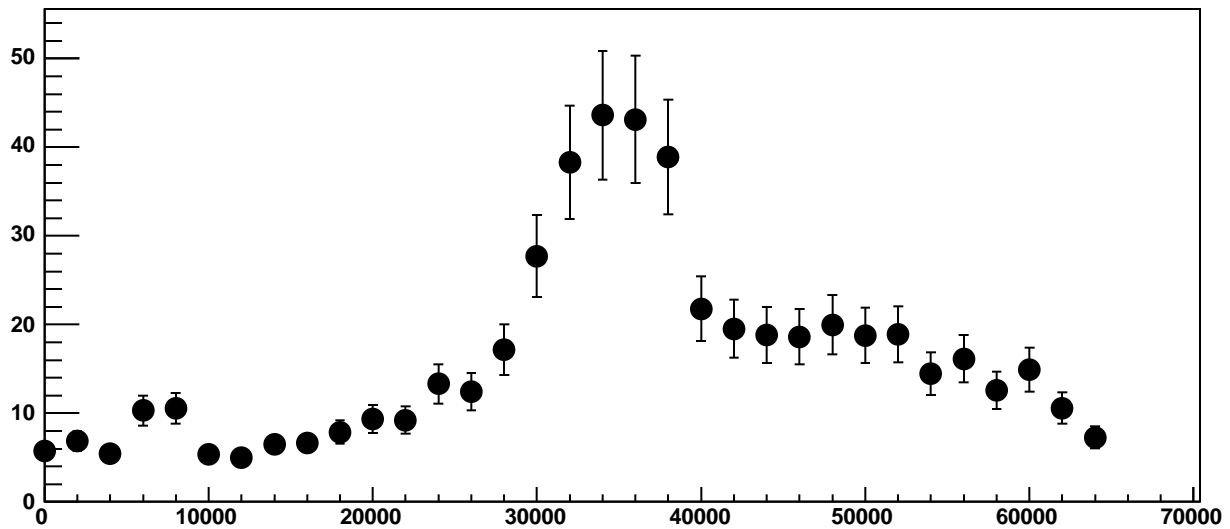


Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

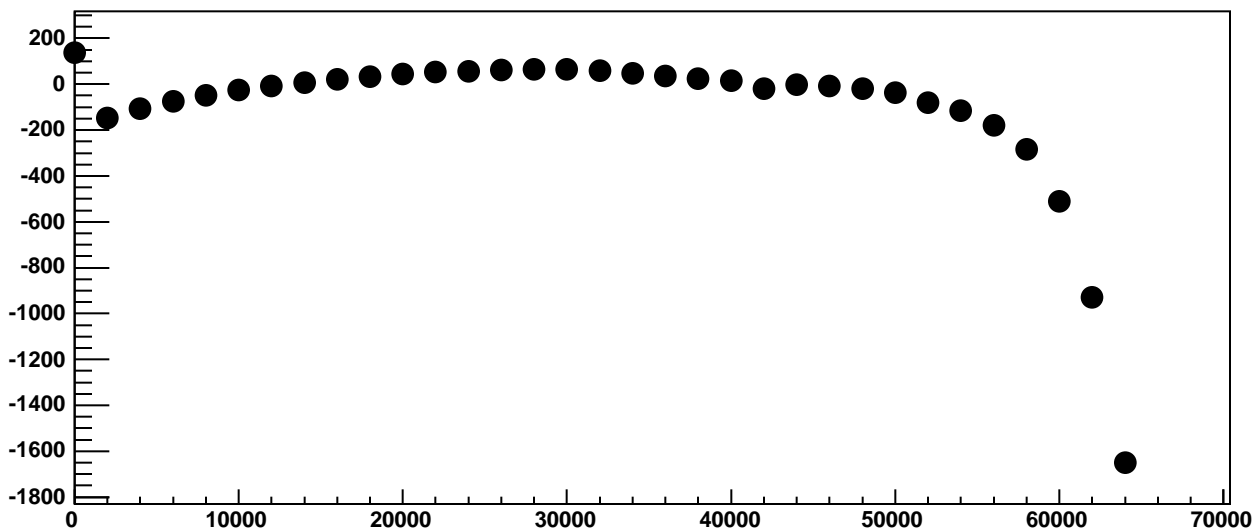
χ^2 / ndf 6398 / 23
p0 873.4 ± 0.915
p1 $0.4908 \pm 4.209\text{e-}05$



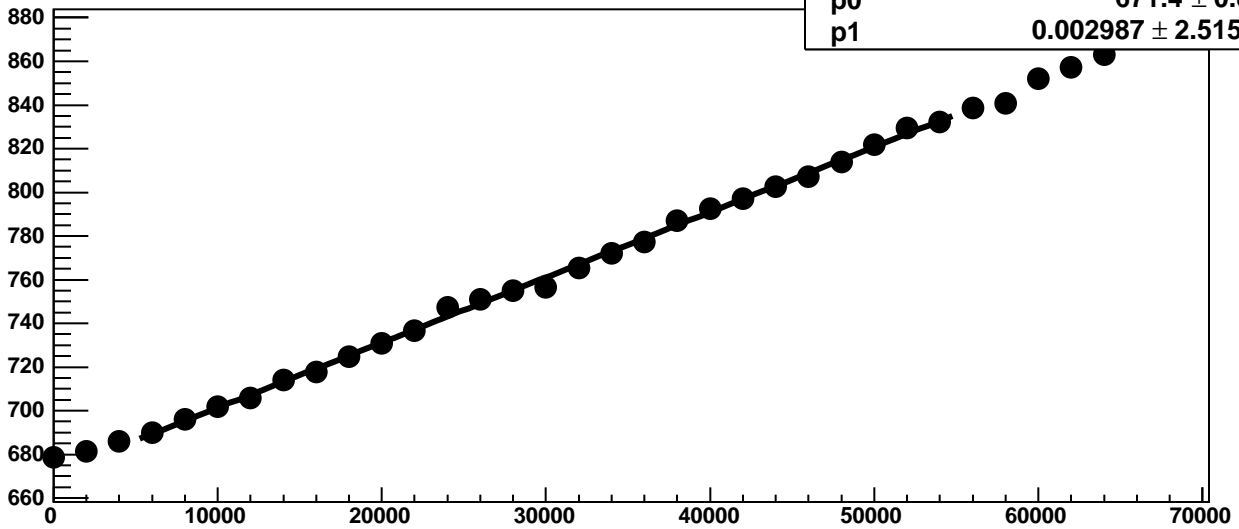
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.69 / 23

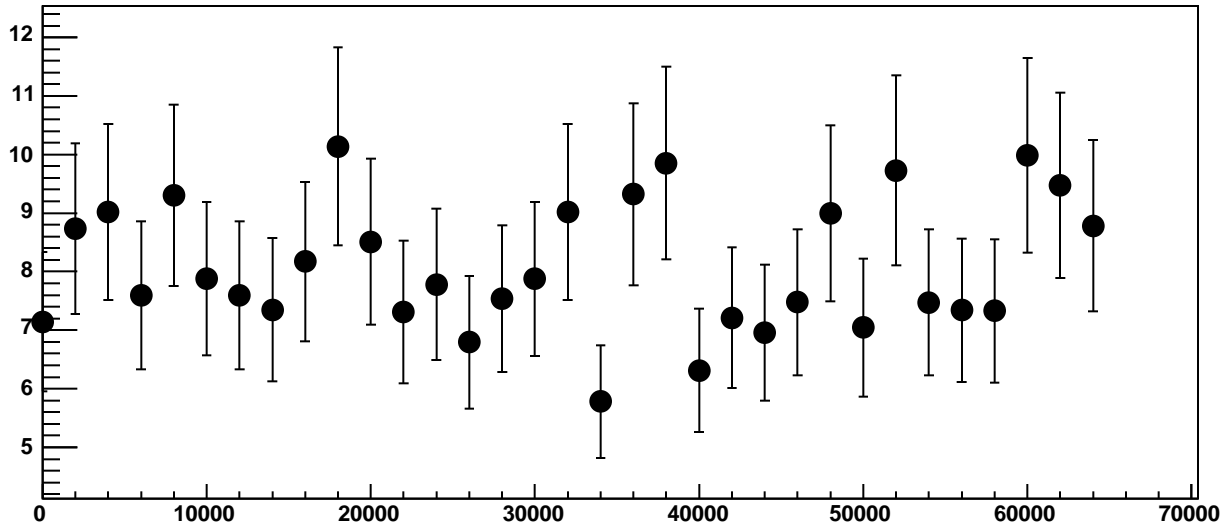
p0

671.4 ± 0.8449

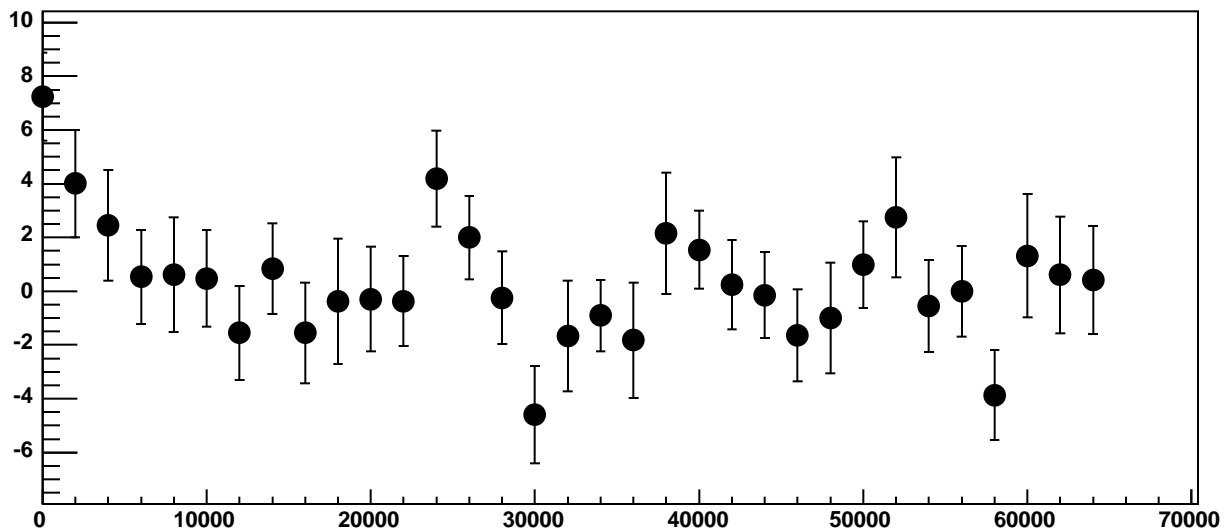
p1

$0.002987 \pm 2.515e-05$

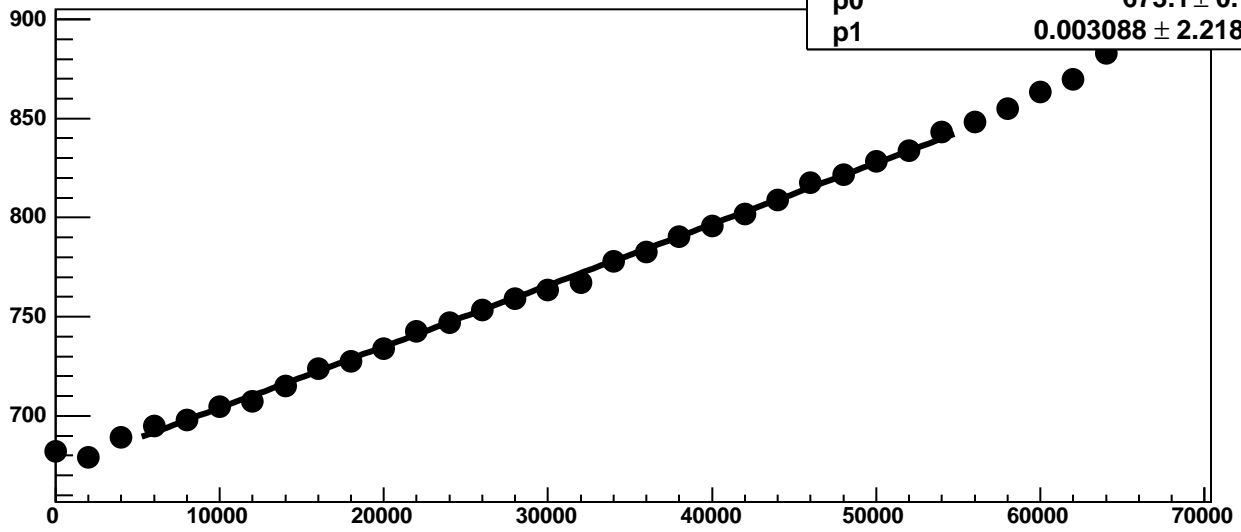
Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30.86 / 23

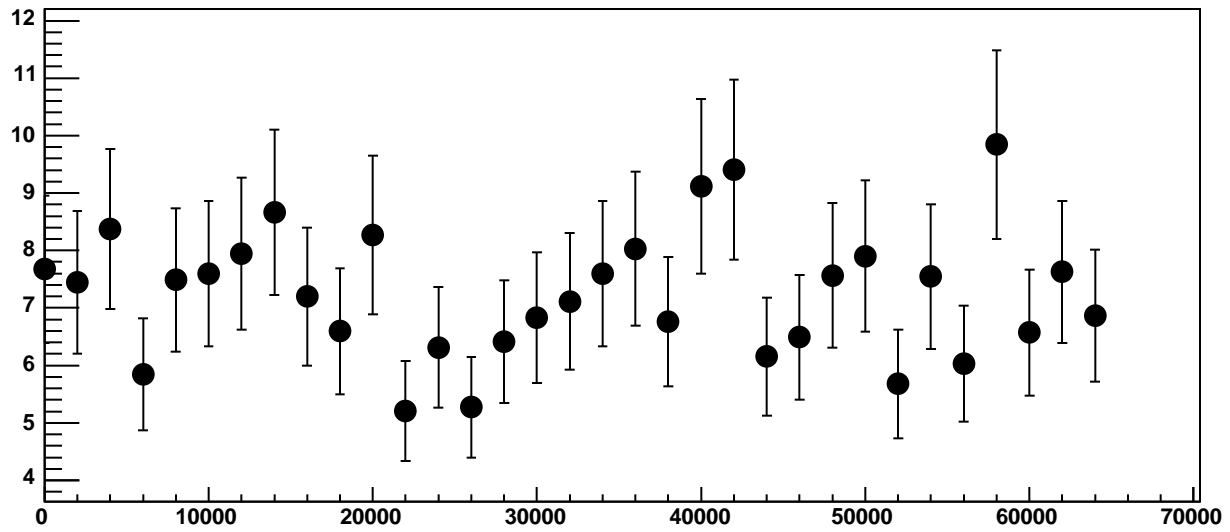
p0

673.1 ± 0.7313

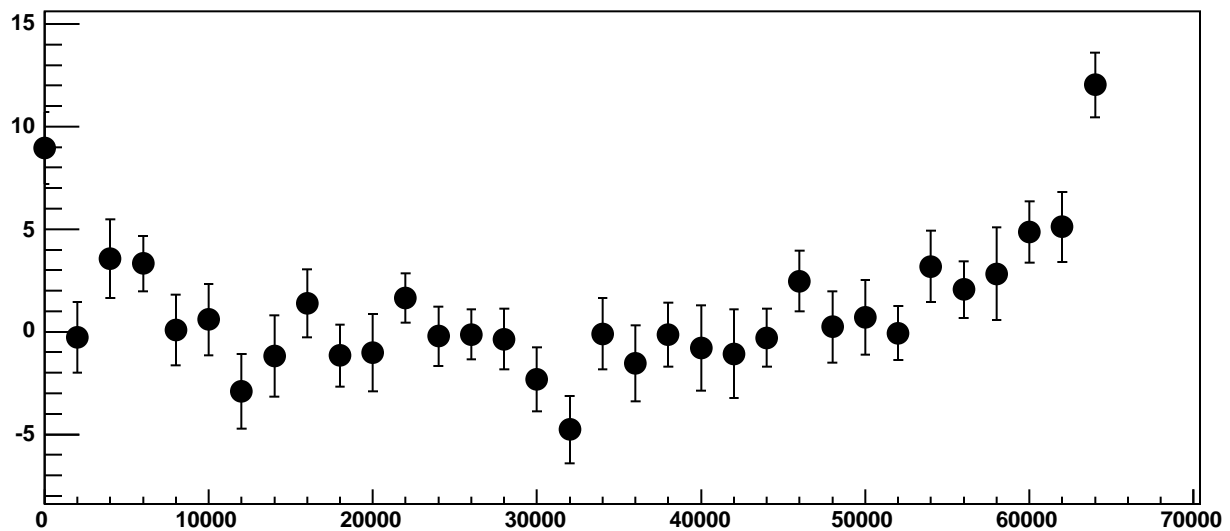
p1

$0.003088 \pm 2.218e-05$

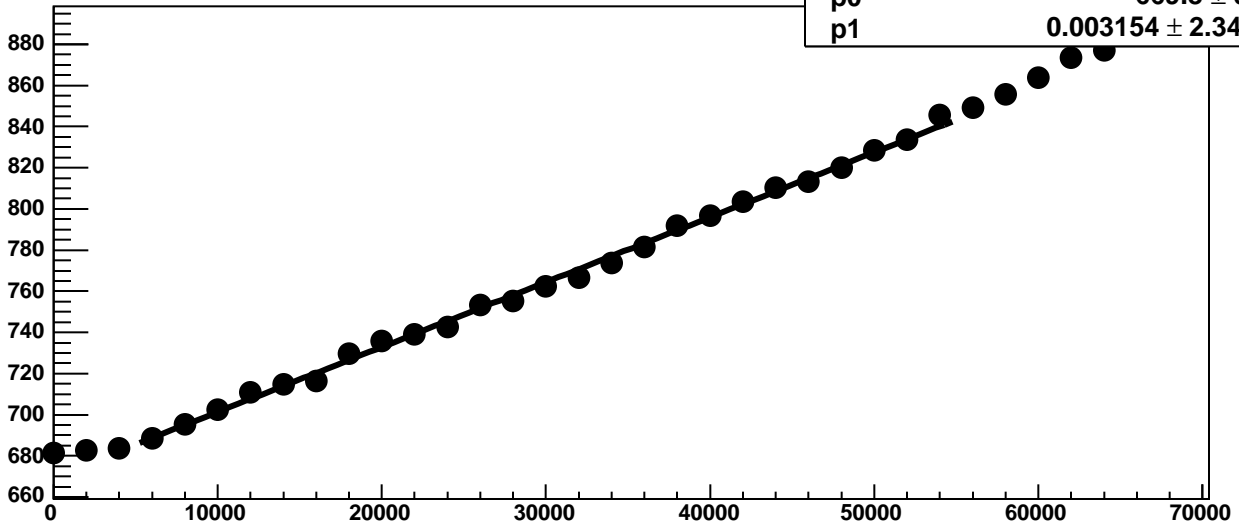
Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



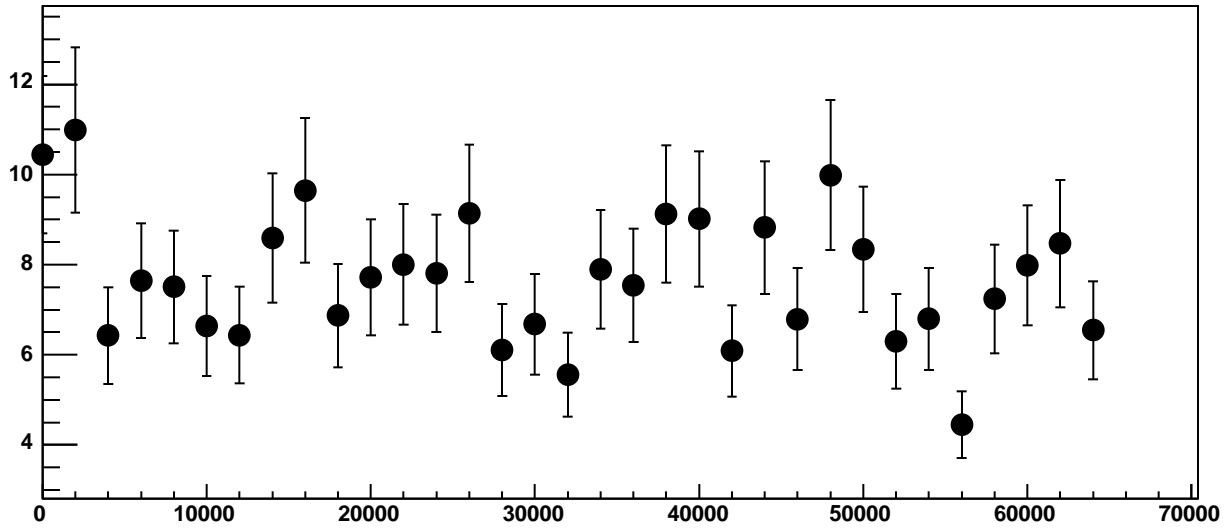
Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



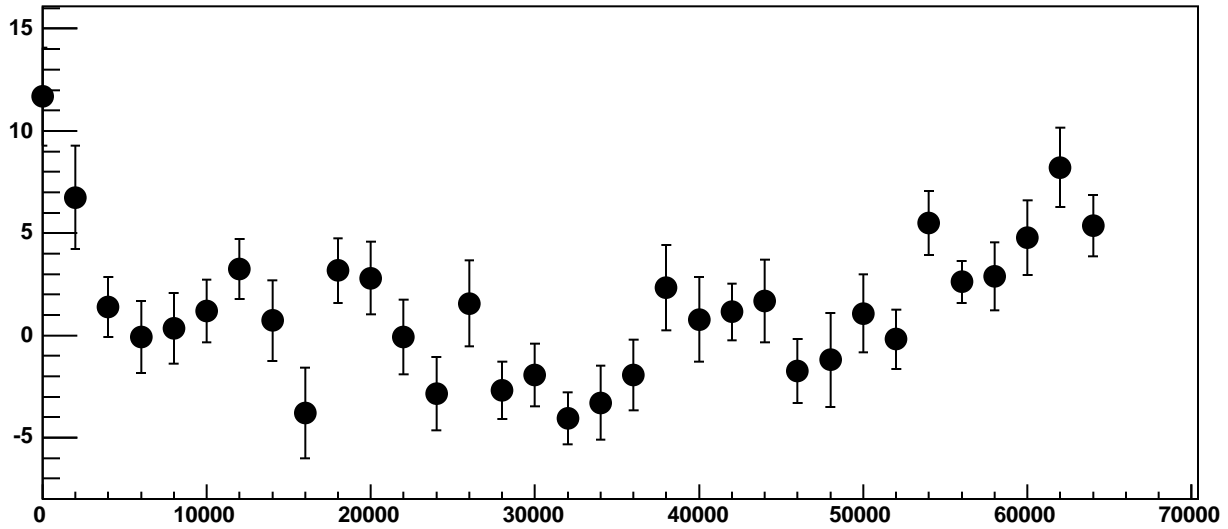
Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



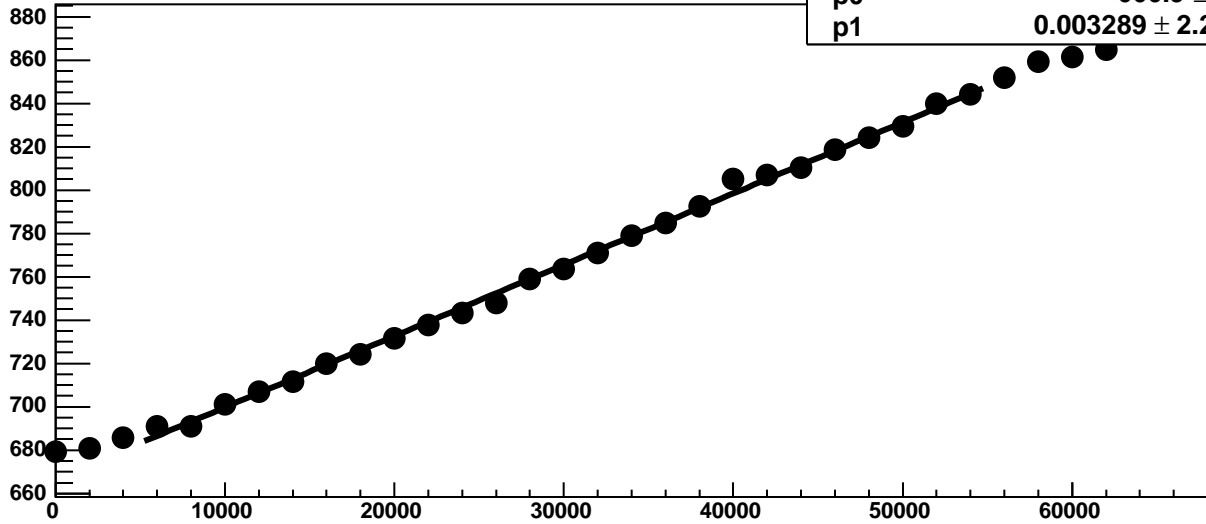
Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



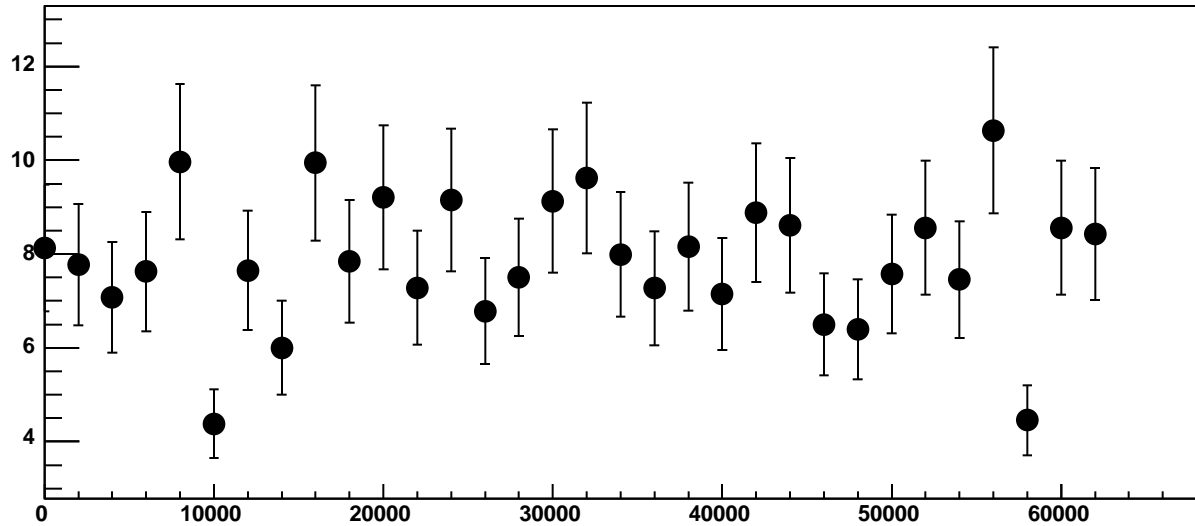
Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



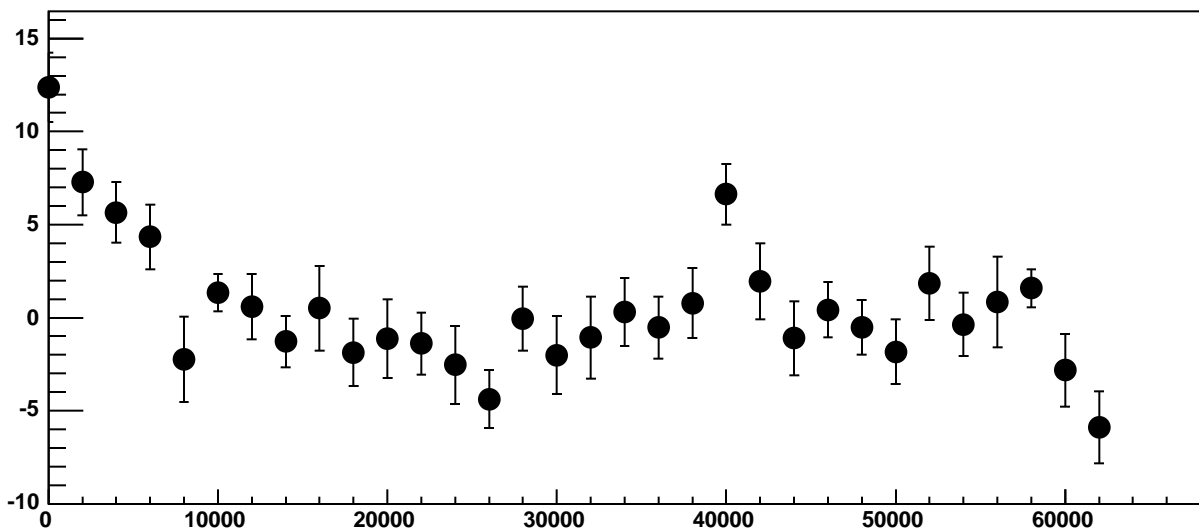
Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



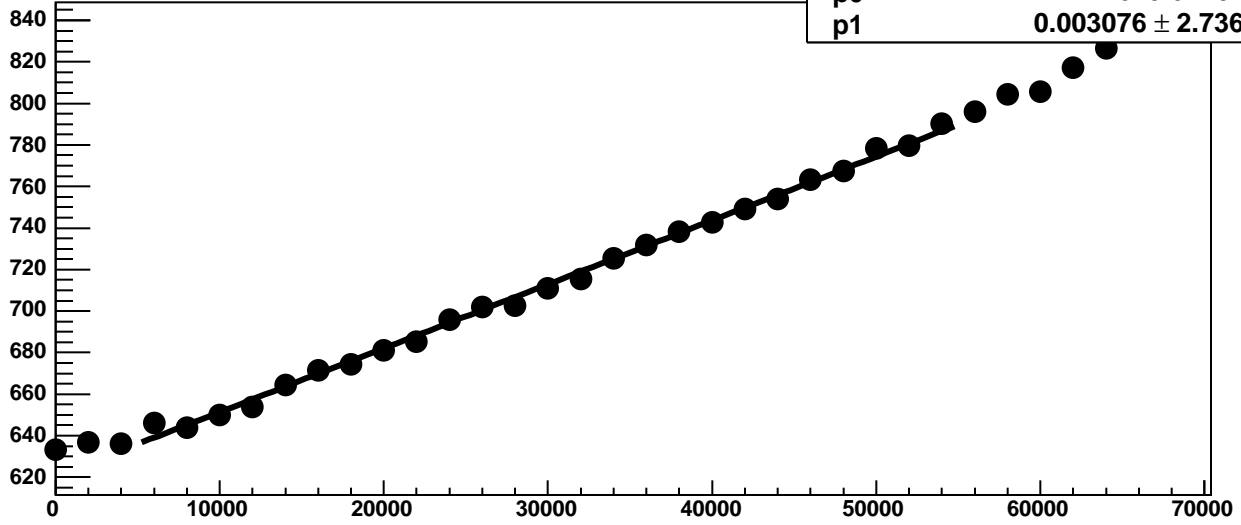
Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

43.83 / 23

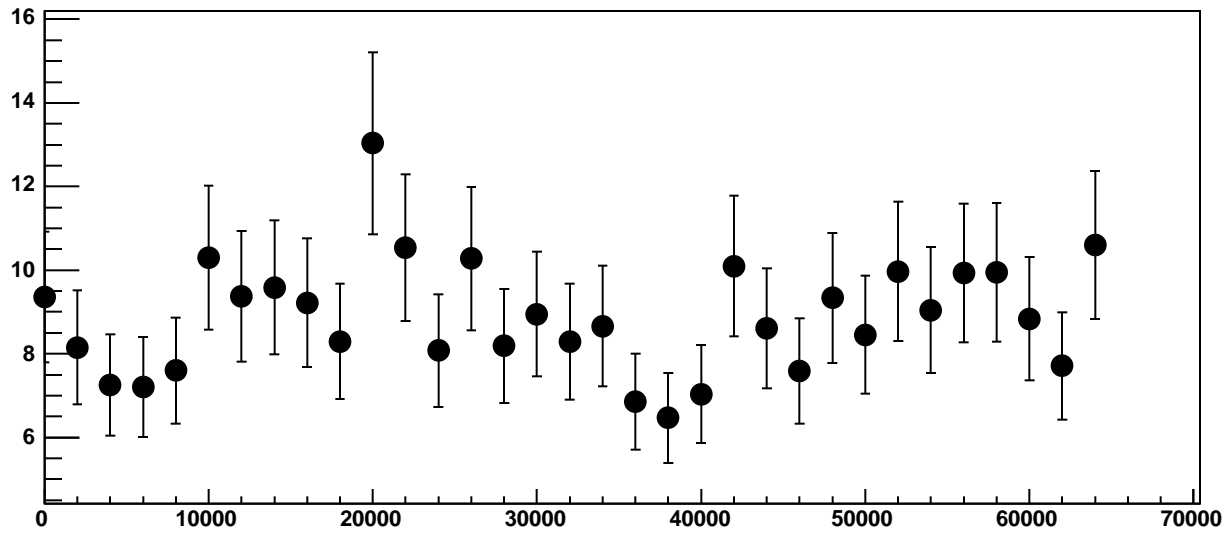
p0

620.6 ± 0.9202

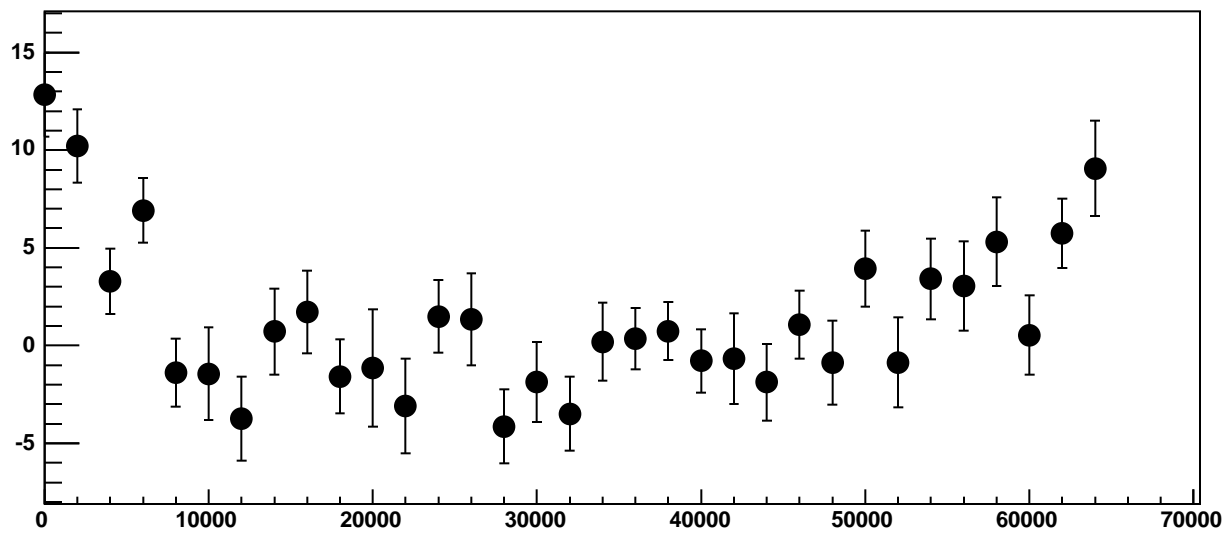
p1

$0.003076 \pm 2.736e-05$

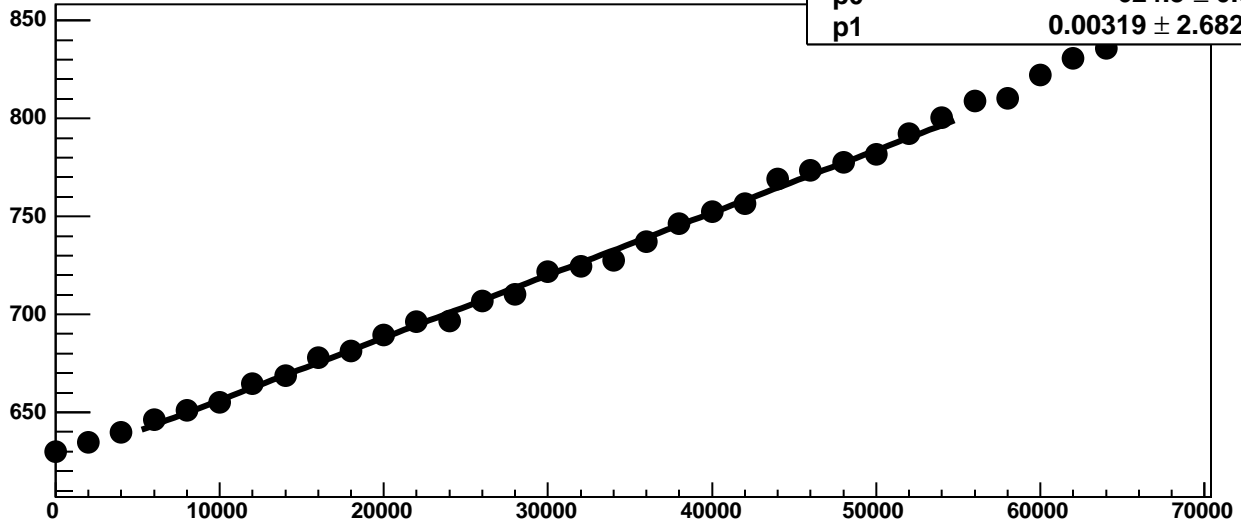
Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

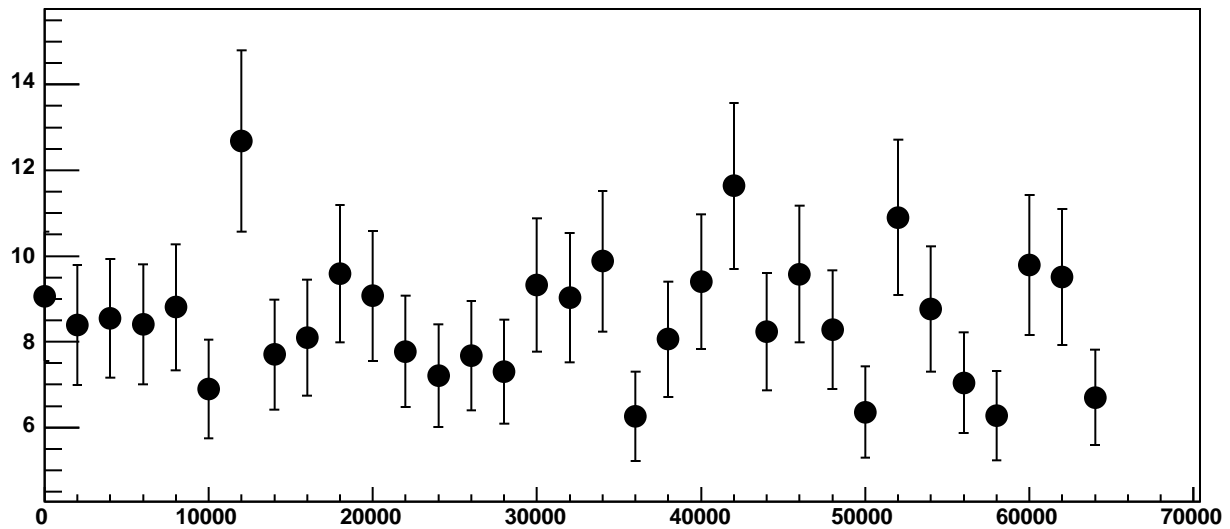


Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

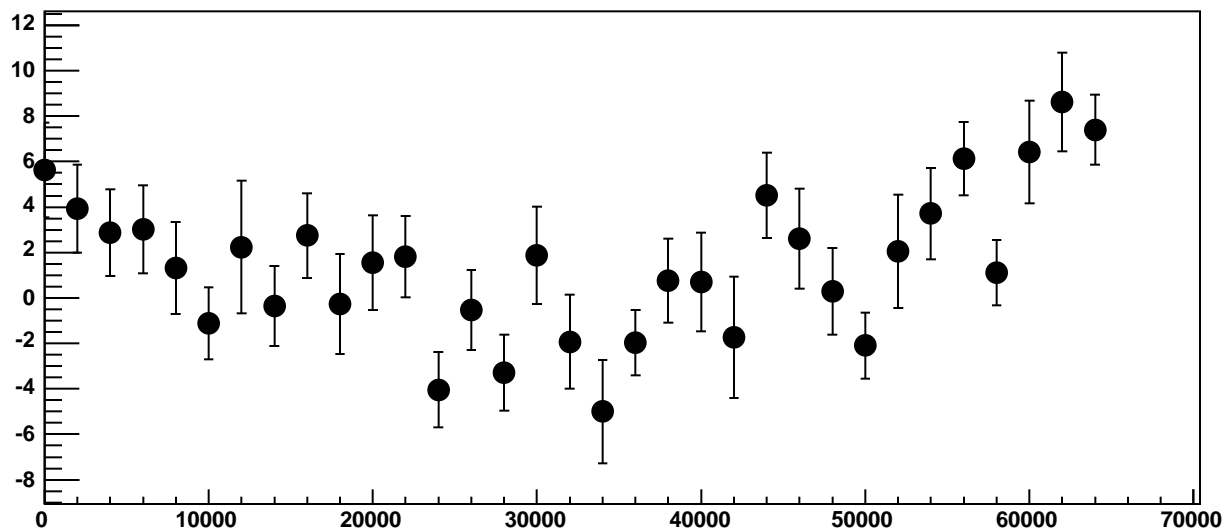


χ^2 / ndf 40.06 / 23
p0 624.3 ± 0.8864
p1 $0.00319 \pm 2.682e-05$

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

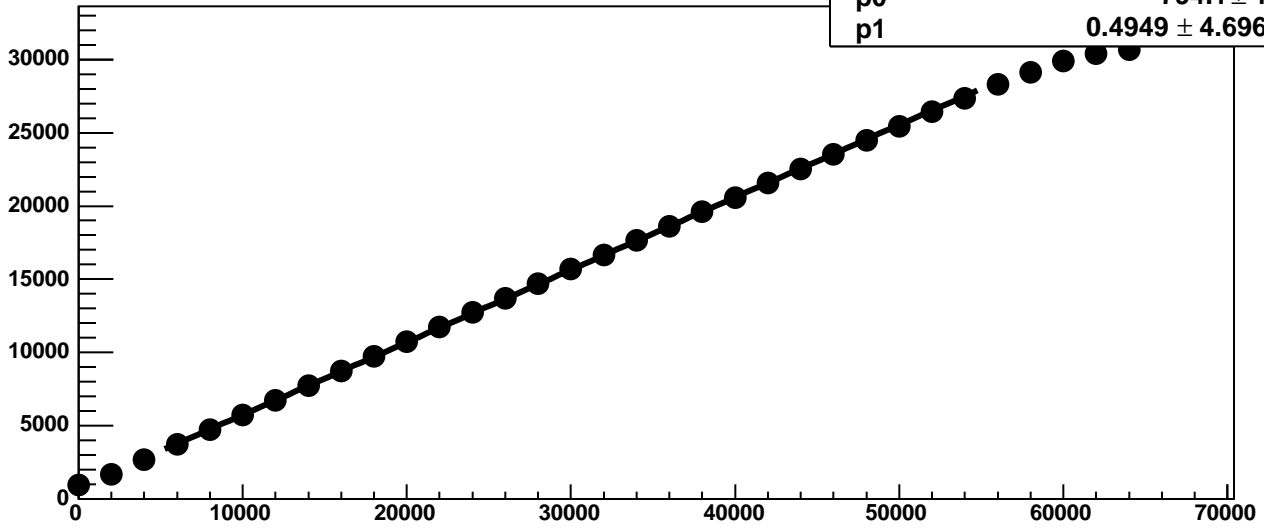


Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

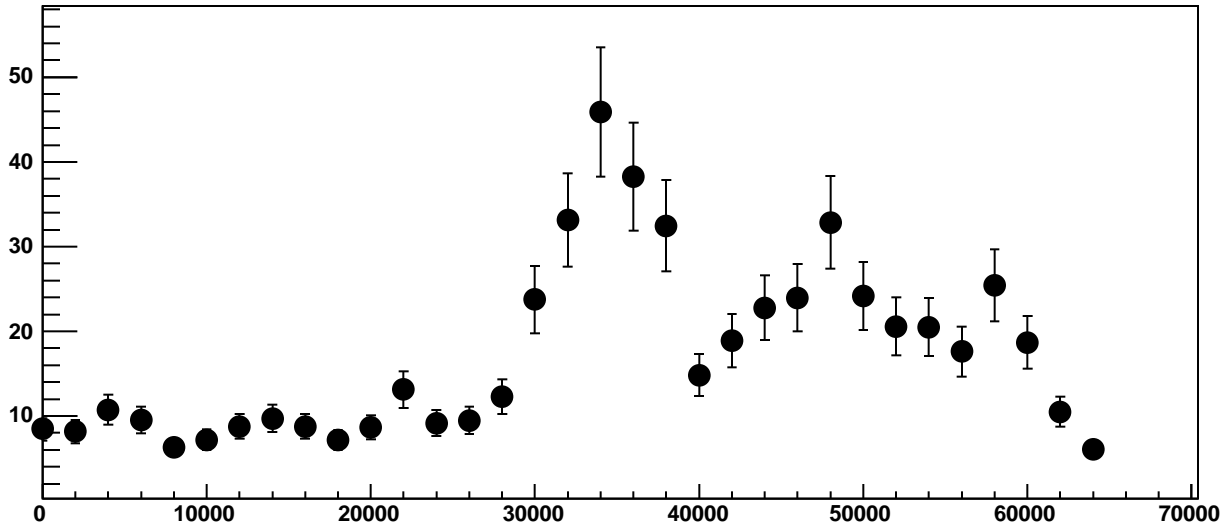


Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

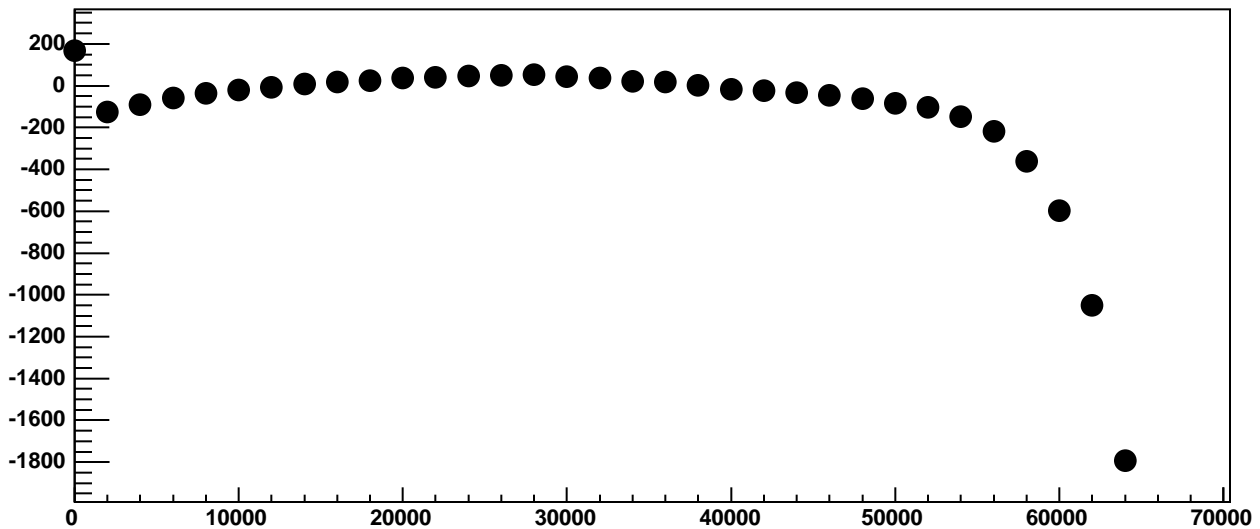
χ^2 / ndf 5847 / 23
p0 794.1 ± 1.039
p1 $0.4949 \pm 4.696e-05$



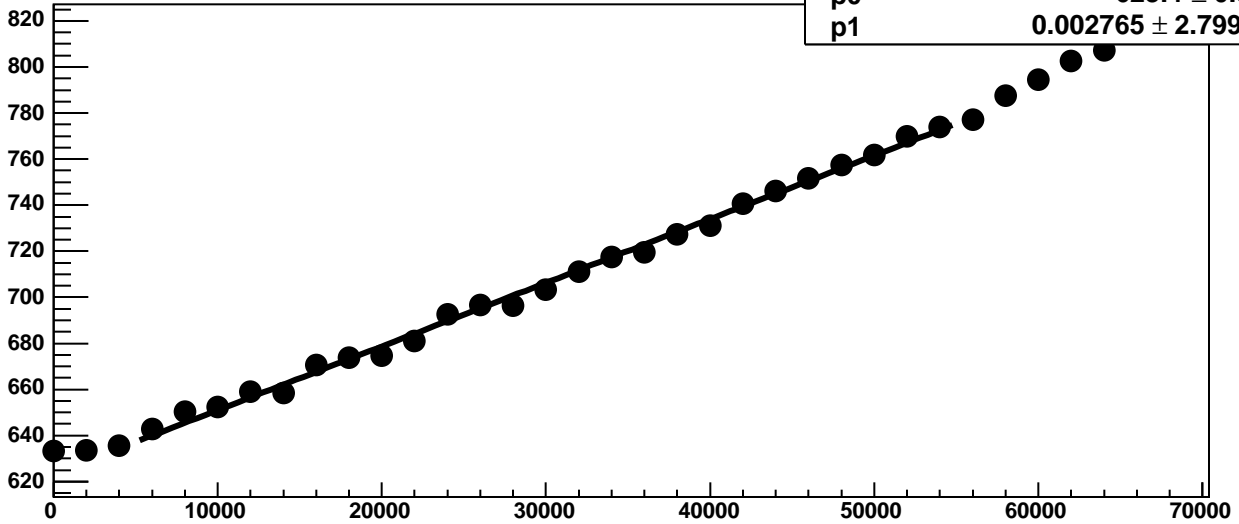
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.86 / 23

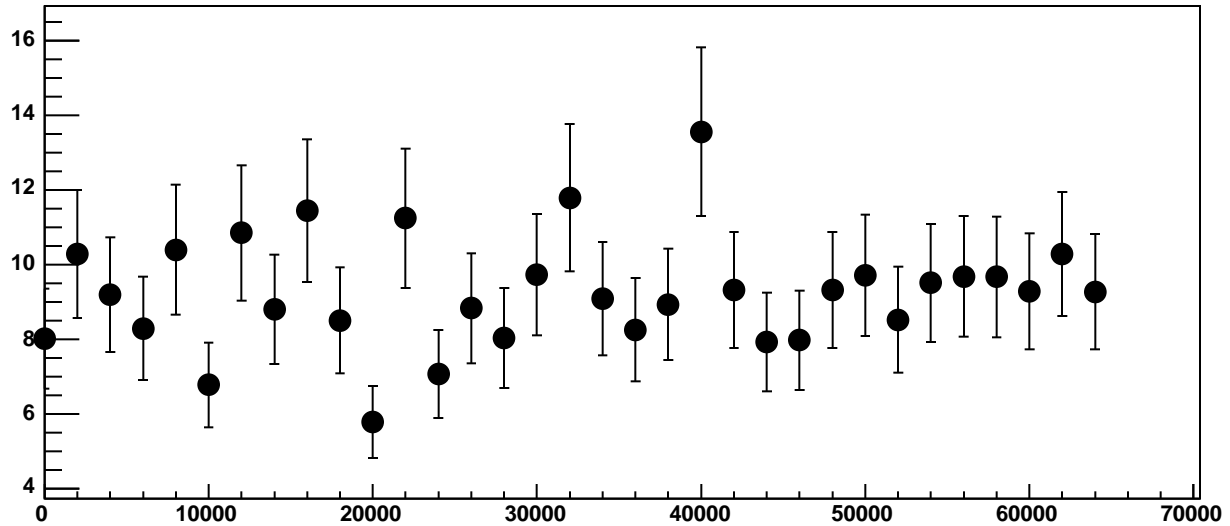
p0

623.4 ± 0.9093

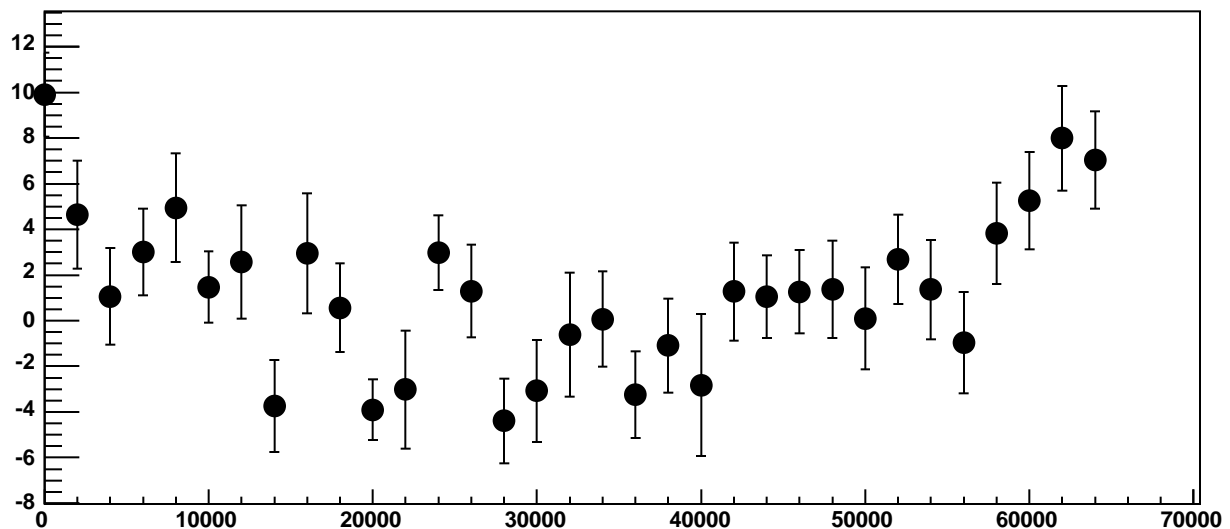
p1

$0.002765 \pm 2.799e-05$

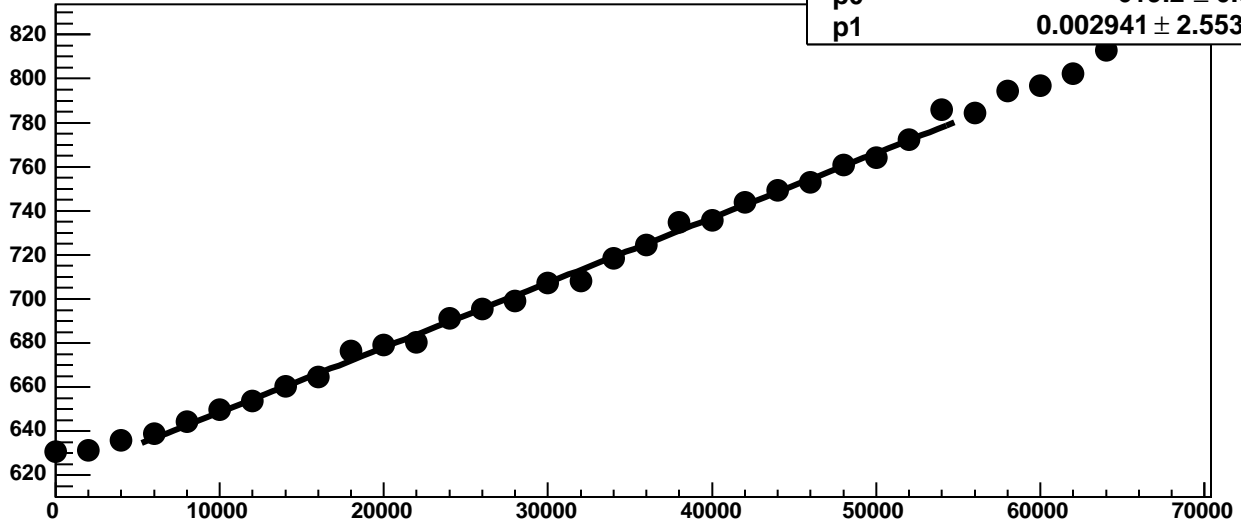
Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.43 / 23

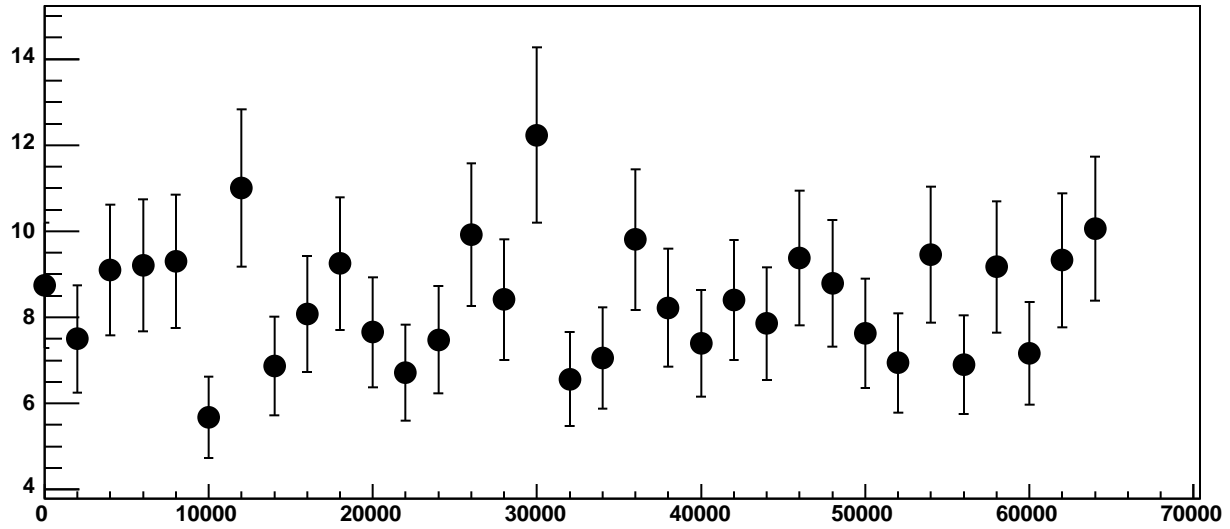
p0

619.2 ± 0.8405

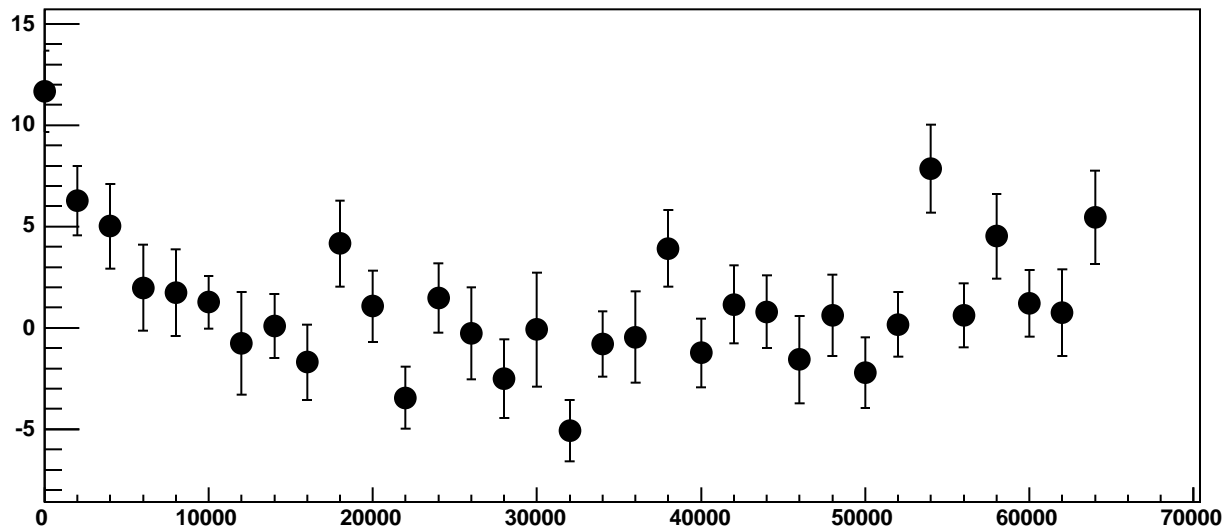
p1

$0.002941 \pm 2.553e-05$

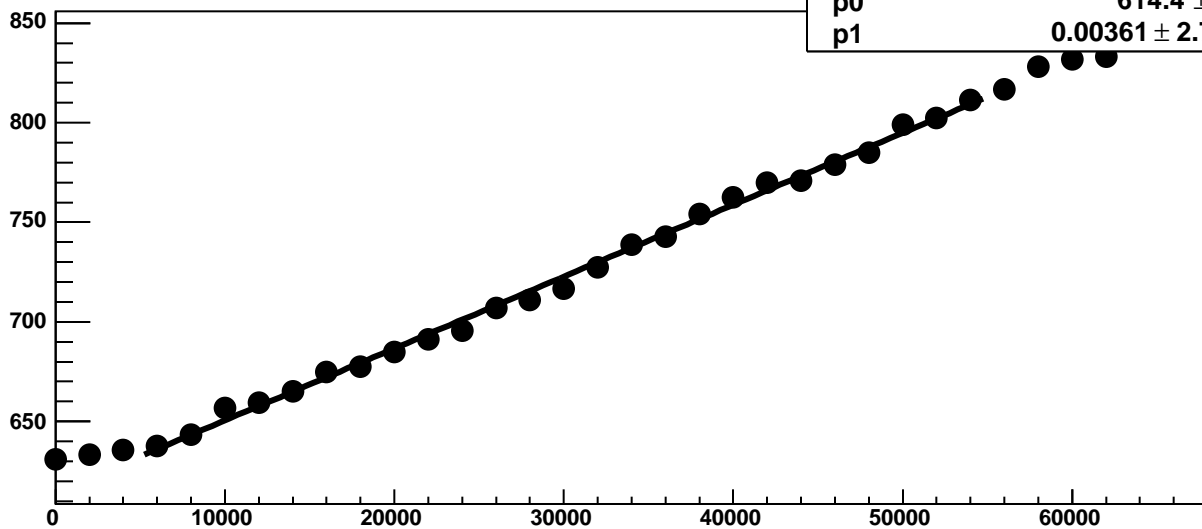
Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

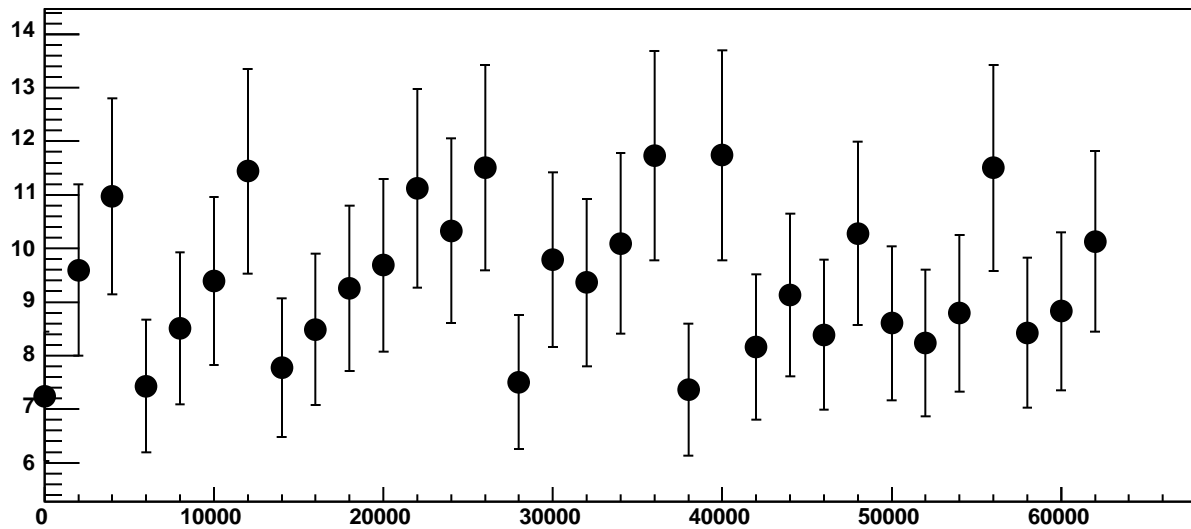


Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

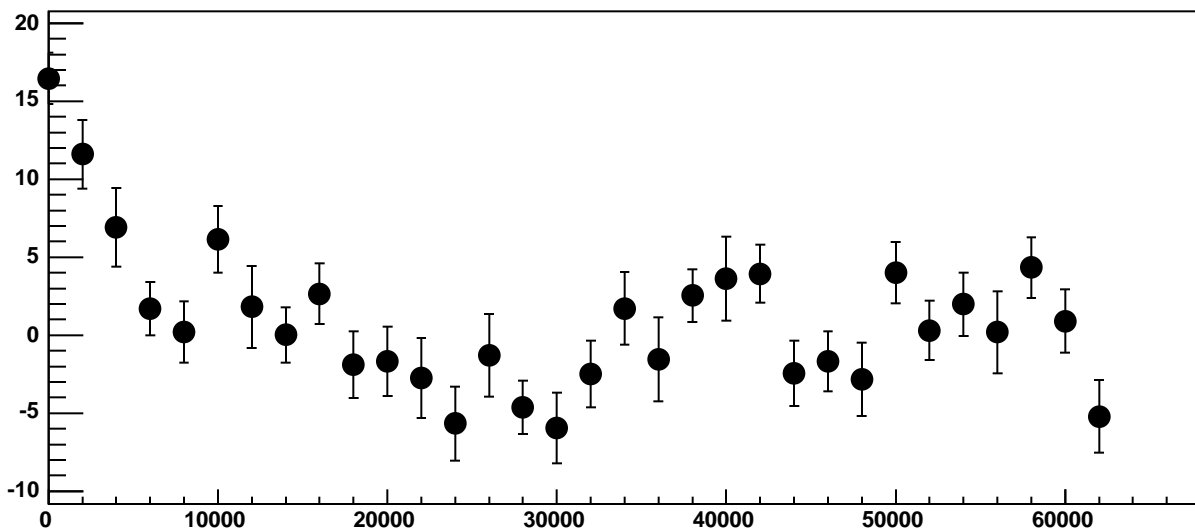


χ^2 / ndf 53.55 / 23
p0 614.4 ± 0.9289
p1 $0.00361 \pm 2.773e-05$

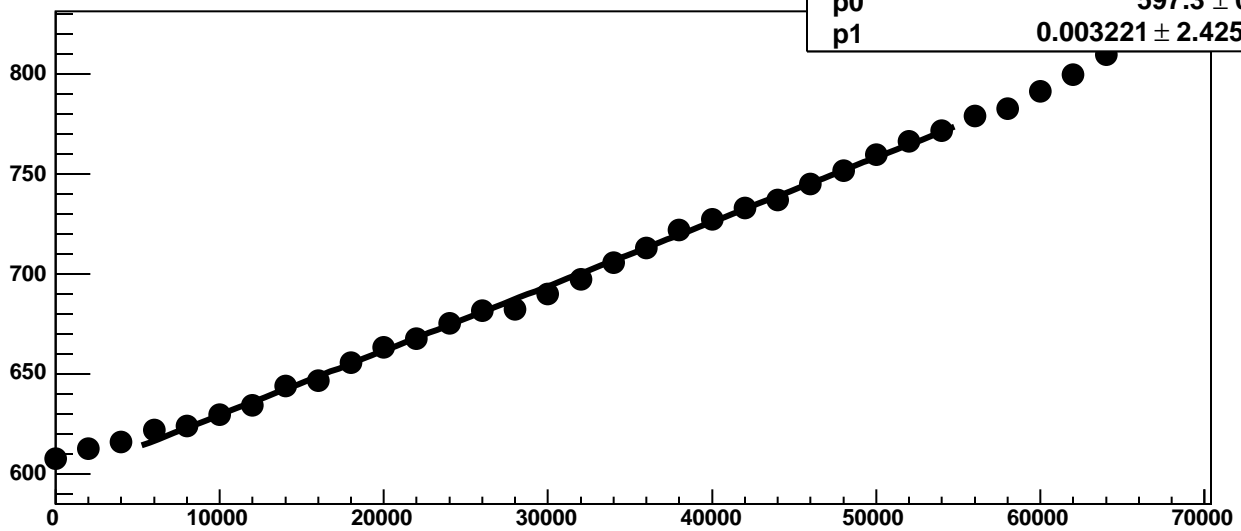
Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

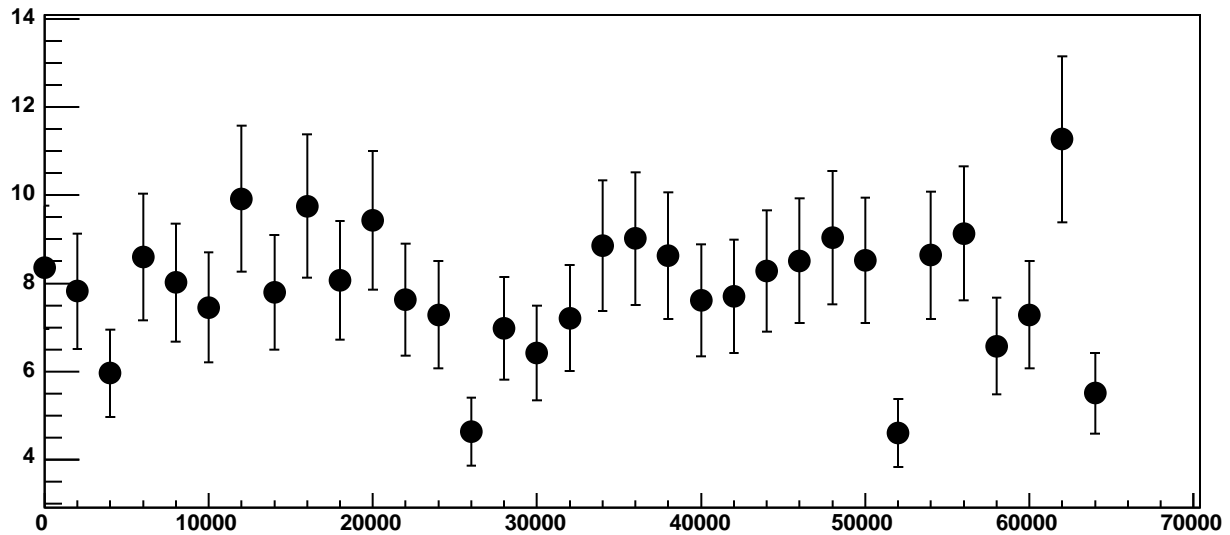


Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

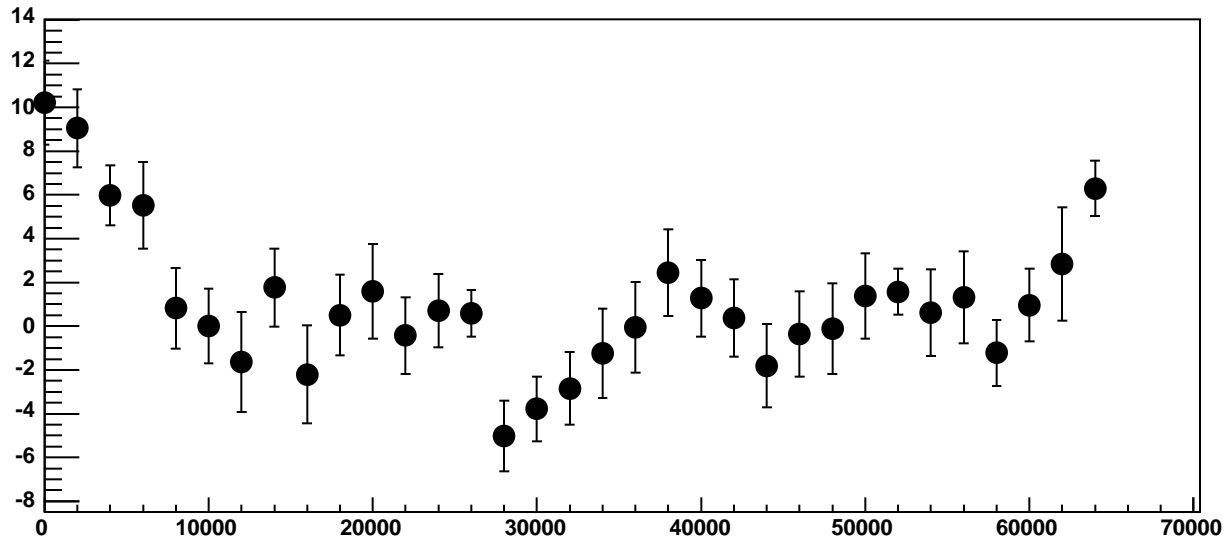


χ^2 / ndf 37.26 / 23
p0 597.3 \pm 0.831
p1 0.003221 \pm 2.425e-05

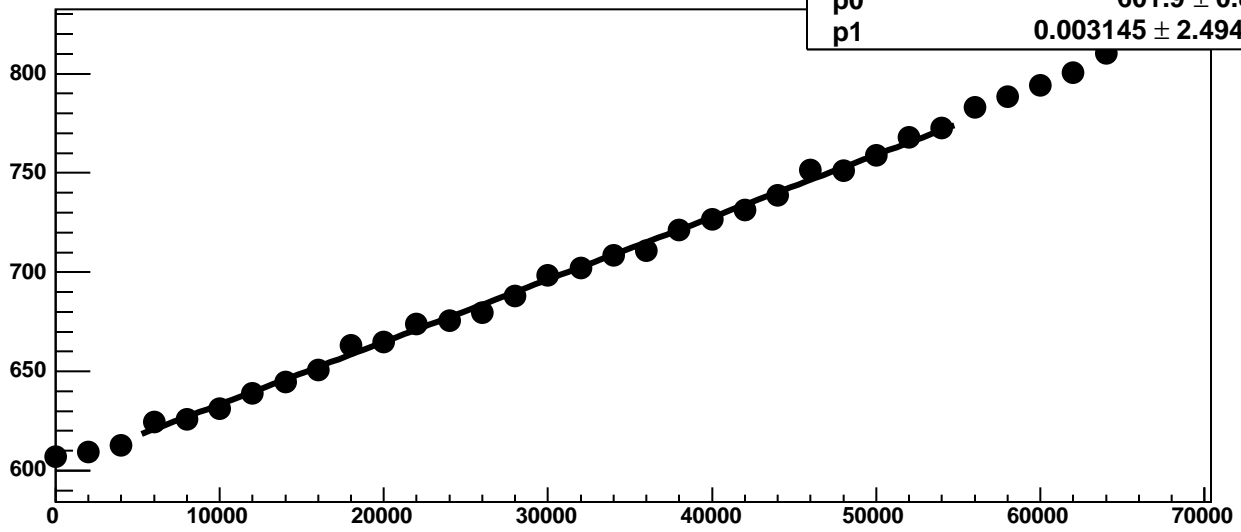
Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.81 / 23

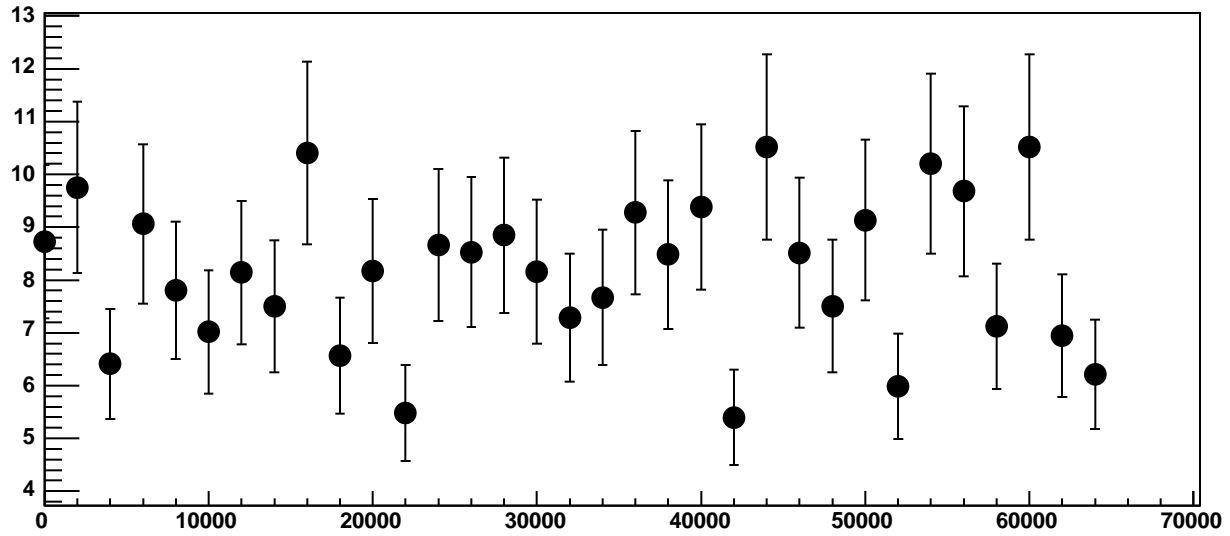
p0

601.9 ± 0.8266

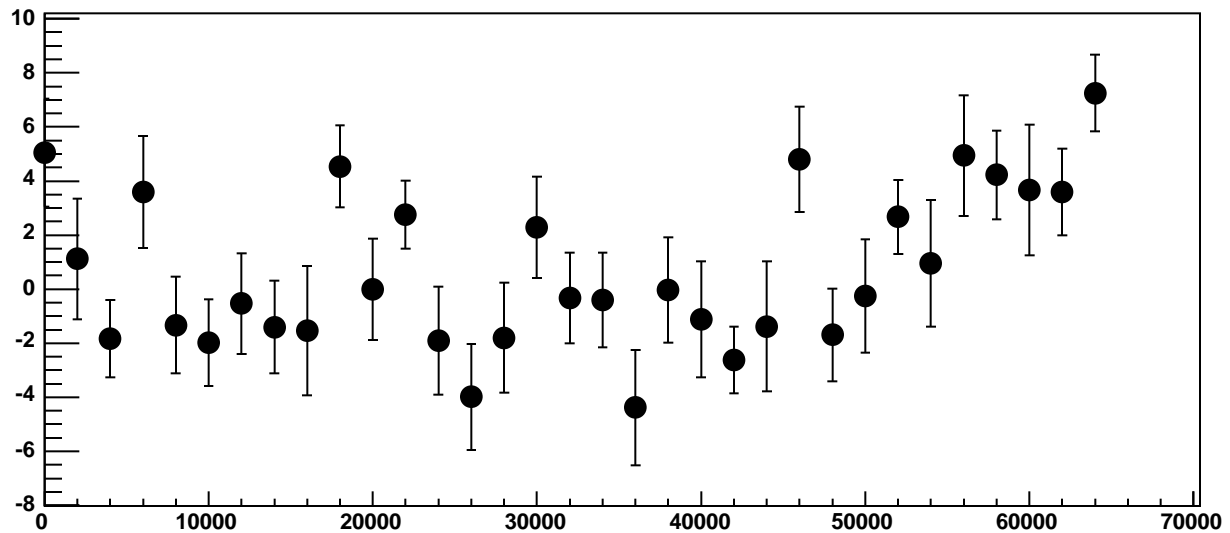
p1

$0.003145 \pm 2.494e-05$

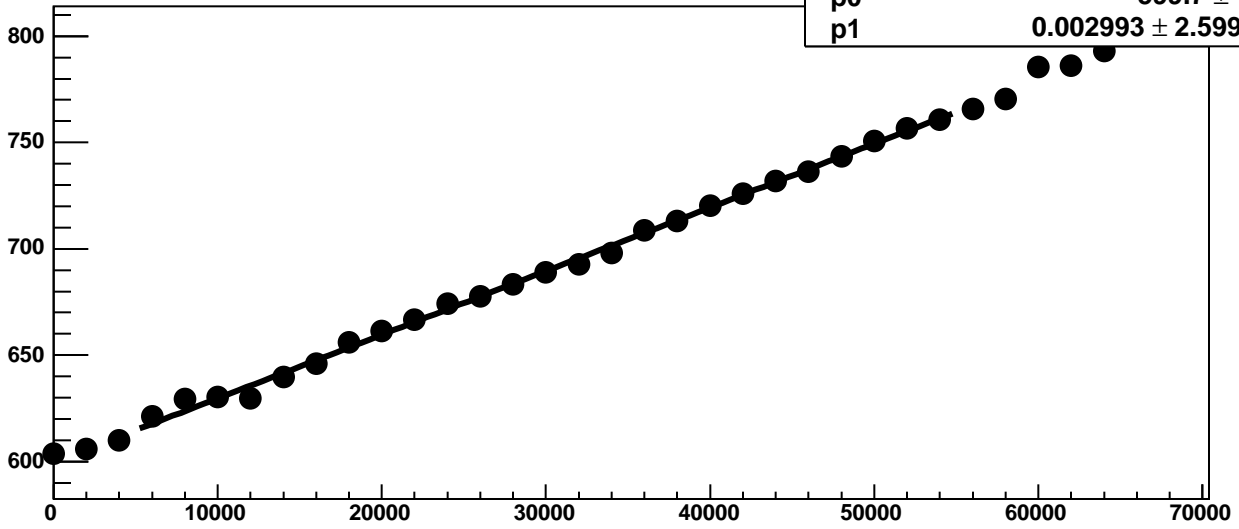
Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

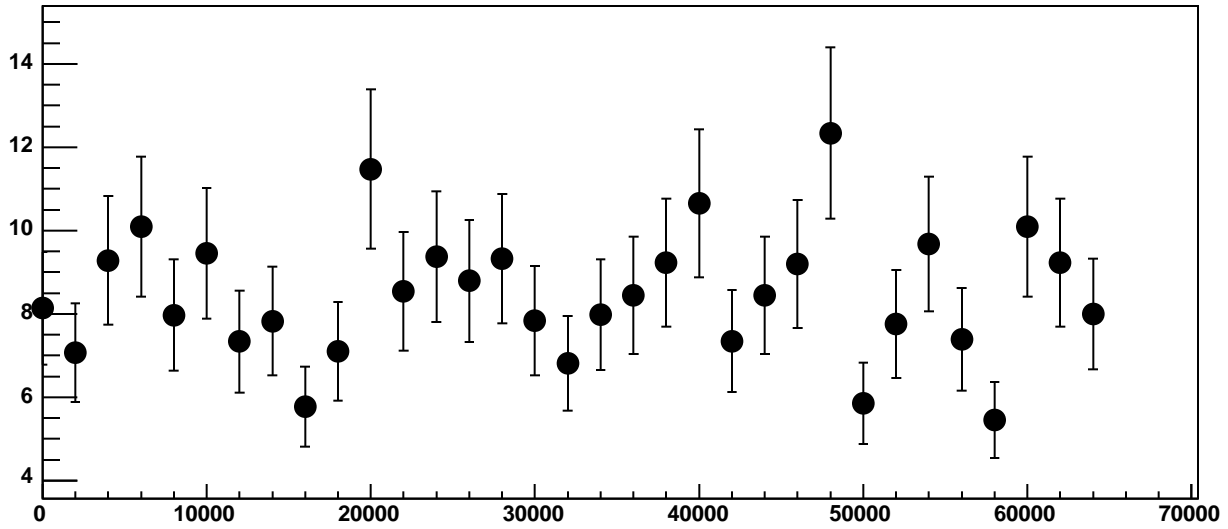


Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

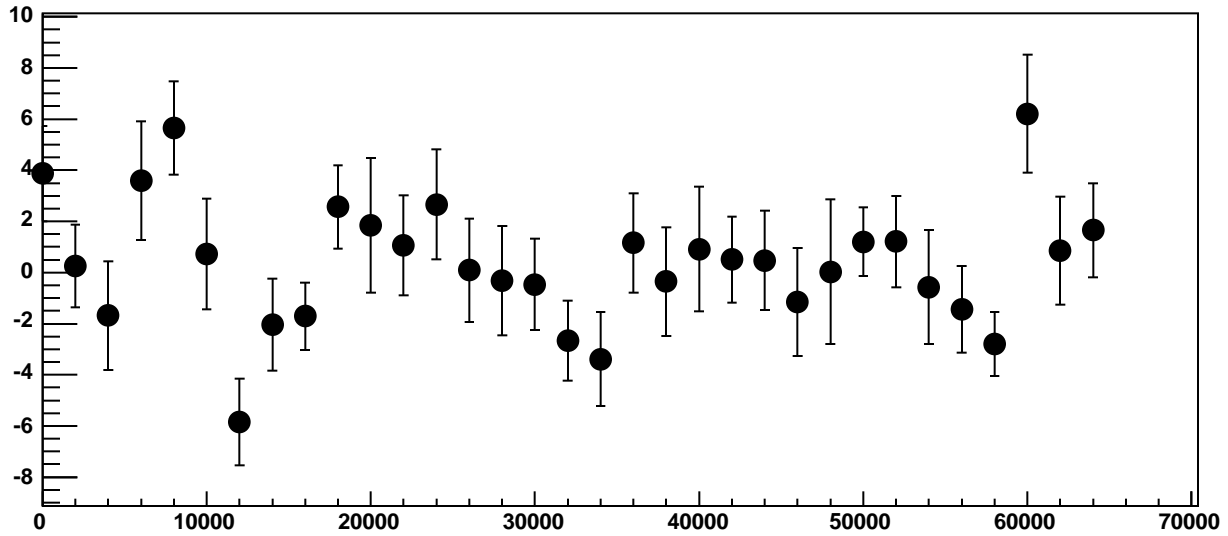


χ^2 / ndf 40.58 / 23
p0 599.7 ± 0.86
p1 $0.002993 \pm 2.599e-05$

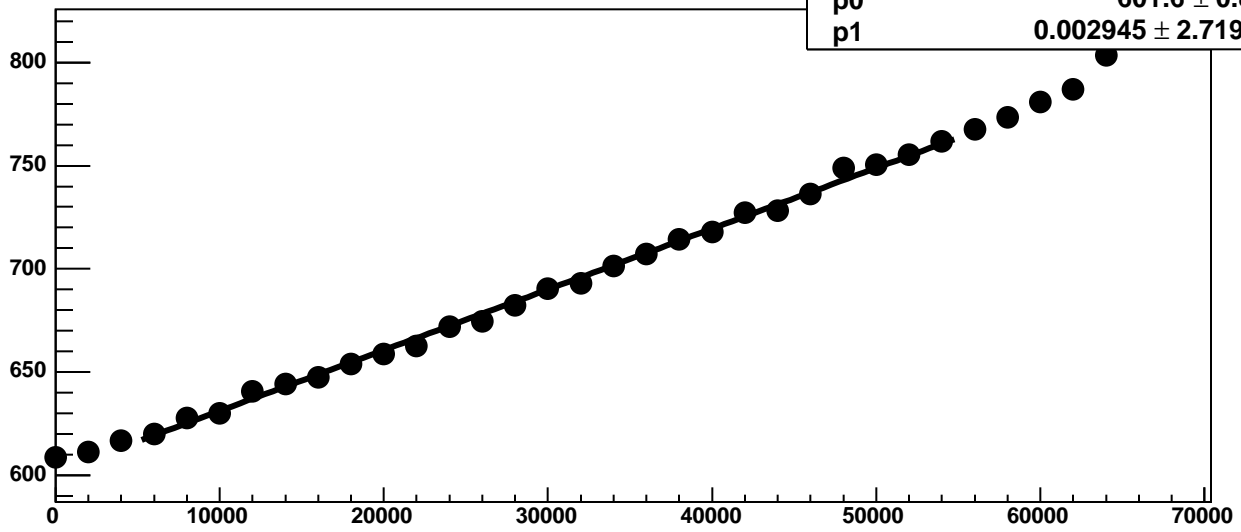
Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

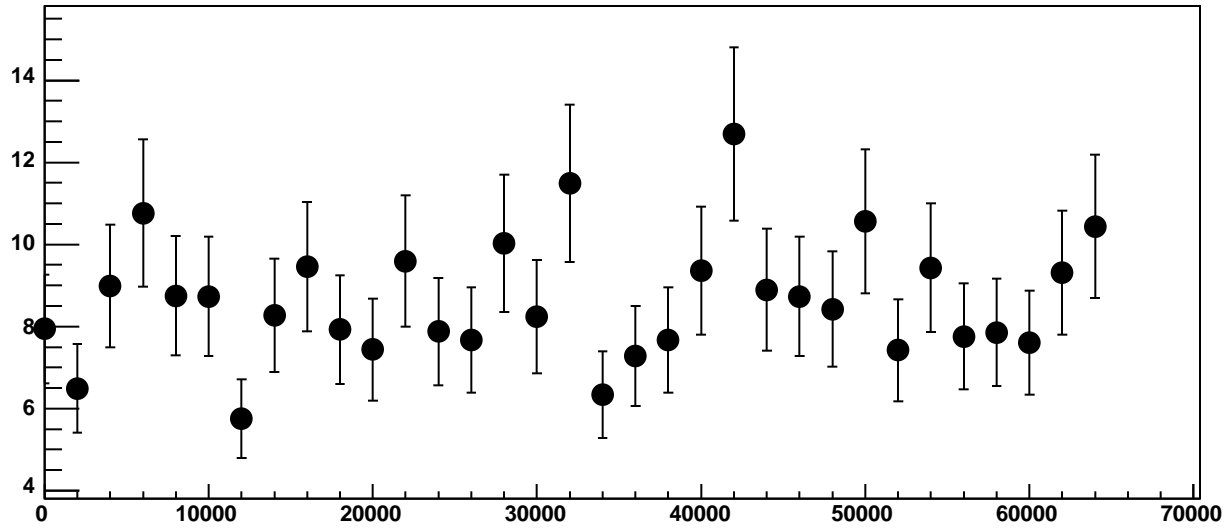


Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

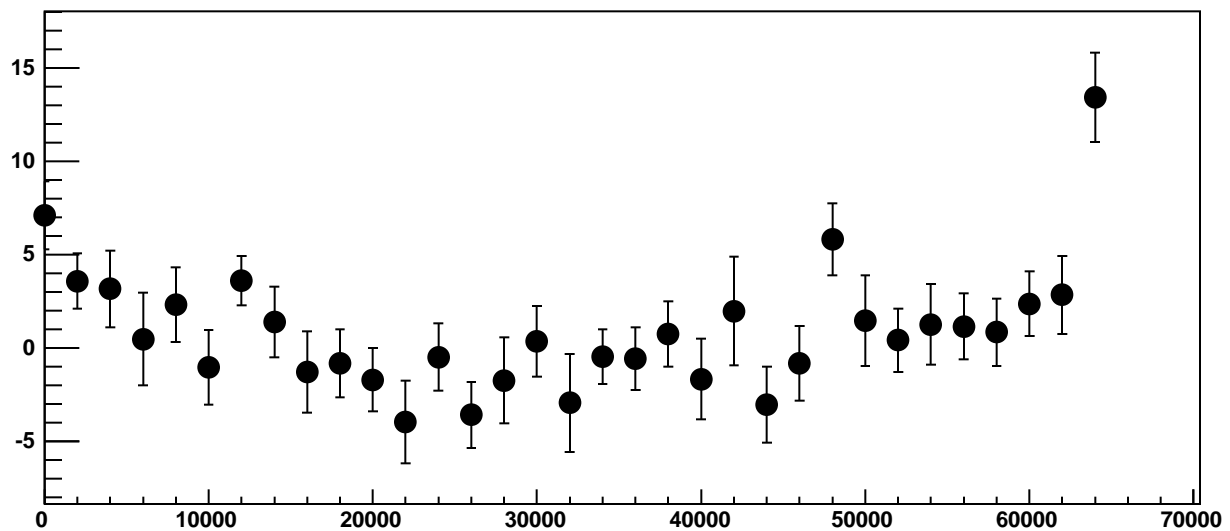


χ^2 / ndf 34.14 / 23
p0 601.6 ± 0.8815
p1 $0.002945 \pm 2.719e-05$

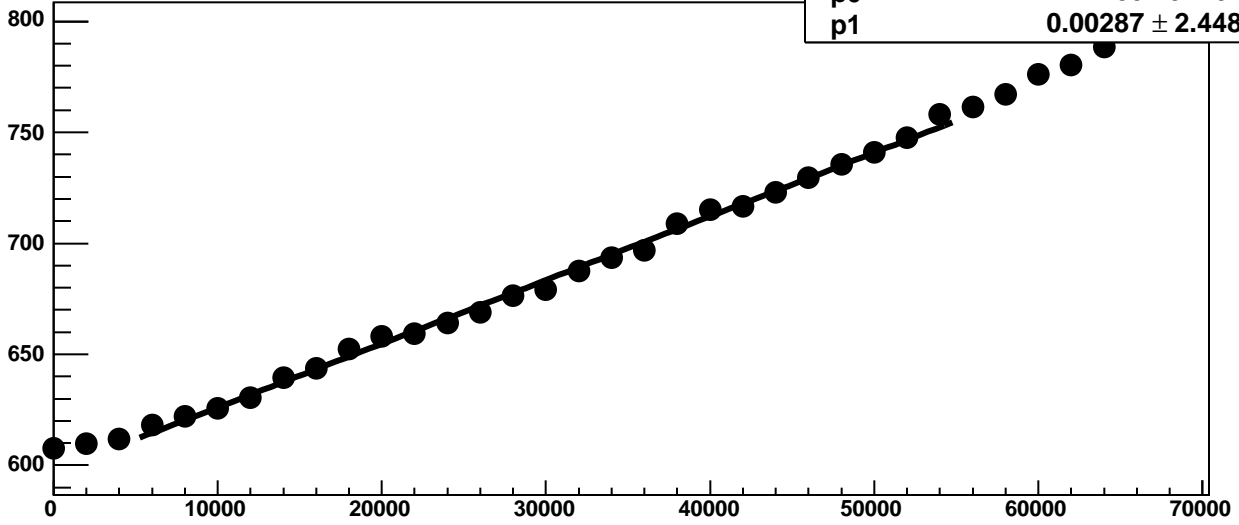
Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.73 / 23

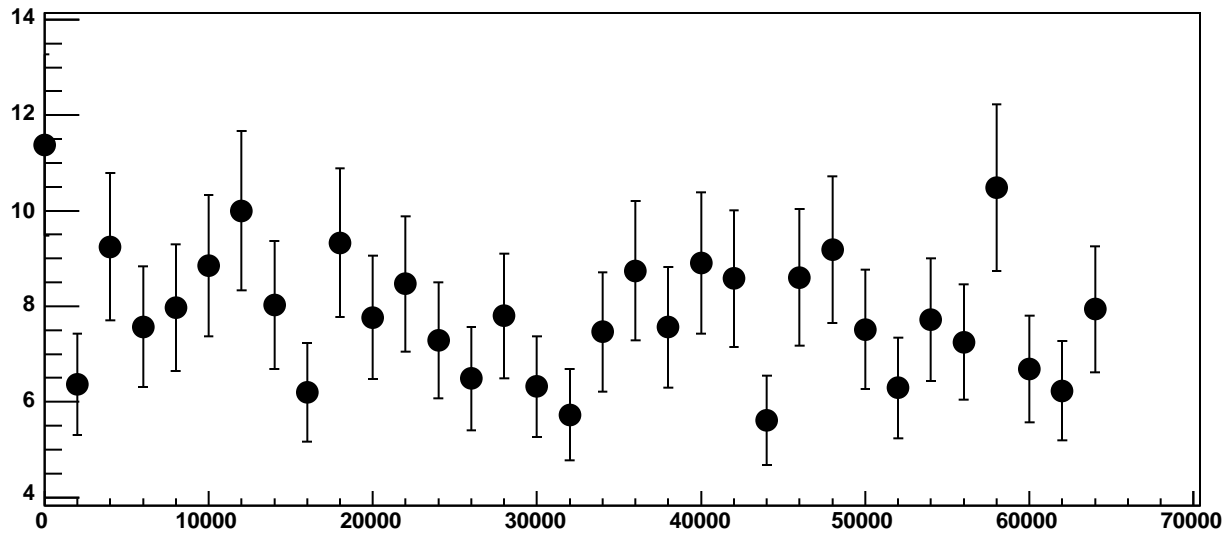
p0

597.3 ± 0.8285

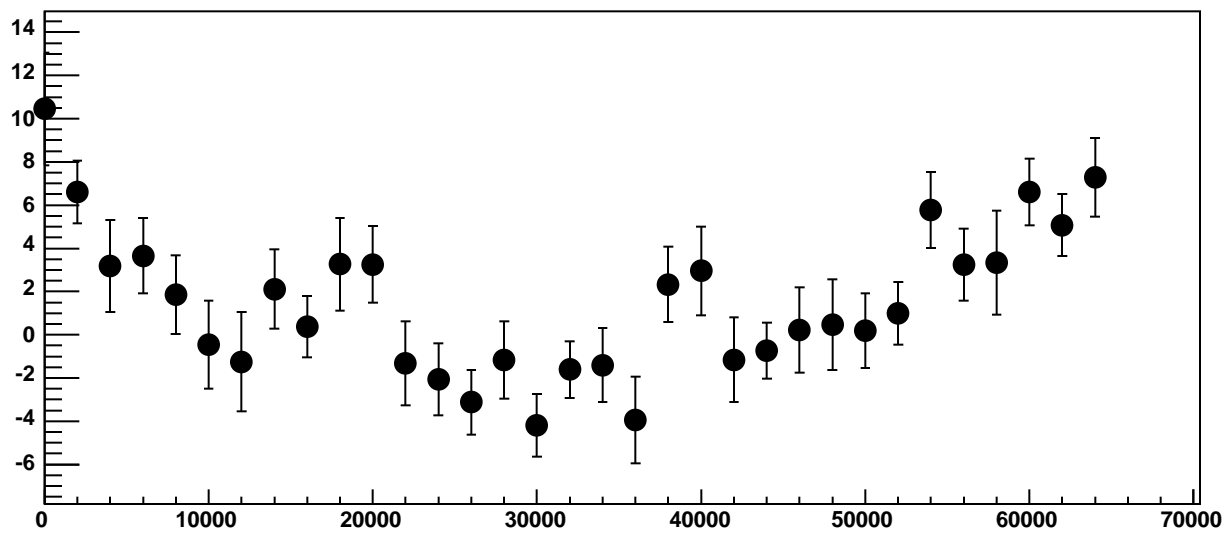
p1

$0.00287 \pm 2.448e-05$

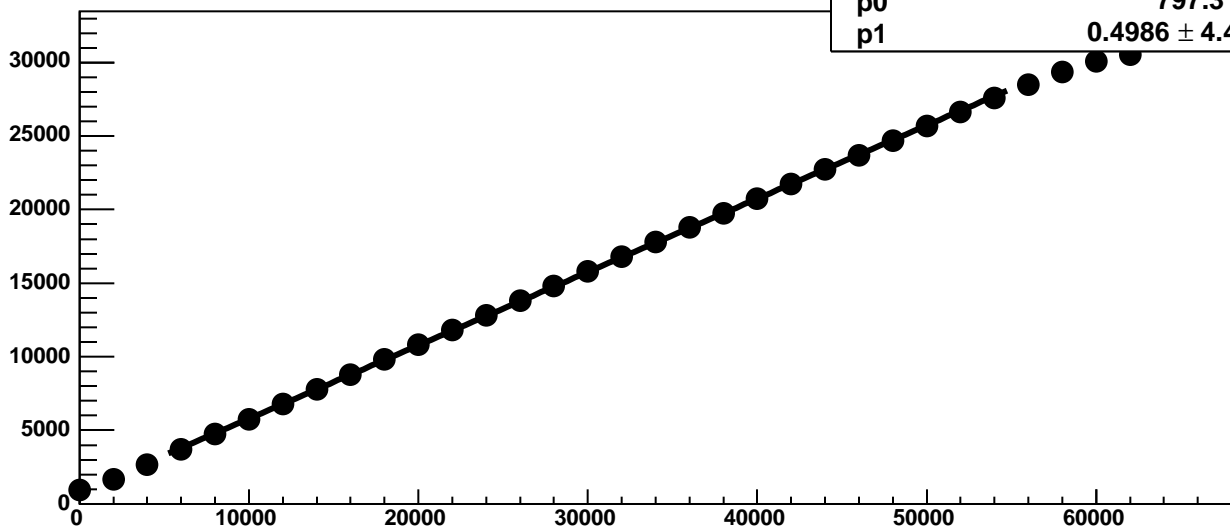
Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5926 / 23

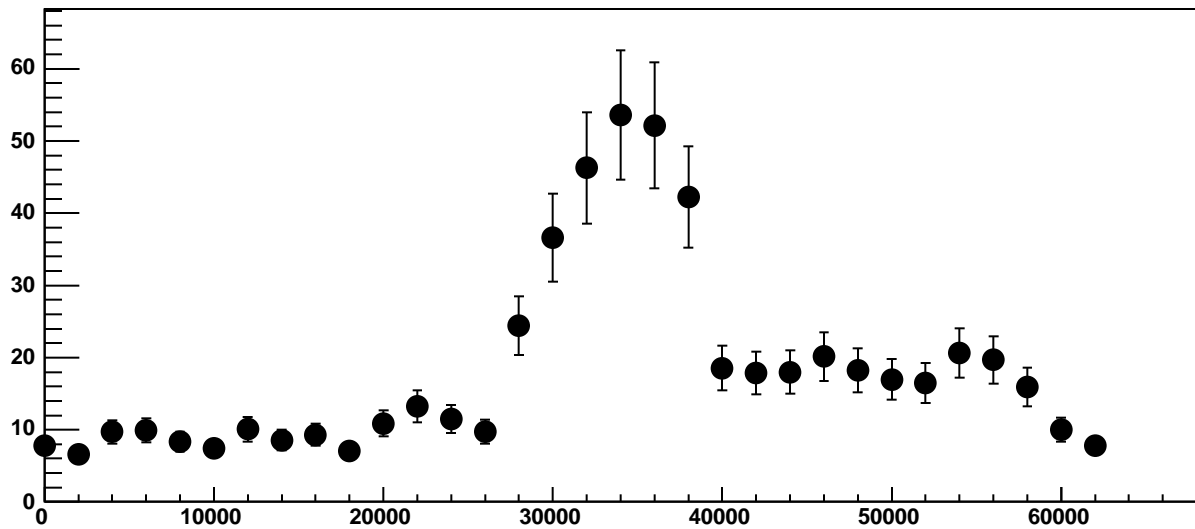
p0

797.3 ± 1.077

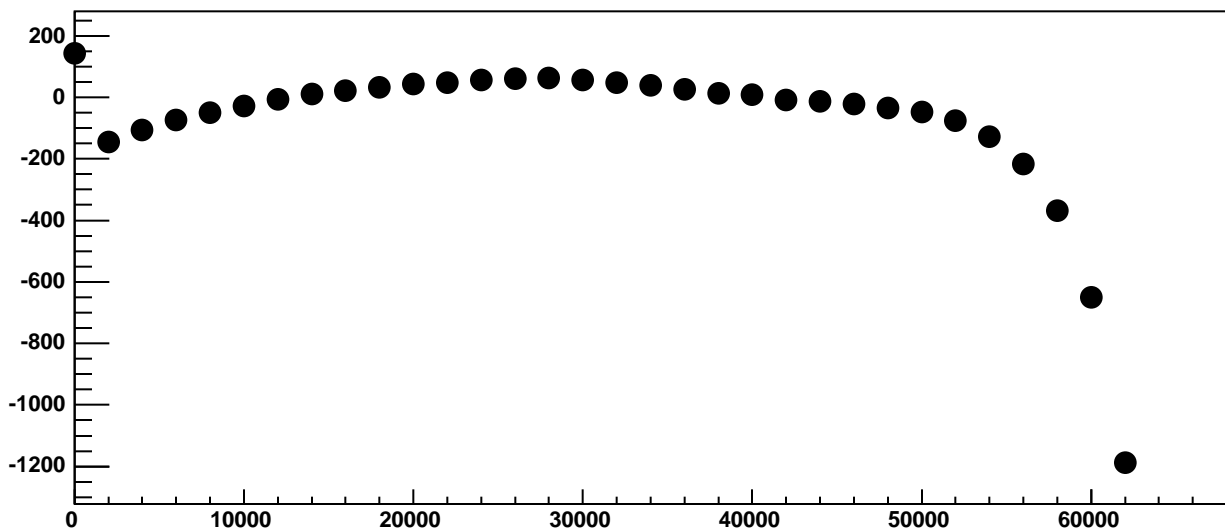
p1

$0.4986 \pm 4.462e-05$

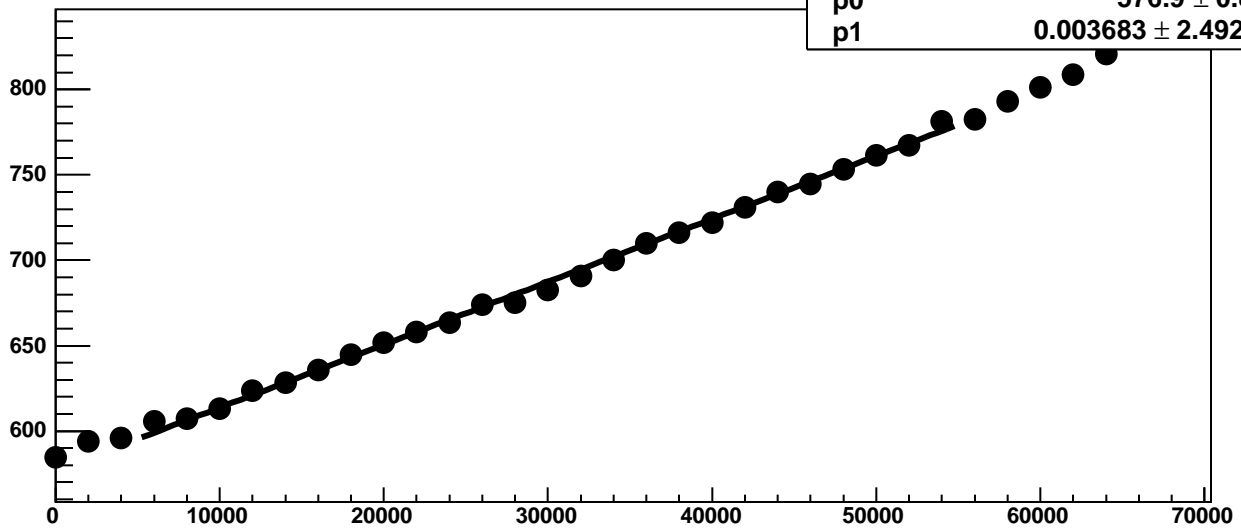
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



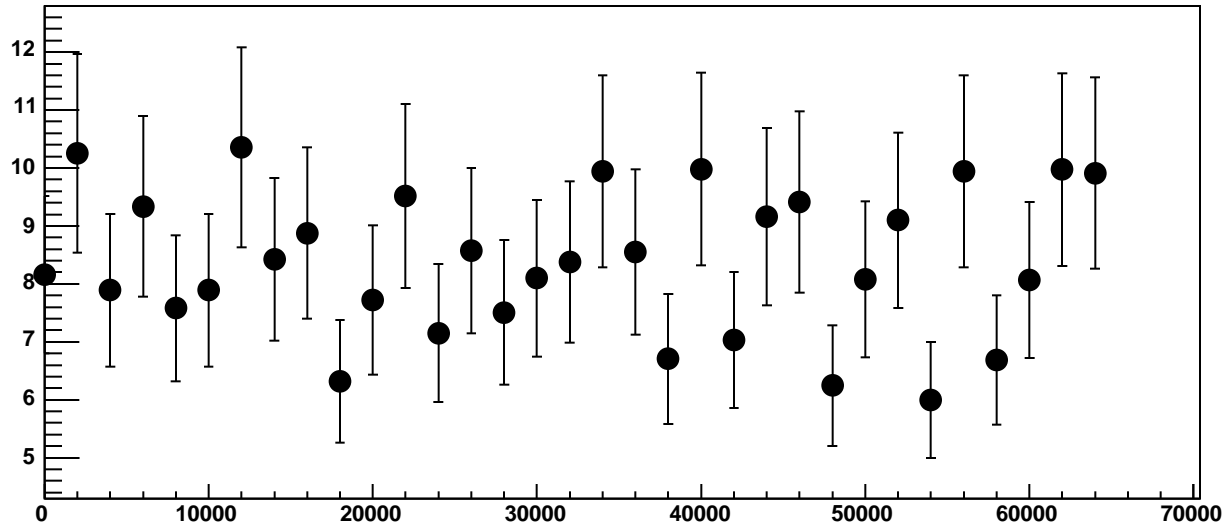
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



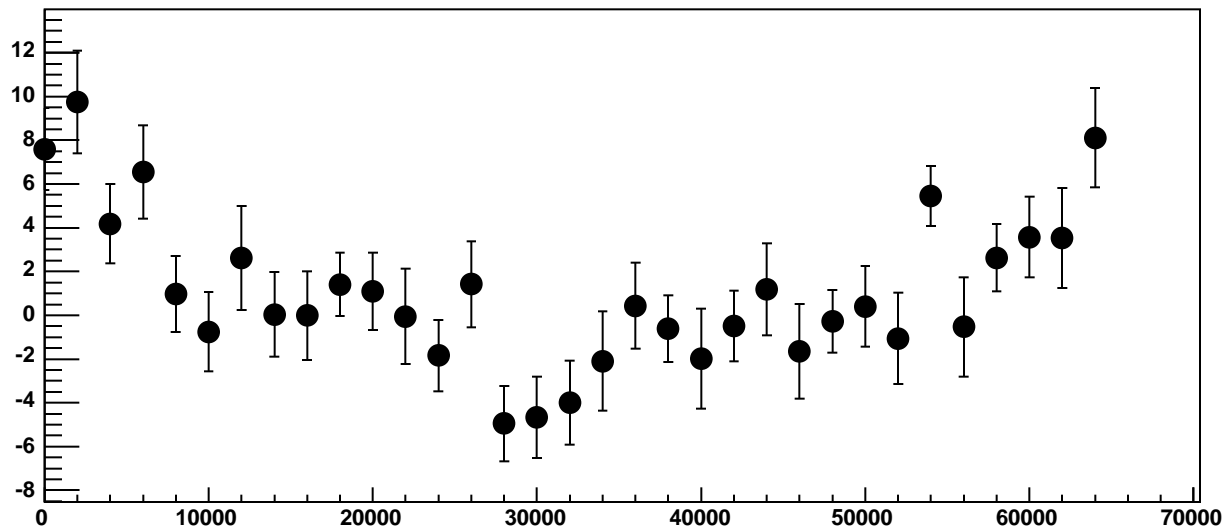
Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



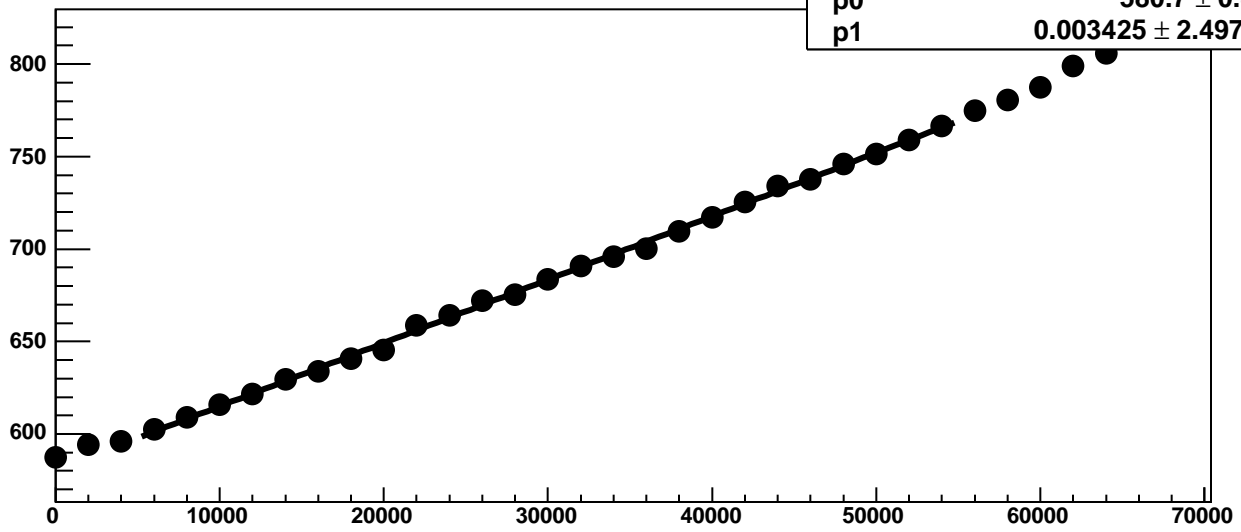
Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

19.86 / 23

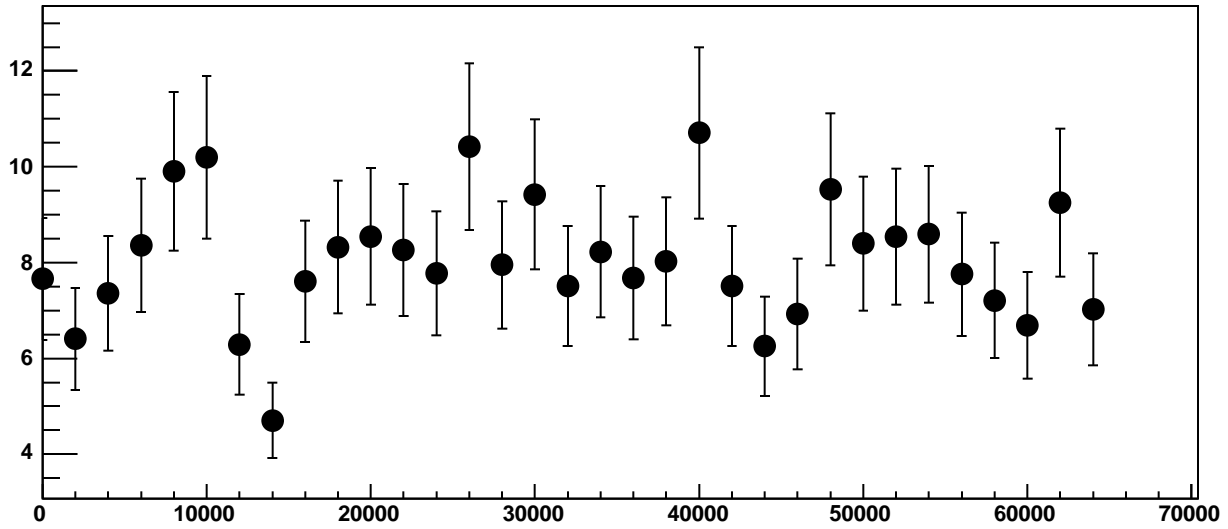
p0

580.7 ± 0.8115

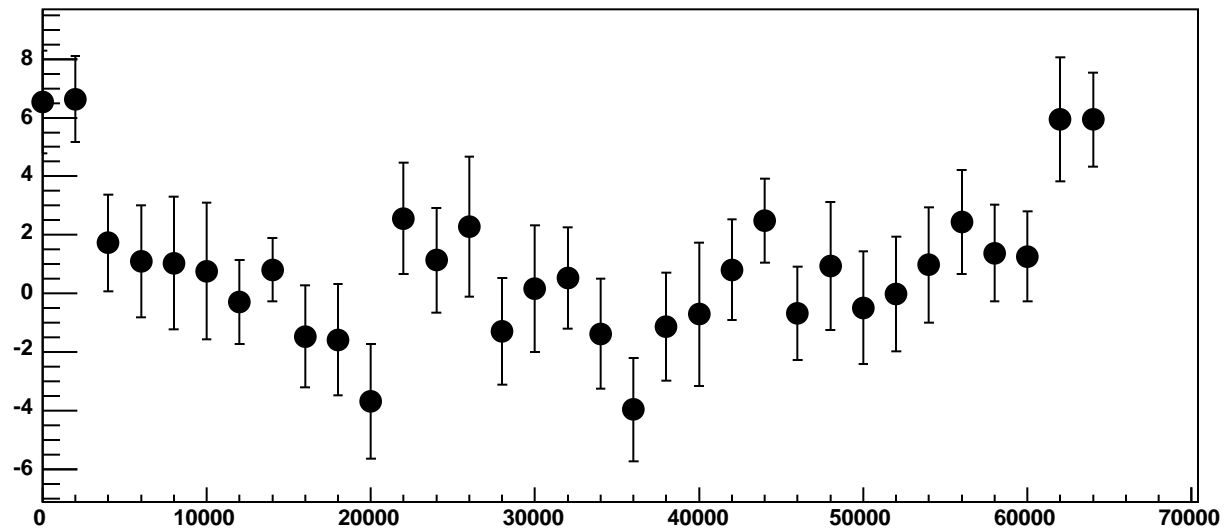
p1

$0.003425 \pm 2.497e-05$

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

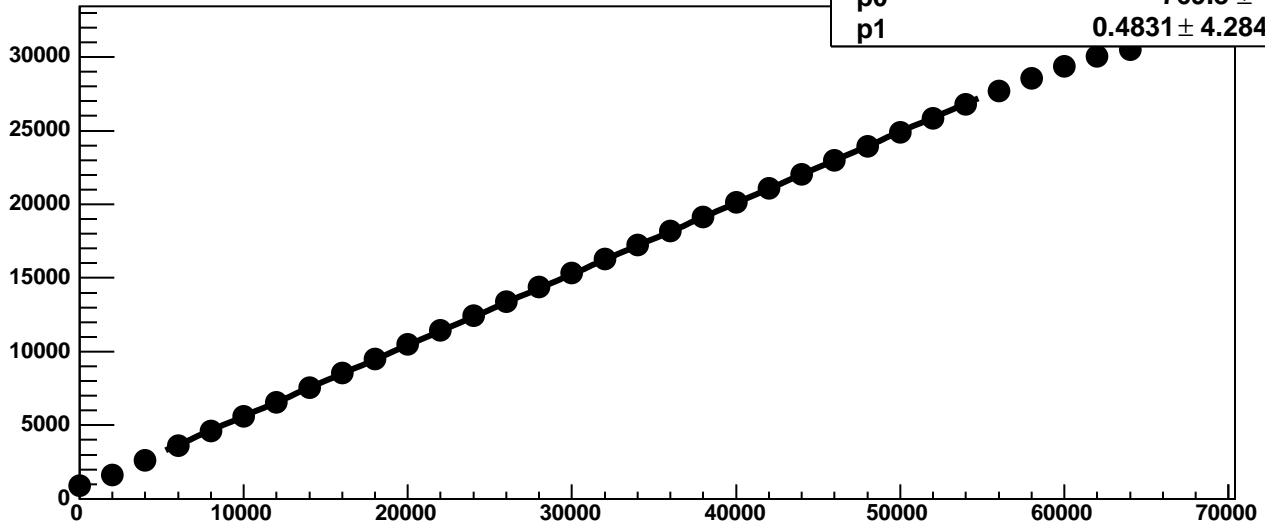


Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

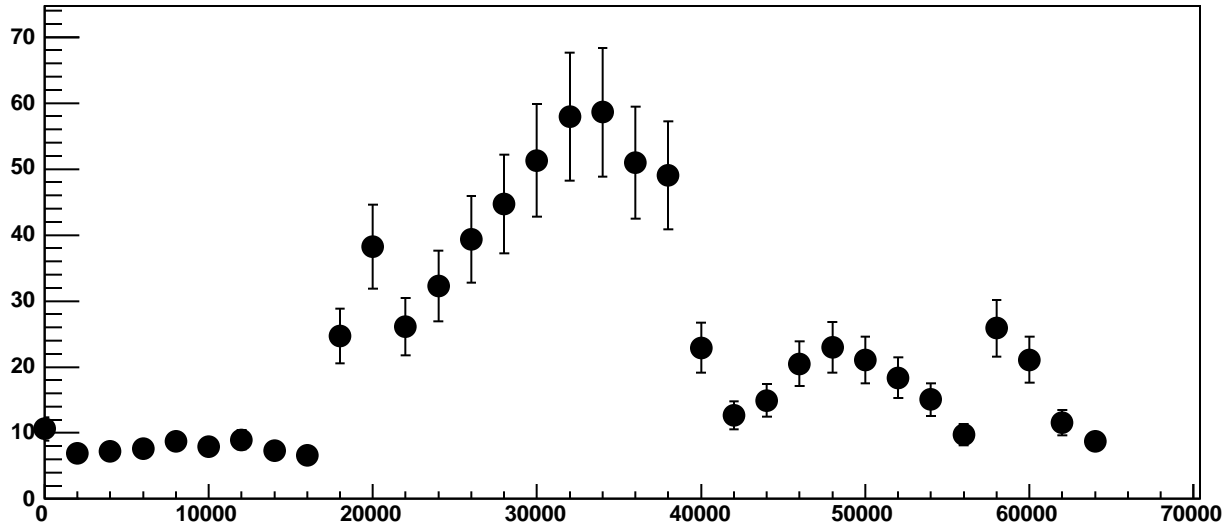


Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

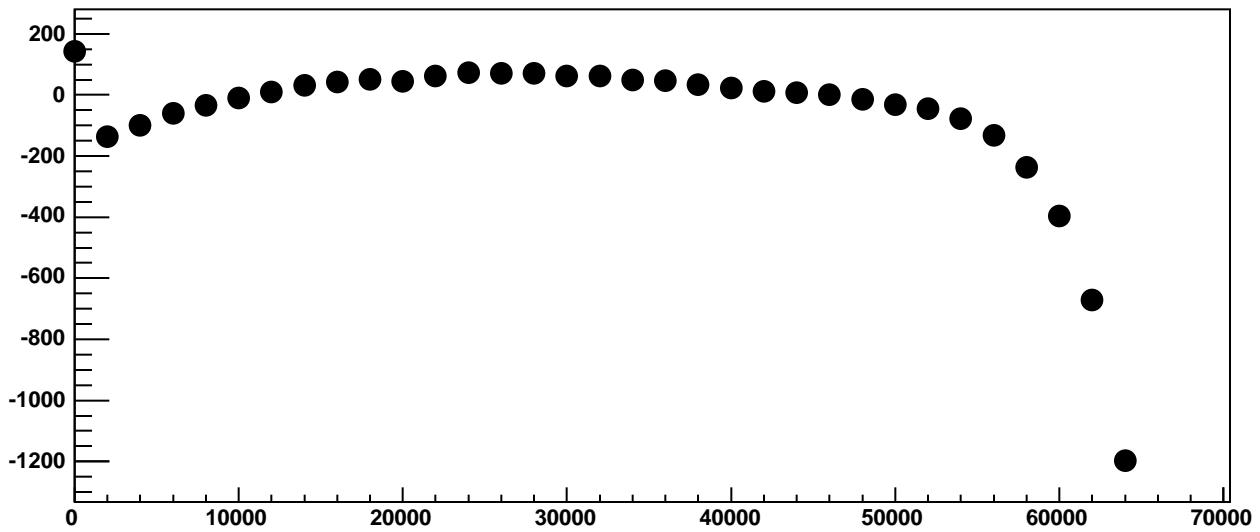
χ^2 / ndf 3897 / 23
p0 769.8 ± 1.031
p1 $0.4831 \pm 4.284\text{e-}05$



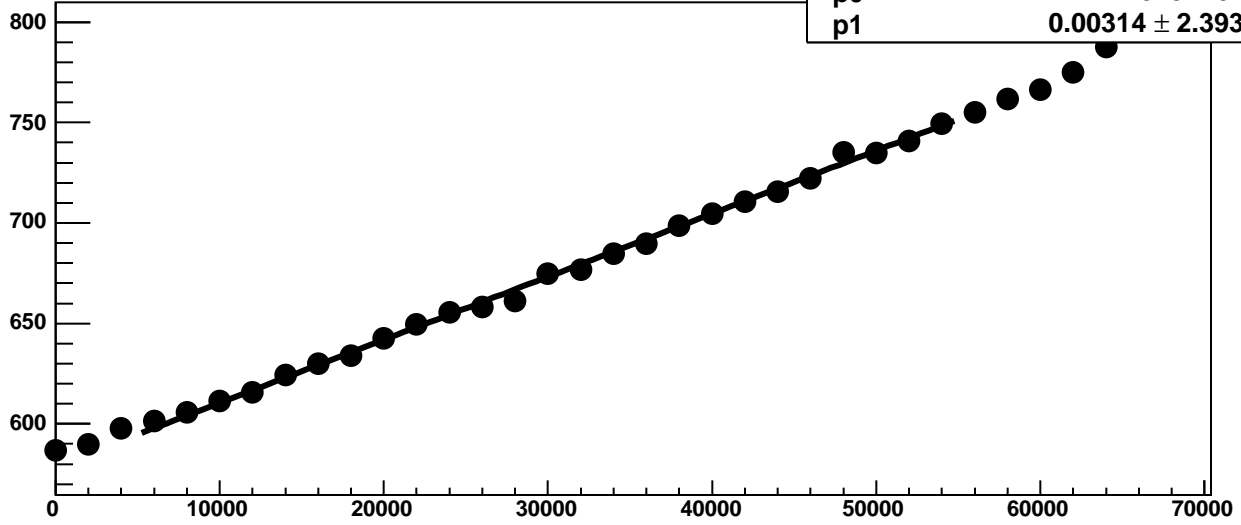
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.27 / 23

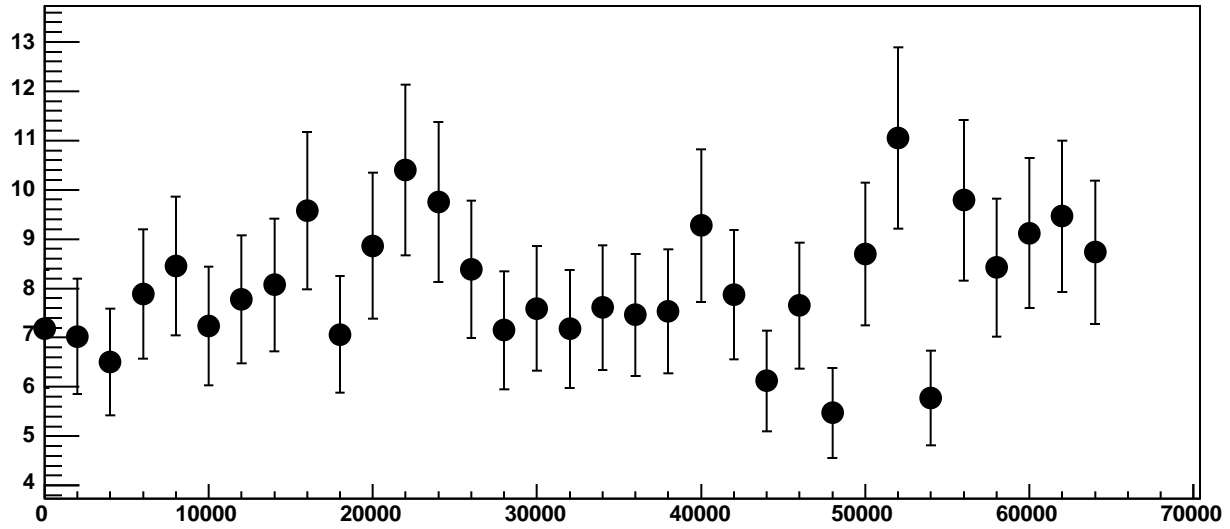
p0

579 ± 0.8356

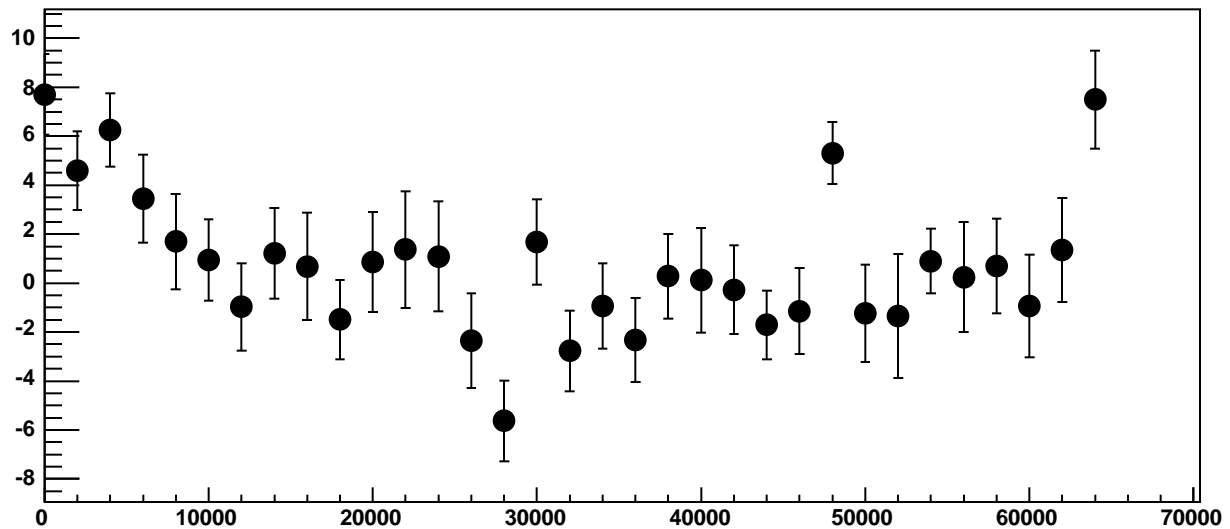
p1

$0.00314 \pm 2.393e-05$

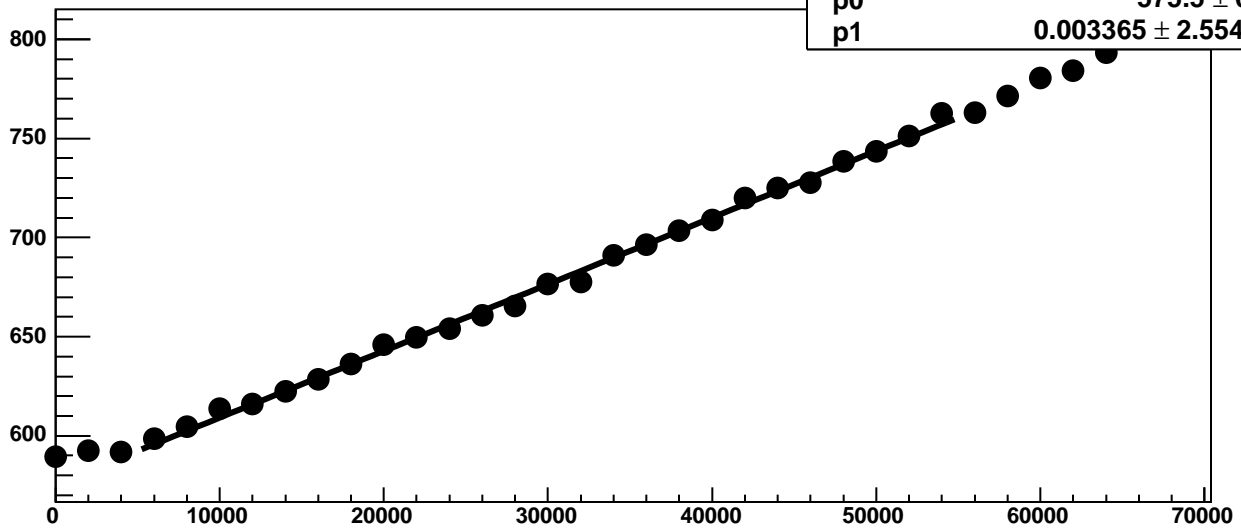
Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

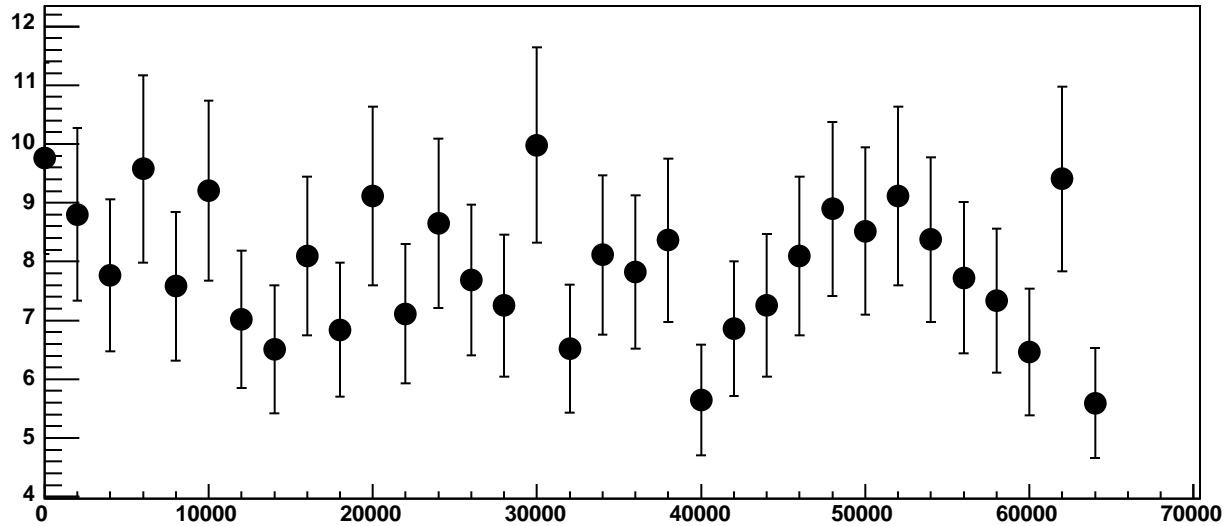


Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

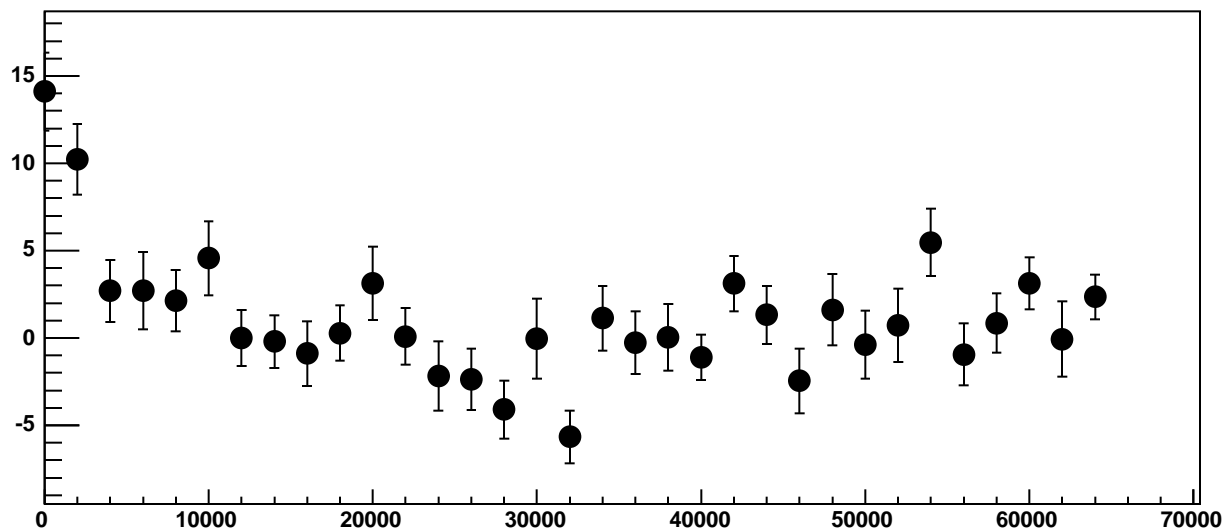


χ^2 / ndf 49.97 / 23
p0 575.5 ± 0.841
p1 $0.003365 \pm 2.554e-05$

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

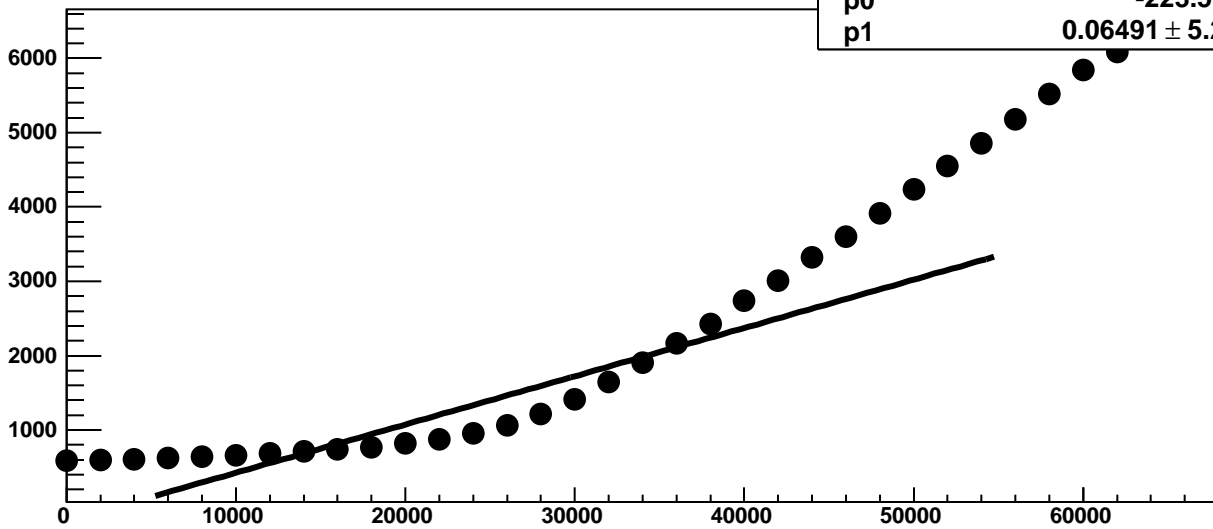


Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

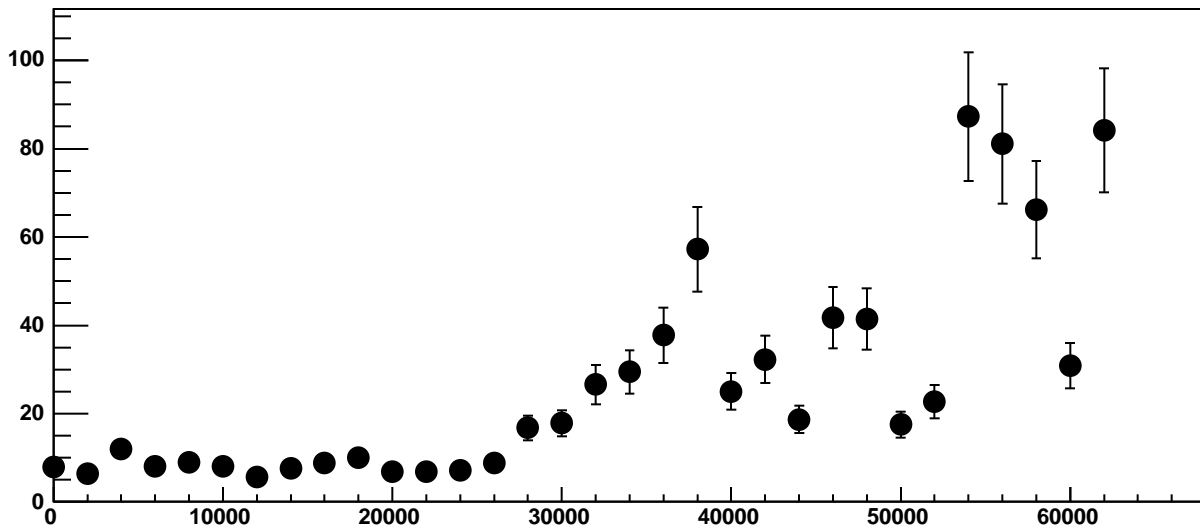


Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

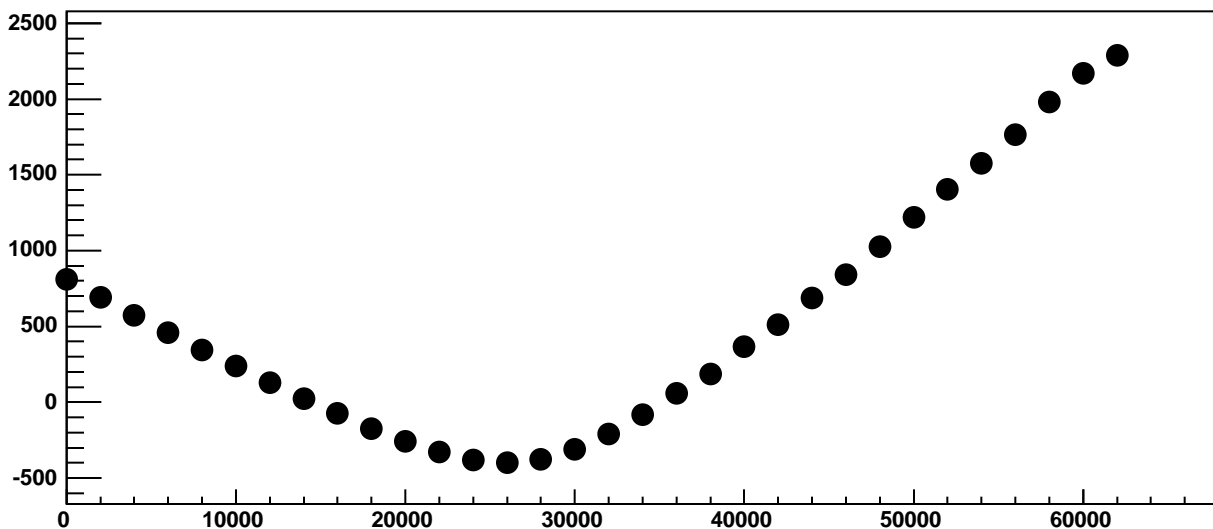
χ^2 / ndf 5.277e+05 / 23
p0 -223.5 ± 1.085
p1 0.06491 ± 5.201e-05



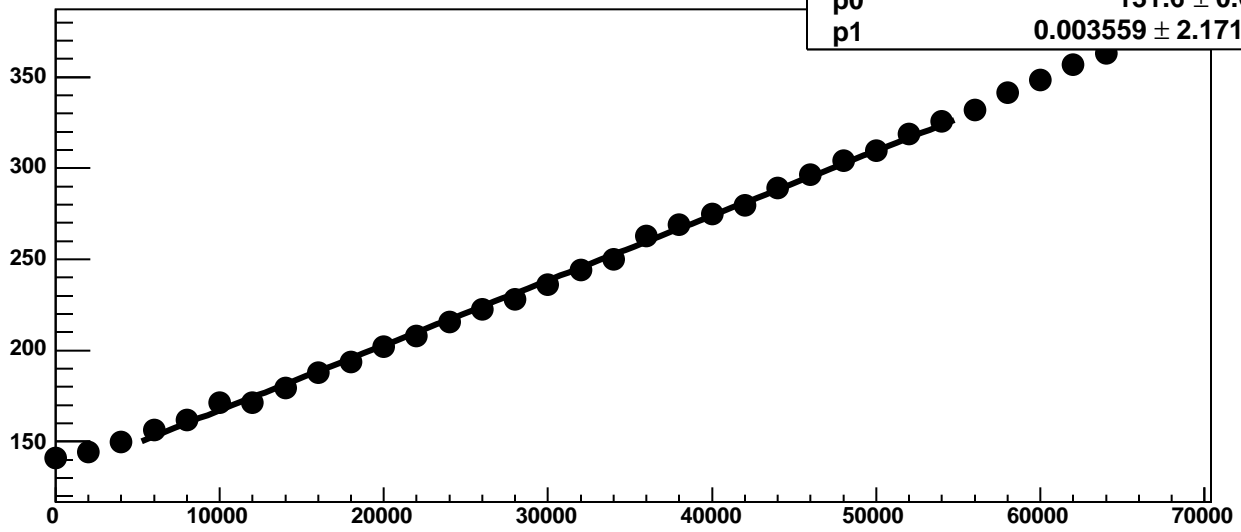
Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

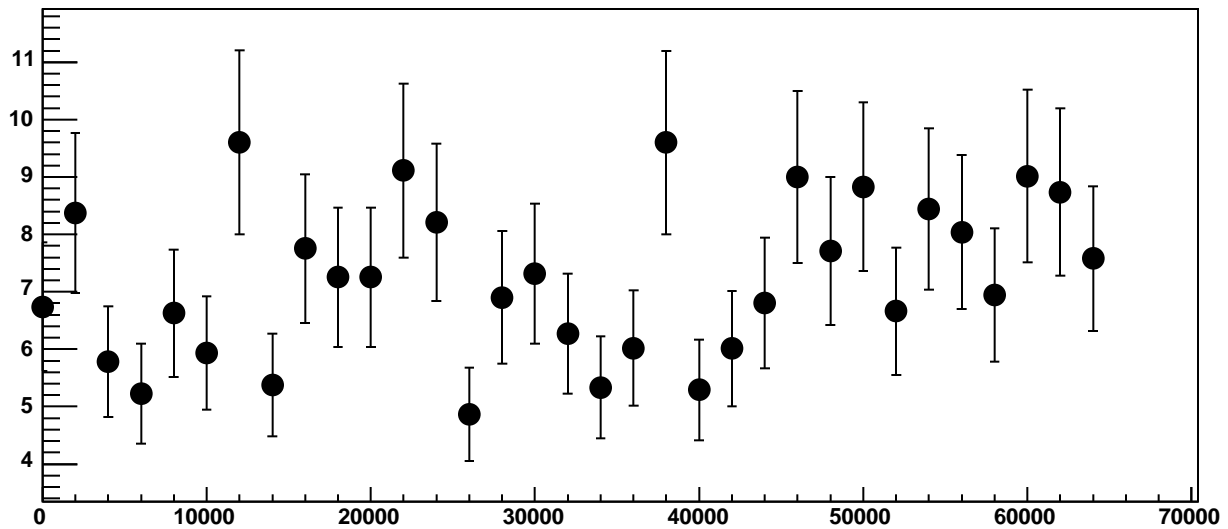


Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

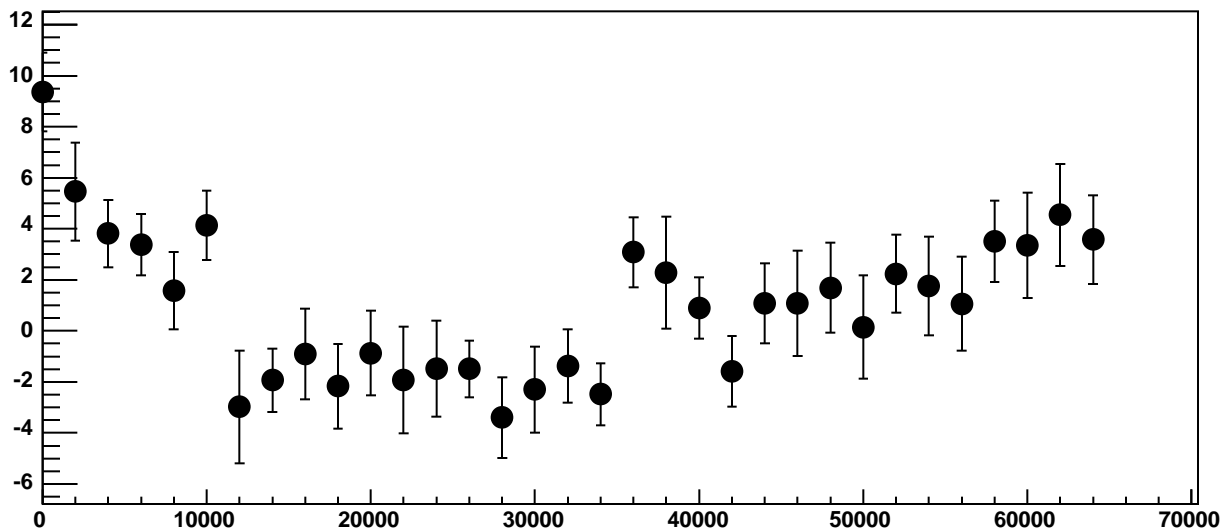


χ^2 / ndf 52.06 / 23
p0 131.6 ± 0.6948
p1 0.003559 ± 2.171e-05

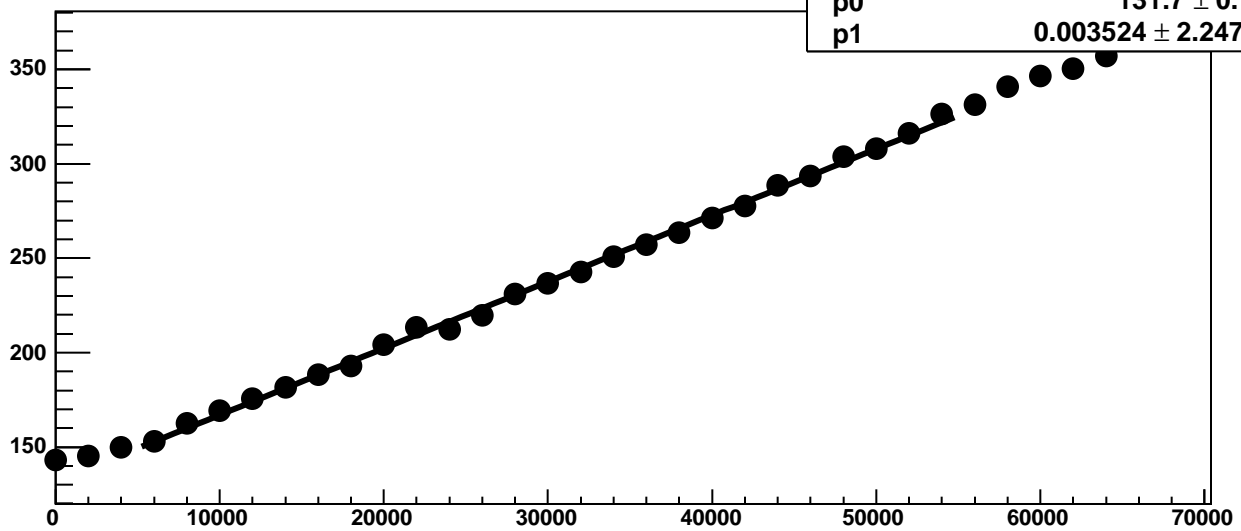
Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



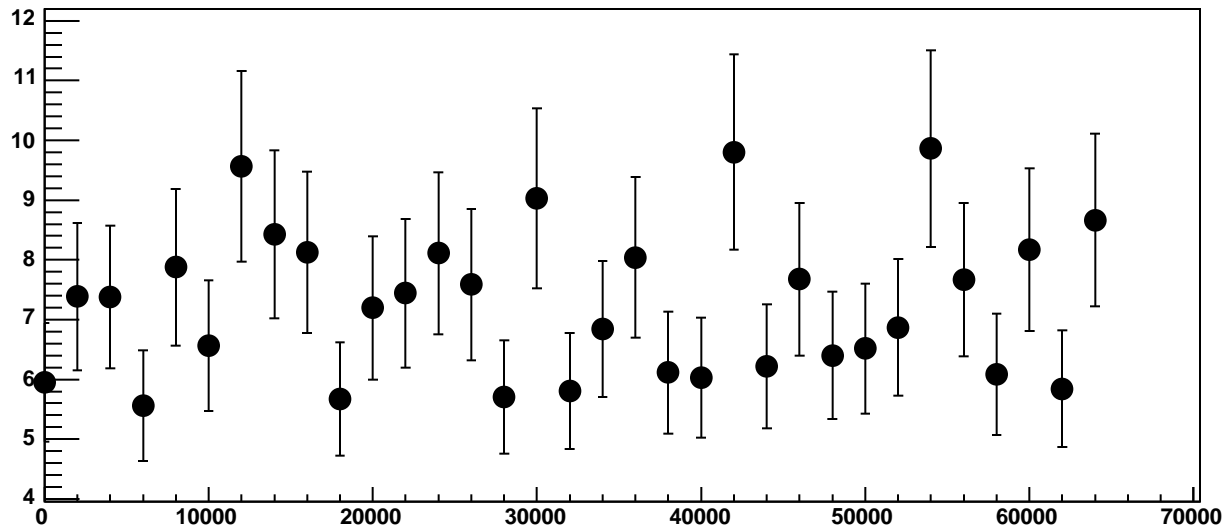
Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



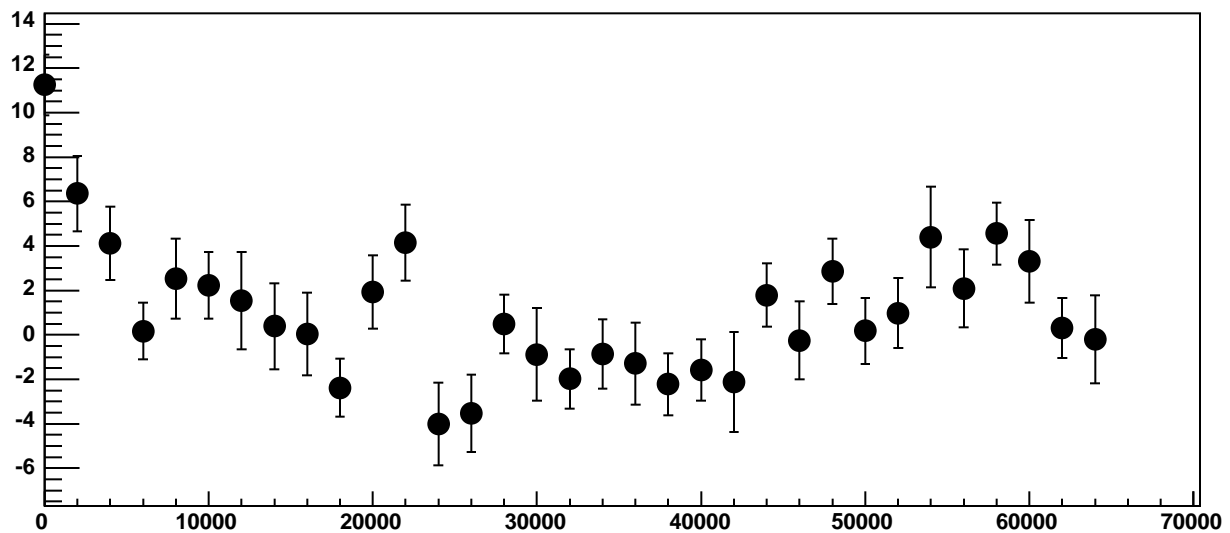
Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



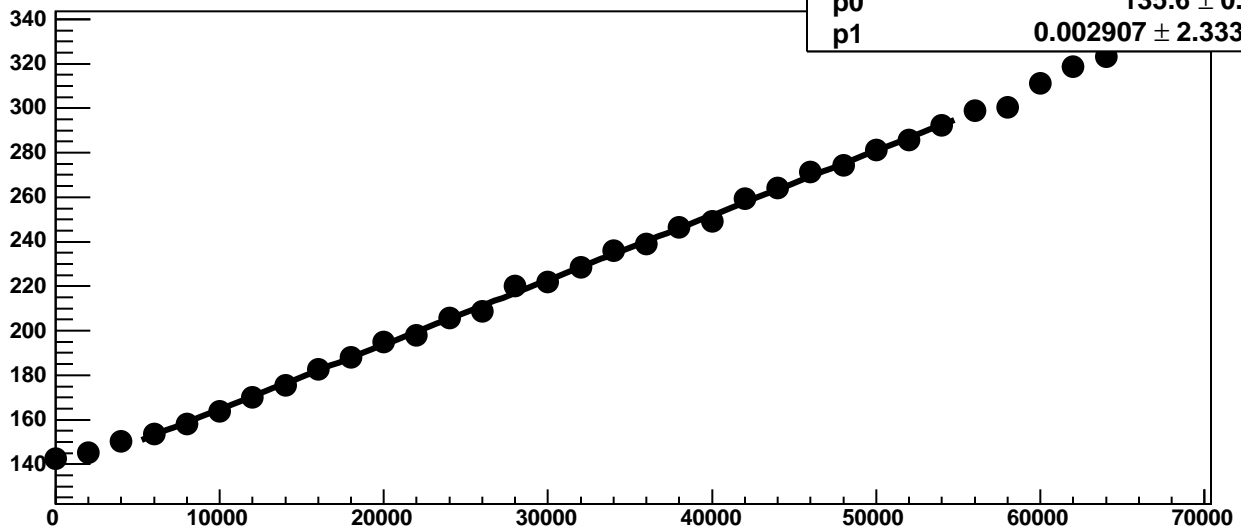
Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

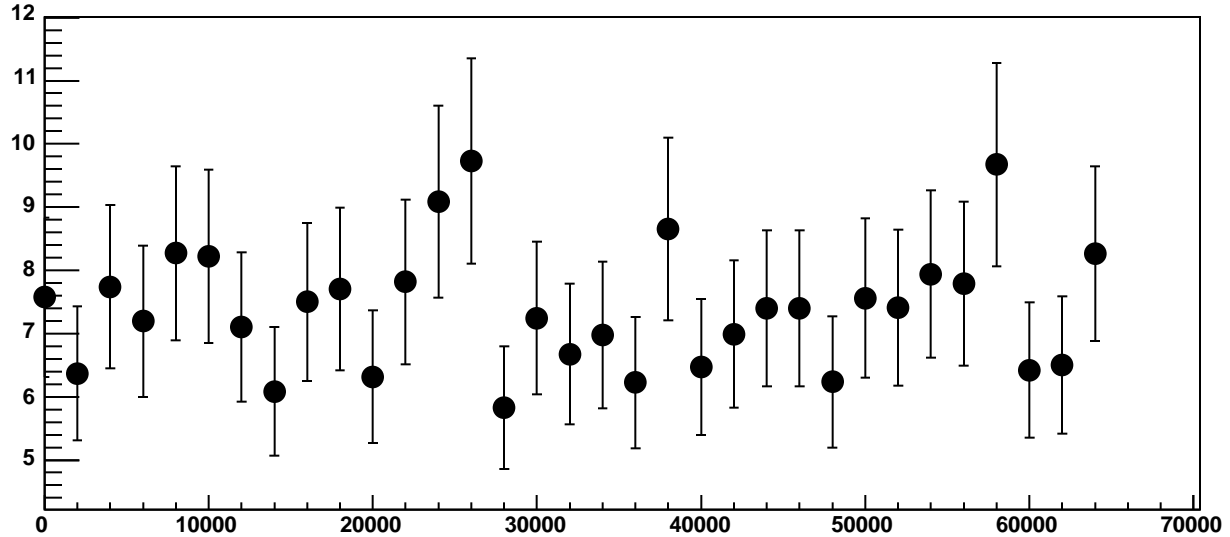


Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

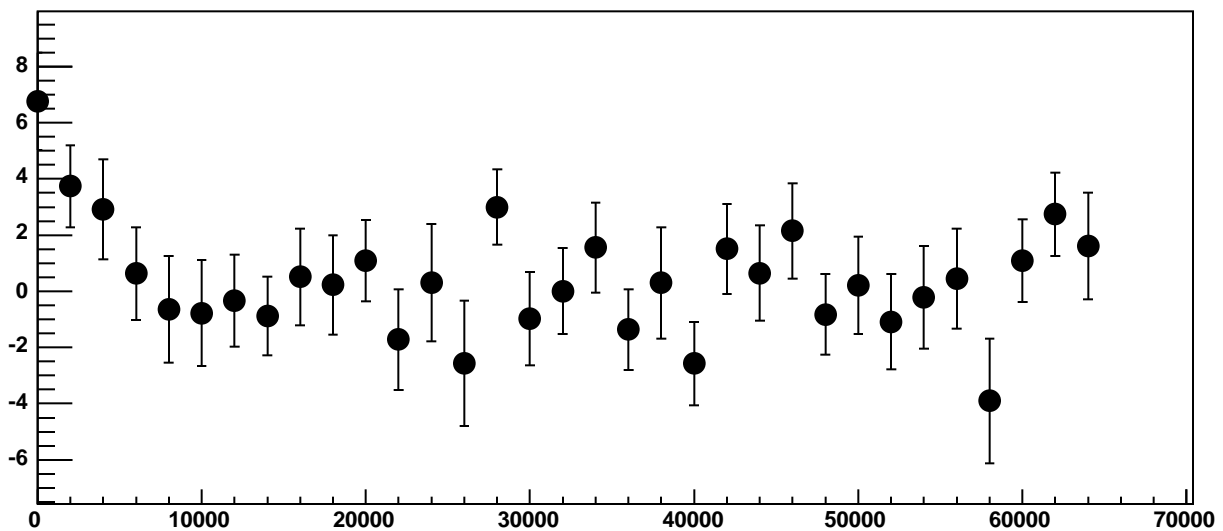


χ^2 / ndf 17.44 / 23
p0 135.6 ± 0.7801
p1 $0.002907 \pm 2.333e-05$

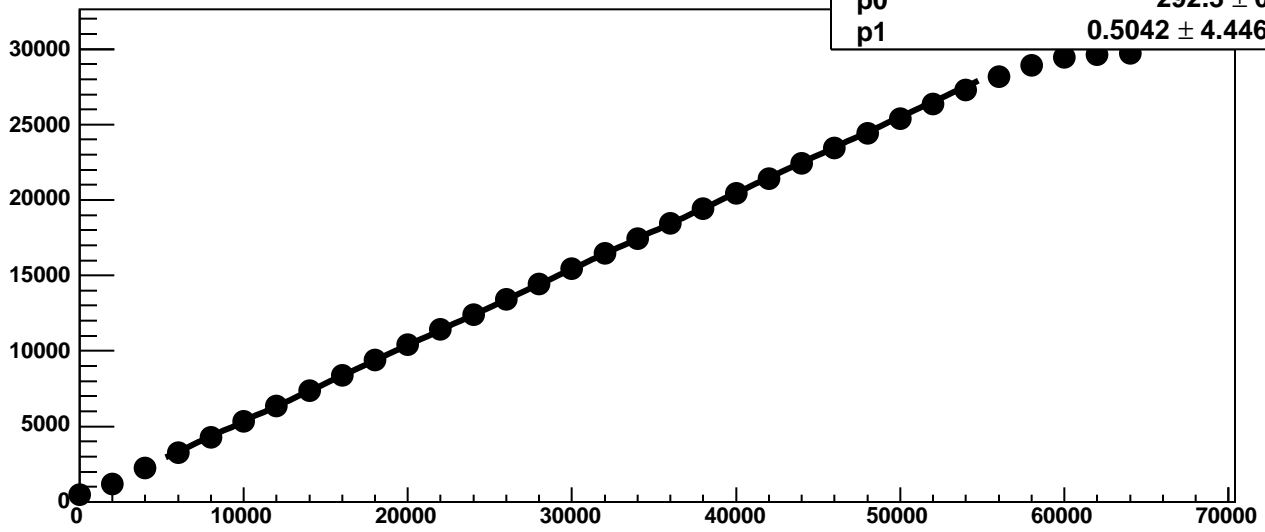
Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



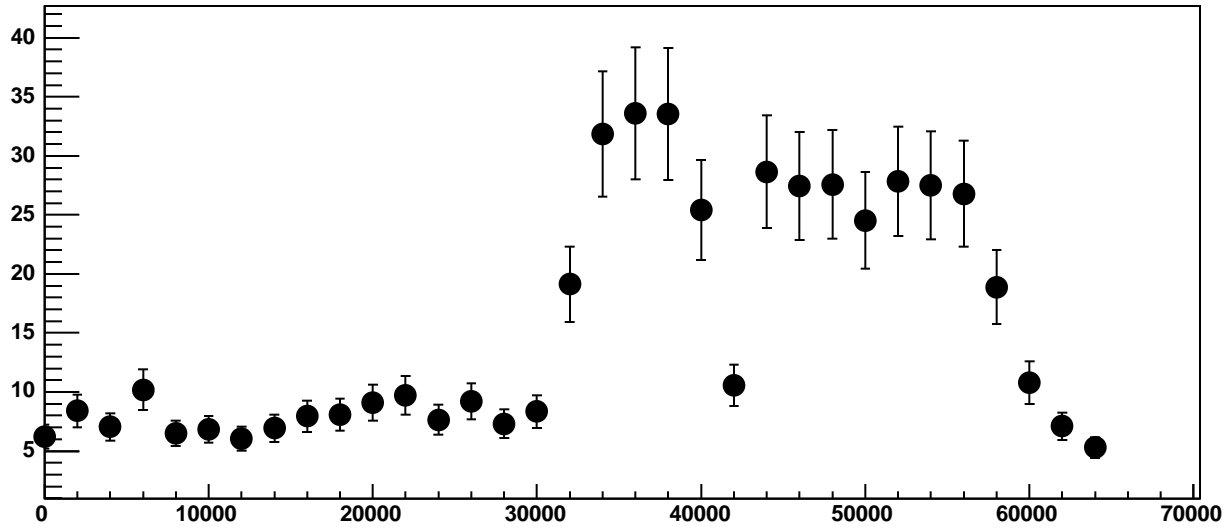
Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



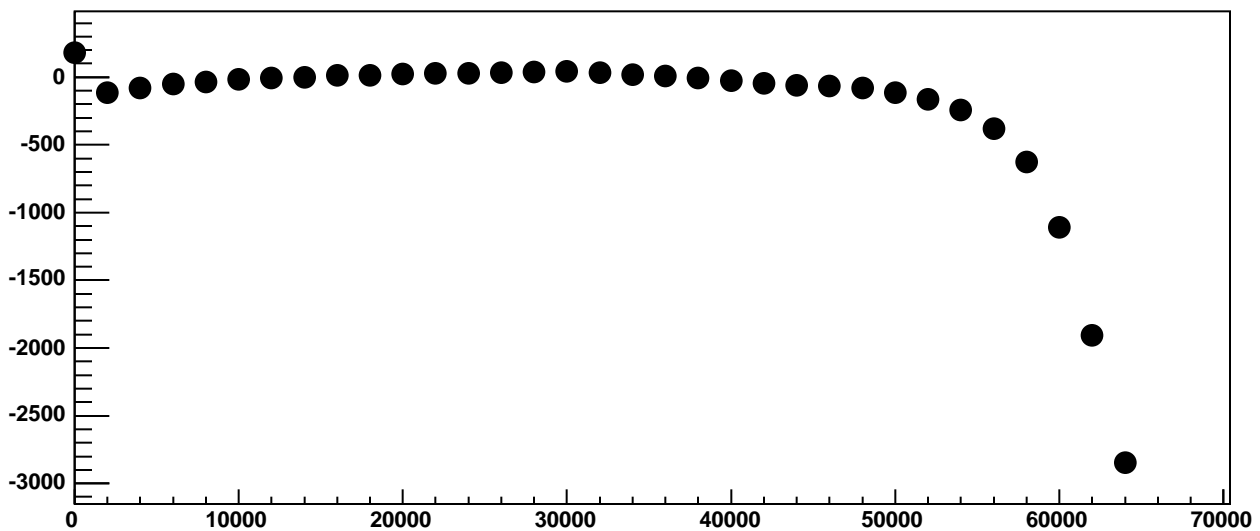
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



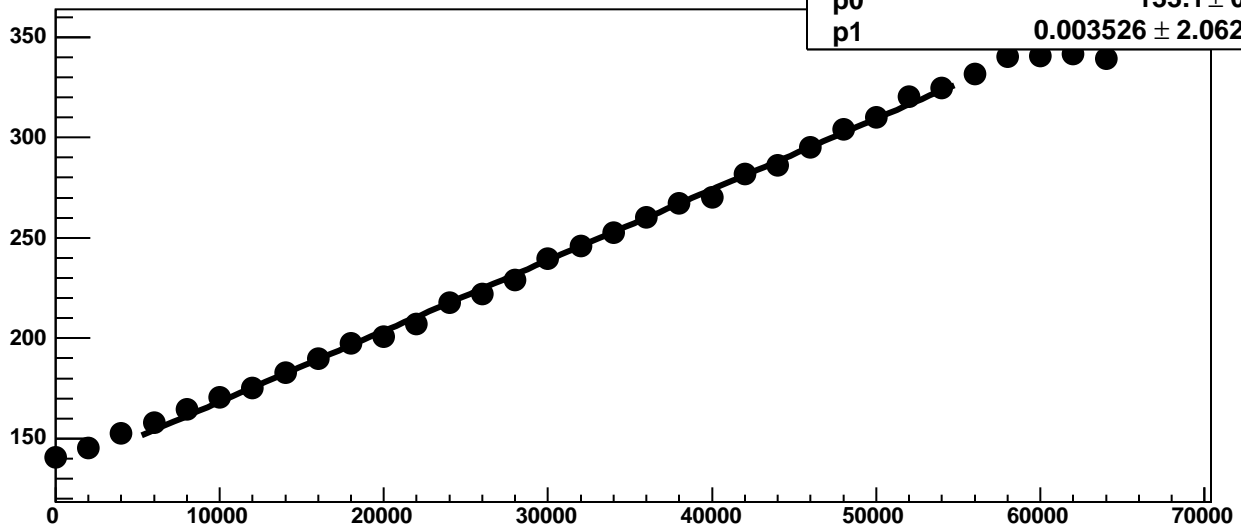
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

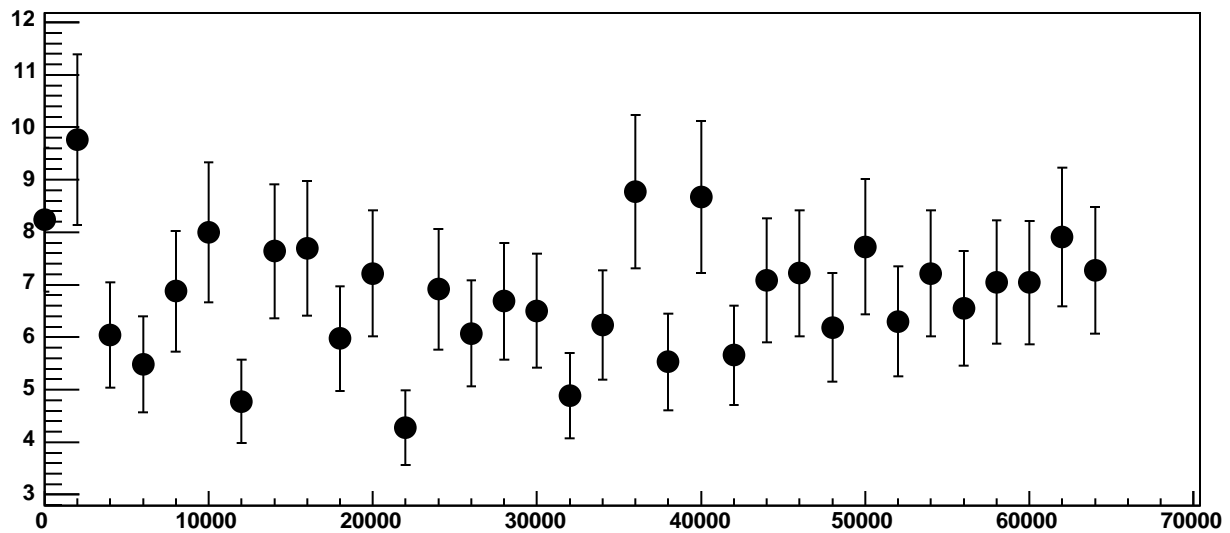


Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

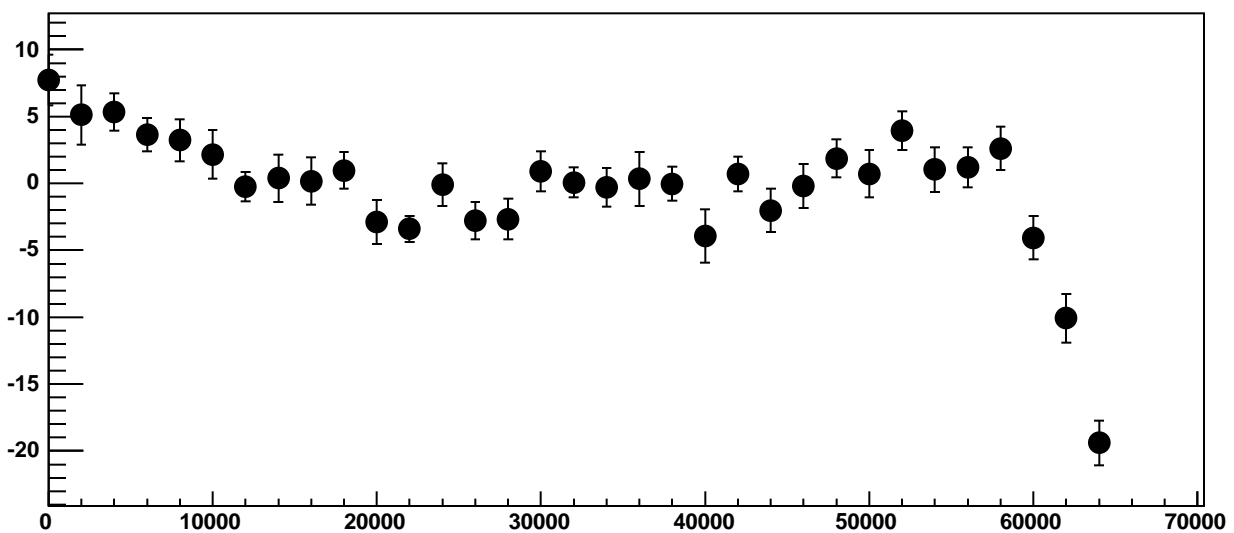


χ^2 / ndf 52.43 / 23
p0 133.1 ± 0.663
p1 0.003526 ± 2.062e-05

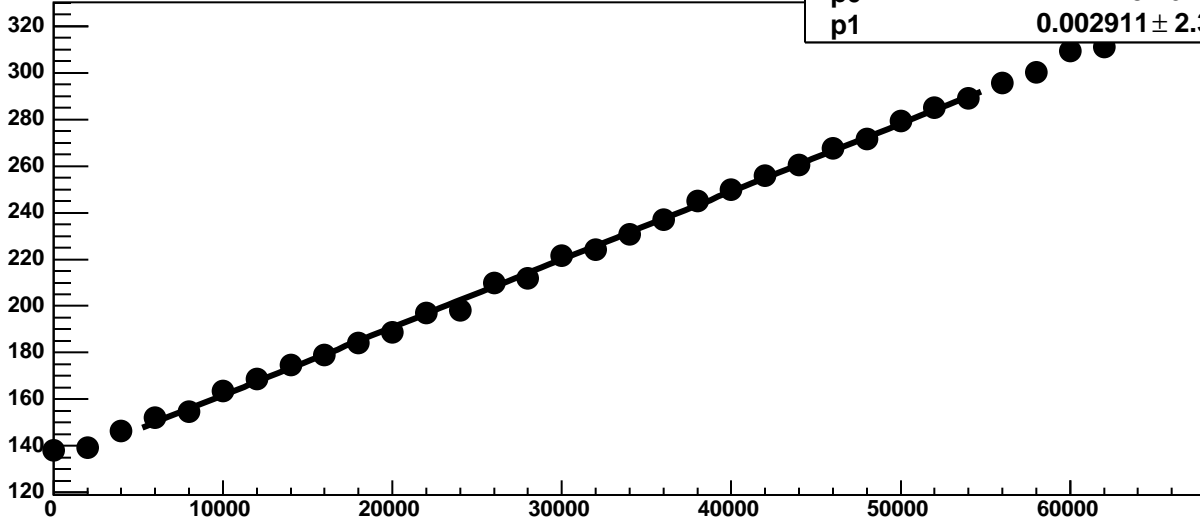
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

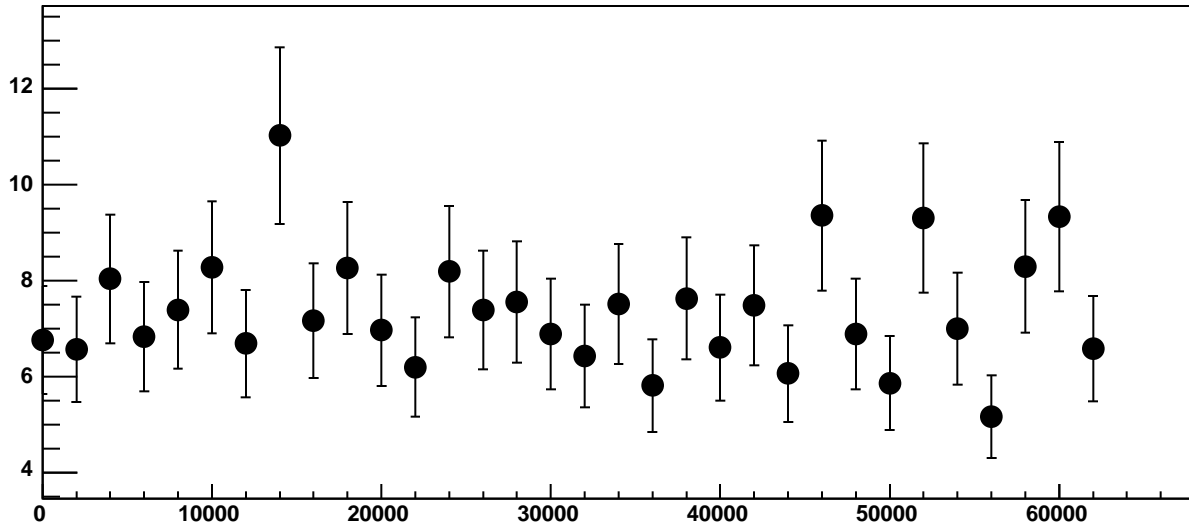


Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

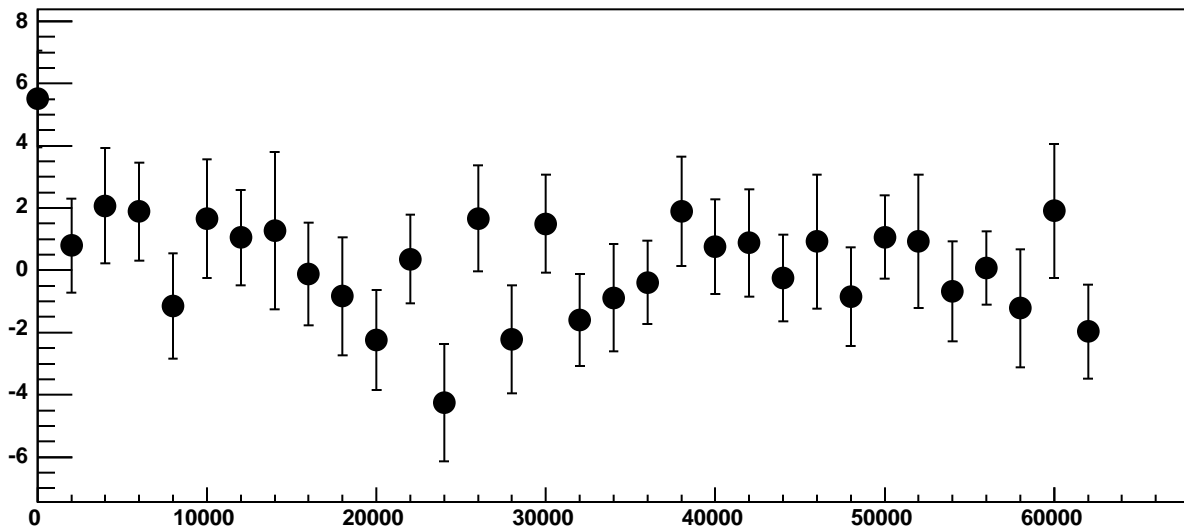


χ^2 / ndf 18.9 / 23
p0 132.6 ± 0.7835
p1 $0.002911 \pm 2.317e-05$

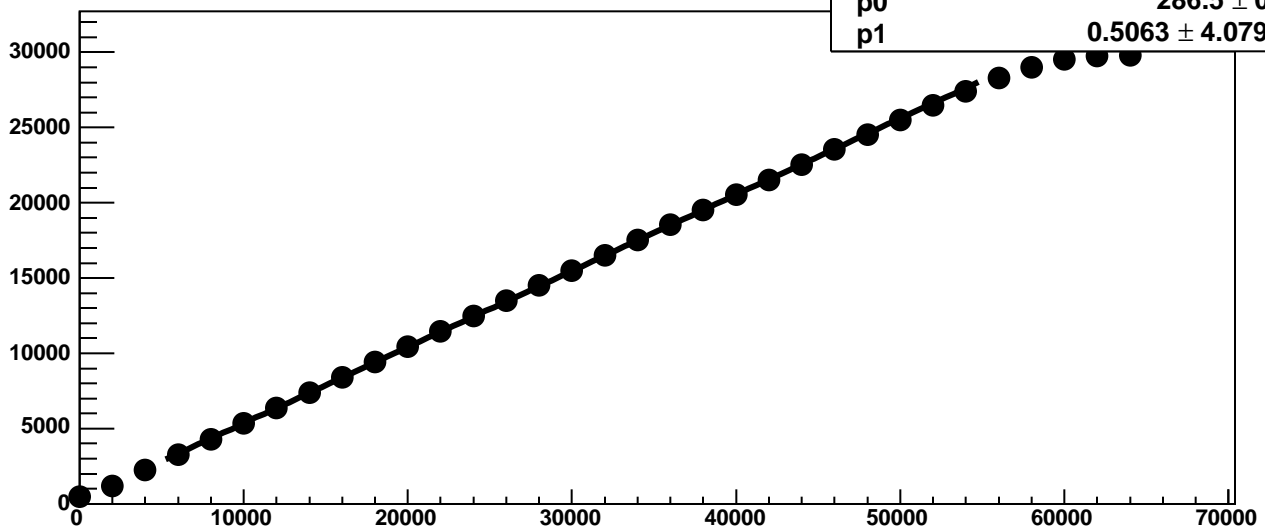
Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



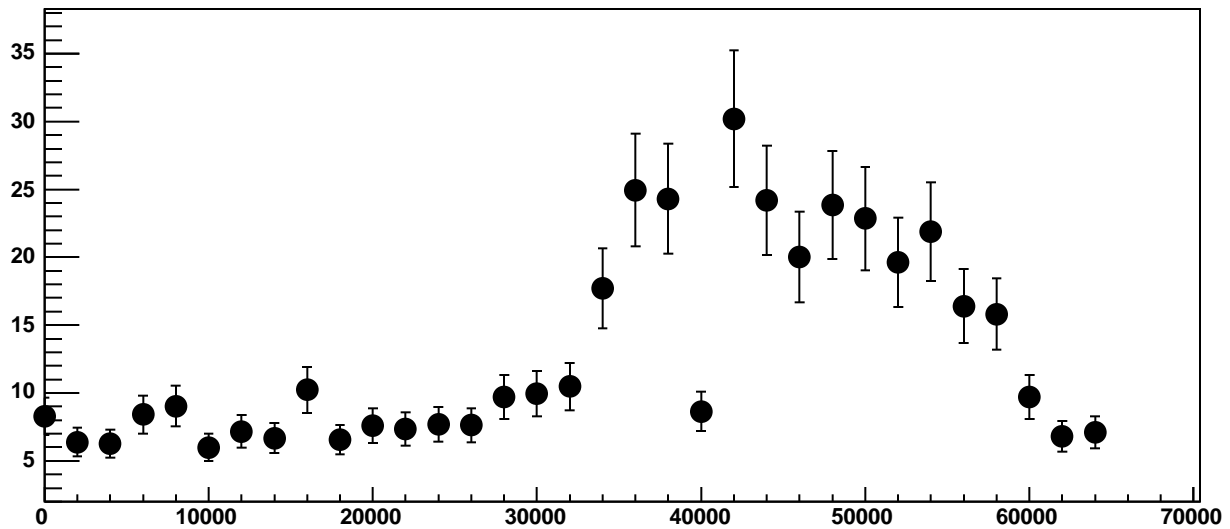
Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



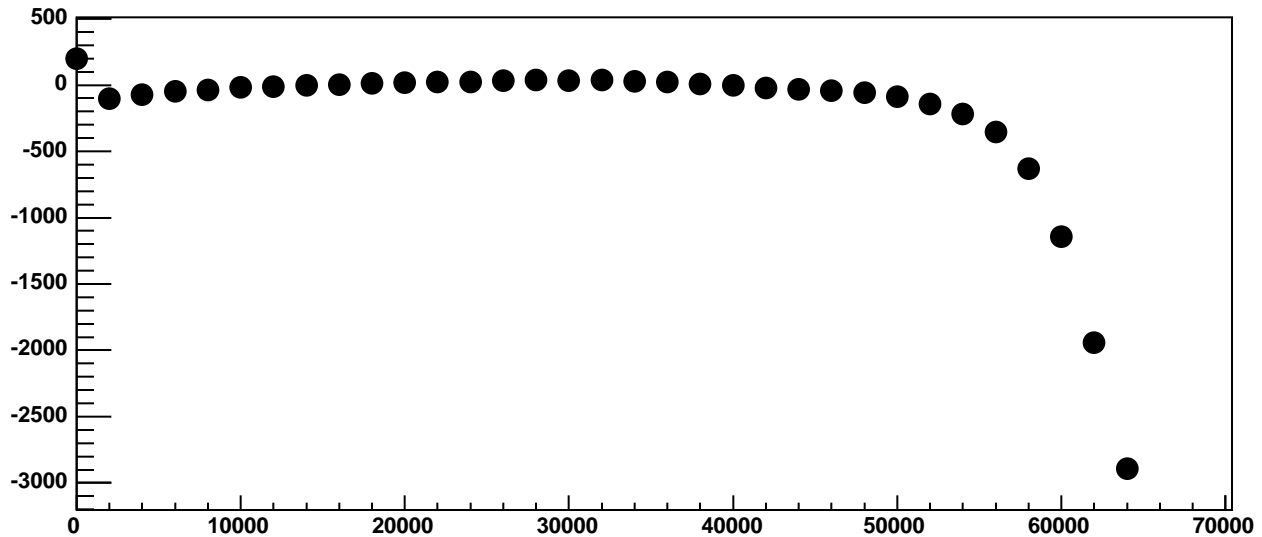
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



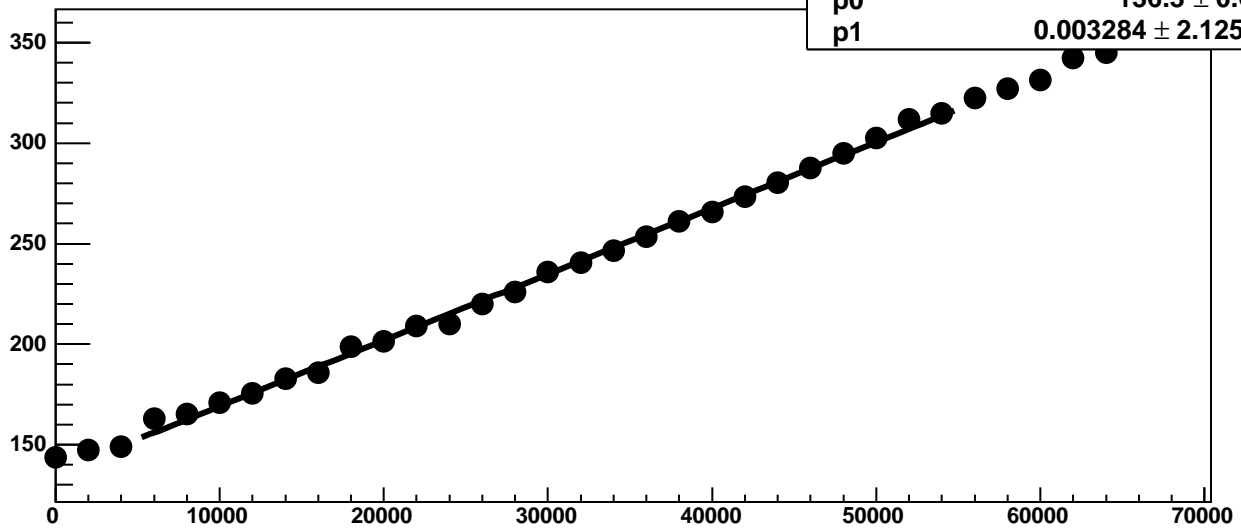
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



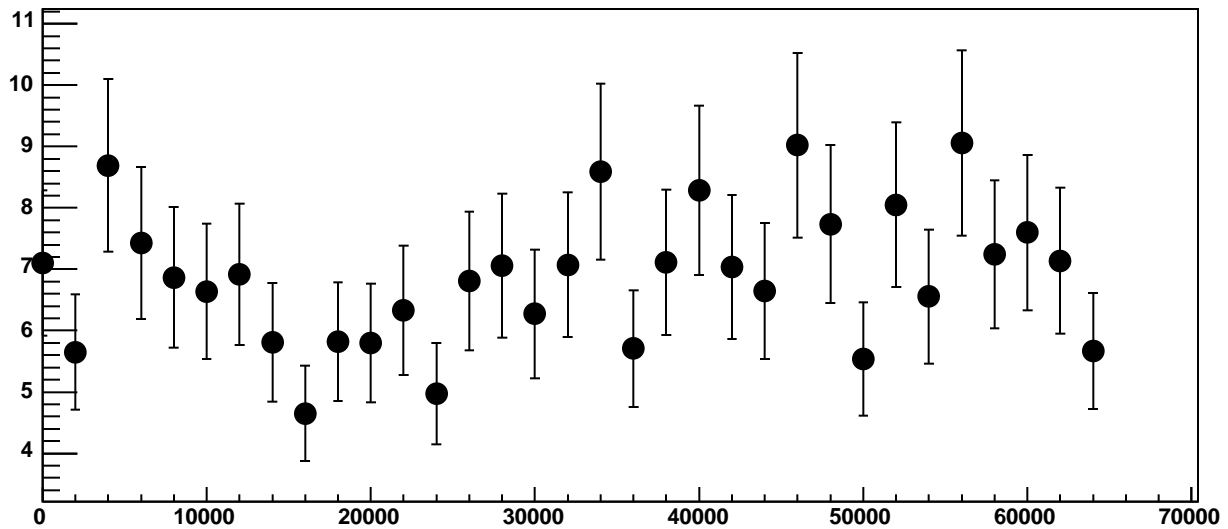
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



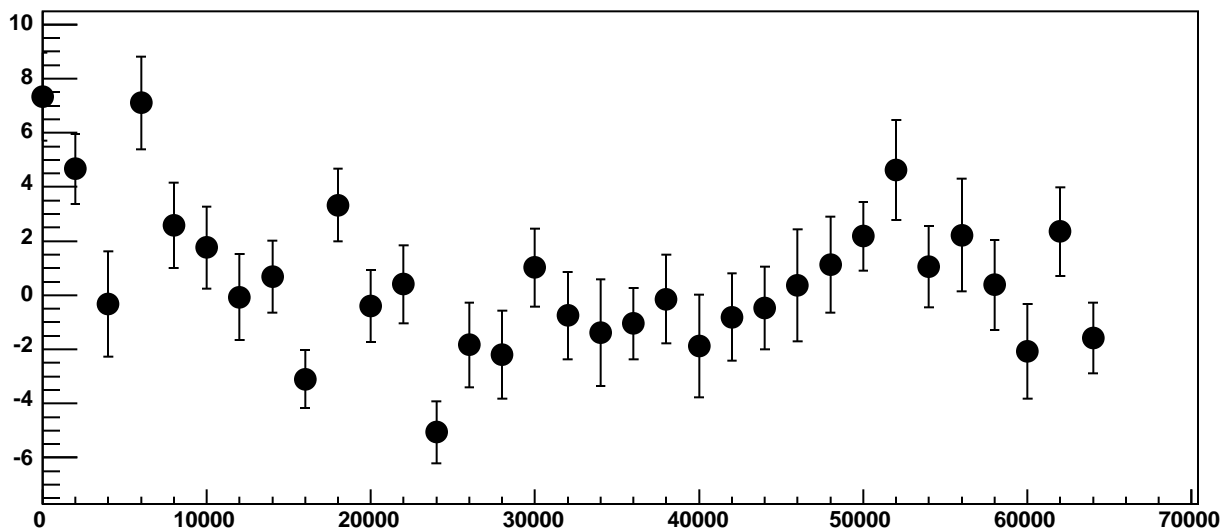
Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



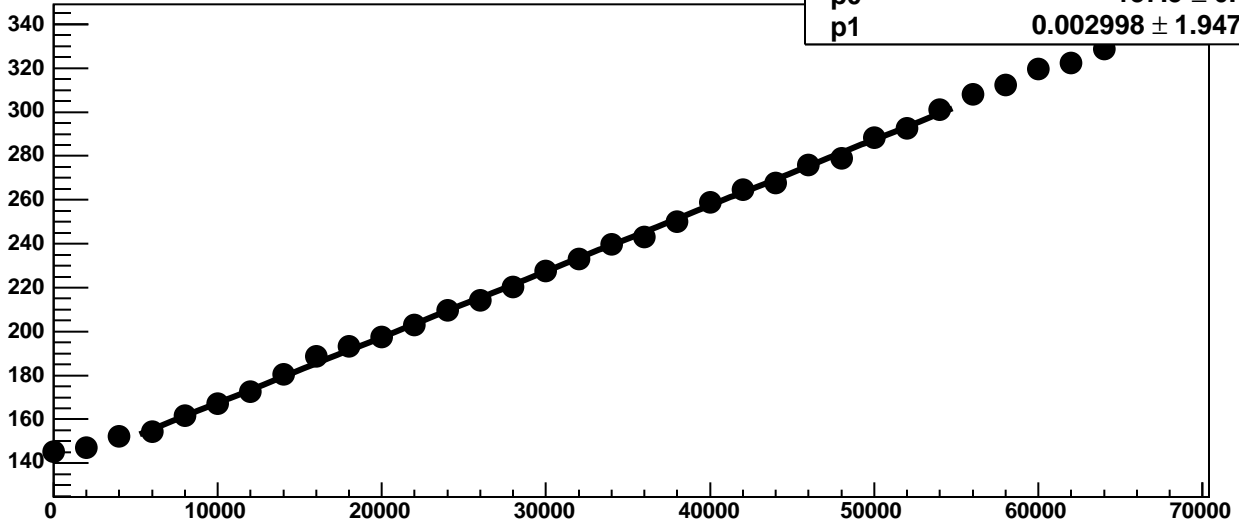
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

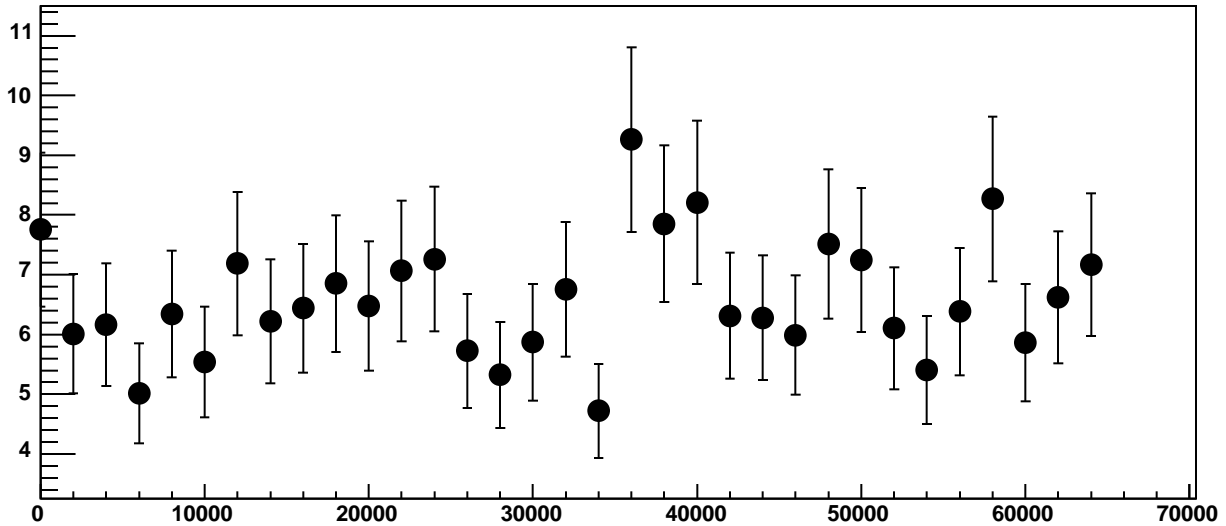


Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

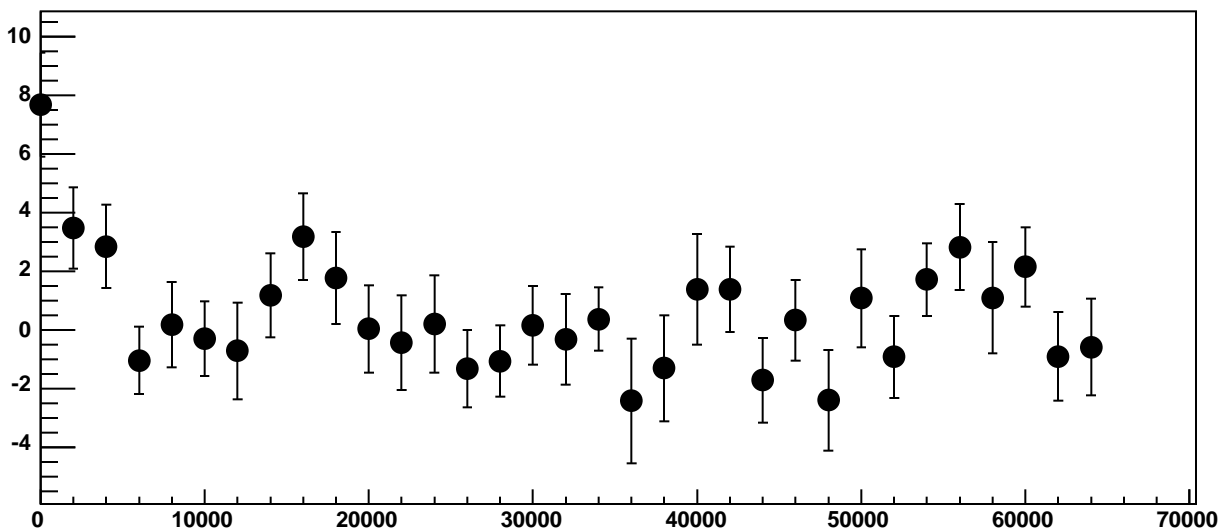


χ^2 / ndf 19.16 / 23
p0 137.5 ± 0.6397
p1 $0.002998 \pm 1.947e-05$

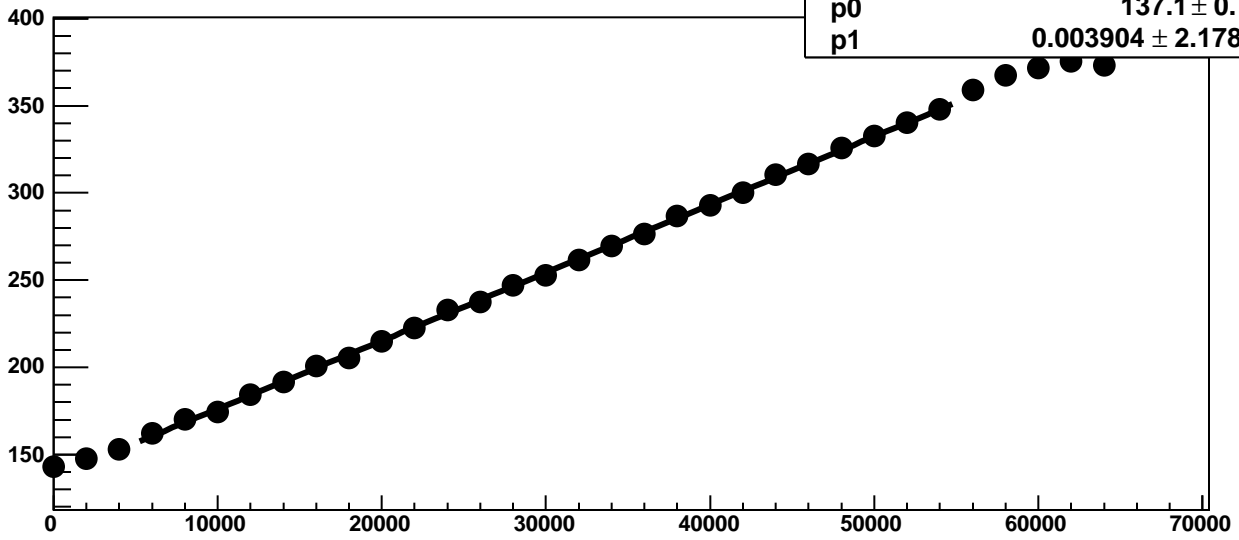
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

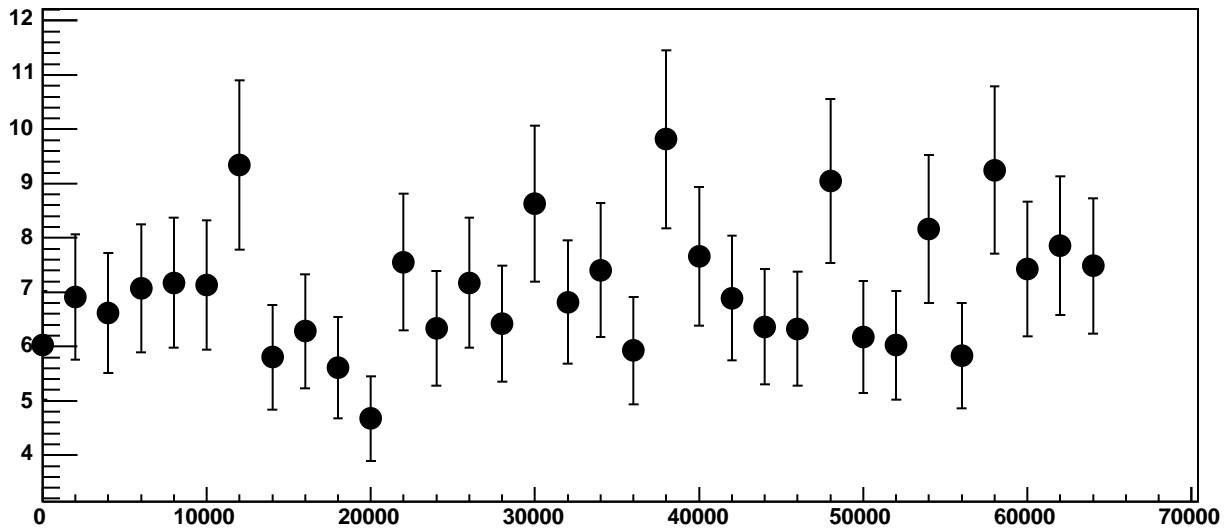


Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

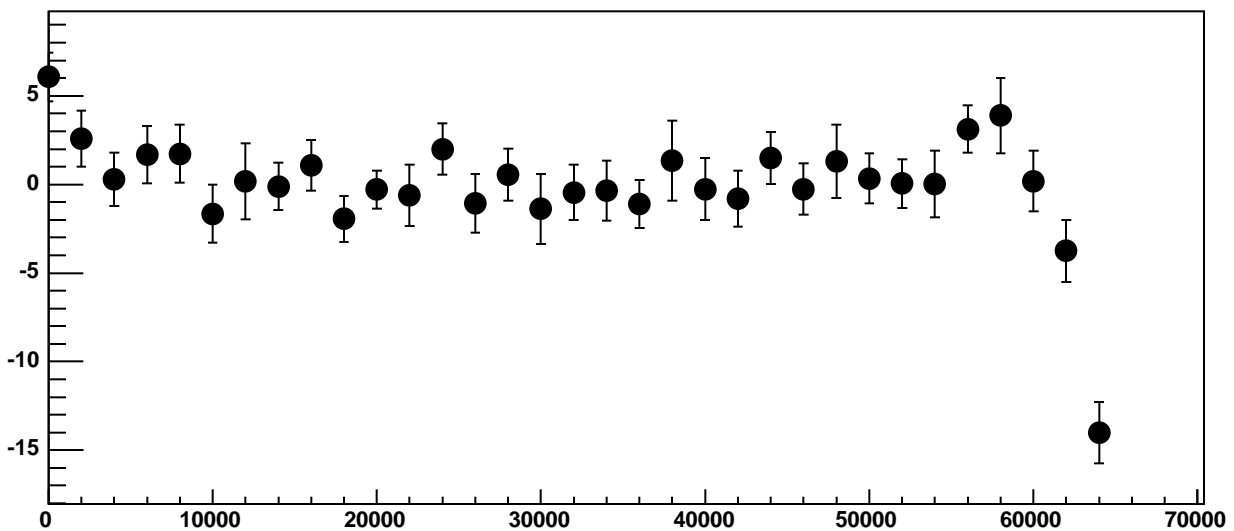


χ^2 / ndf 12.16 / 23
p0 137.1 ± 0.7108
p1 $0.003904 \pm 2.178e-05$

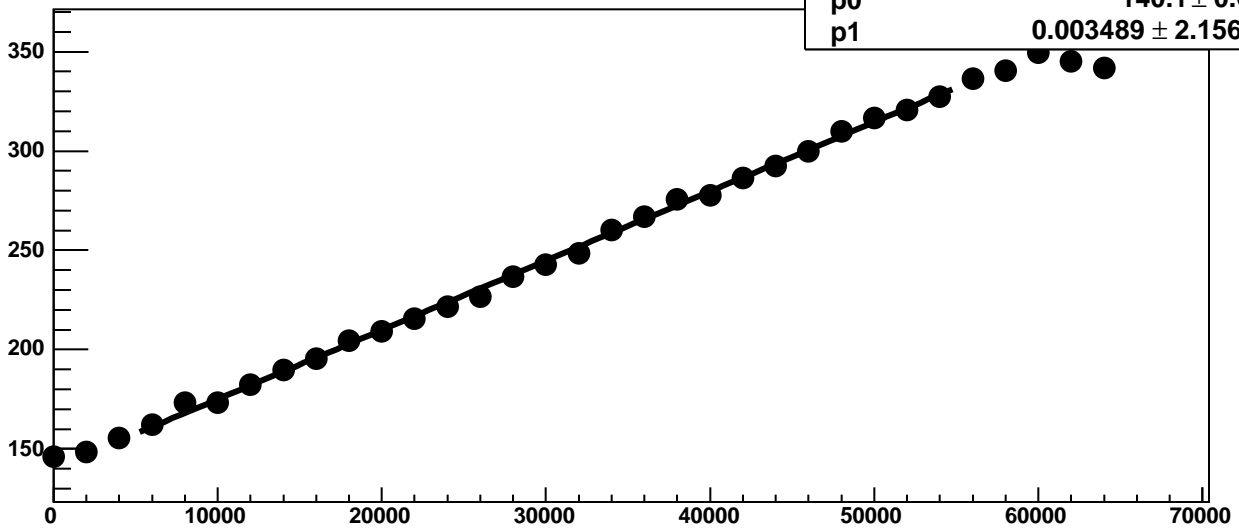
Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

45.23 / 23

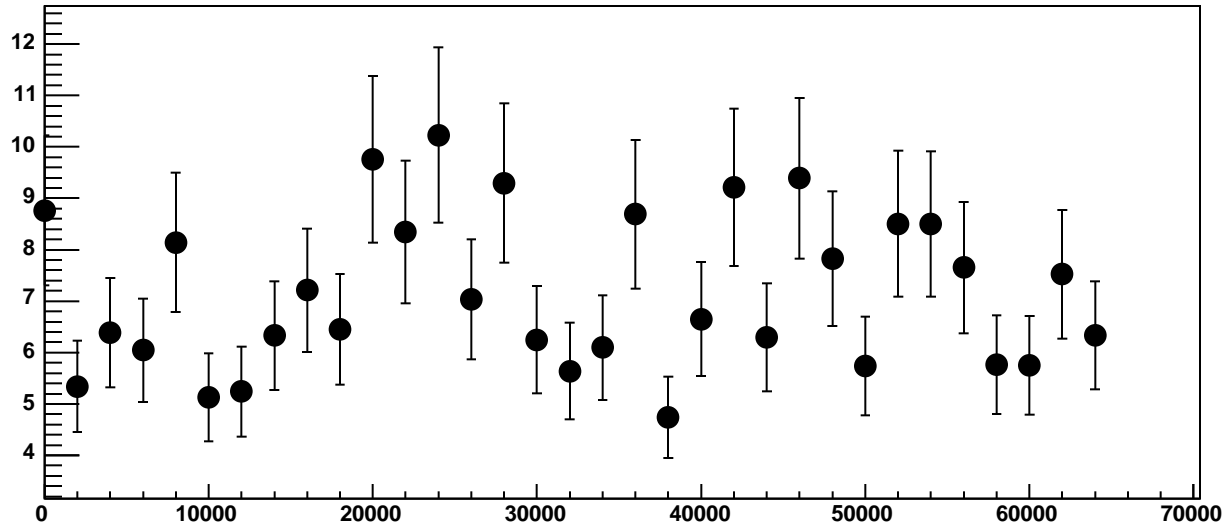
p0

140.1 ± 0.6935

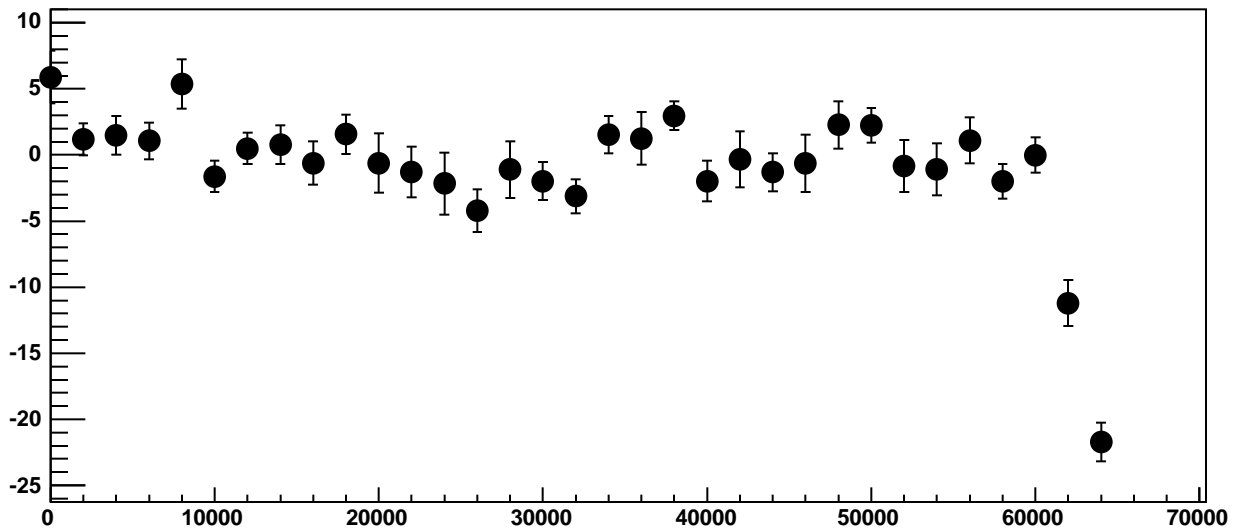
p1

$0.003489 \pm 2.156e-05$

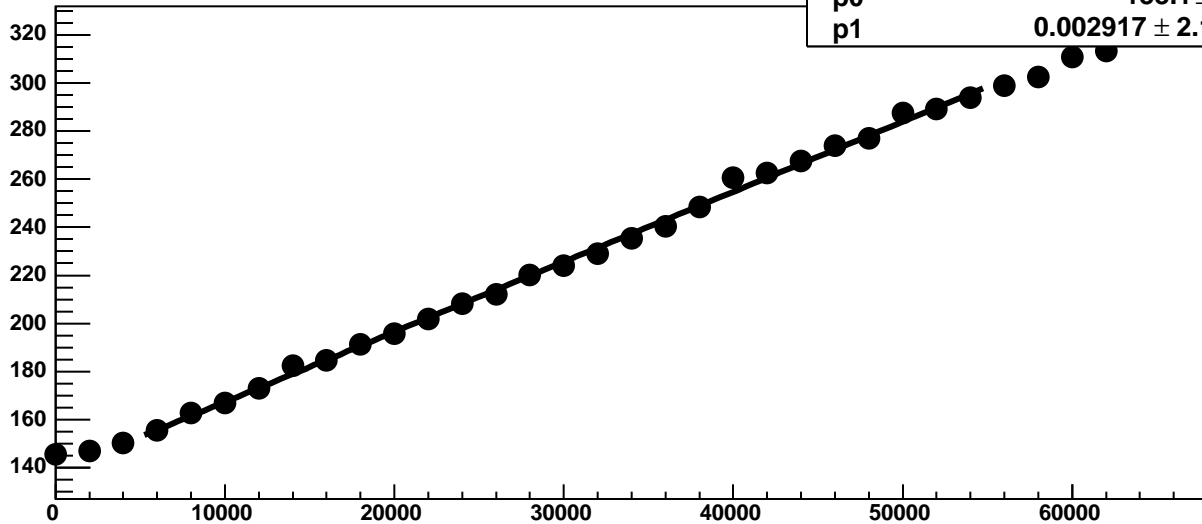
Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



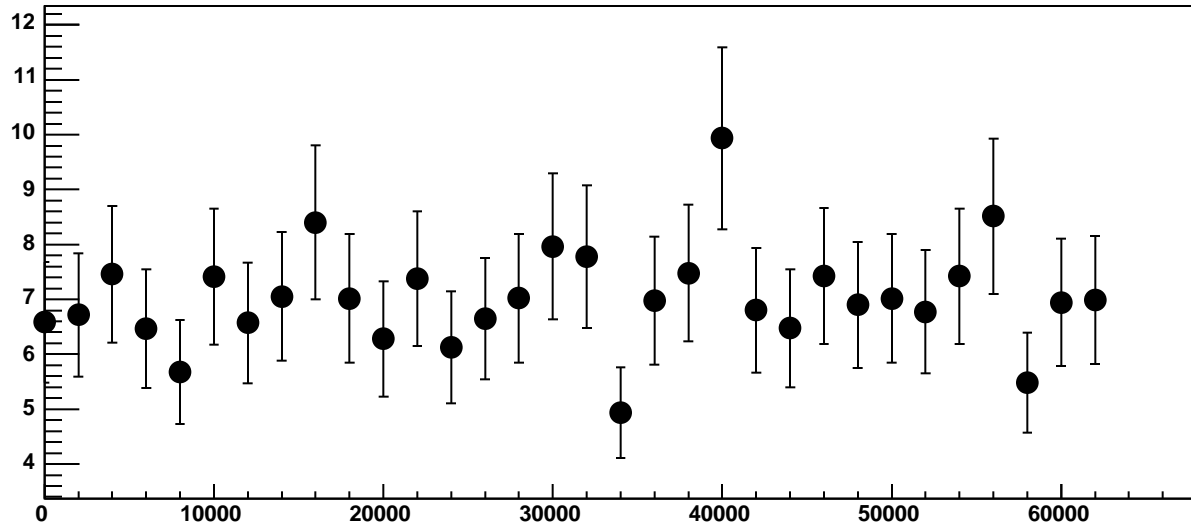
Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



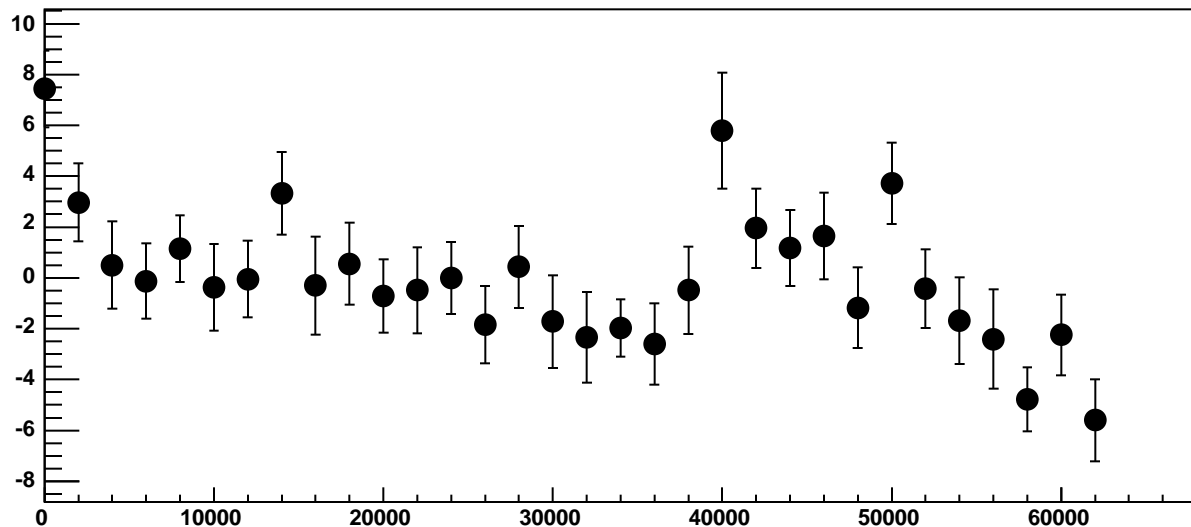
Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



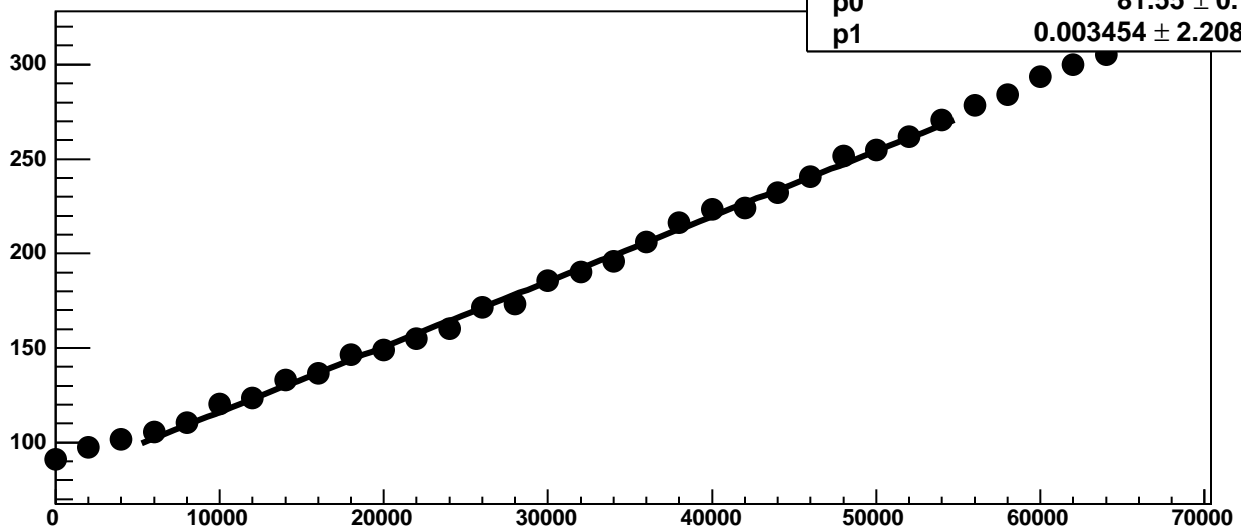
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

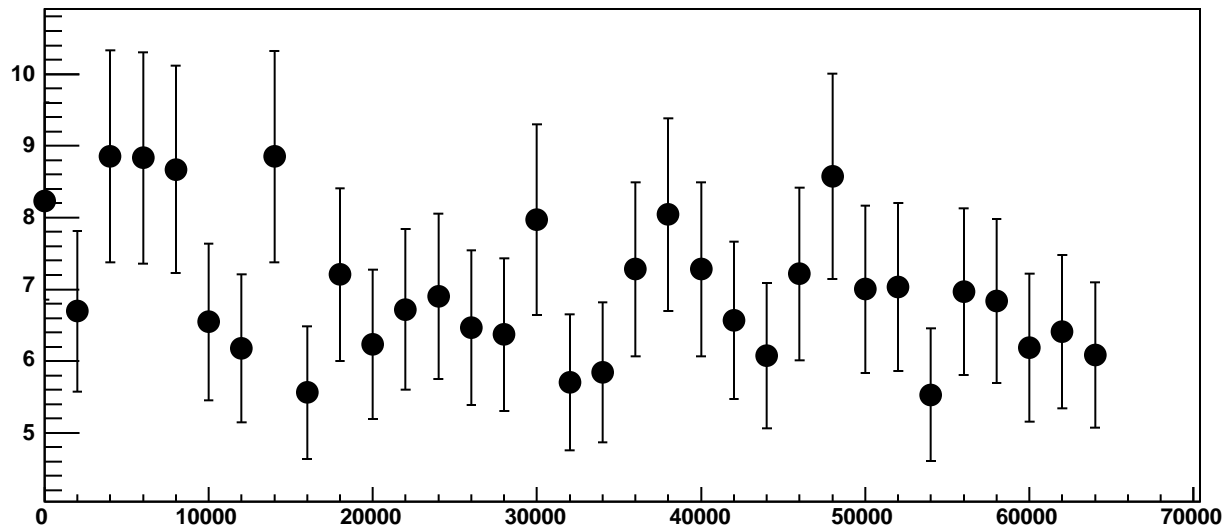


Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

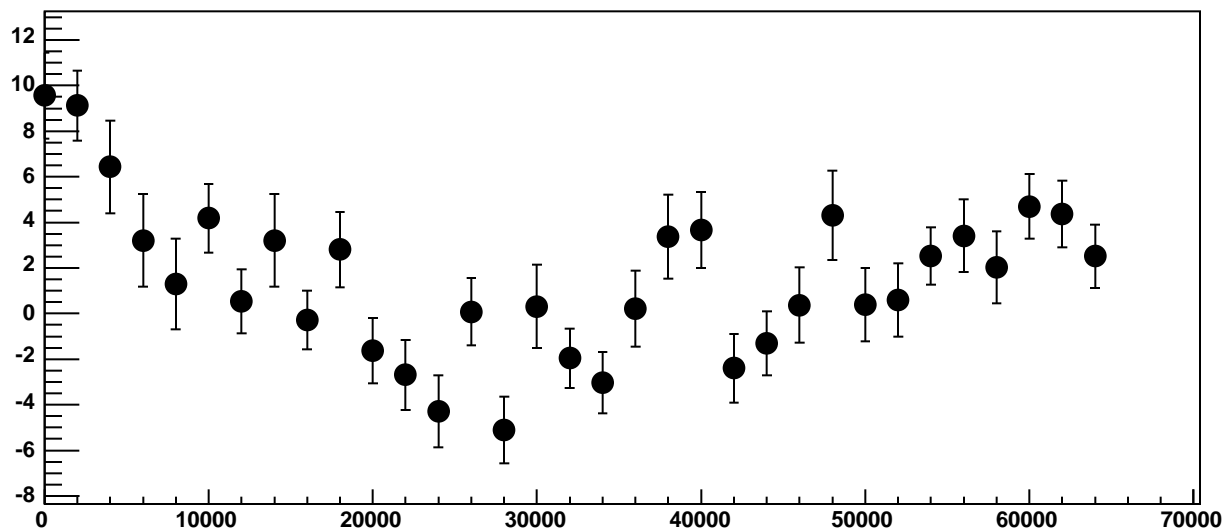


χ^2 / ndf 67.96 / 23
p0 81.55 ± 0.7437
p1 $0.003454 \pm 2.208e-05$

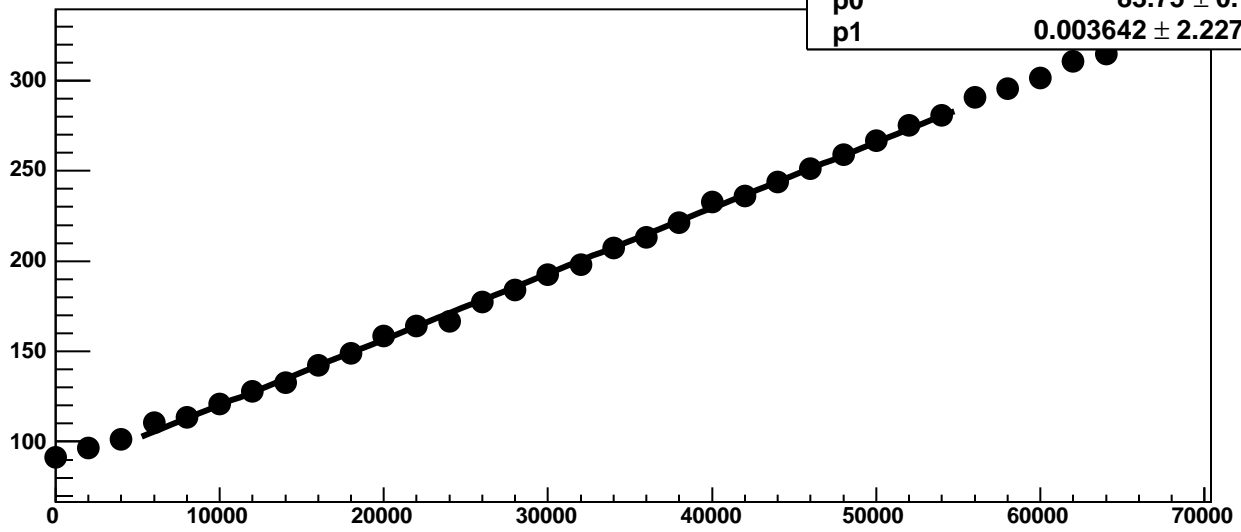
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

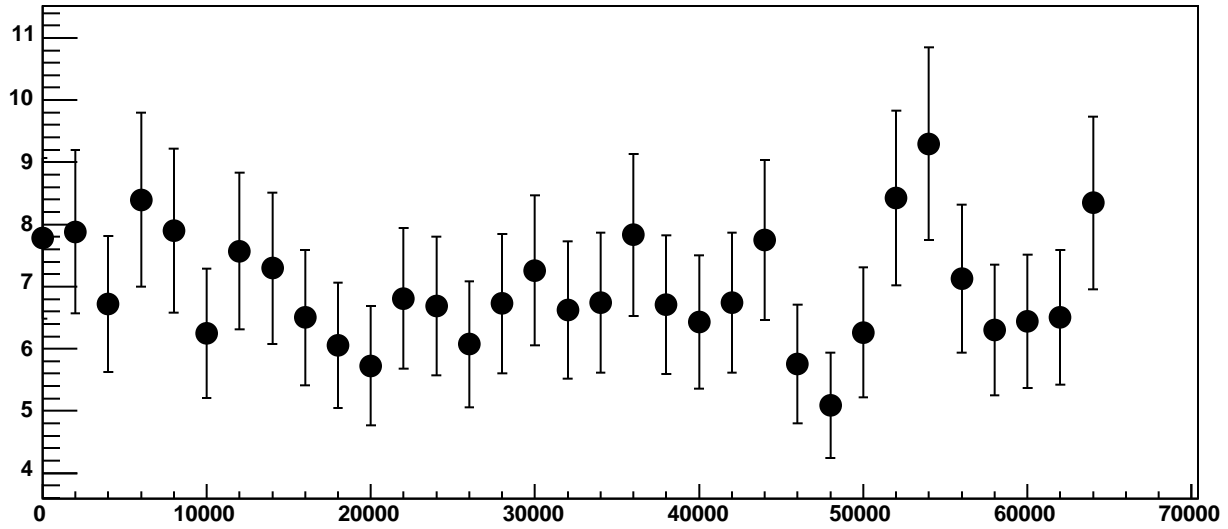


Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

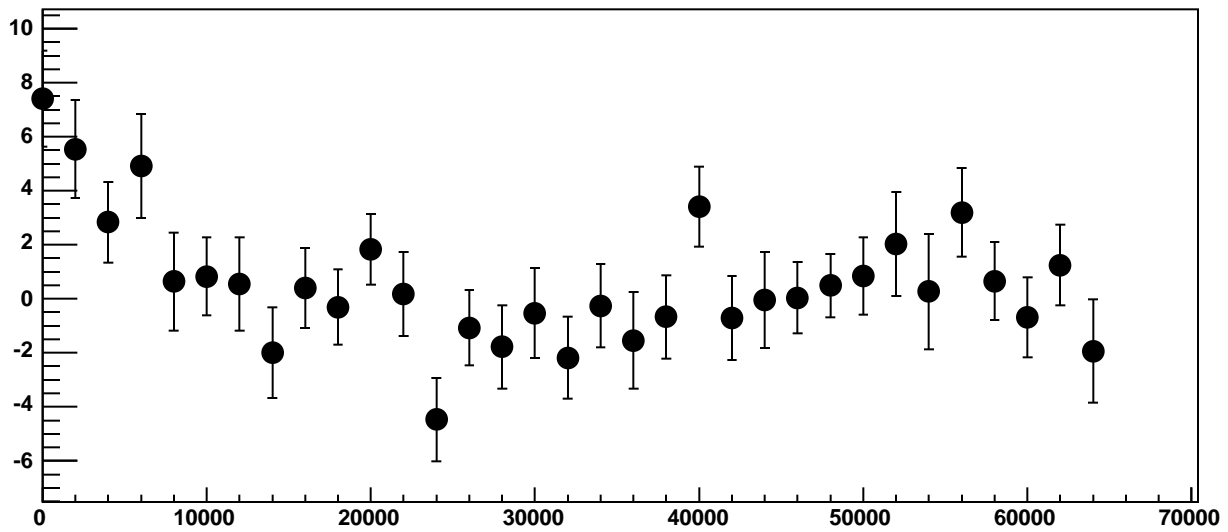


χ^2 / ndf 31.34 / 23
p0 83.75 ± 0.7427
p1 $0.003642 \pm 2.227\text{e-}05$

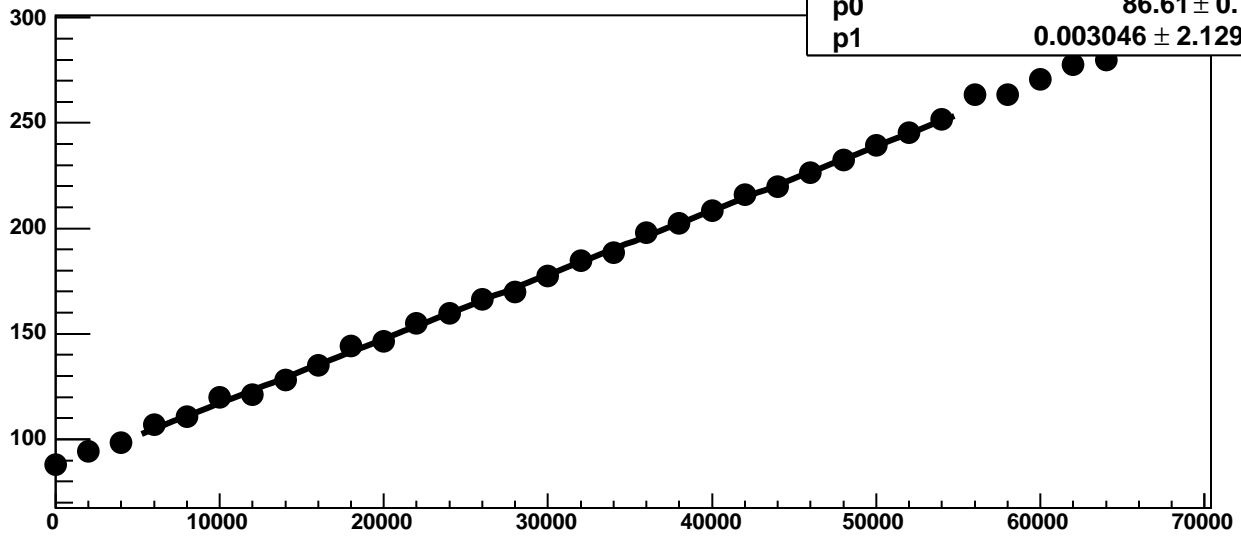
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



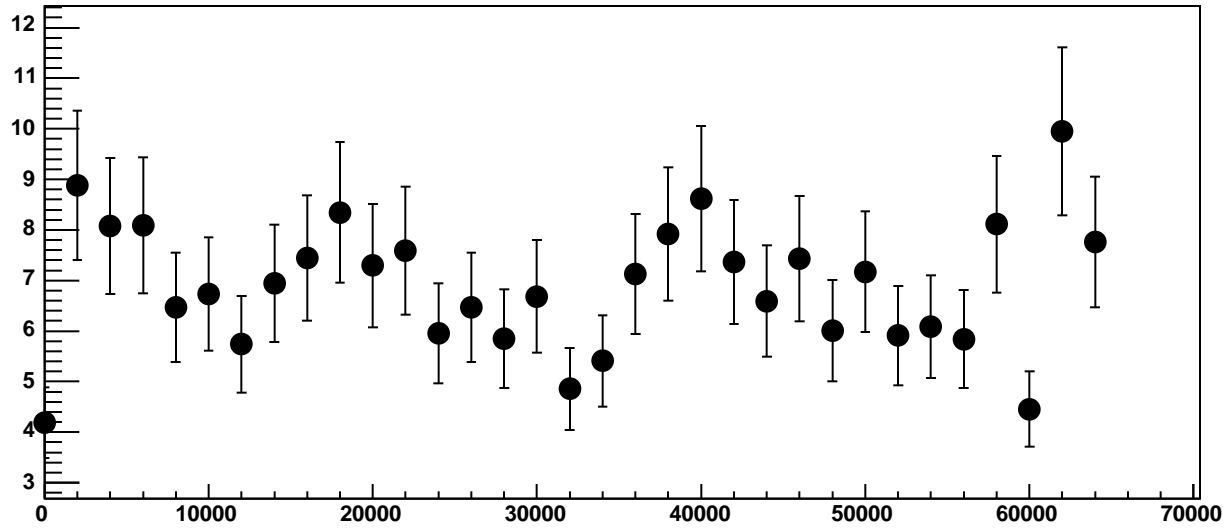
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



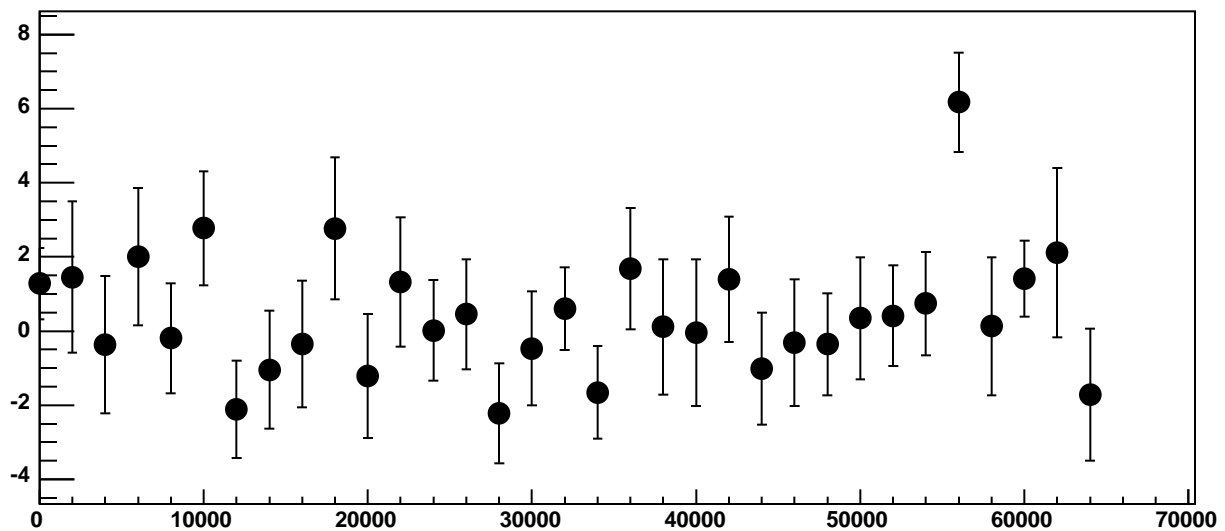
Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



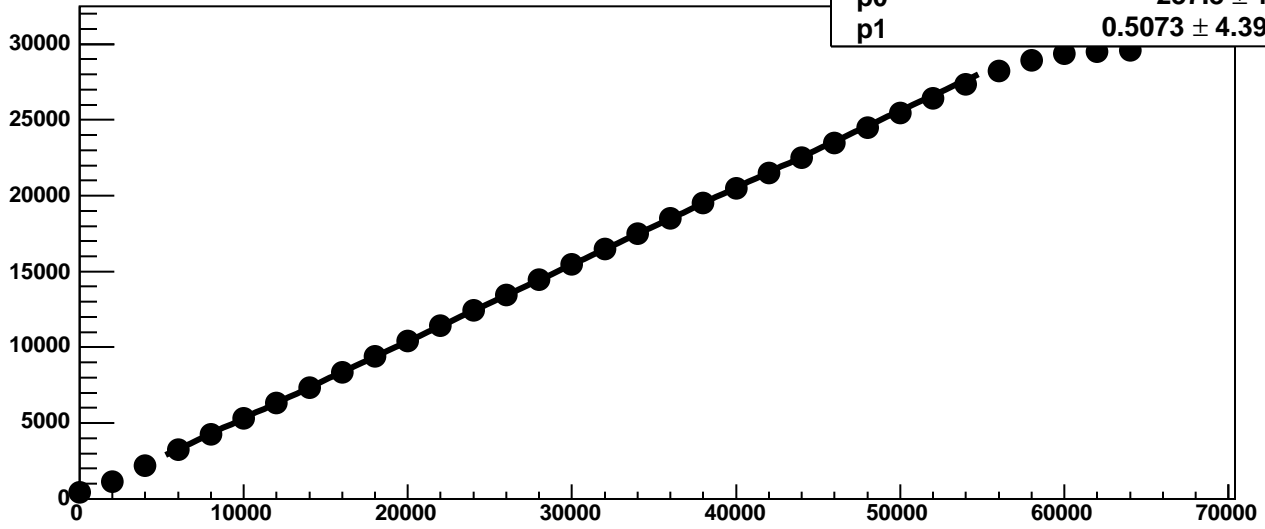
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



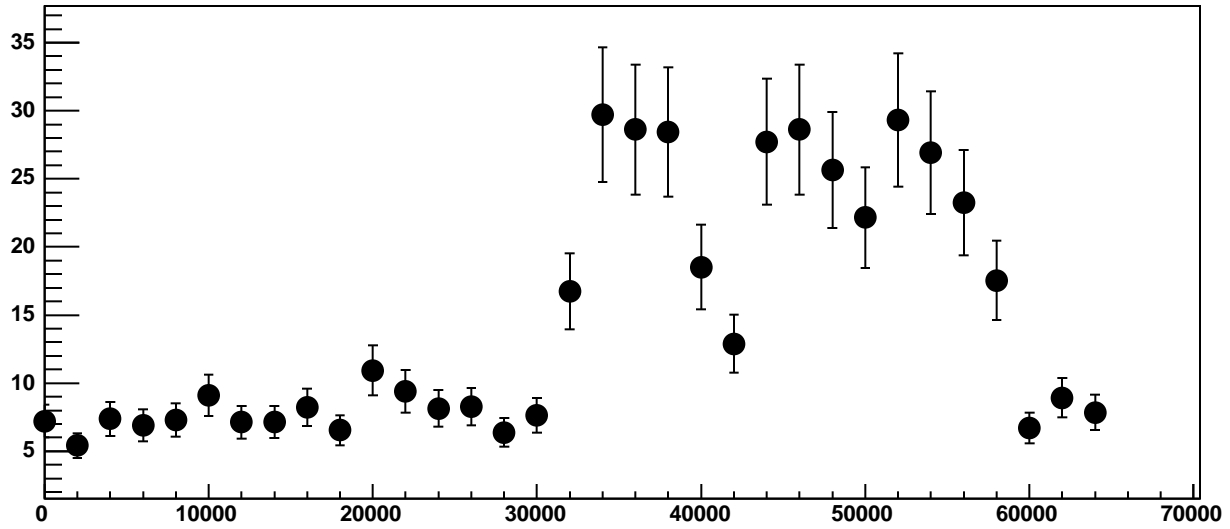
Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



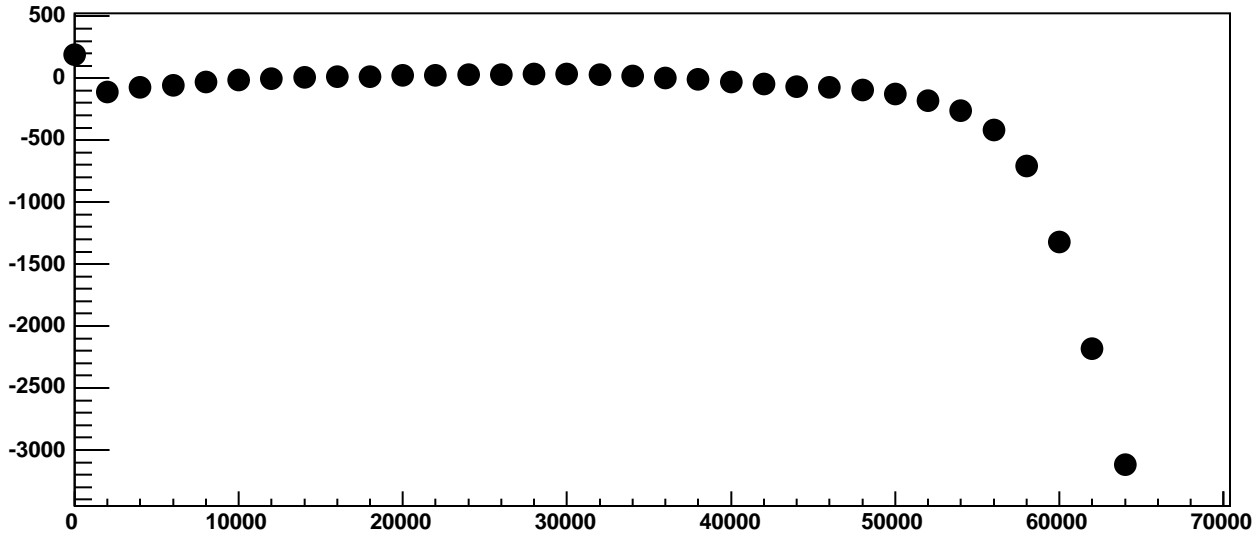
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



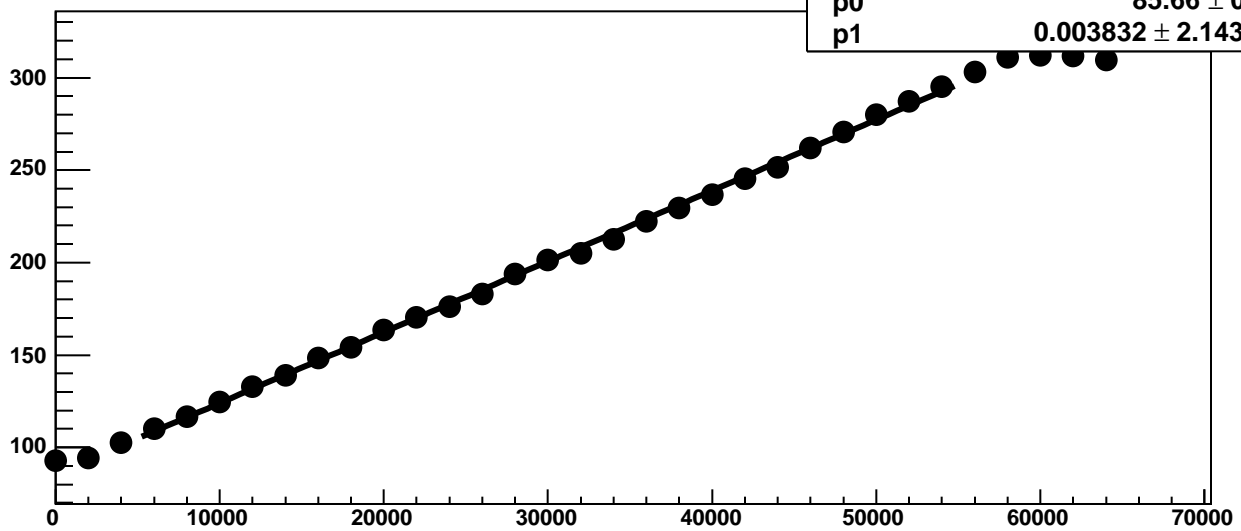
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

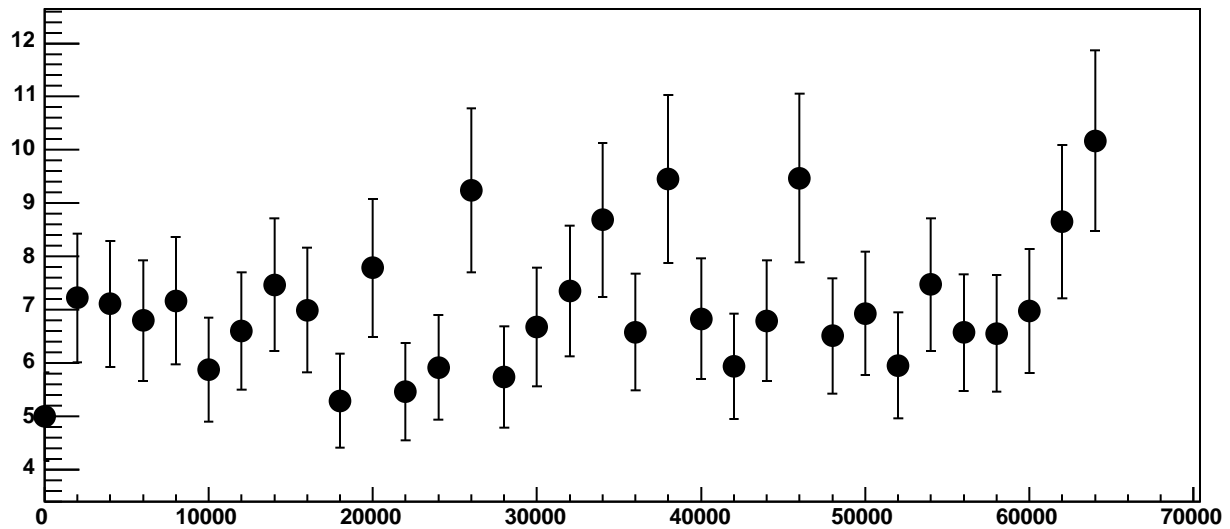


Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

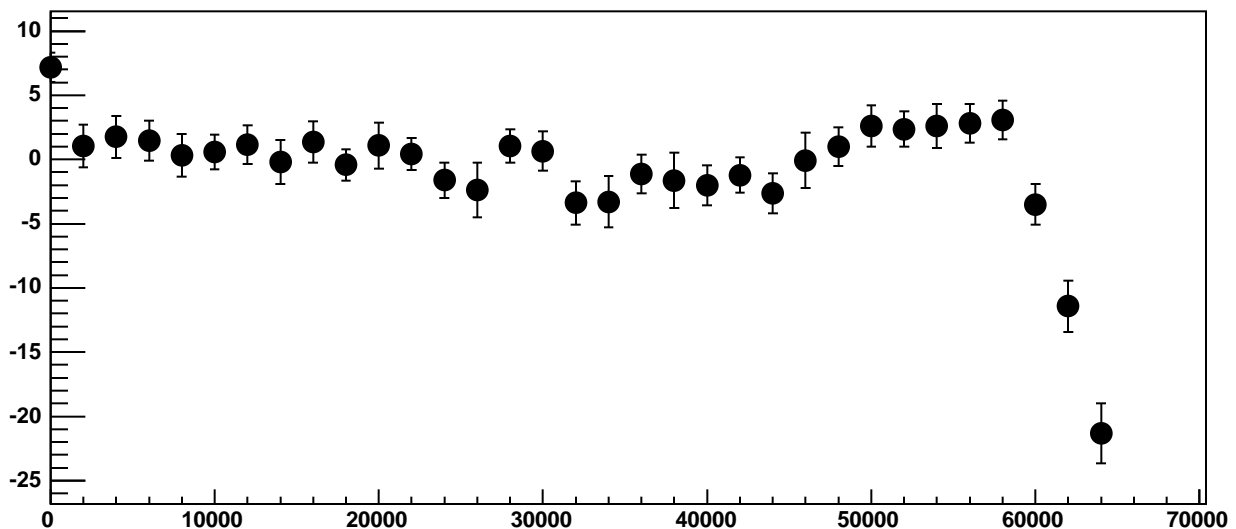


χ^2 / ndf 28.1 / 23
p0 85.66 ± 0.699
p1 $0.003832 \pm 2.143\text{e-}05$

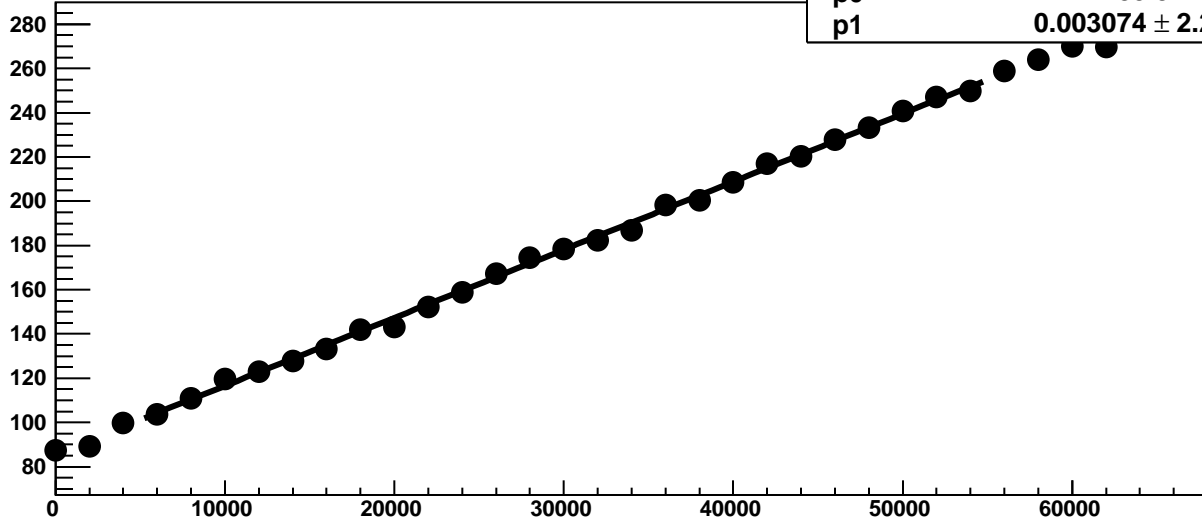
Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

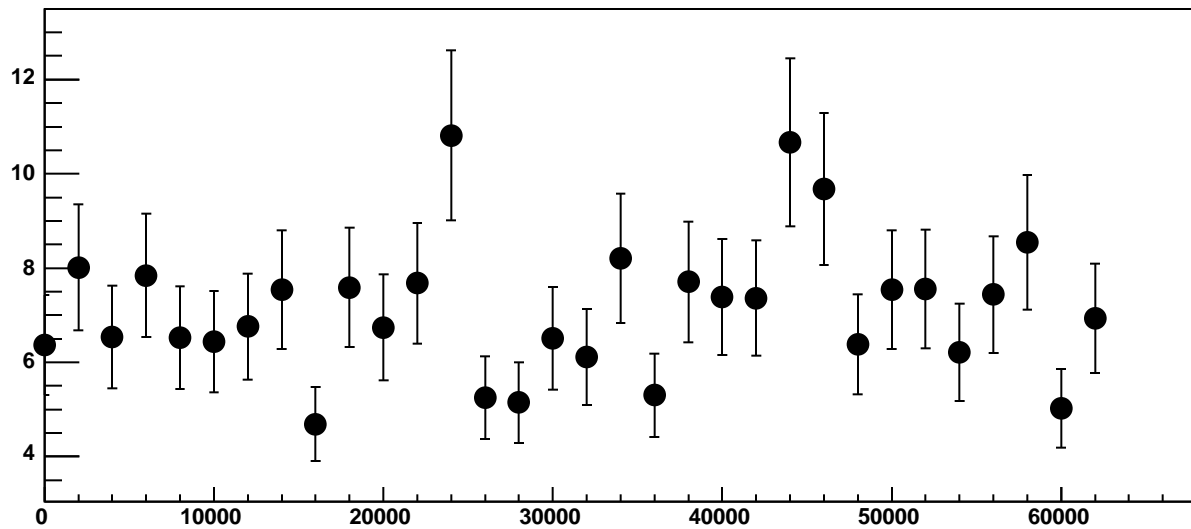


Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

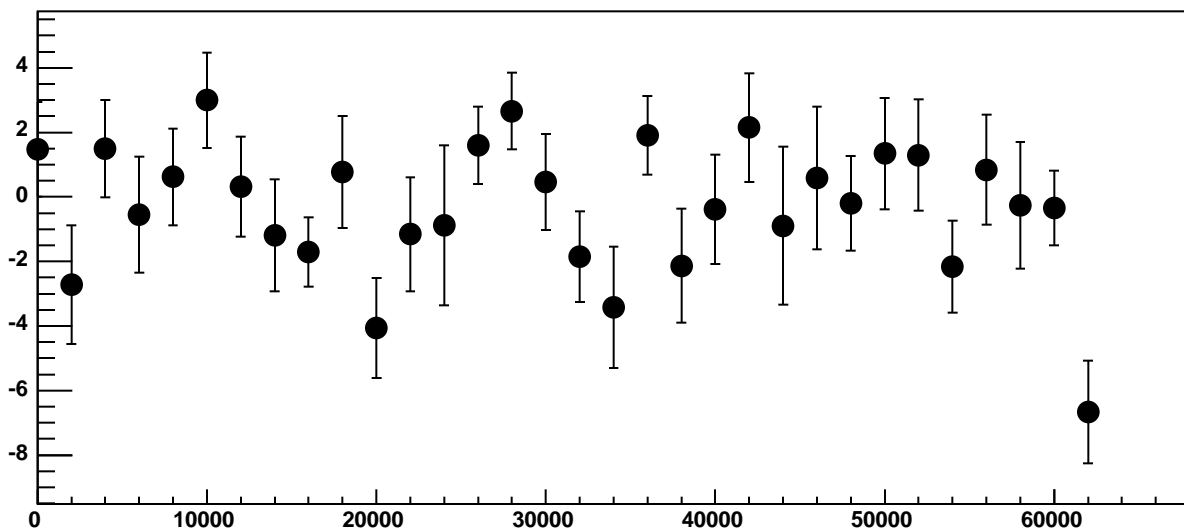


χ^2 / ndf 36.32 / 23
p0 85.84 ± 0.7127
p1 0.003074 ± 2.216e-05

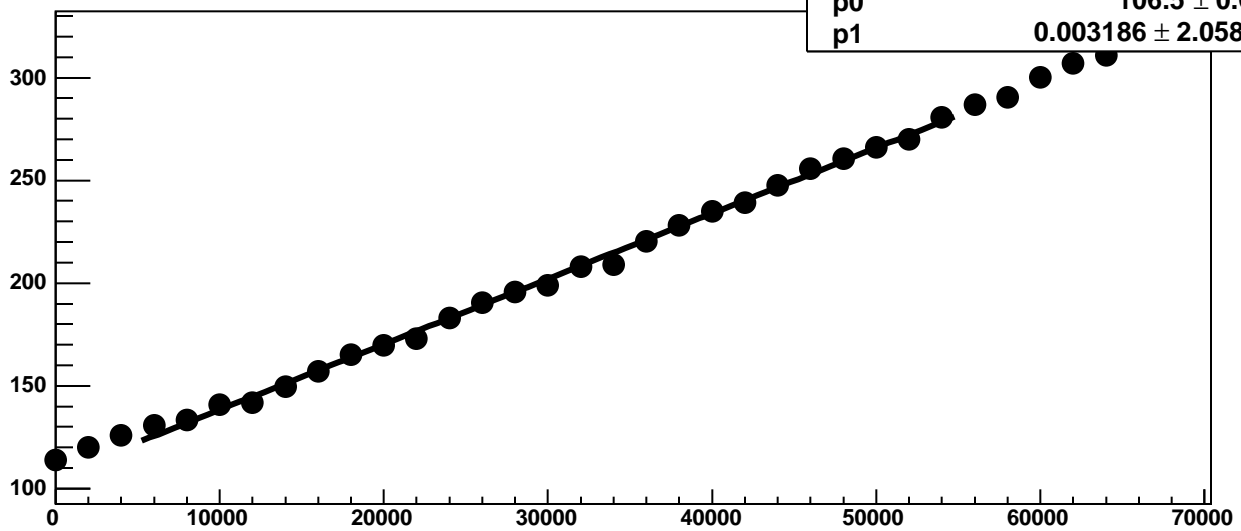
Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

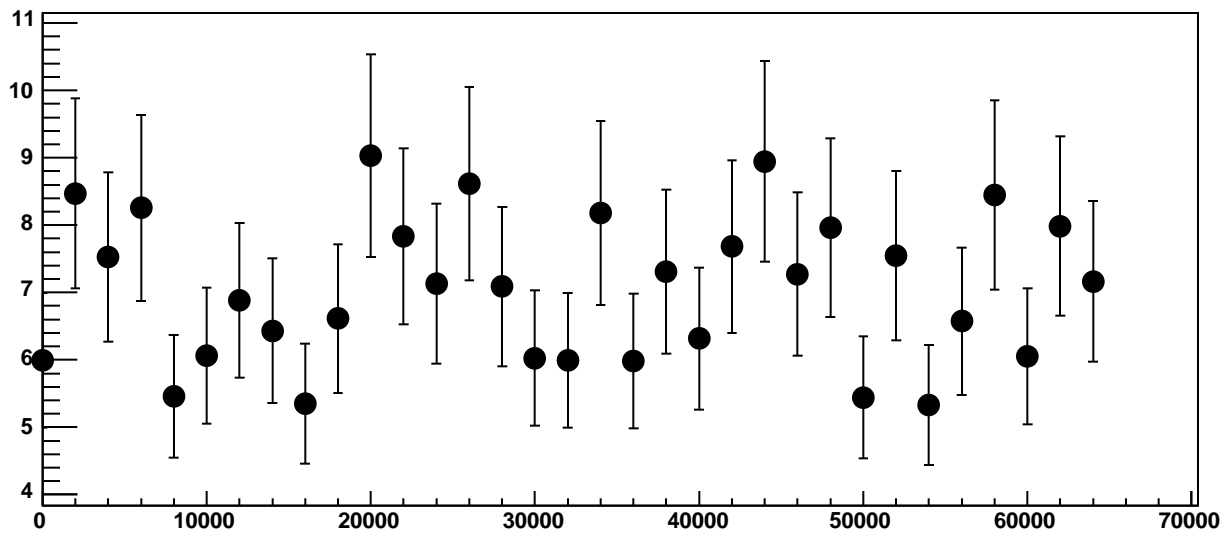


Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

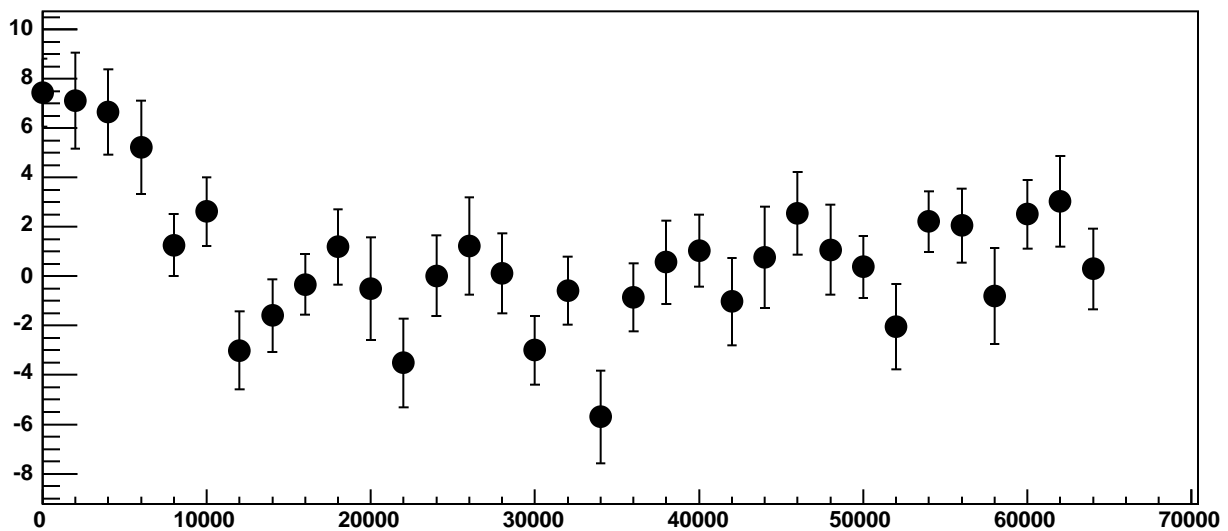


χ^2 / ndf 44.88 / 23
p0 106.5 ± 0.6878
p1 $0.003186 \pm 2.058e-05$

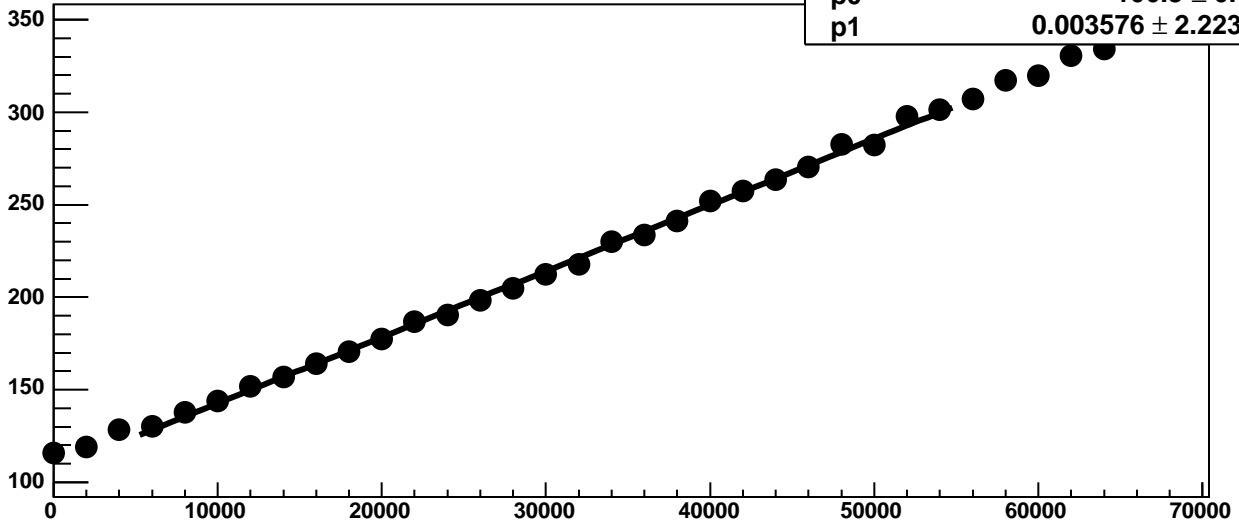
Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



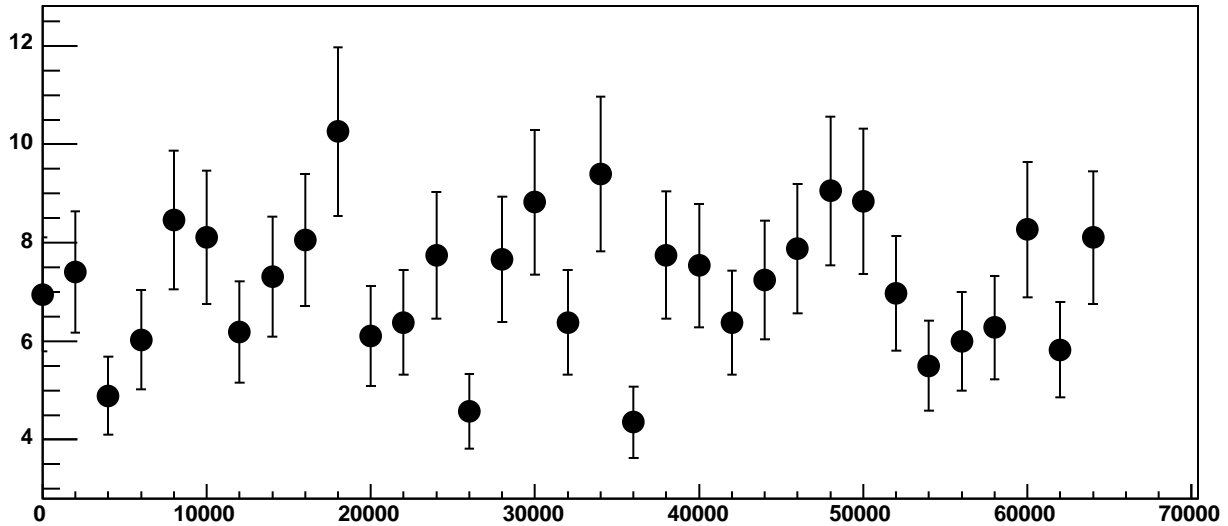
Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



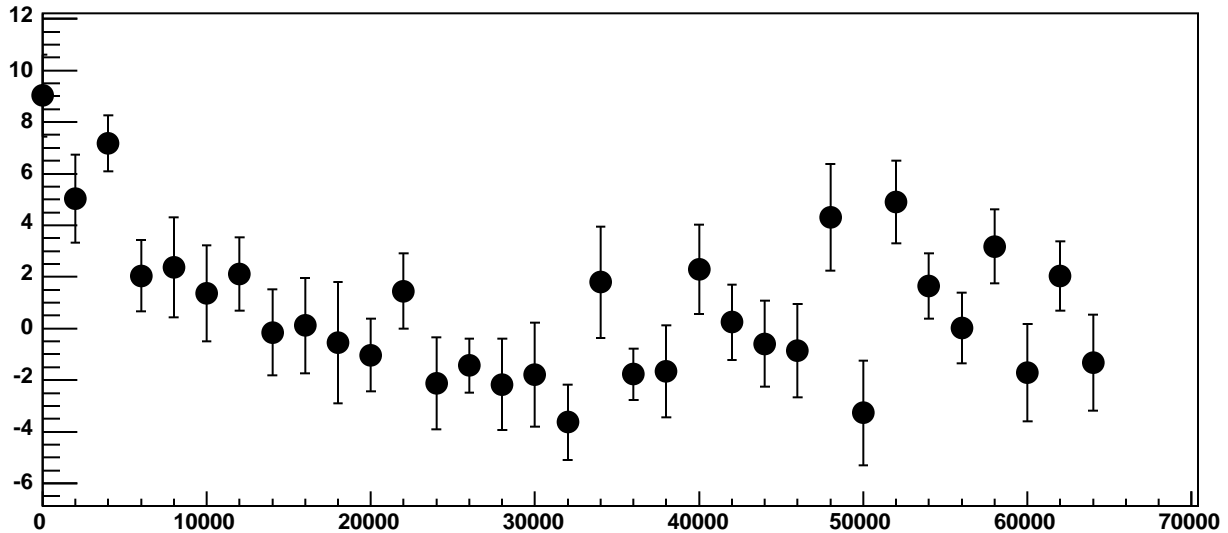
Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



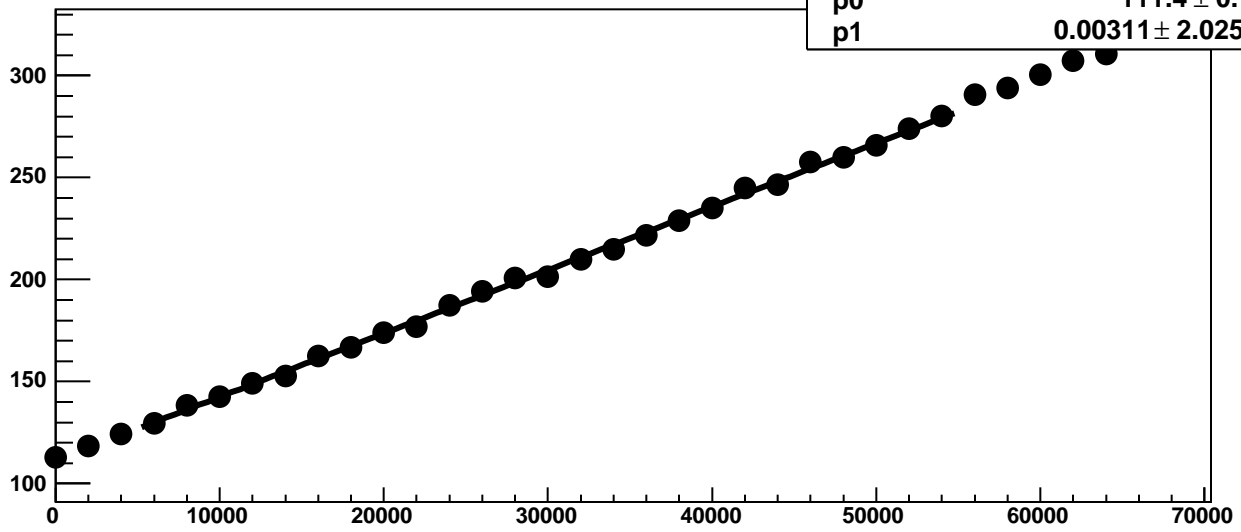
Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

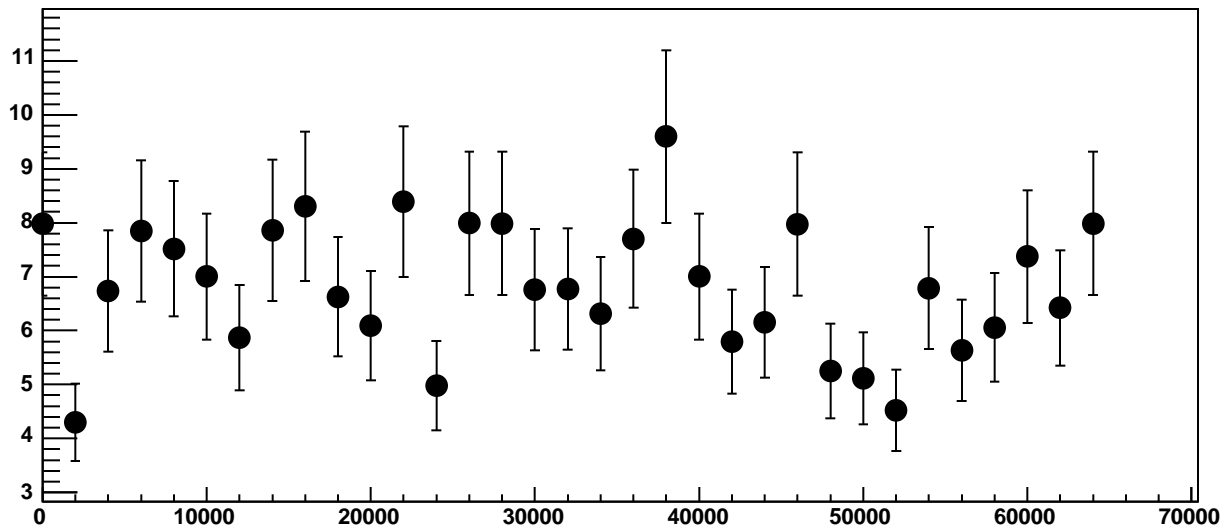


Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

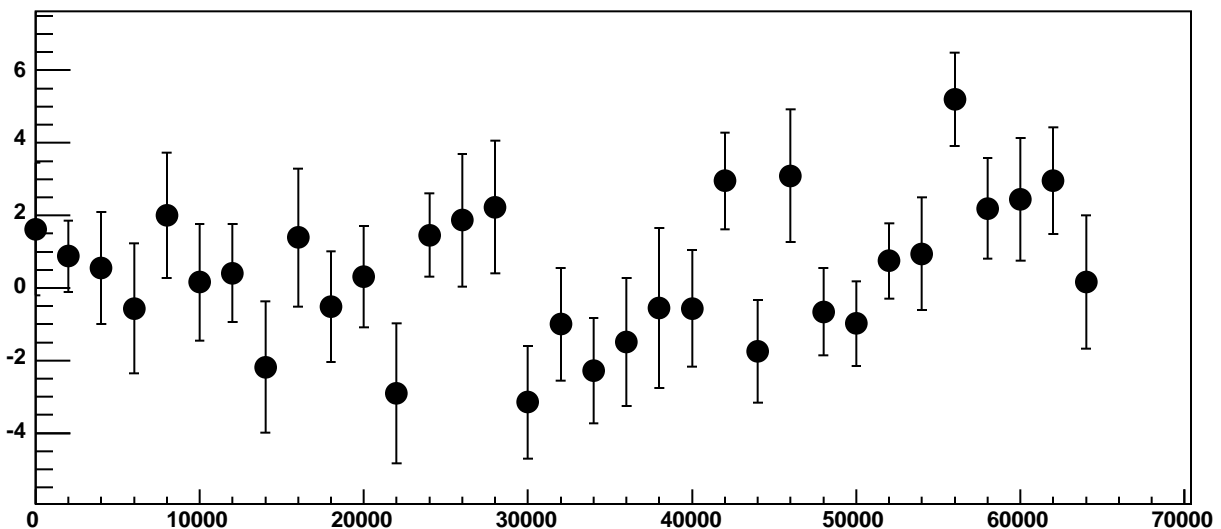


χ^2 / ndf 29.21 / 23
p0 111.4 ± 0.7203
p1 $0.00311 \pm 2.025e-05$

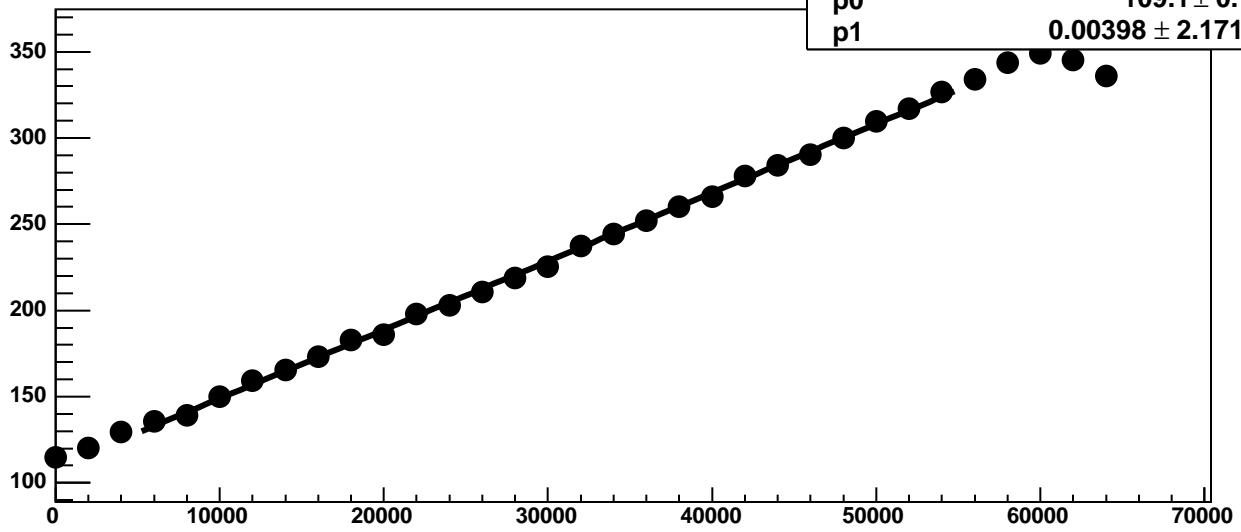
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

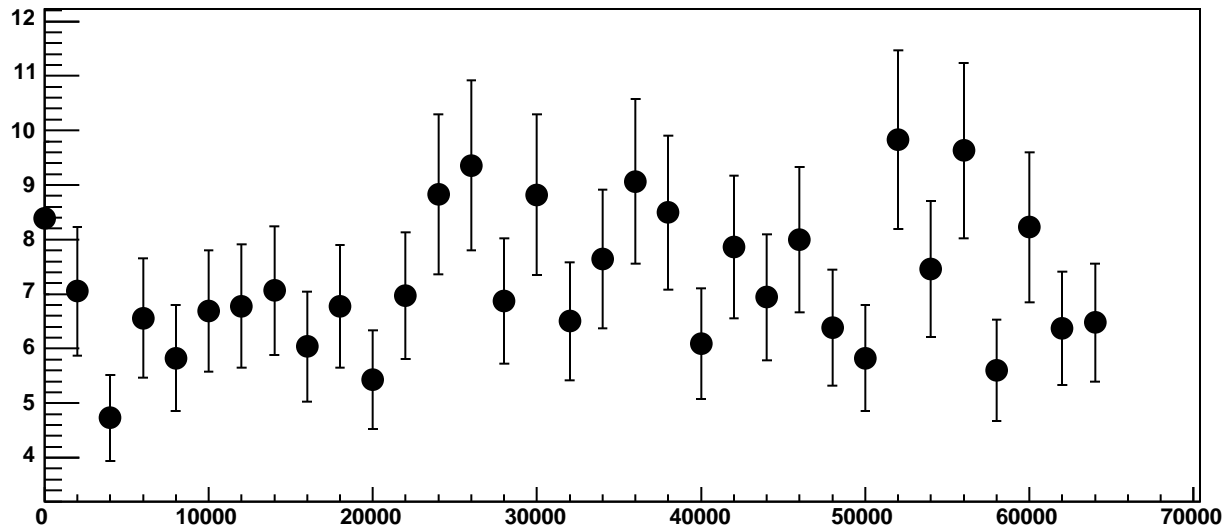


Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

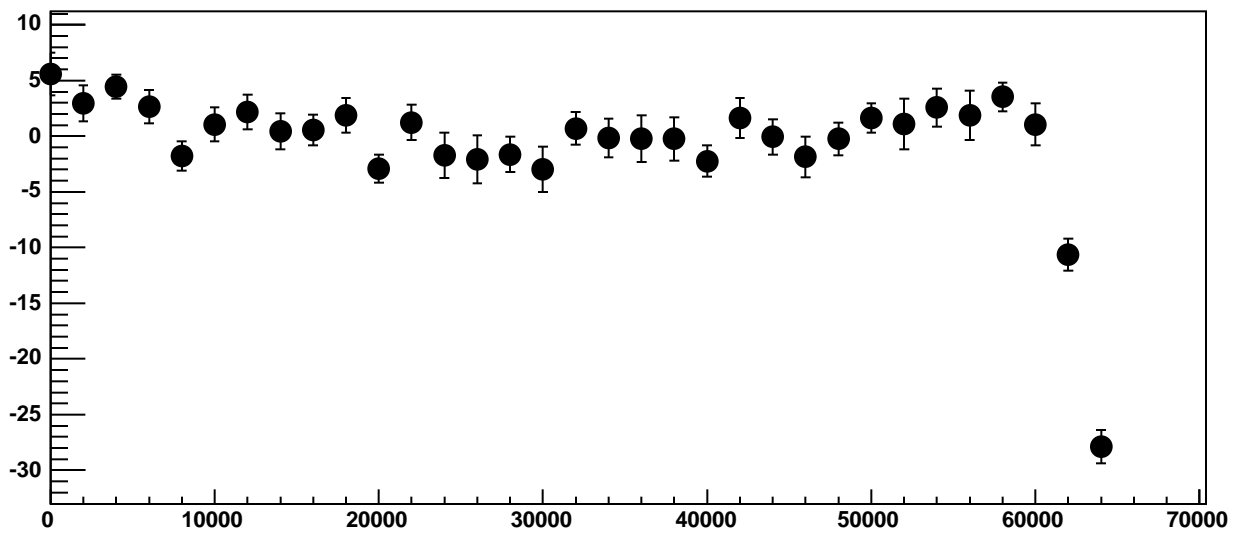


χ^2 / ndf 28.73 / 23
p0 109.1 ± 0.7004
p1 $0.00398 \pm 2.171e-05$

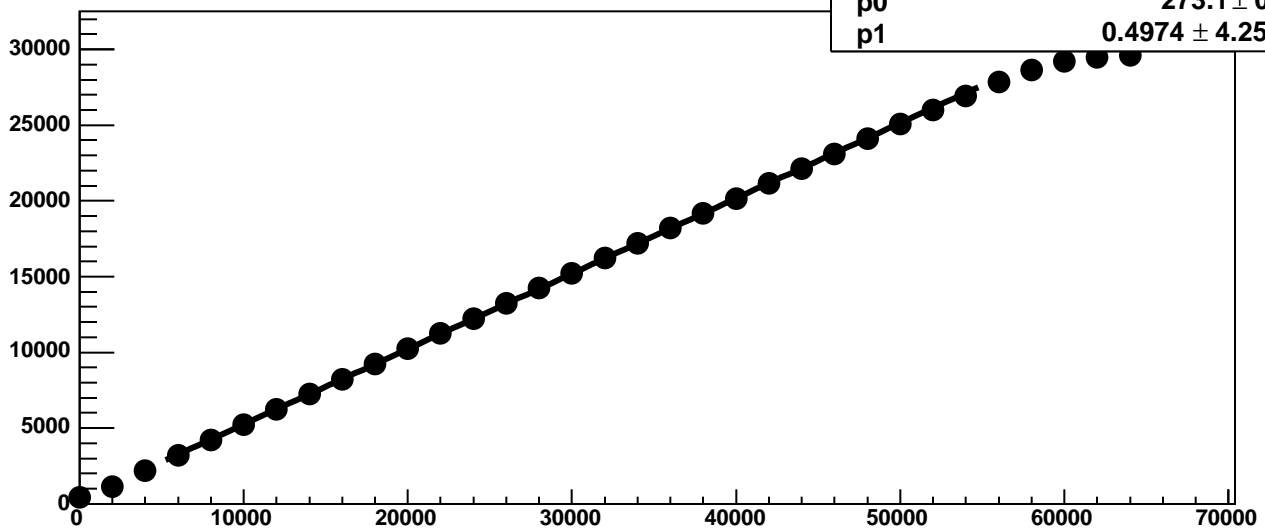
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



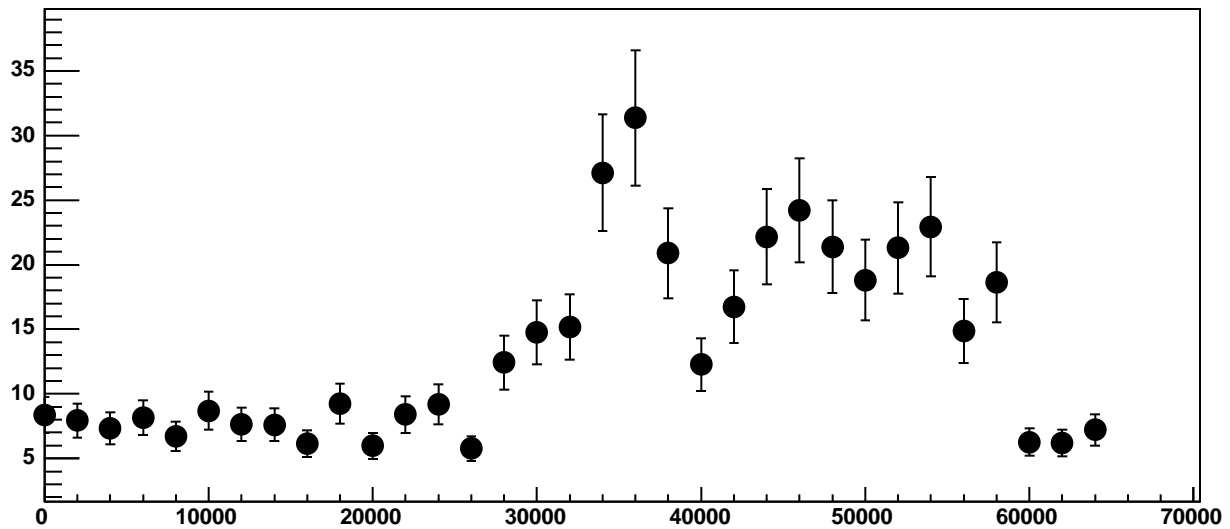
Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



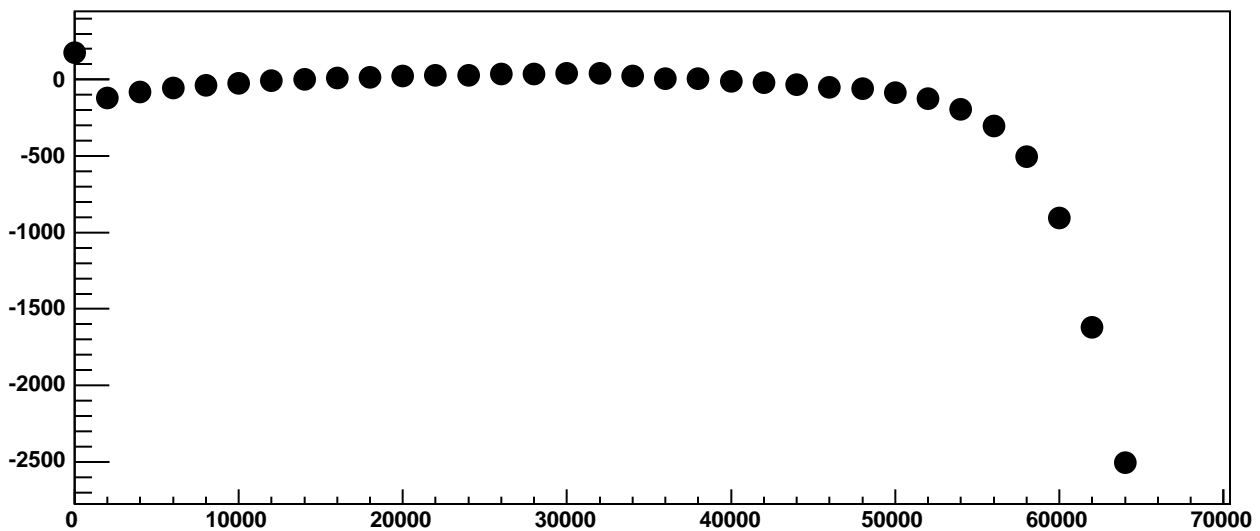
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



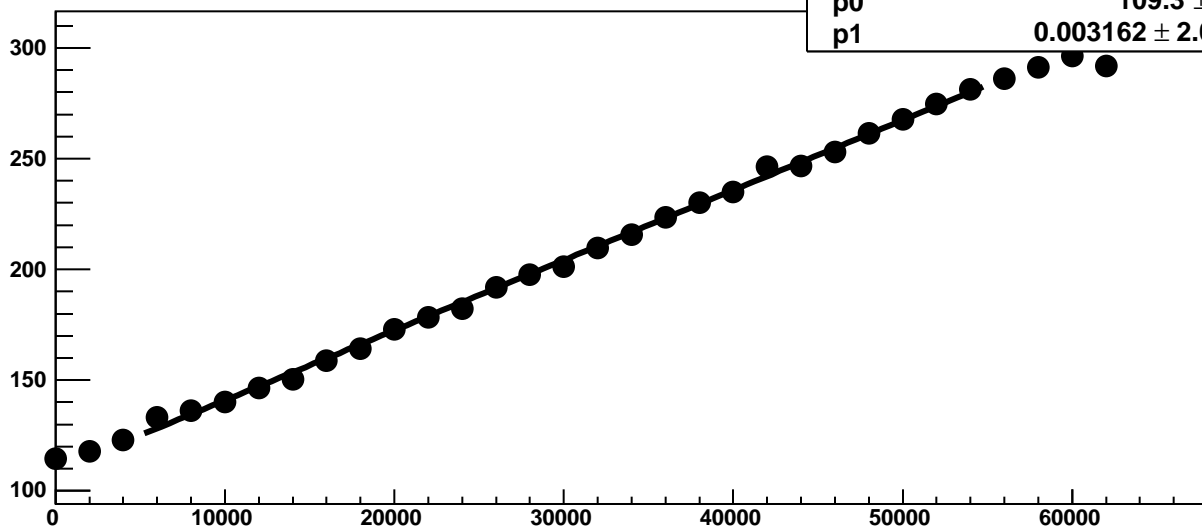
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

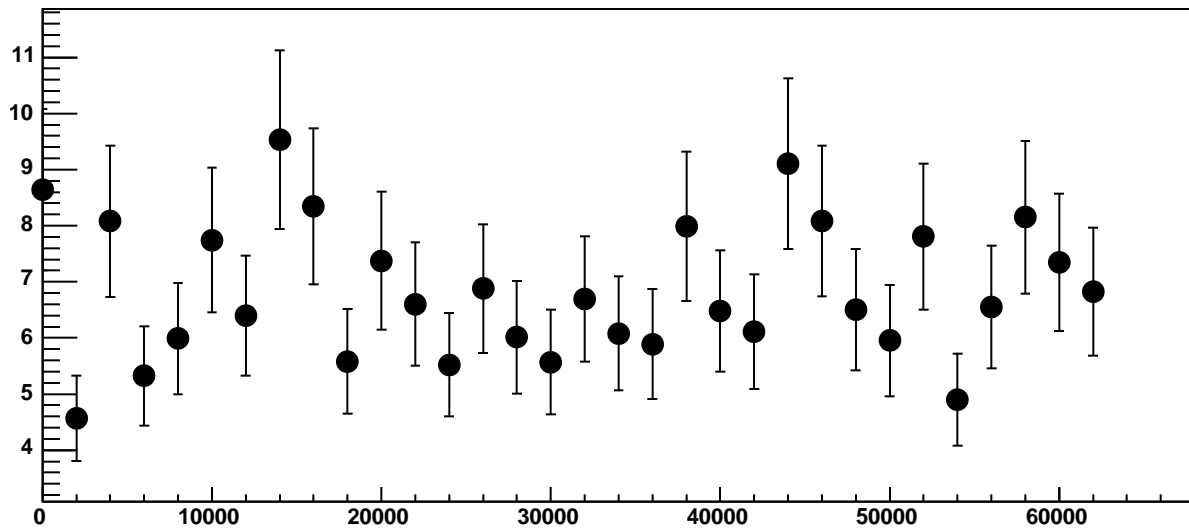


Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

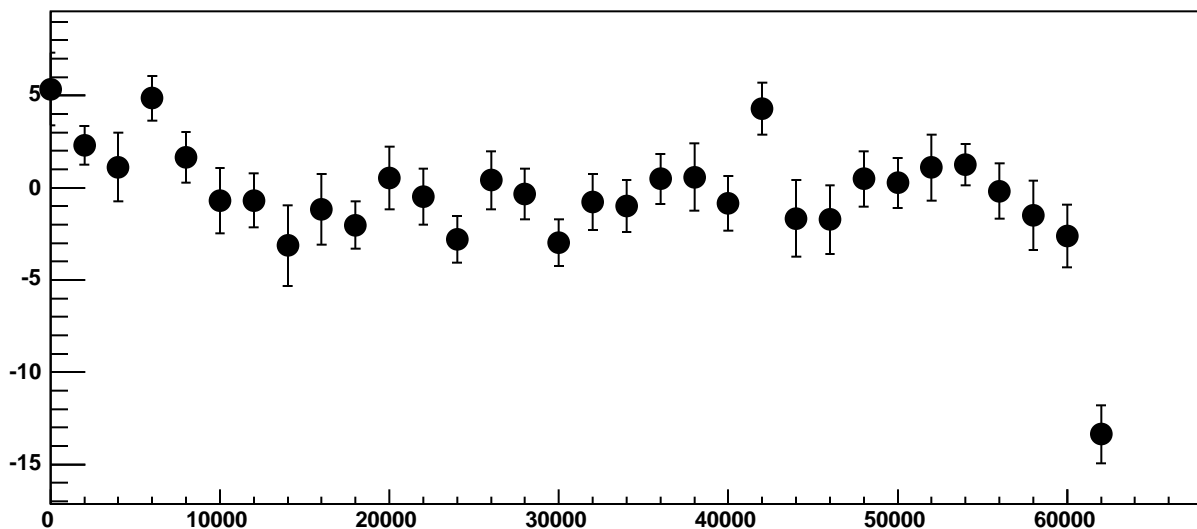


χ^2 / ndf 47.09 / 23
p0 109.3 ± 0.6756
p1 $0.003162 \pm 2.015e-05$

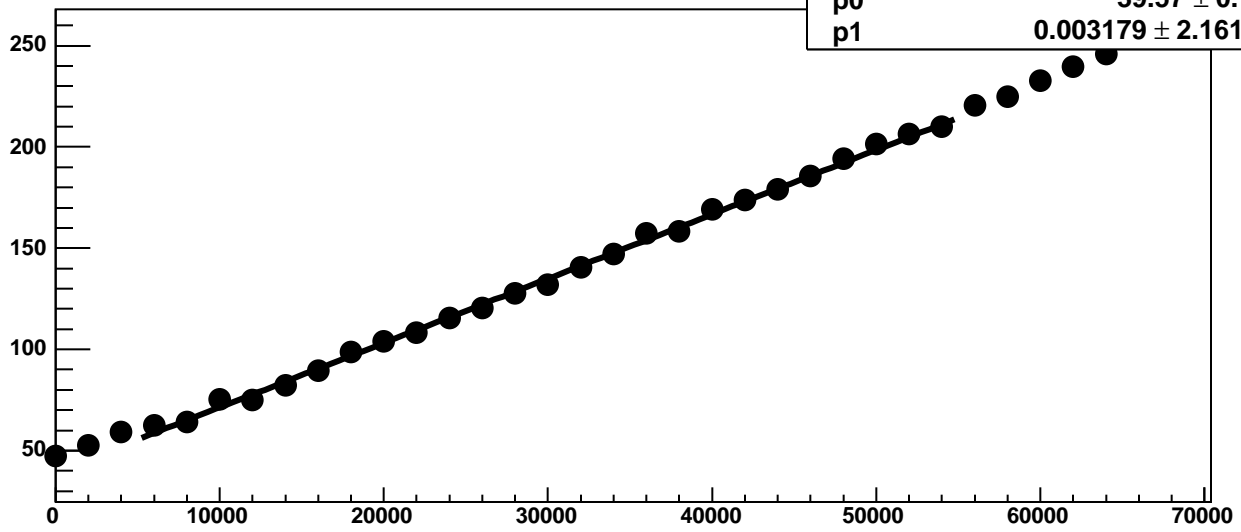
Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

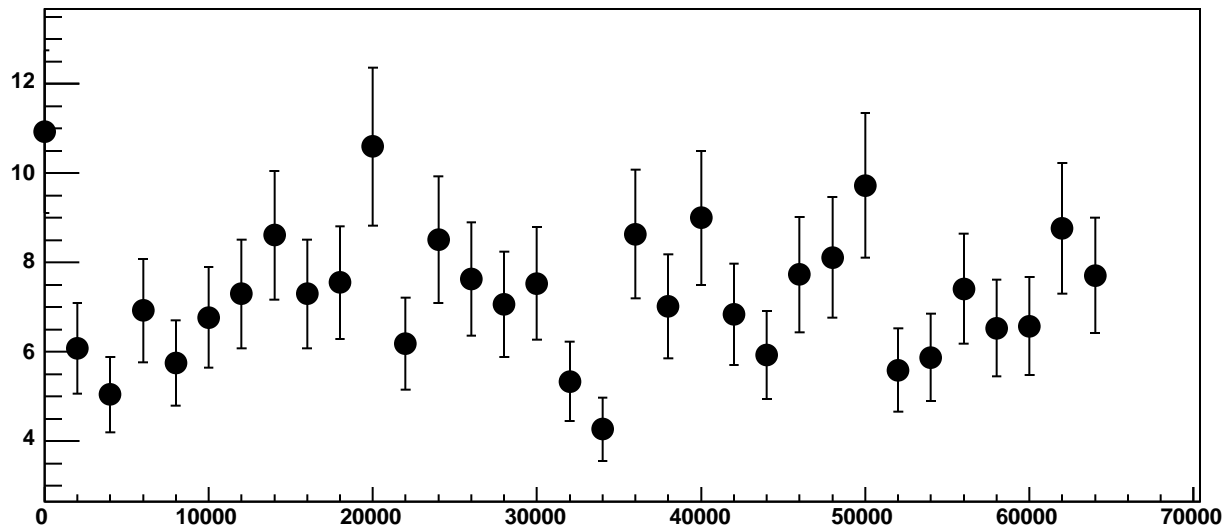


Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

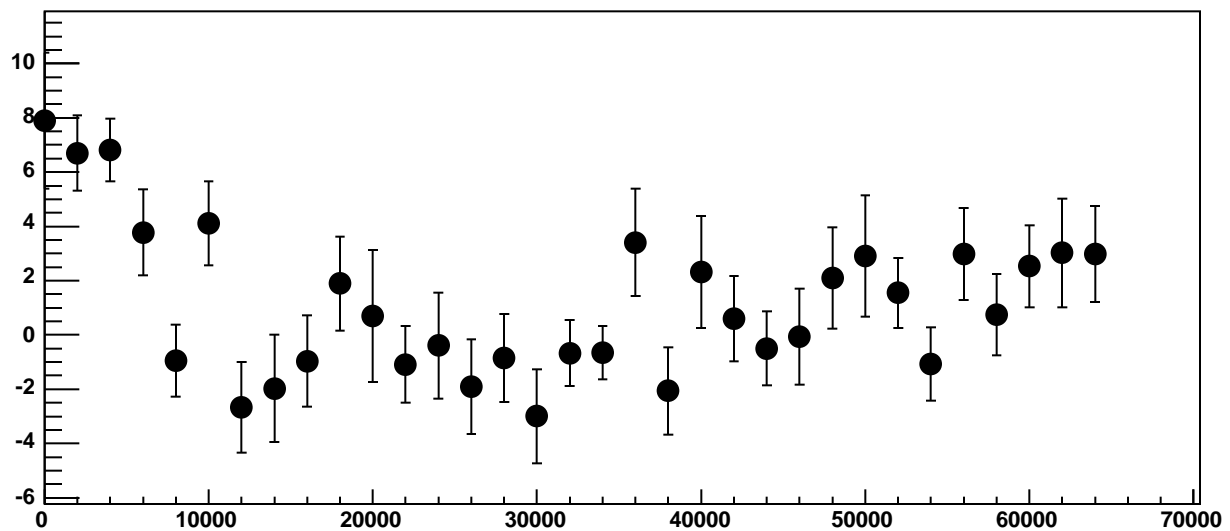


χ^2 / ndf 35.35 / 23
p0 39.57 ± 0.7329
p1 $0.003179 \pm 2.161e-05$

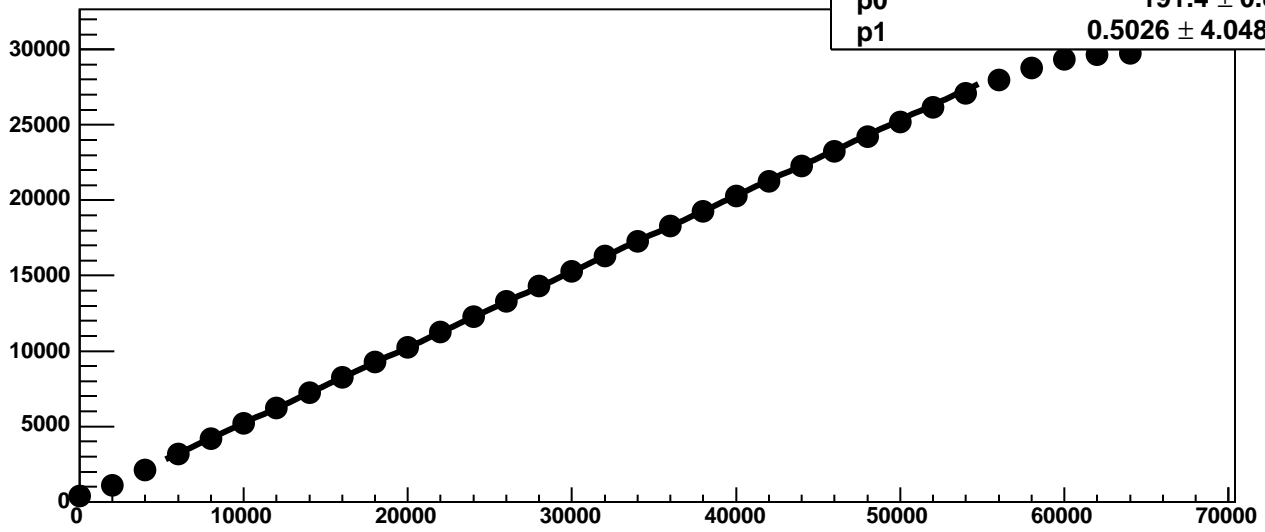
Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



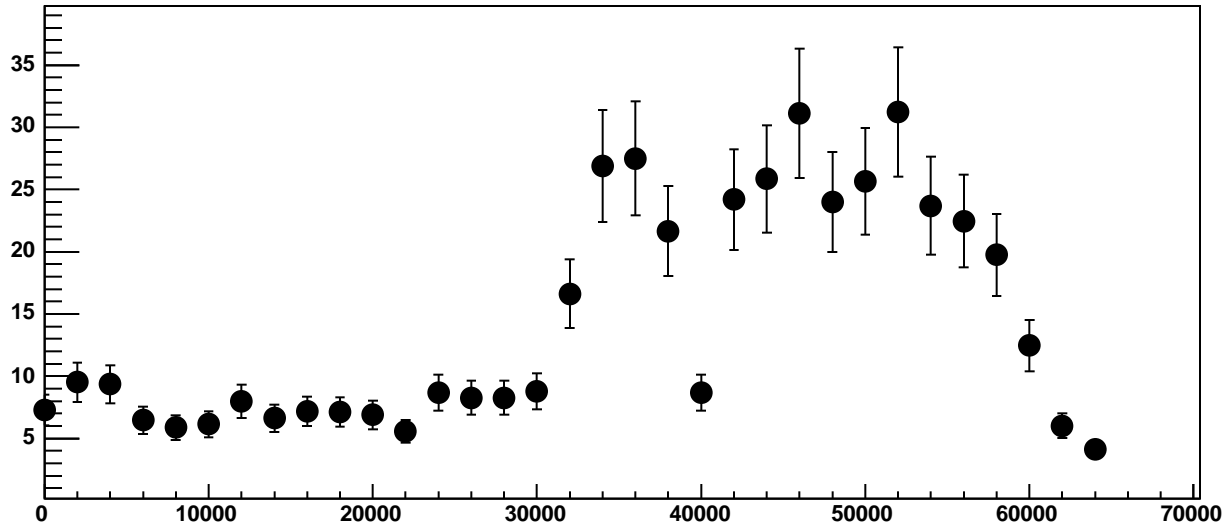
Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



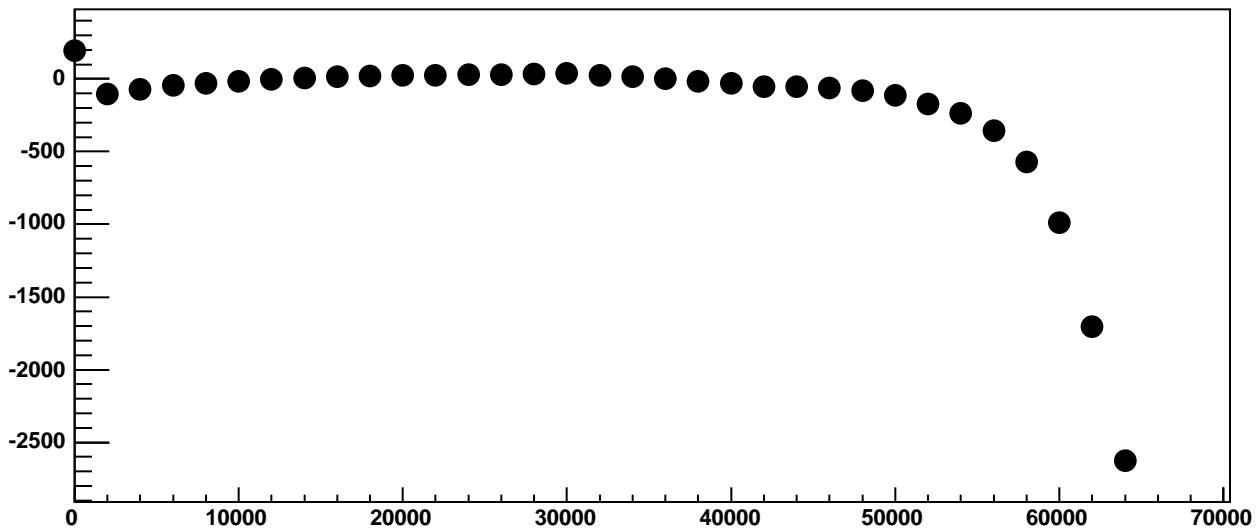
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



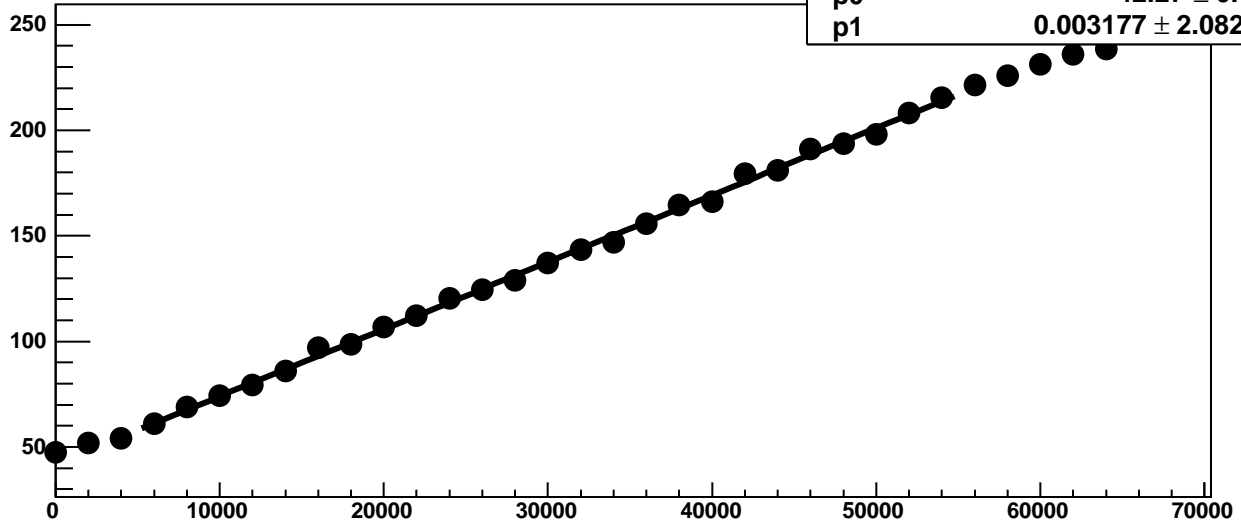
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

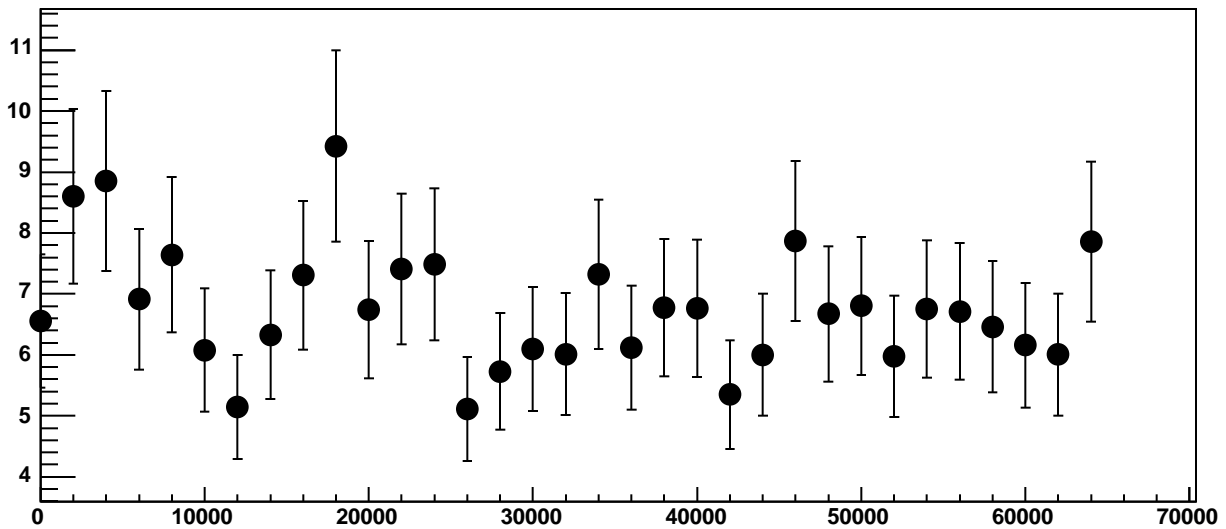


Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

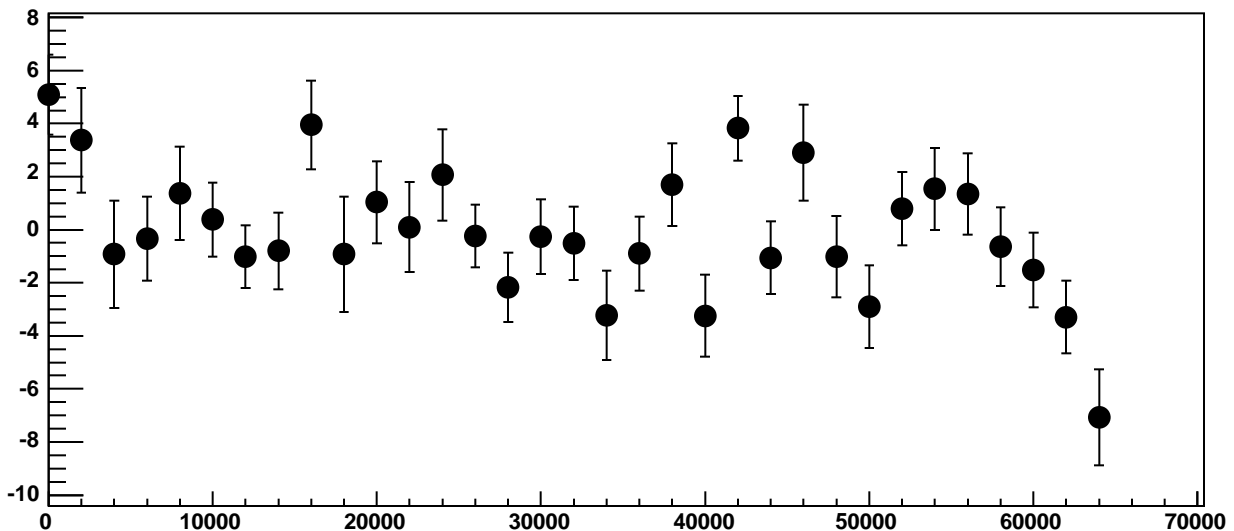


χ^2 / ndf 40.17 / 23
p0 42.27 ± 0.6955
p1 $0.003177 \pm 2.082e-05$

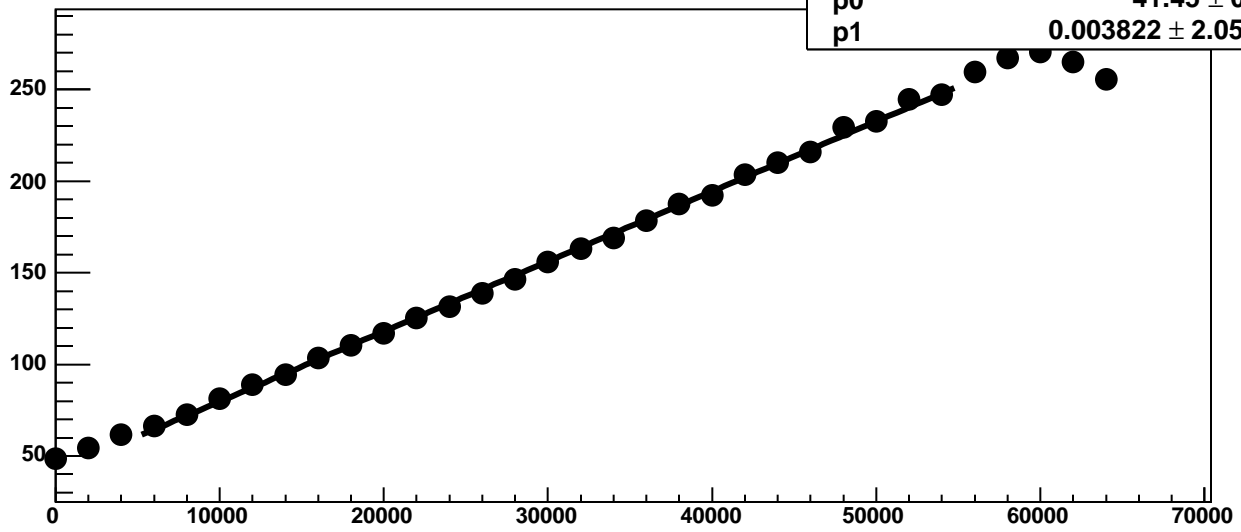
Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

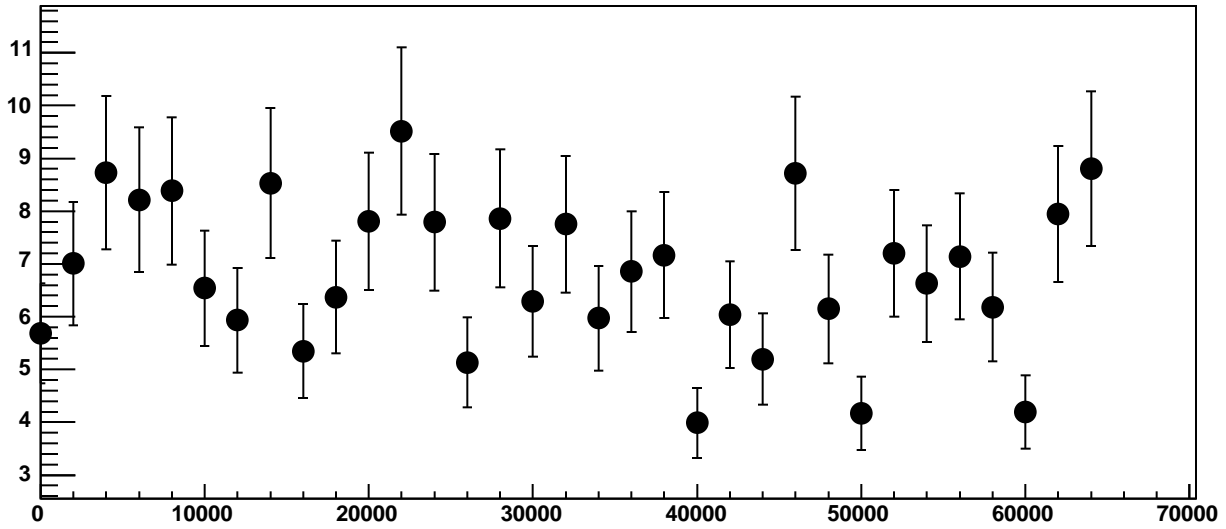


Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

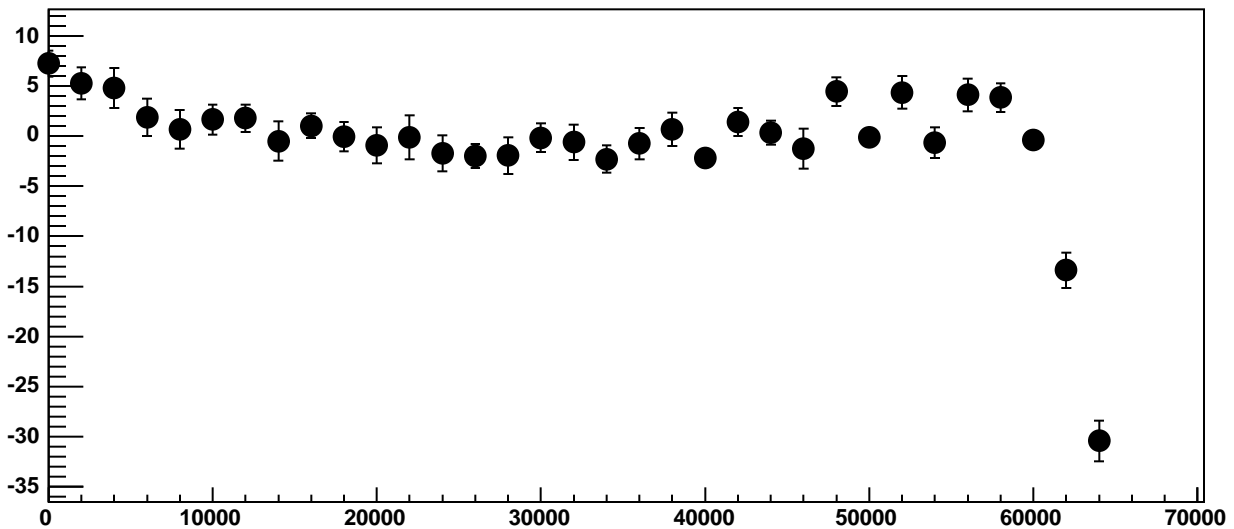


χ^2 / ndf 37.67 / 23
p0 41.45 ± 0.725
p1 $0.003822 \pm 2.05e-05$

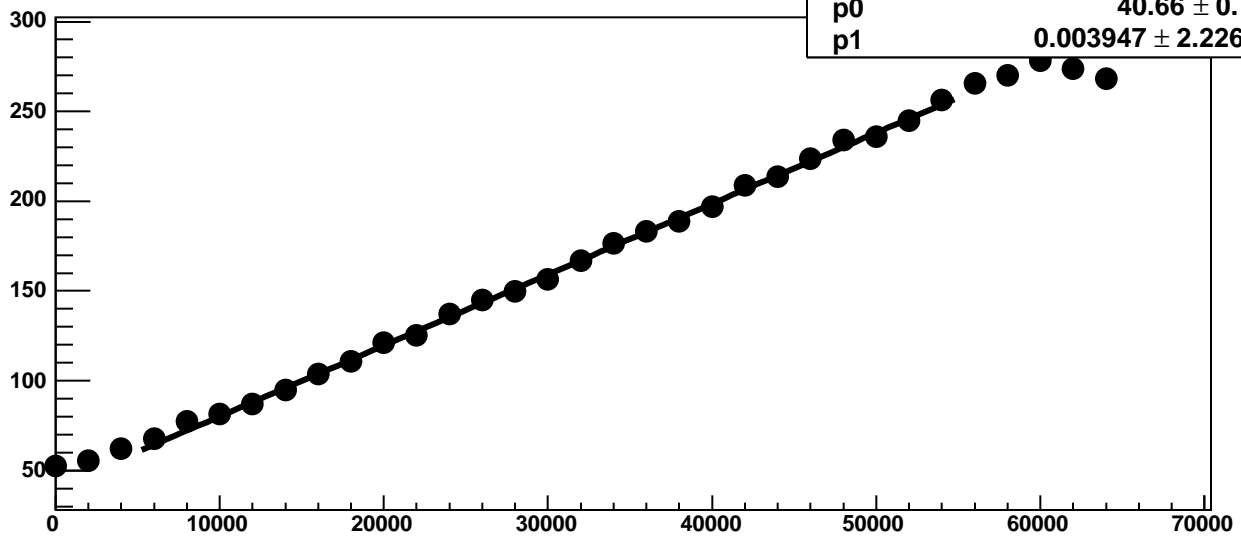
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

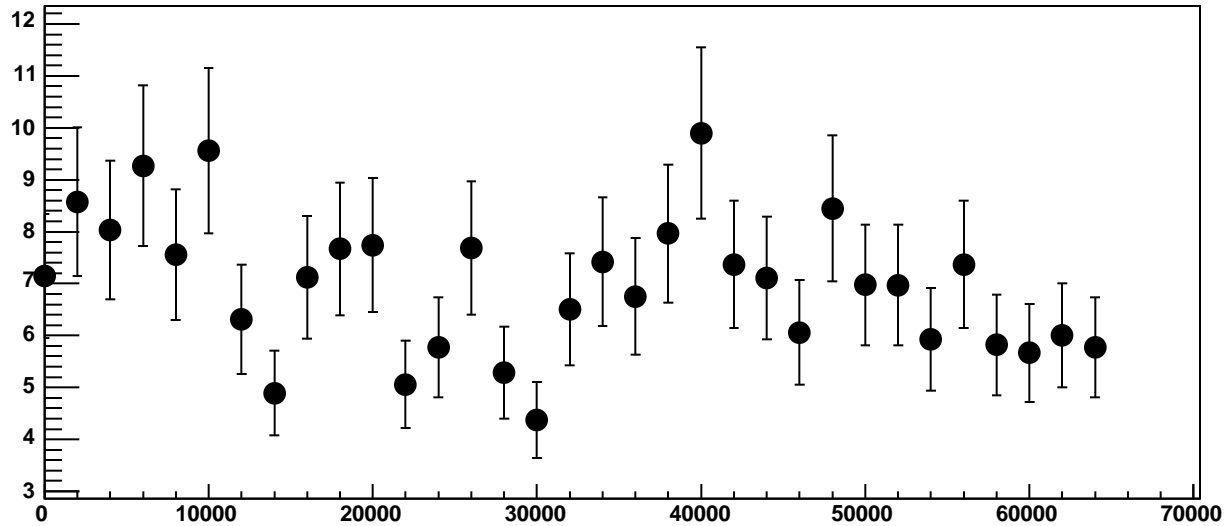


Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

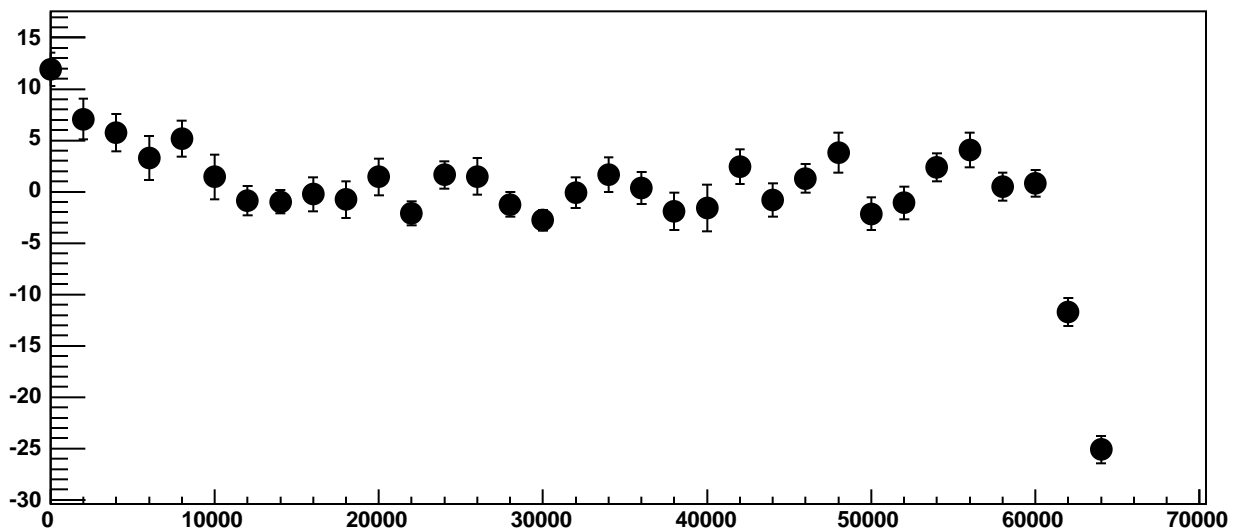


χ^2 / ndf 42.89 / 23
p0 40.66 ± 0.7309
p1 $0.003947 \pm 2.226e-05$

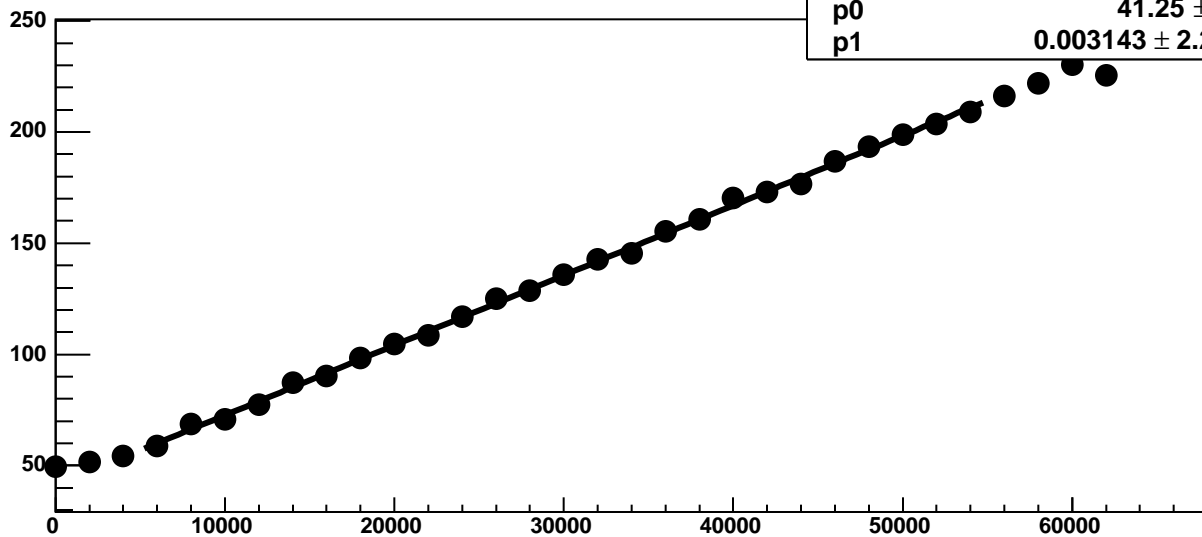
Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

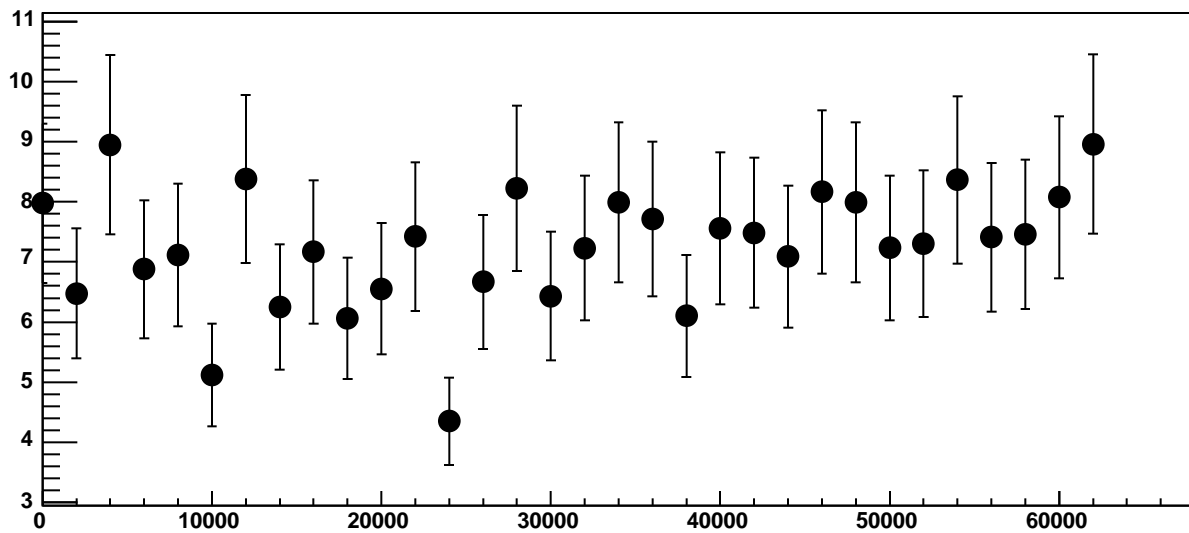


Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

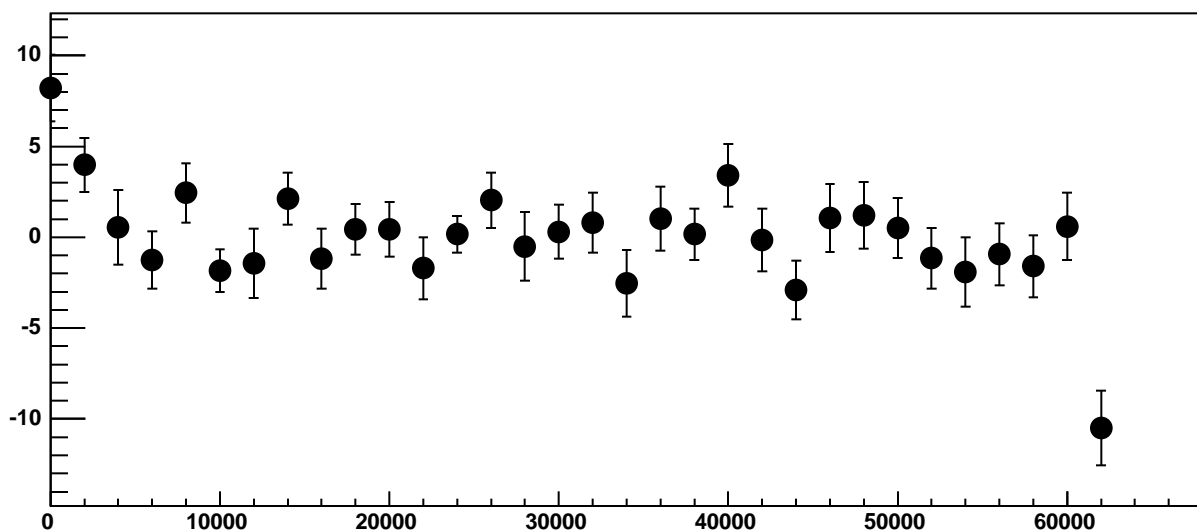


χ^2 / ndf 23.54 / 23
p0 41.25 ± 0.7036
p1 $0.003143 \pm 2.245e-05$

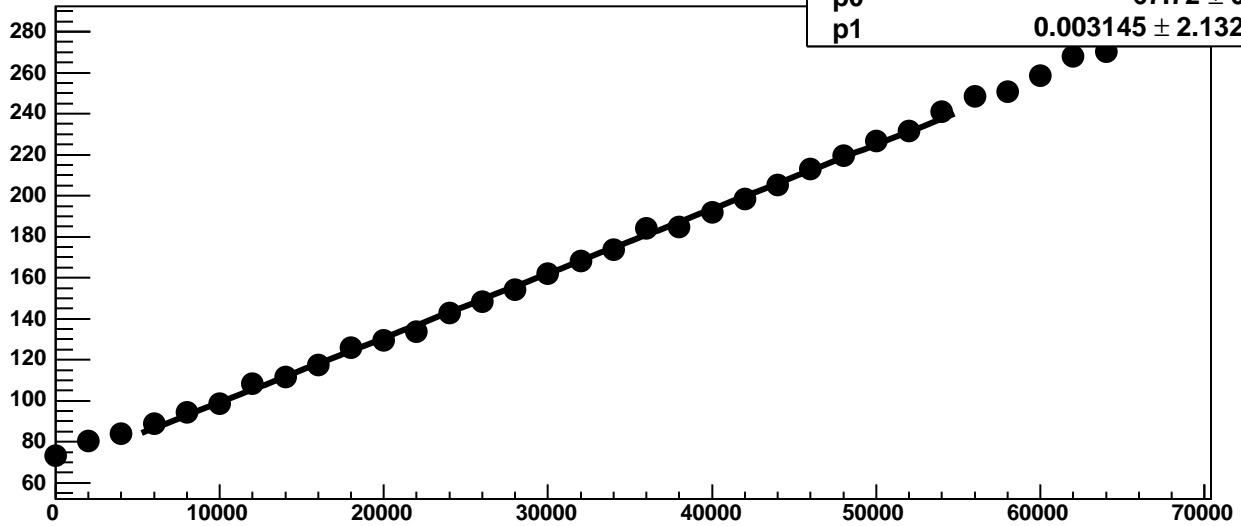
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.63 / 23

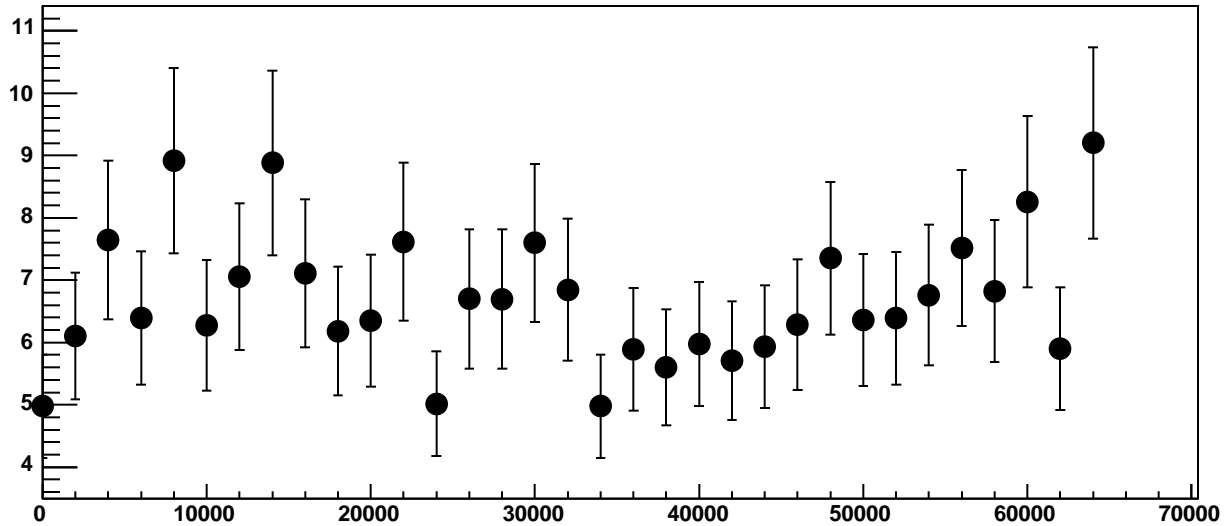
p0

67.72 ± 0.726

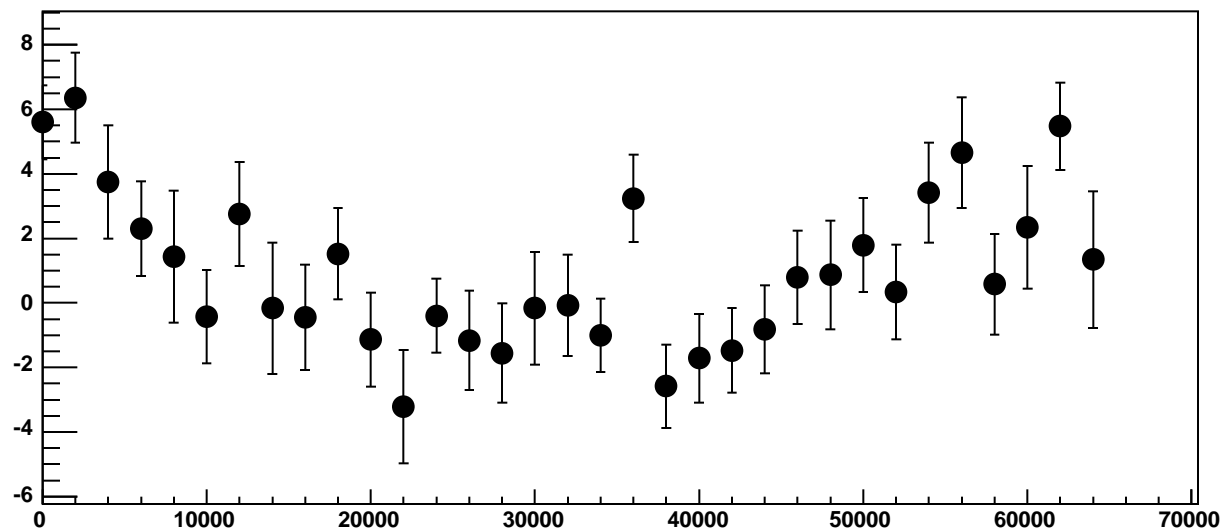
p1

$0.003145 \pm 2.132e-05$

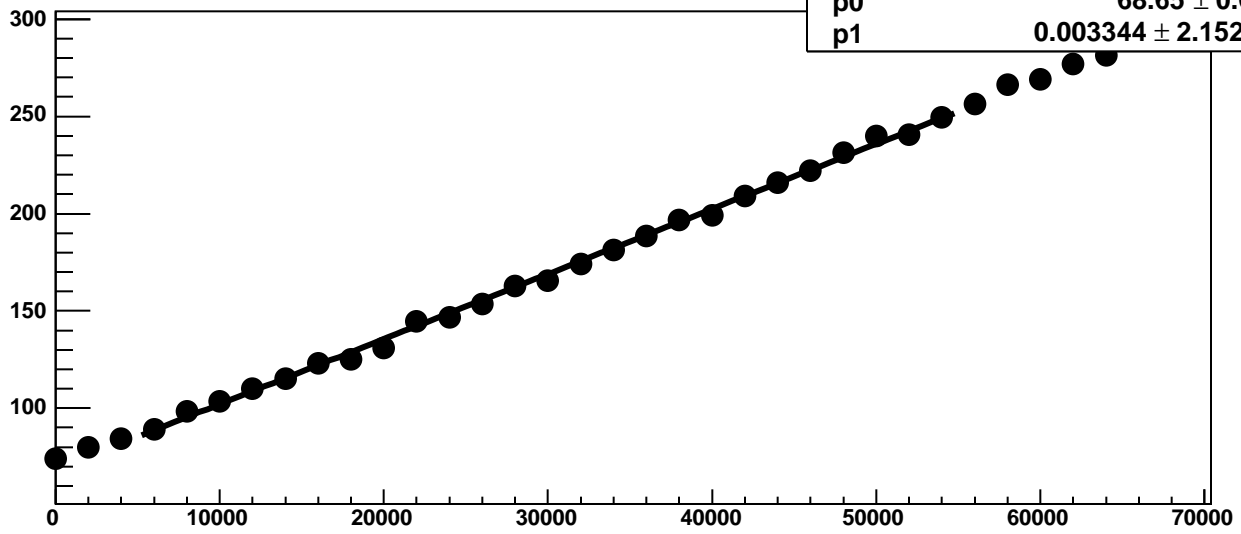
Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



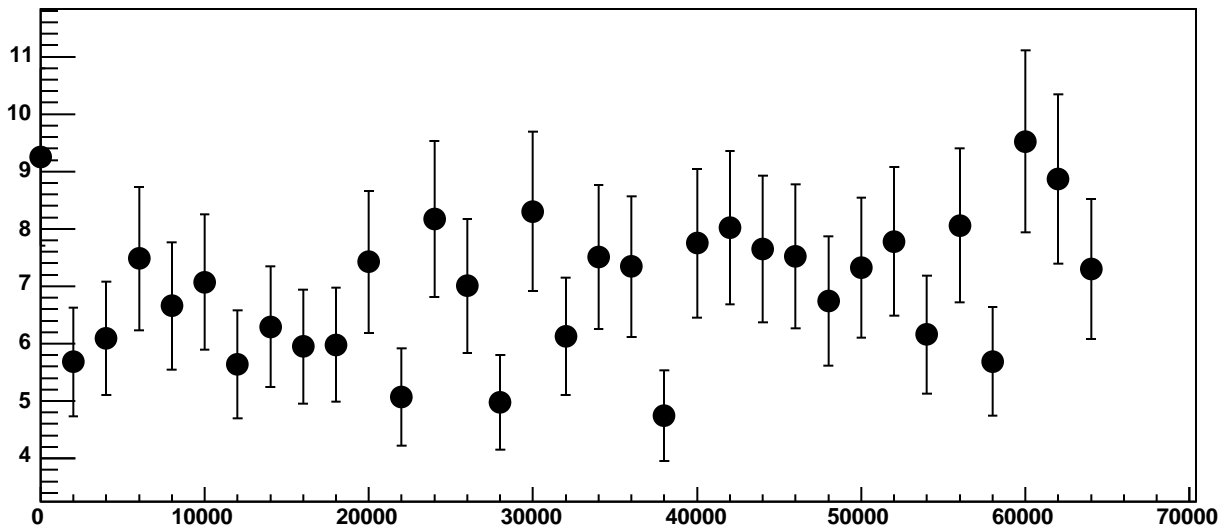
Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



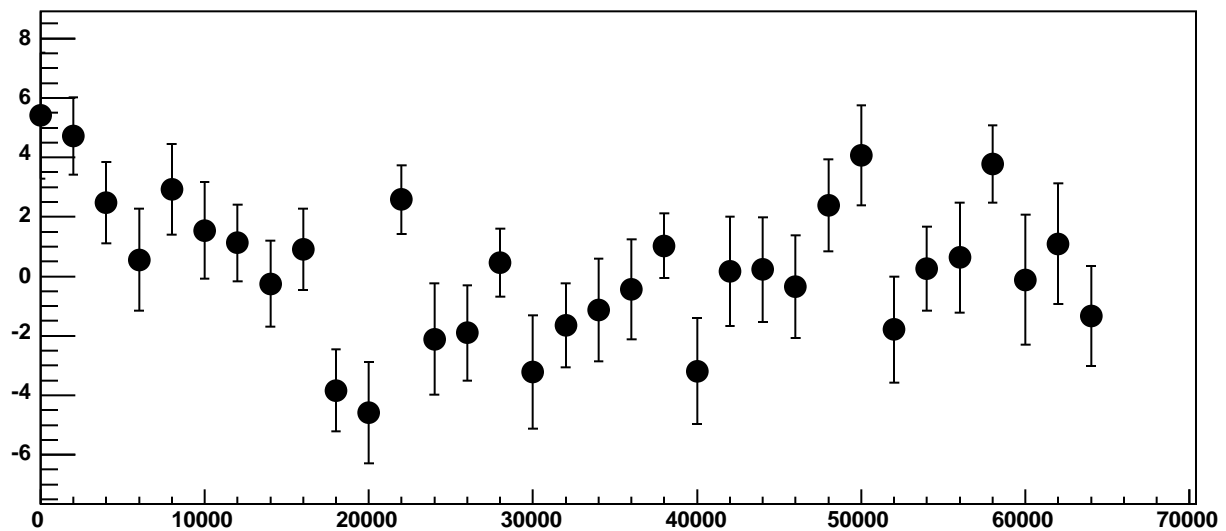
Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



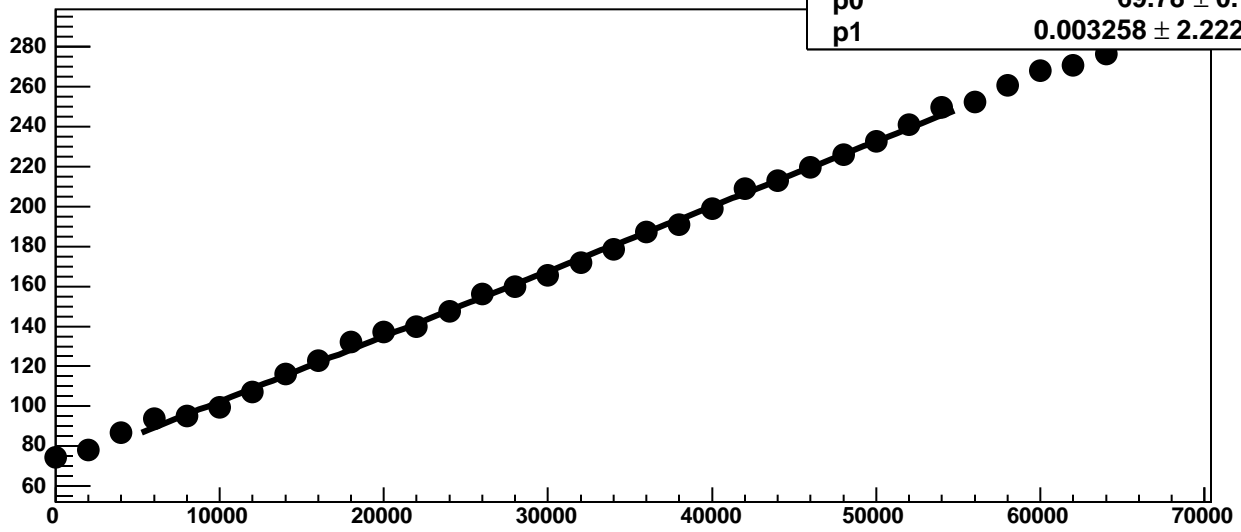
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

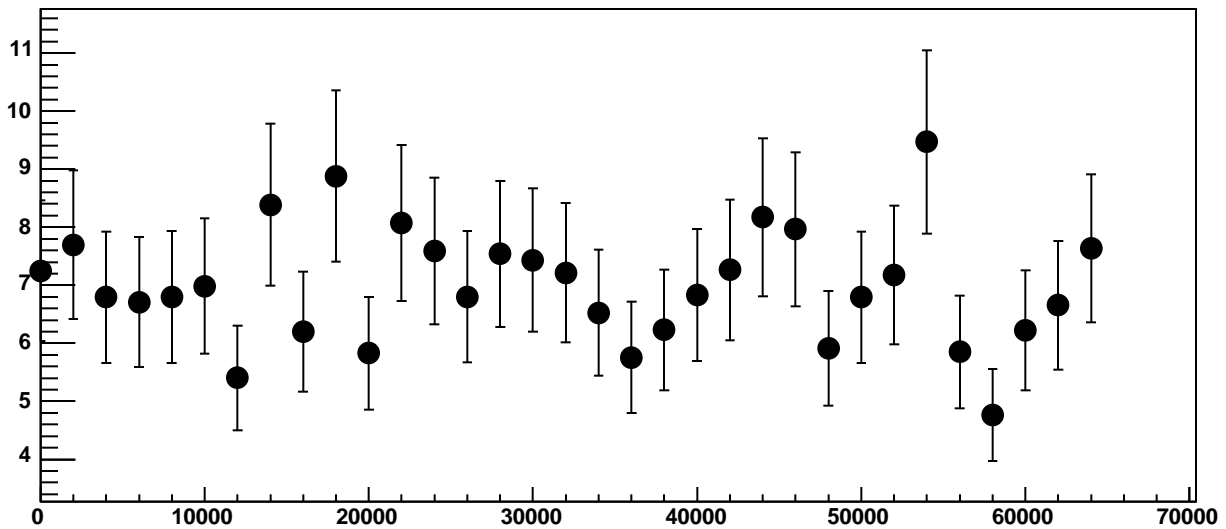


Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

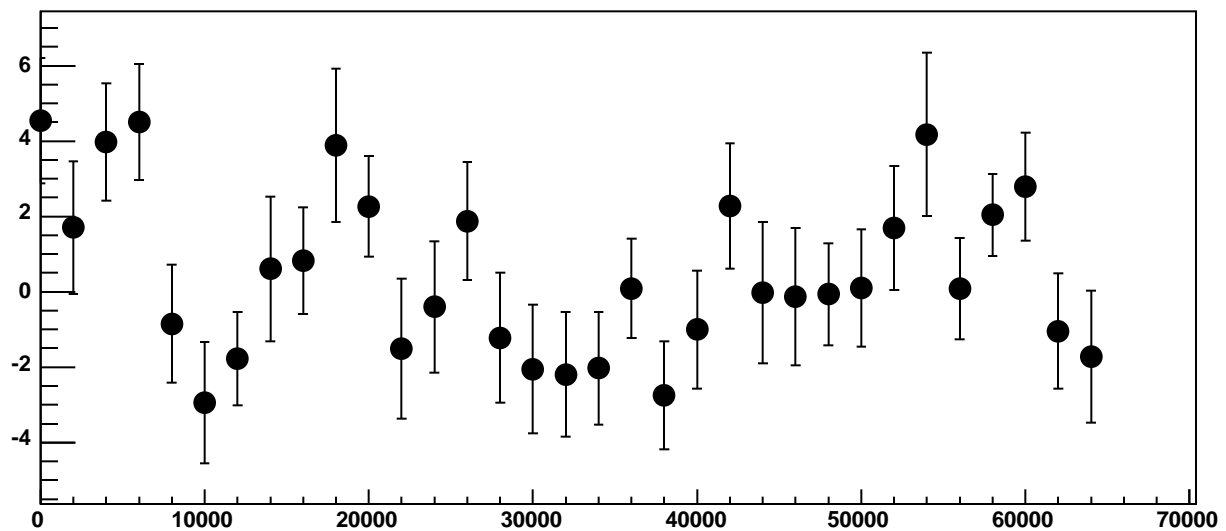


χ^2 / ndf 39.7 / 23
p0 69.78 ± 0.7247
p1 $0.003258 \pm 2.222e-05$

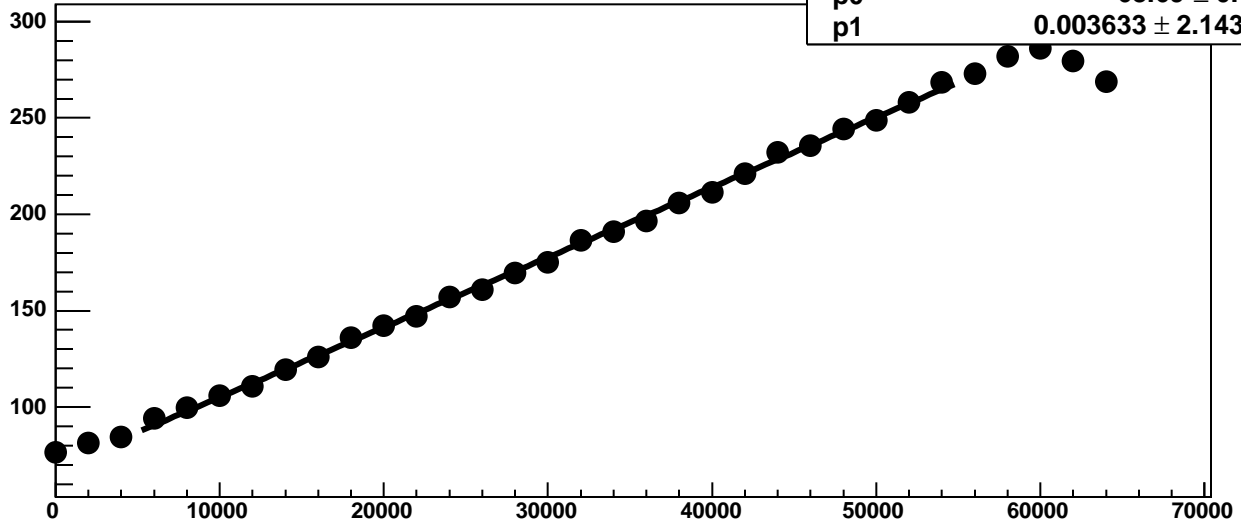
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

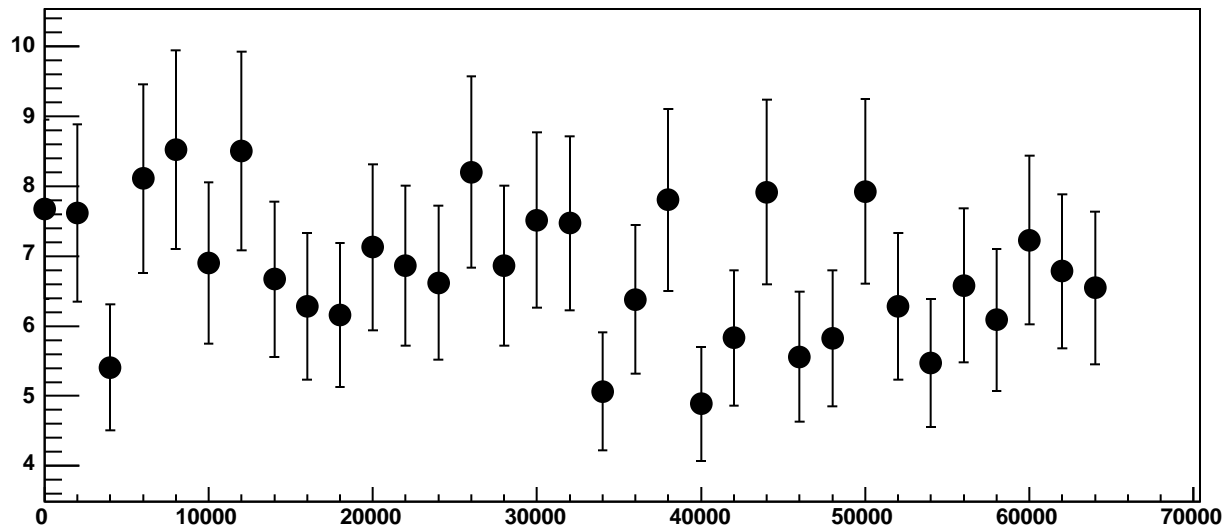


Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

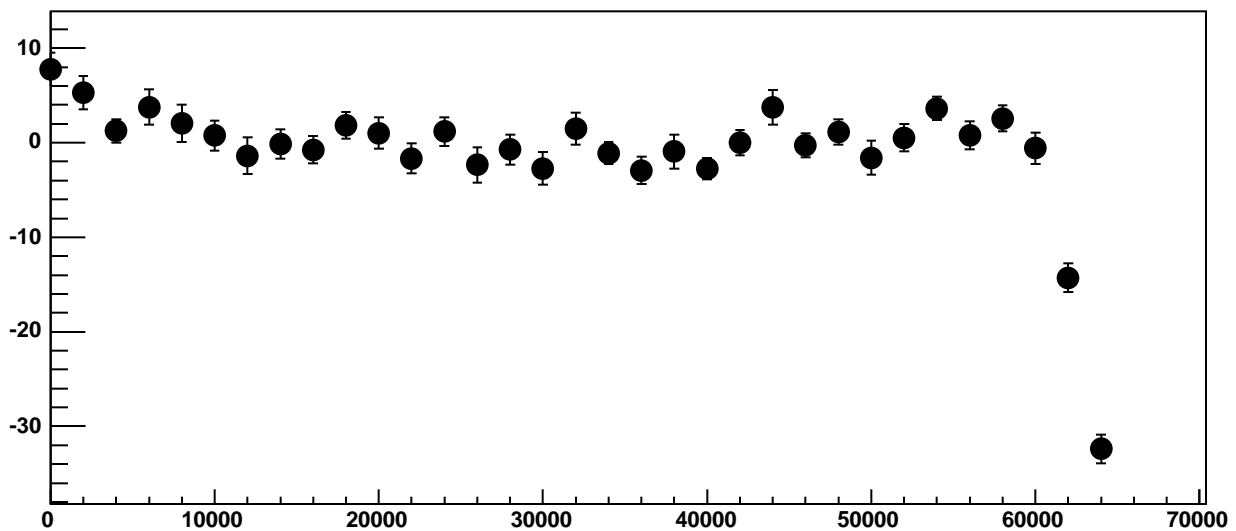


χ^2 / ndf 40.41 / 23
p0 68.69 ± 0.7515
p1 $0.003633 \pm 2.143e-05$

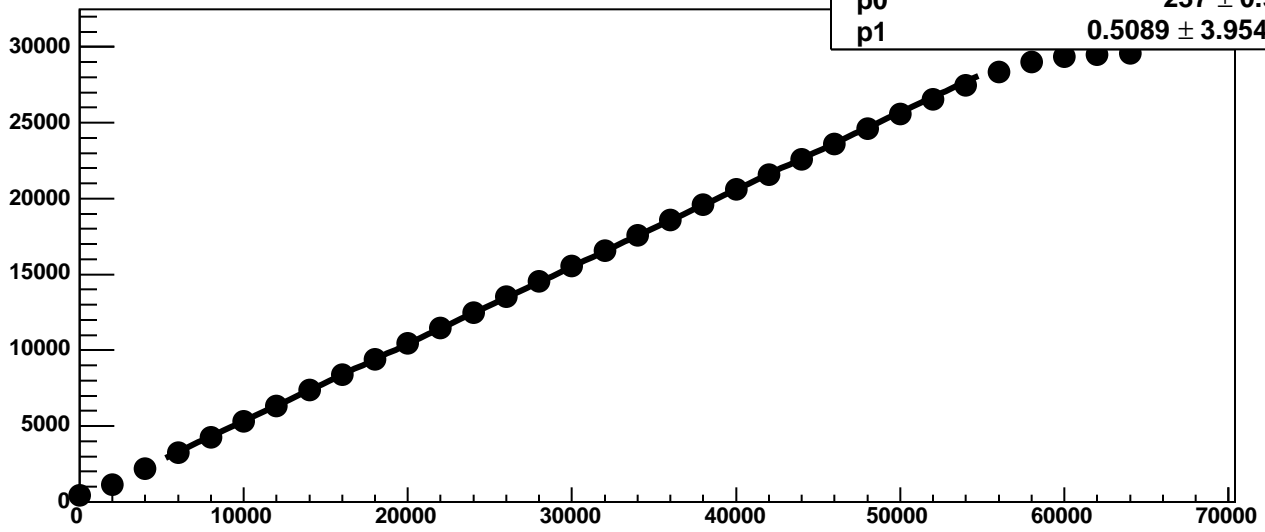
Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



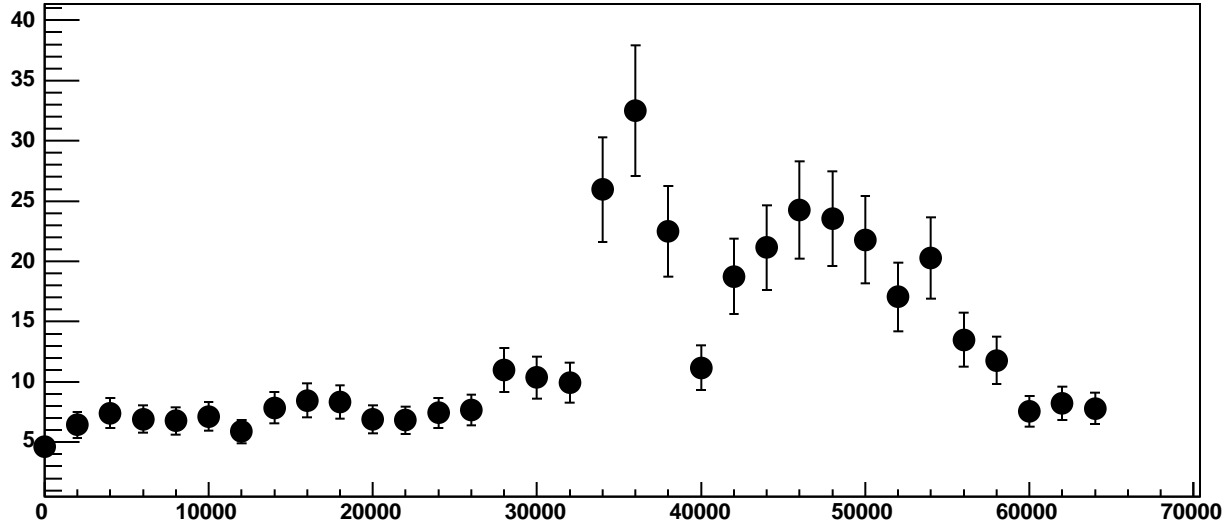
Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



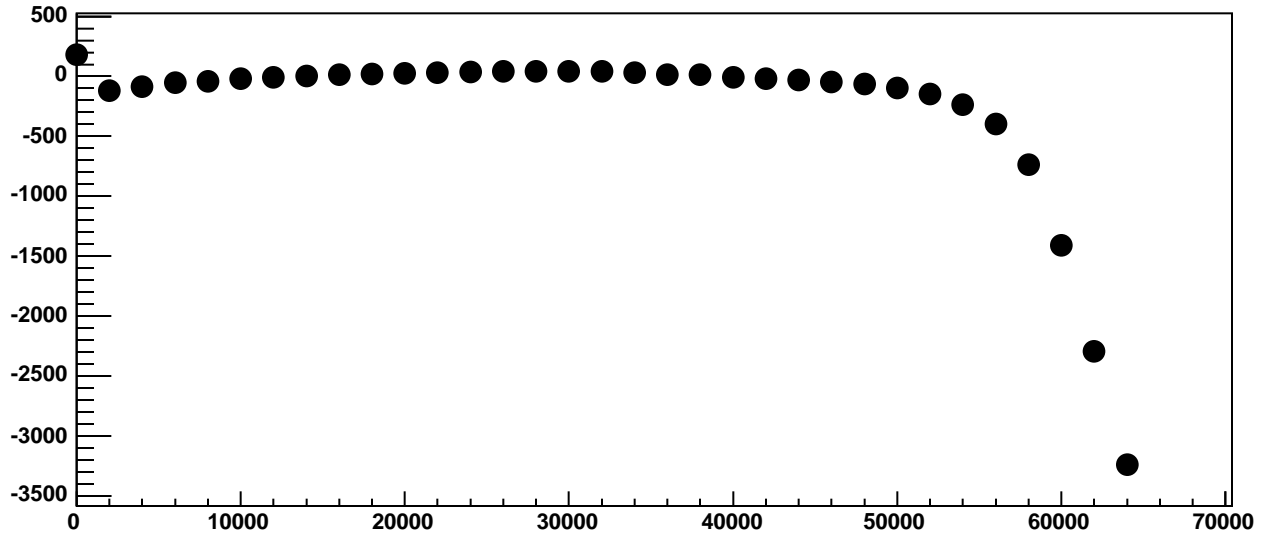
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



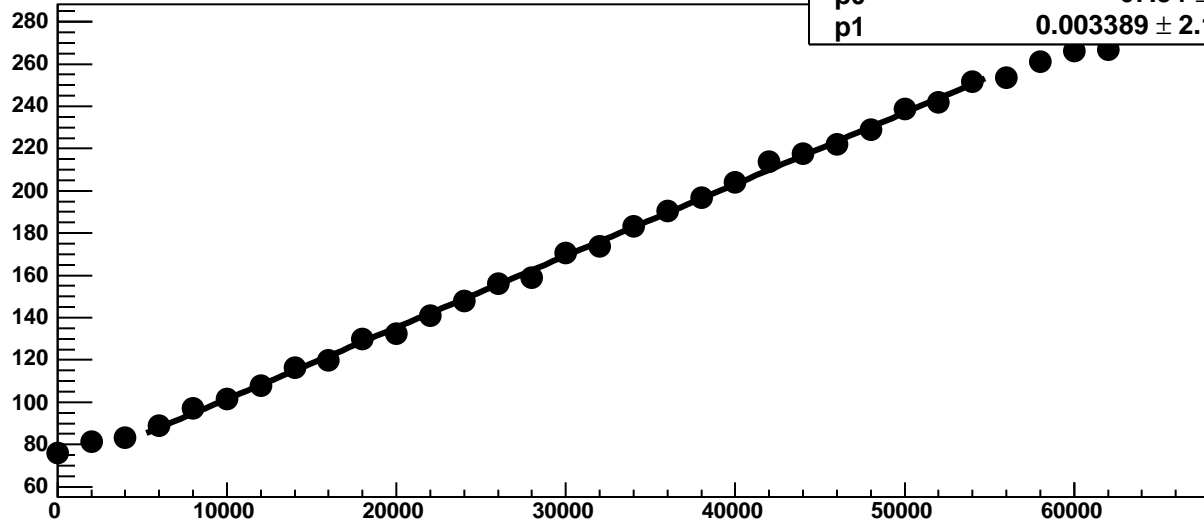
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

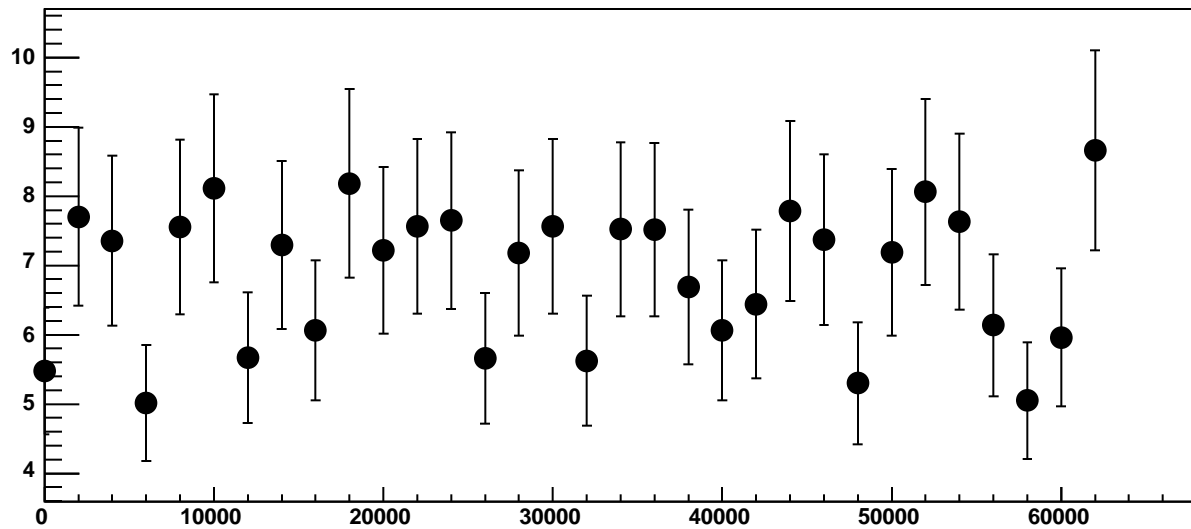


Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

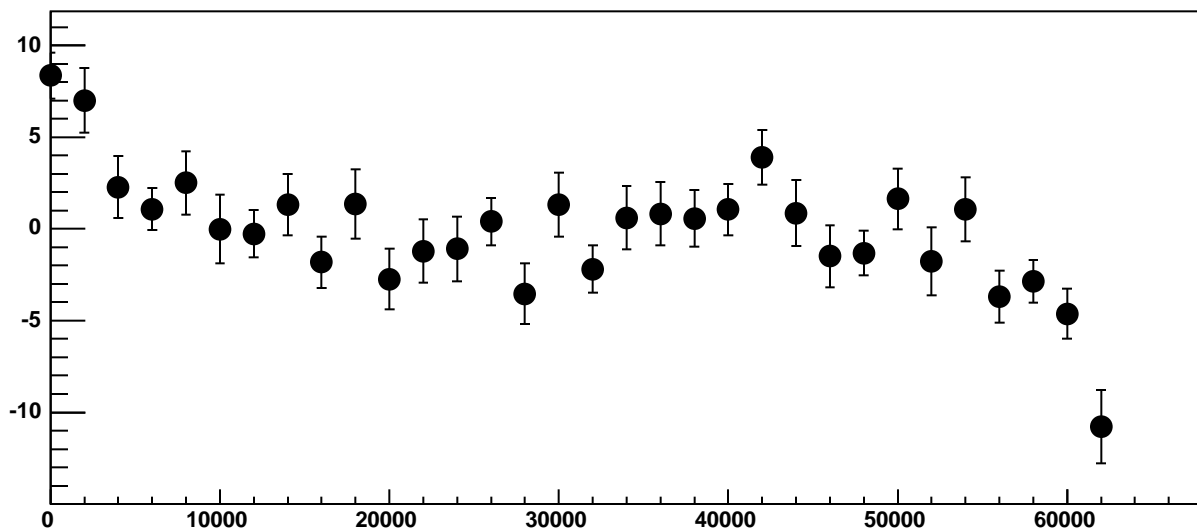


χ^2 / ndf 30.19 / 23
p0 67.54 ± 0.6945
p1 $0.003389 \pm 2.121e-05$

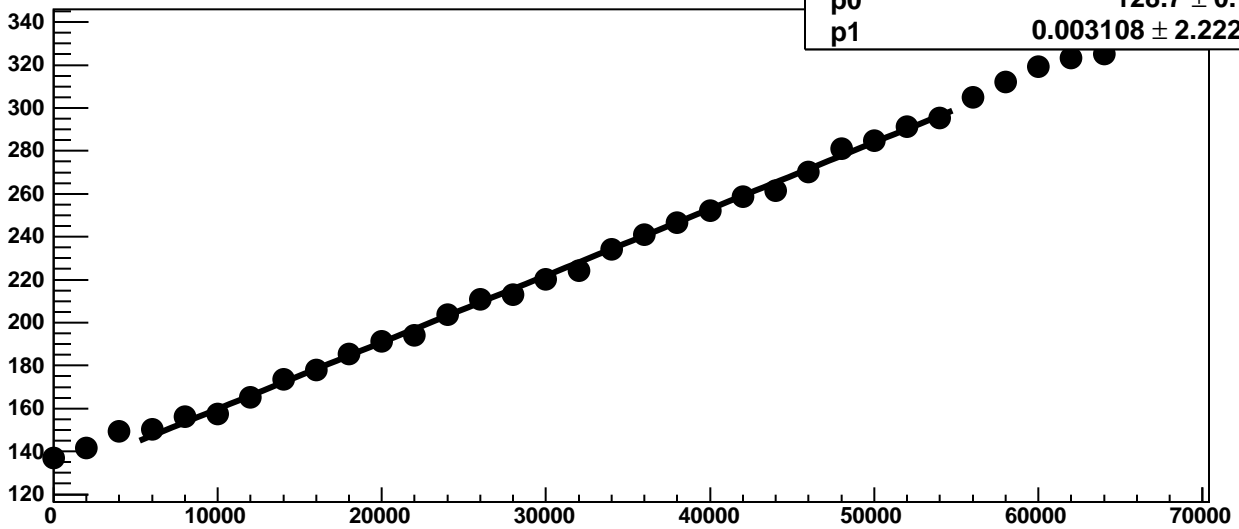
Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

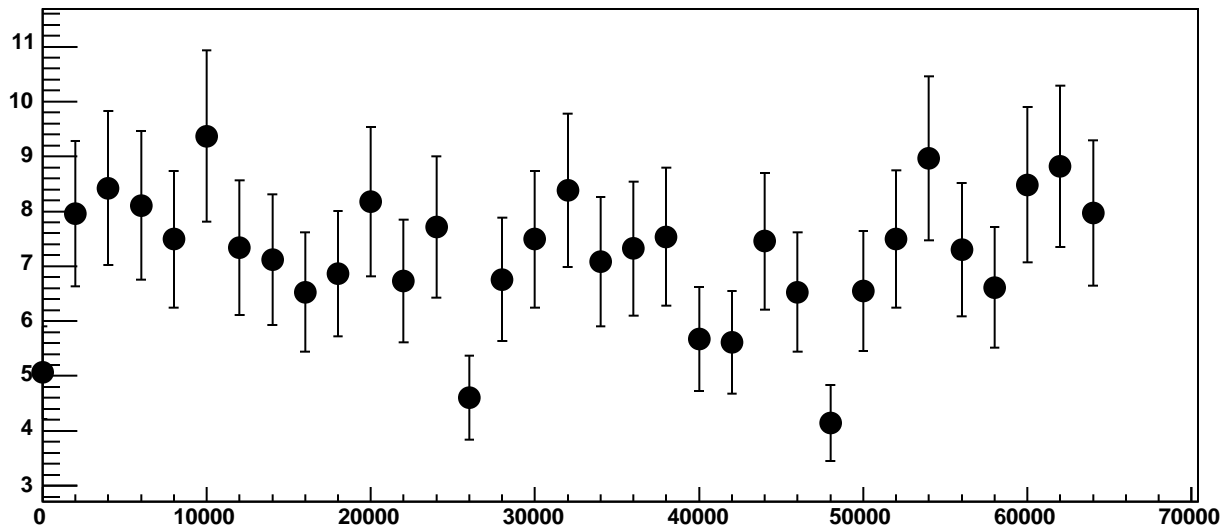


Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

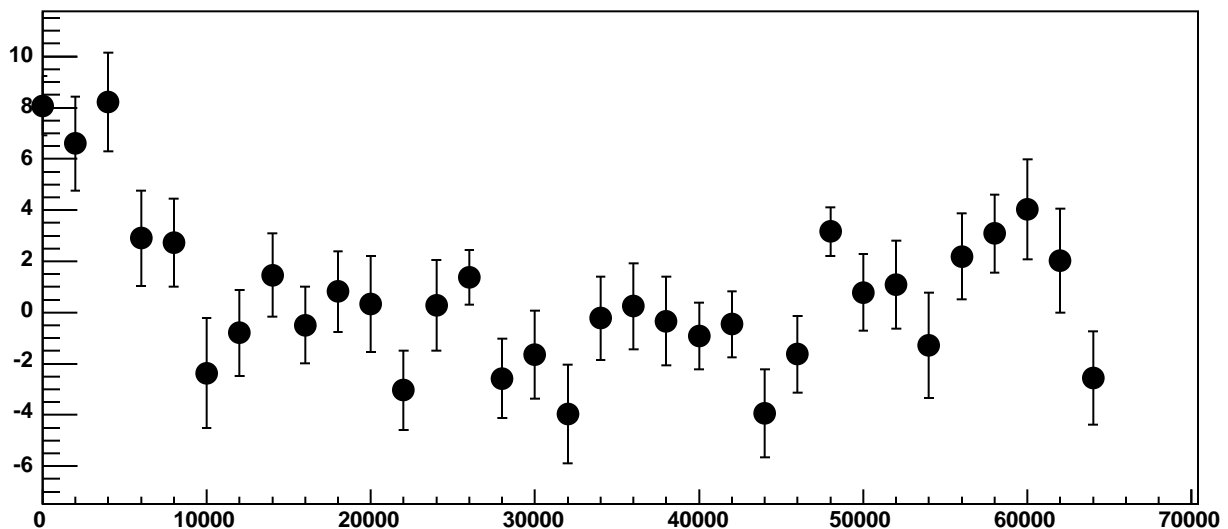


χ^2 / ndf 40.4 / 23
p0 128.7 ± 0.7713
p1 $0.003108 \pm 2.222e-05$

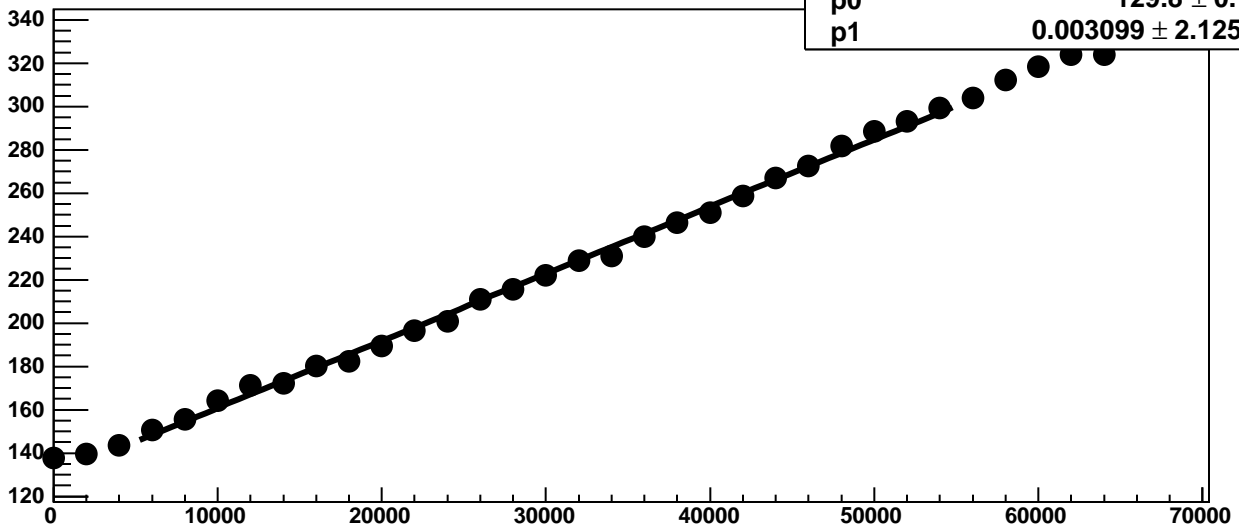
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

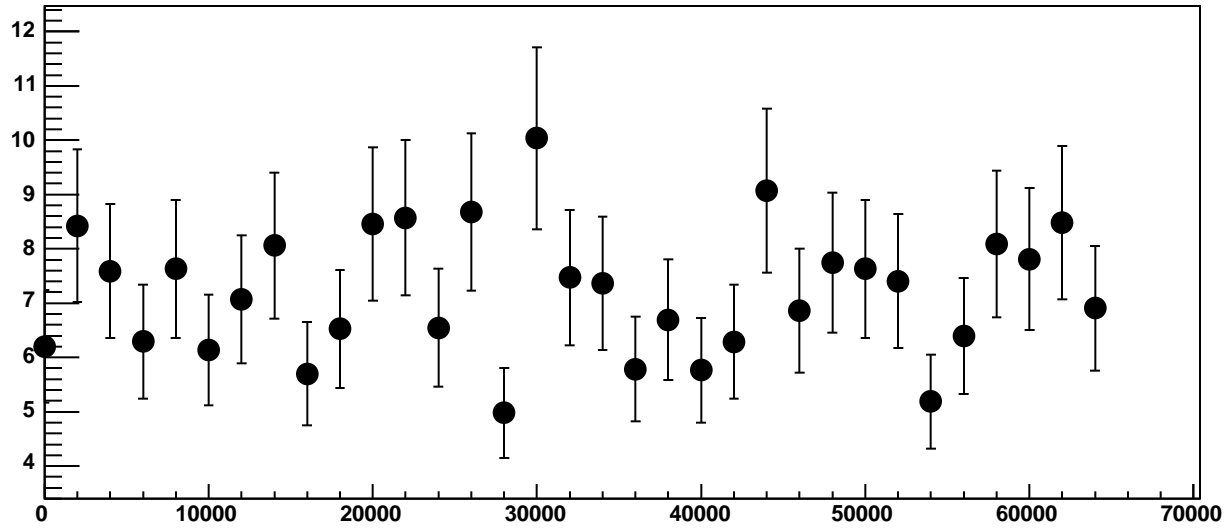


Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

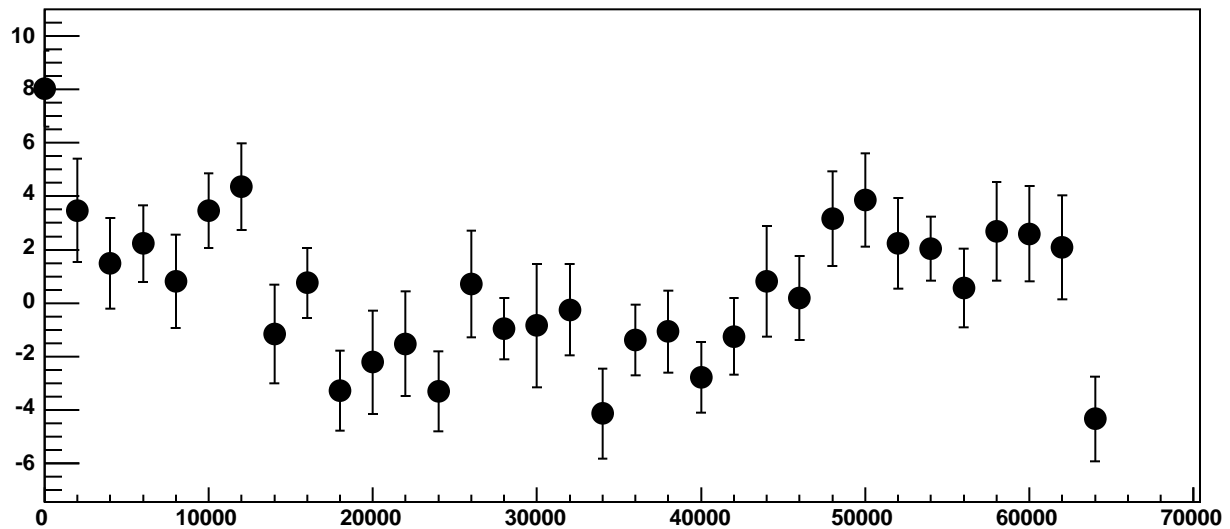


χ^2 / ndf 54.7 / 23
p0 129.8 ± 0.7155
p1 $0.003099 \pm 2.125e-05$

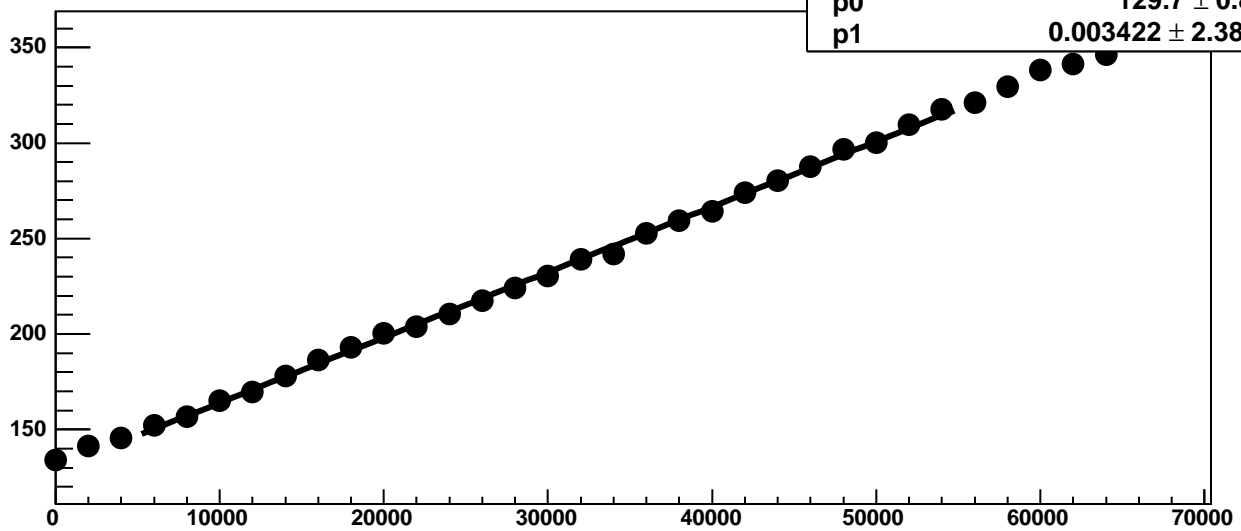
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

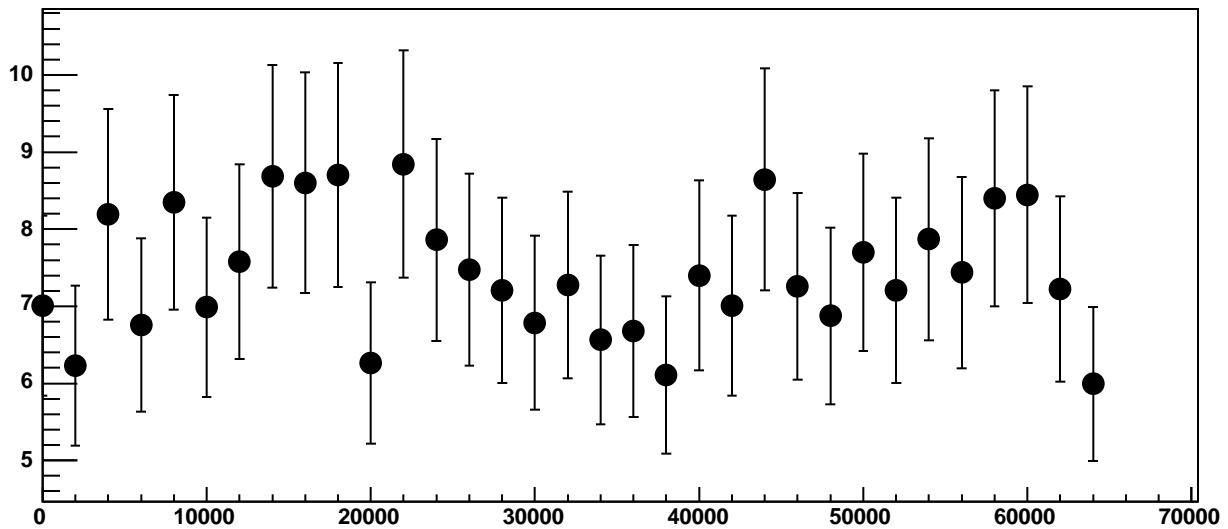


Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

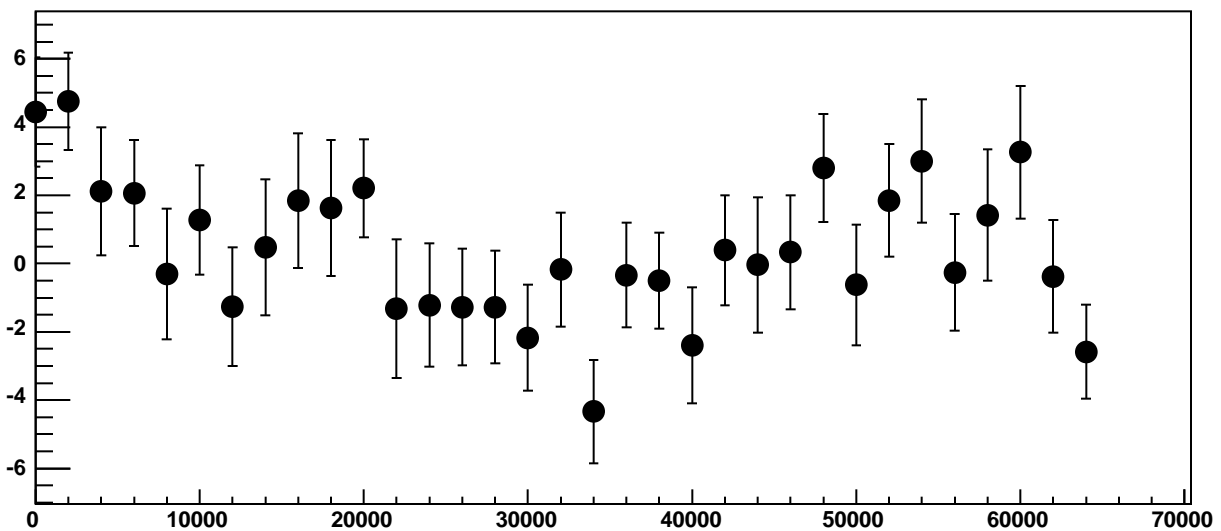


χ^2 / ndf 28.77 / 23
p0 129.7 ± 0.8006
p1 $0.003422 \pm 2.38e-05$

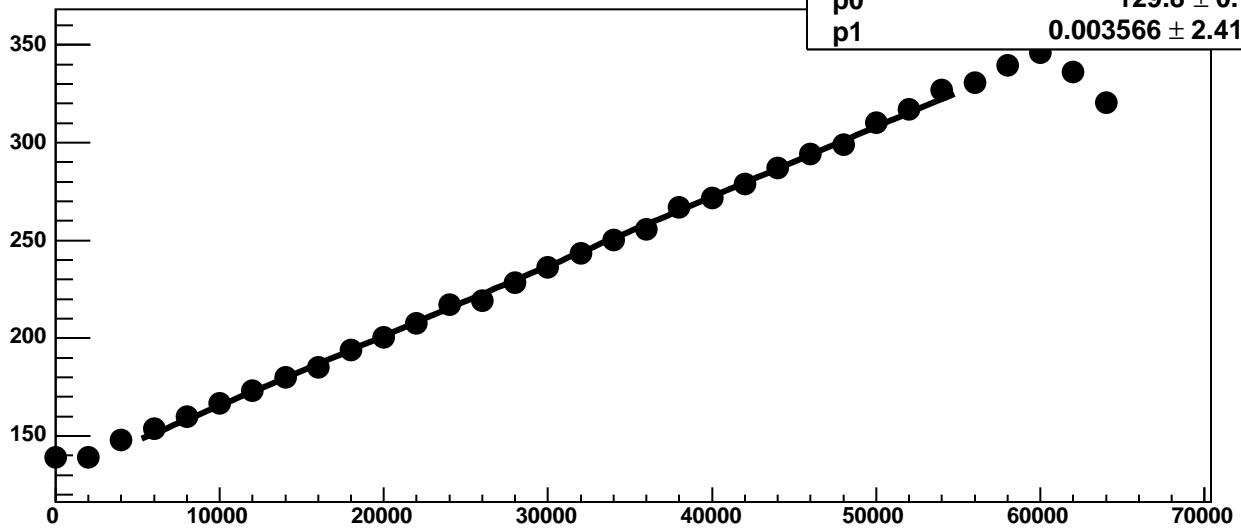
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

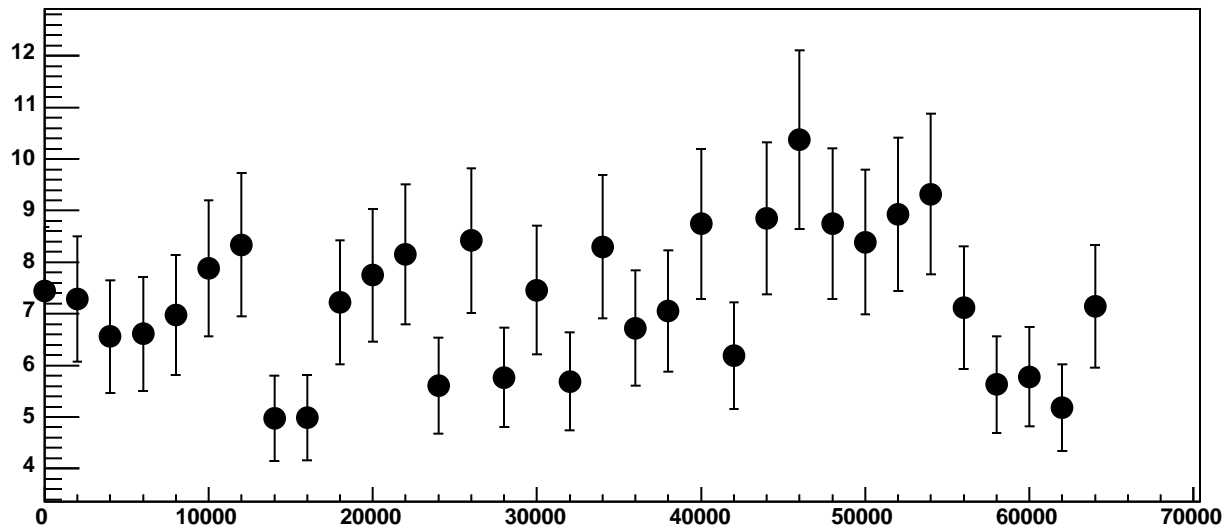


Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

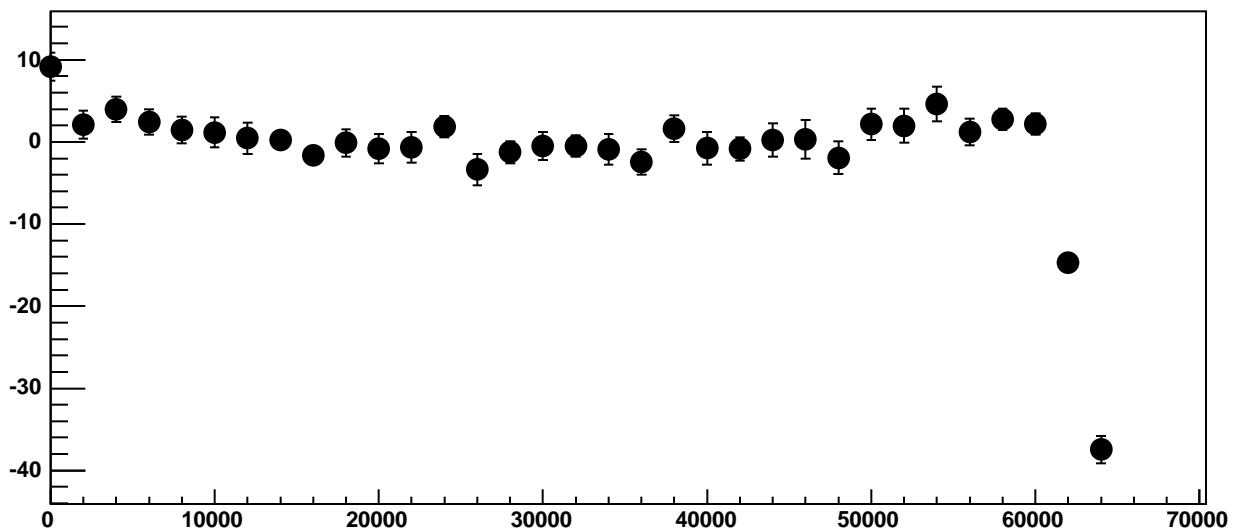


χ^2 / ndf 24.63 / 23
p0 129.8 ± 0.7332
p1 $0.003566 \pm 2.41\text{e-}05$

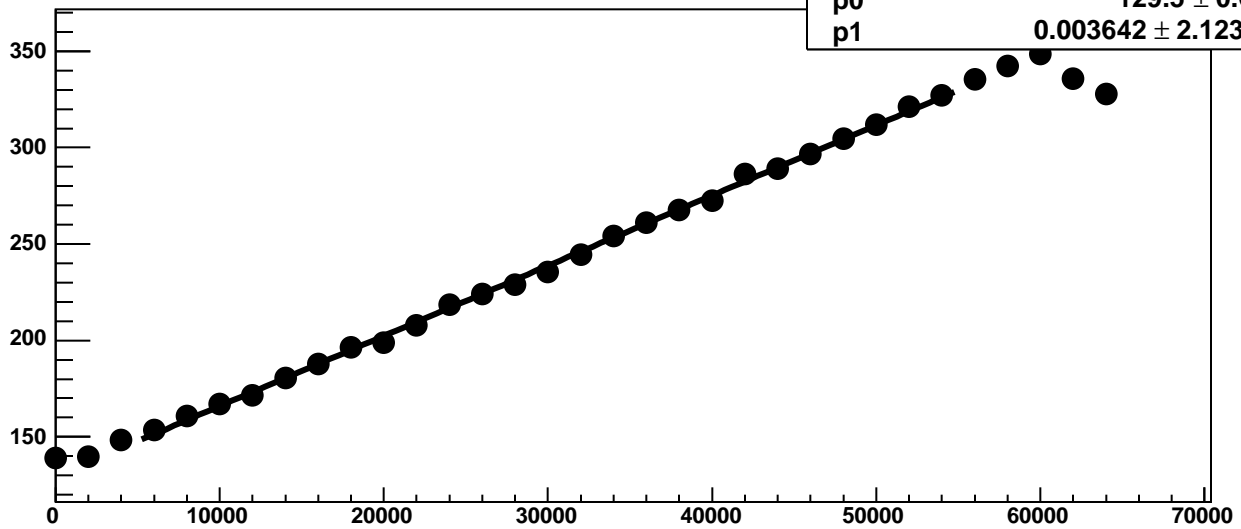
Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

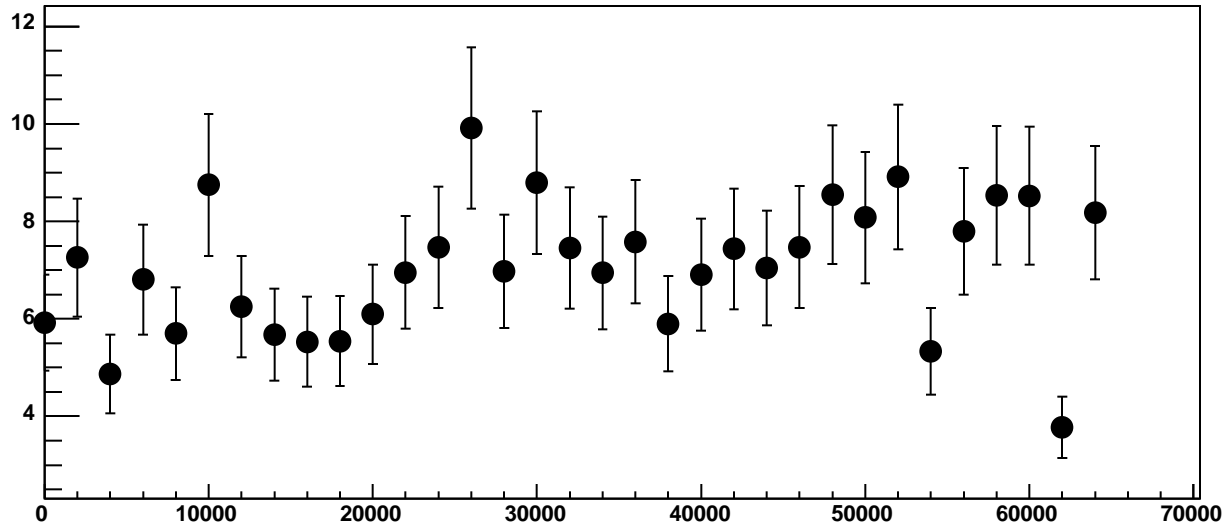


Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

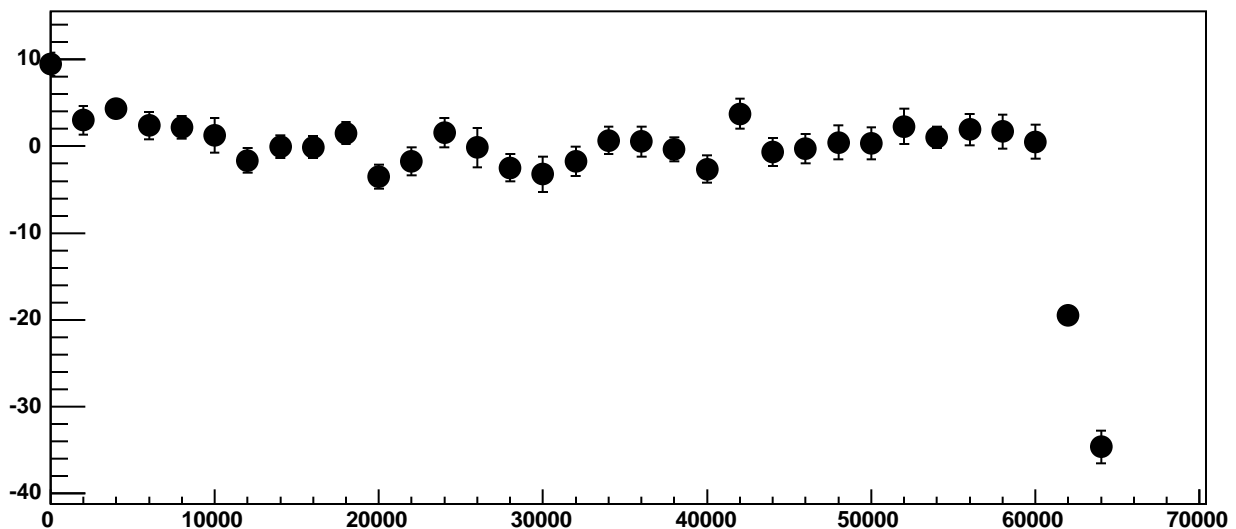


χ^2 / ndf 32.78 / 23
p0 129.5 ± 0.6829
p1 $0.003642 \pm 2.123e-05$

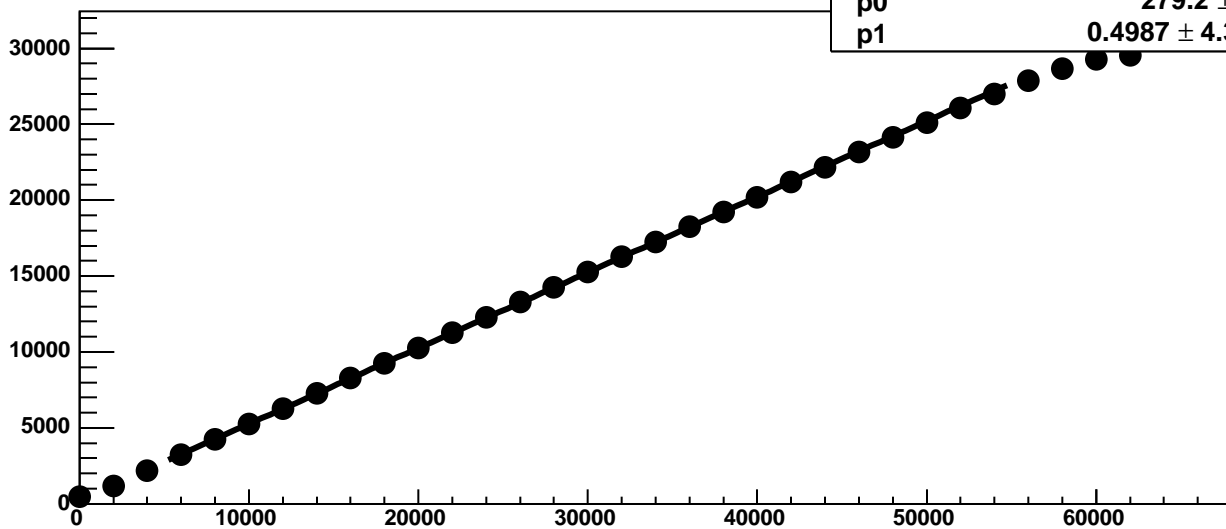
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



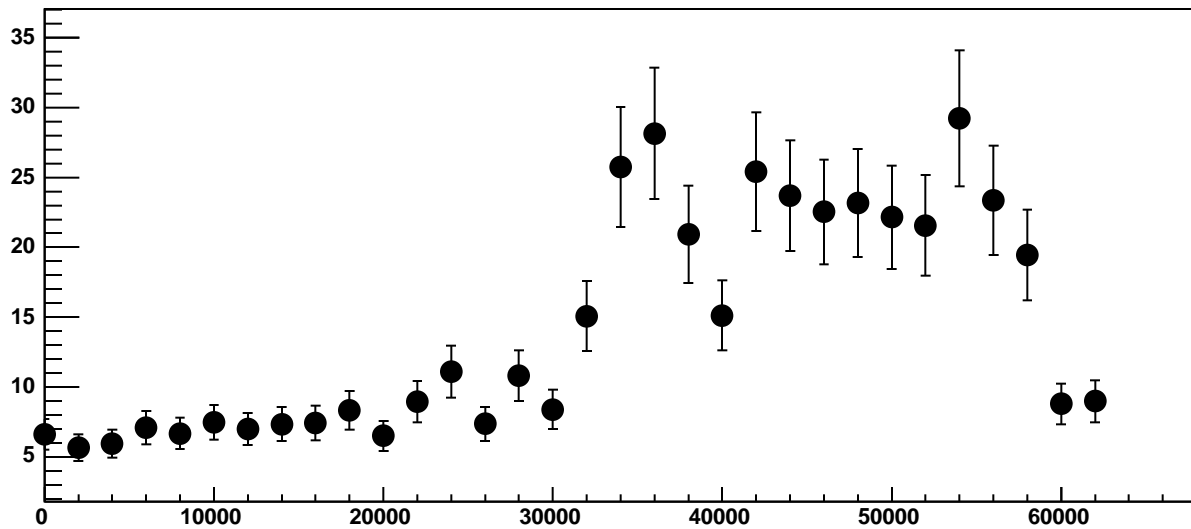
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



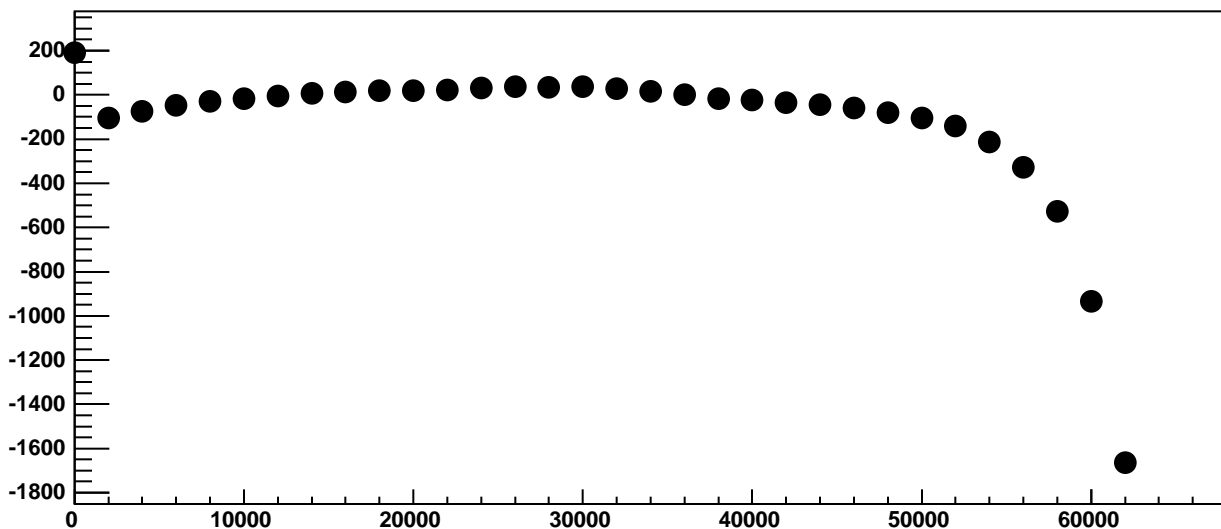
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



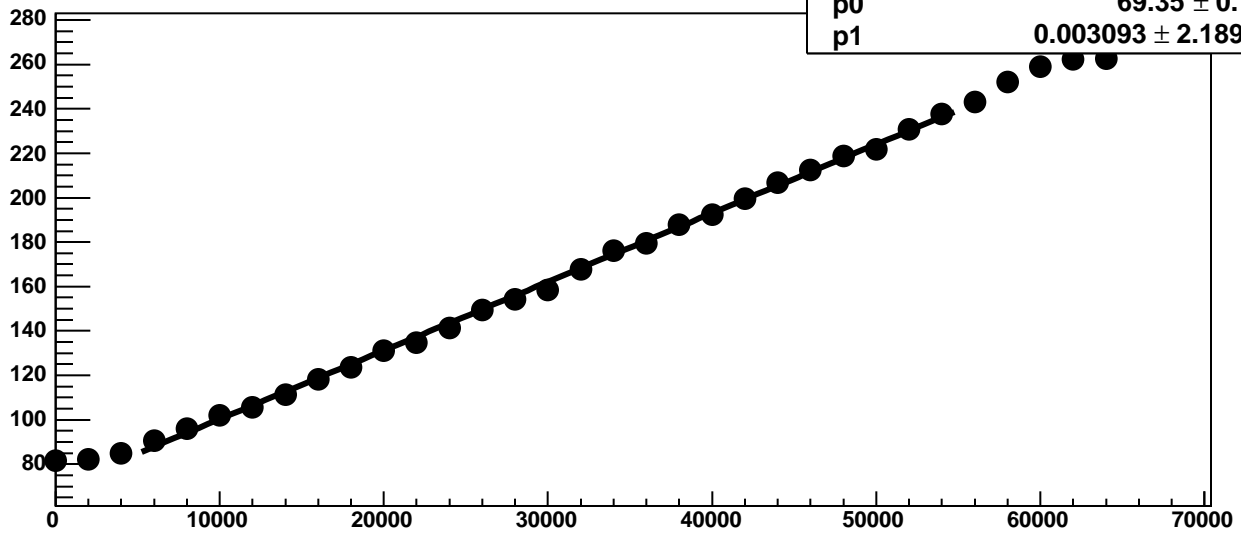
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

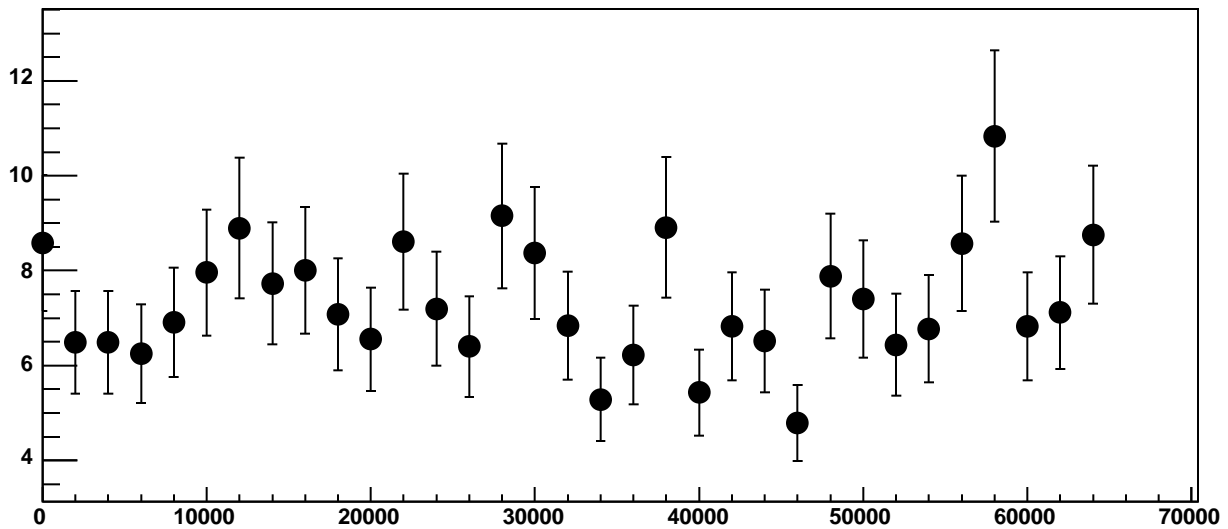


Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

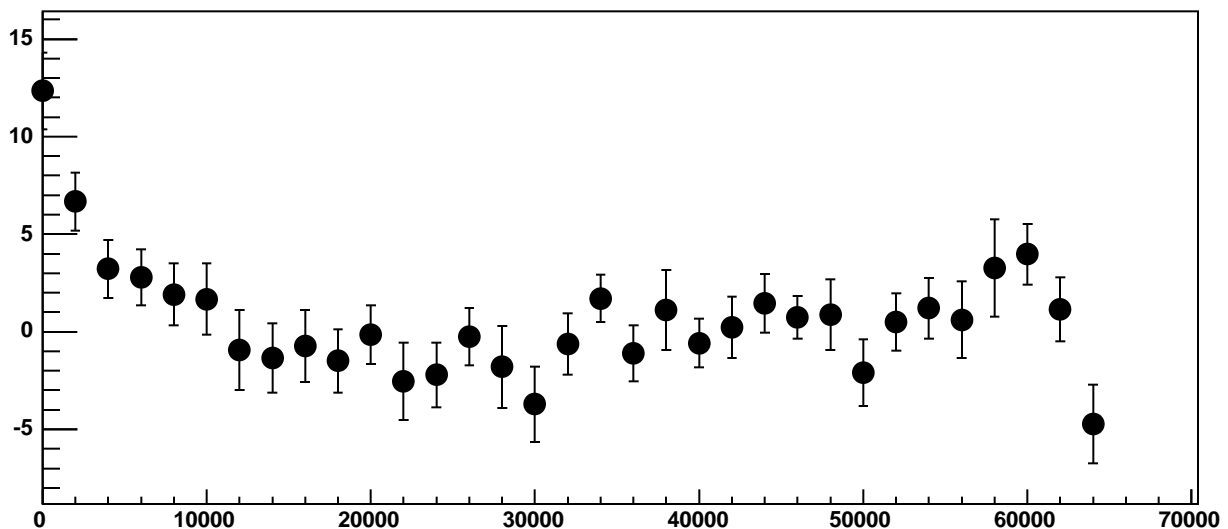


χ^2 / ndf 22.99 / 23
p0 69.35 ± 0.7597
p1 $0.003093 \pm 2.189\text{e-}05$

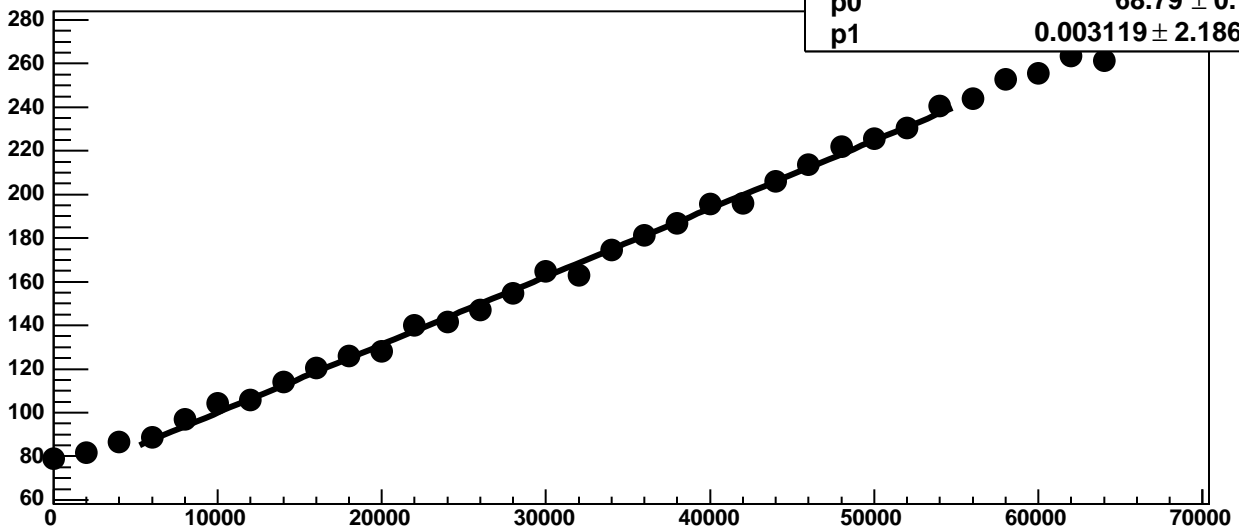
Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

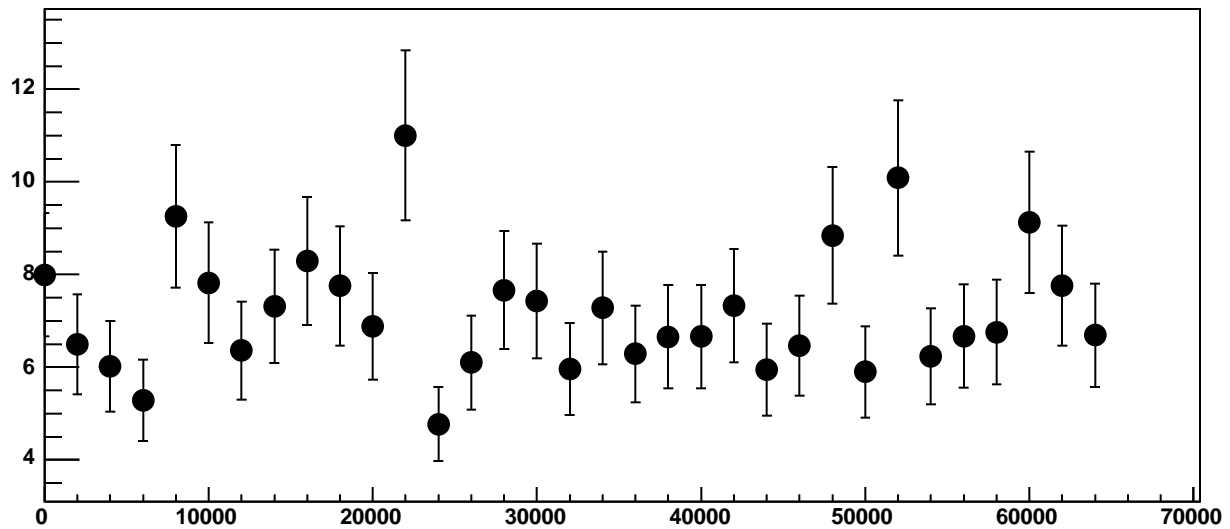


Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

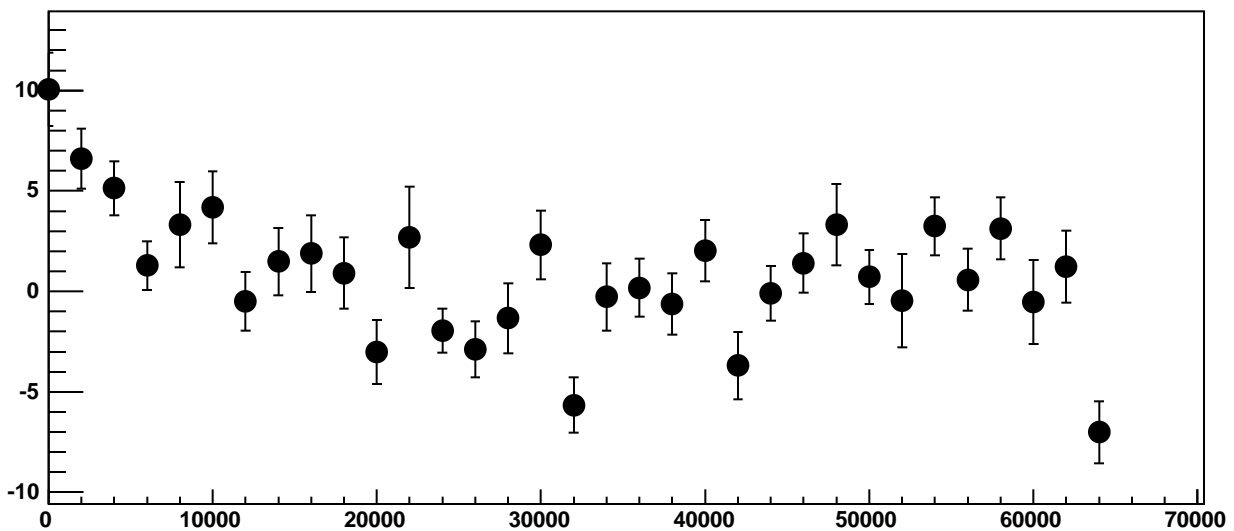


χ^2 / ndf 58.8 / 23
p0 68.79 ± 0.7286
p1 $0.003119 \pm 2.186e-05$

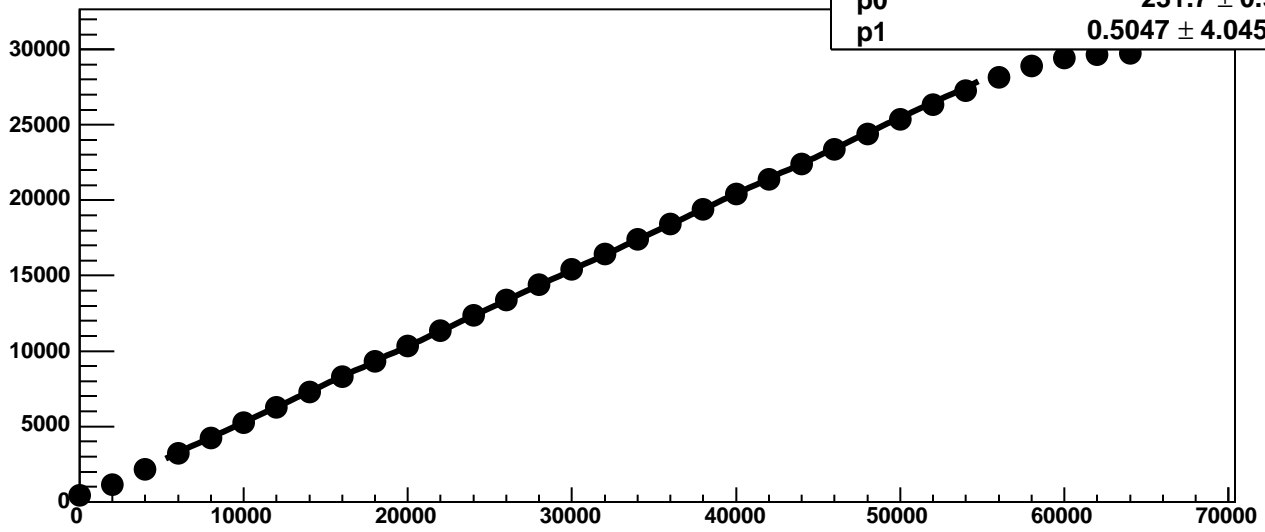
Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



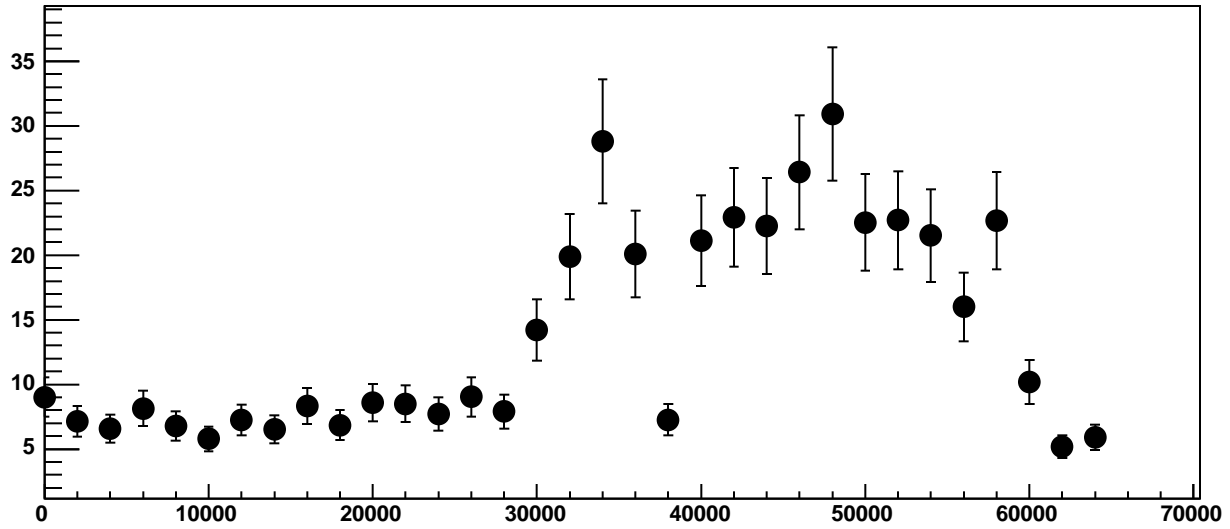
Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



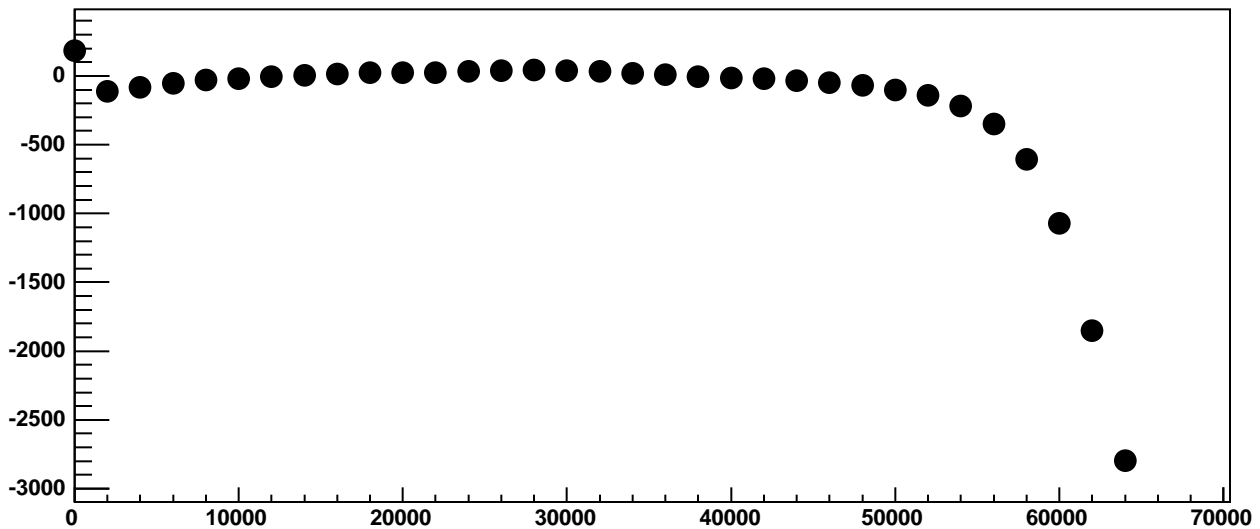
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



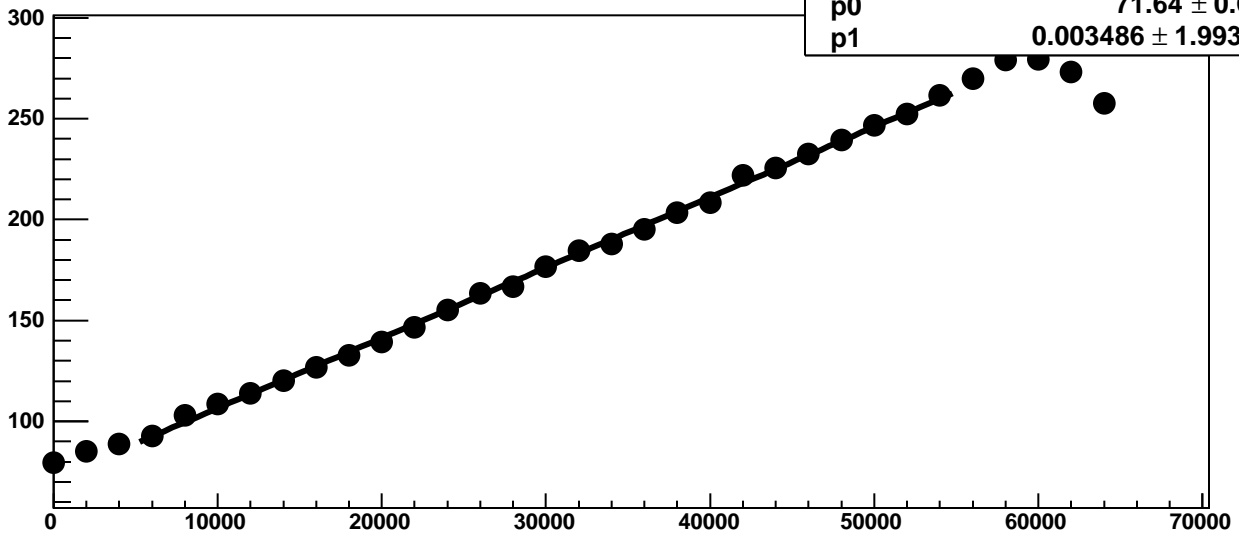
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



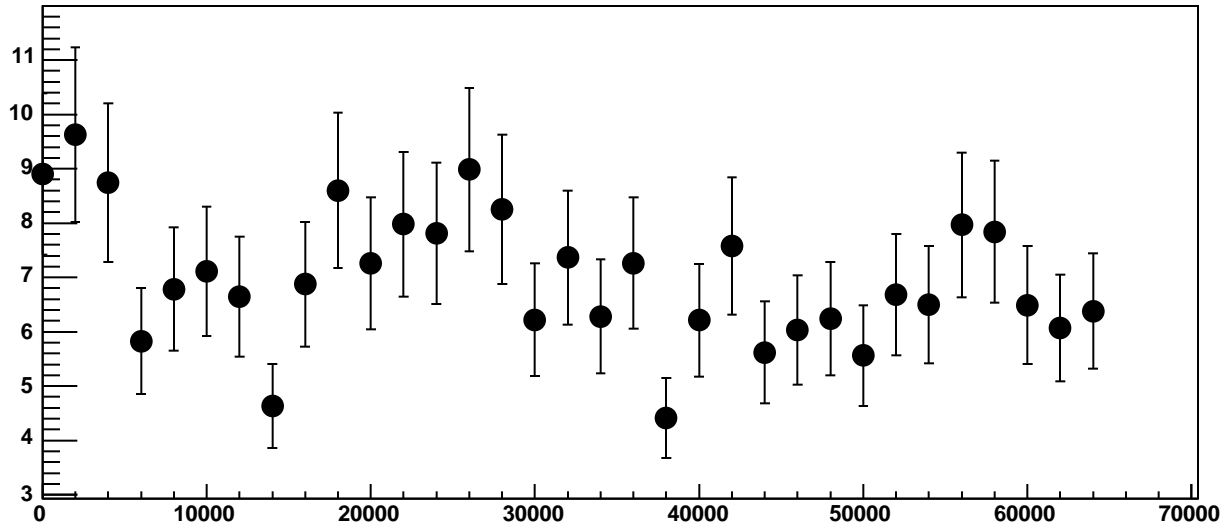
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



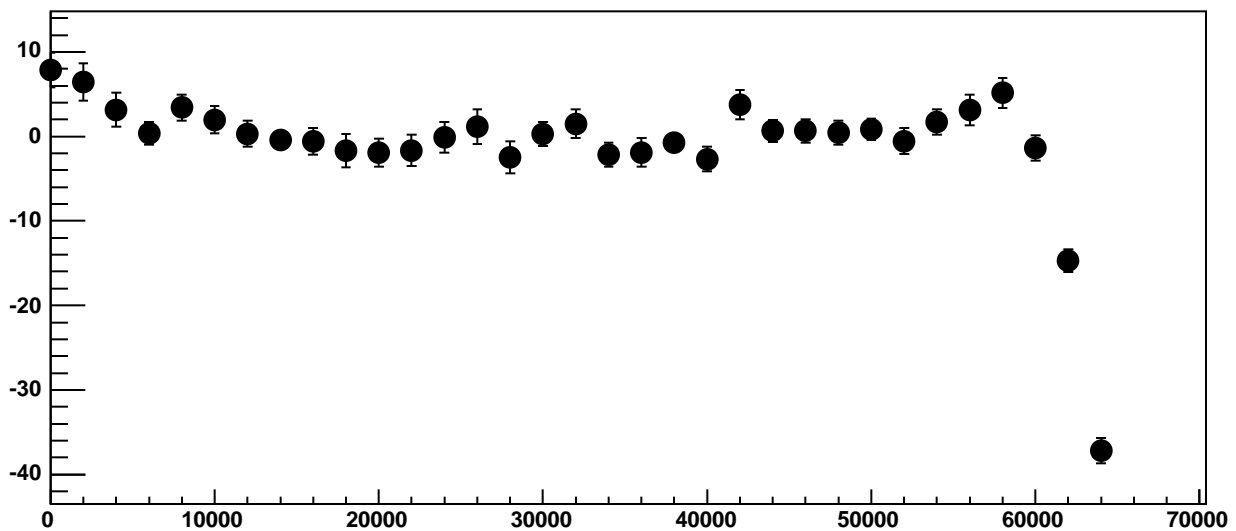
Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



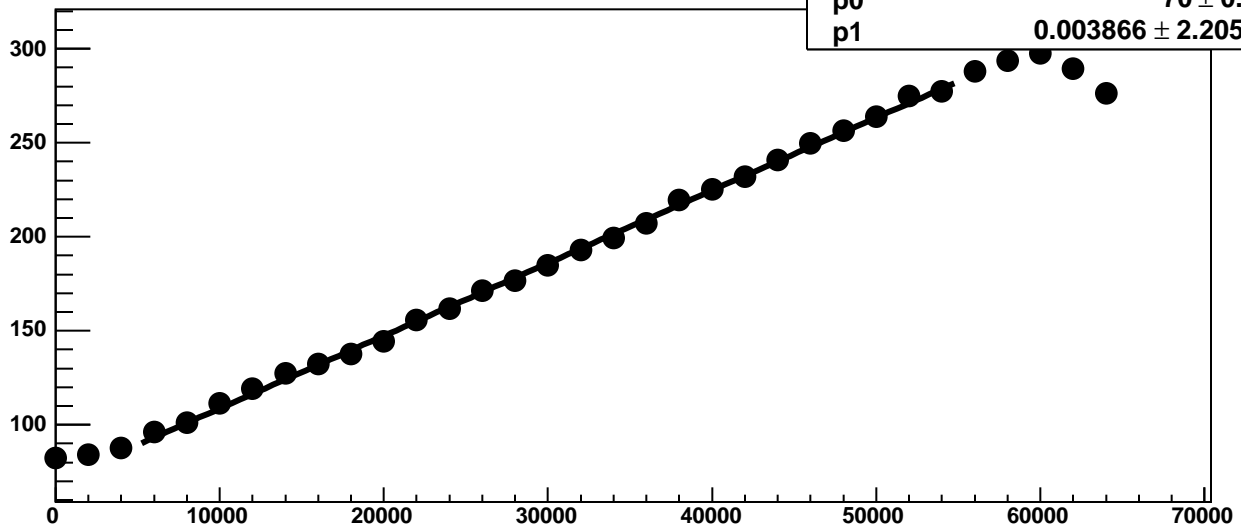
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

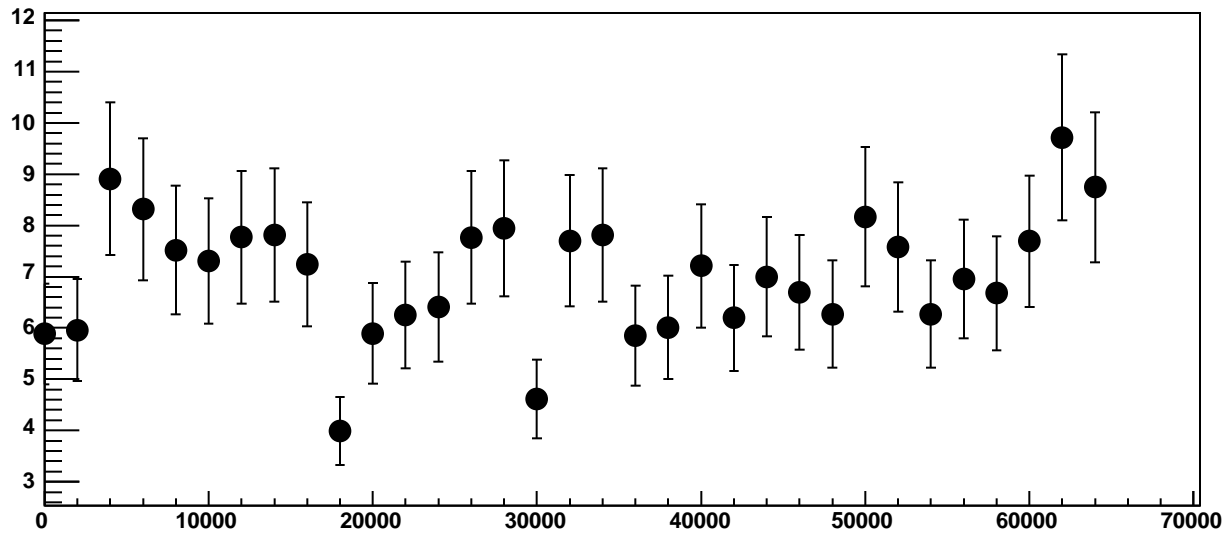


Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

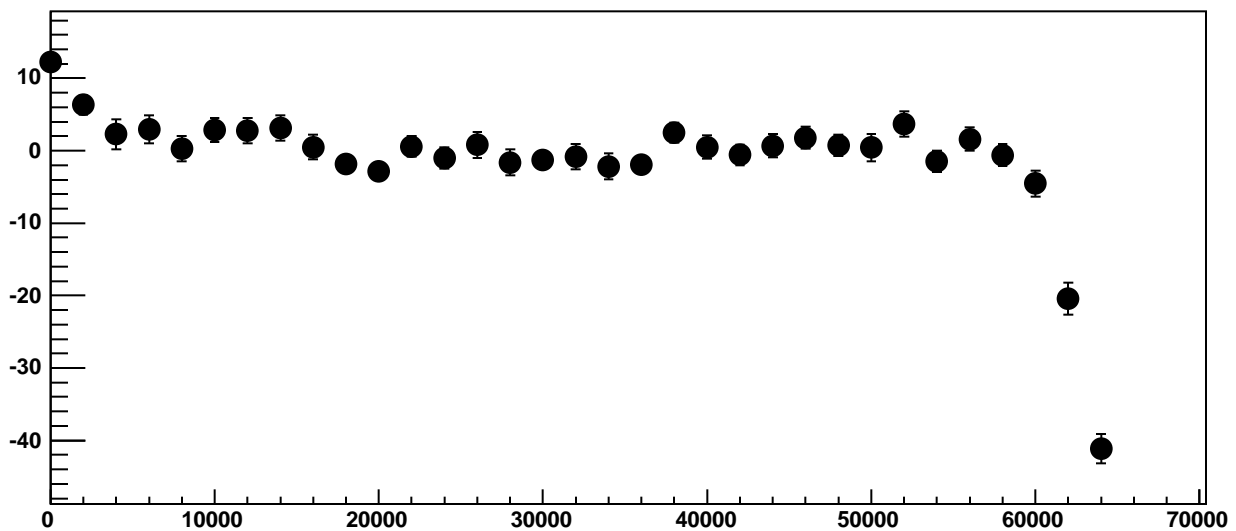


χ^2 / ndf 36.95 / 23
p0 70 ± 0.7251
p1 $0.003866 \pm 2.205e-05$

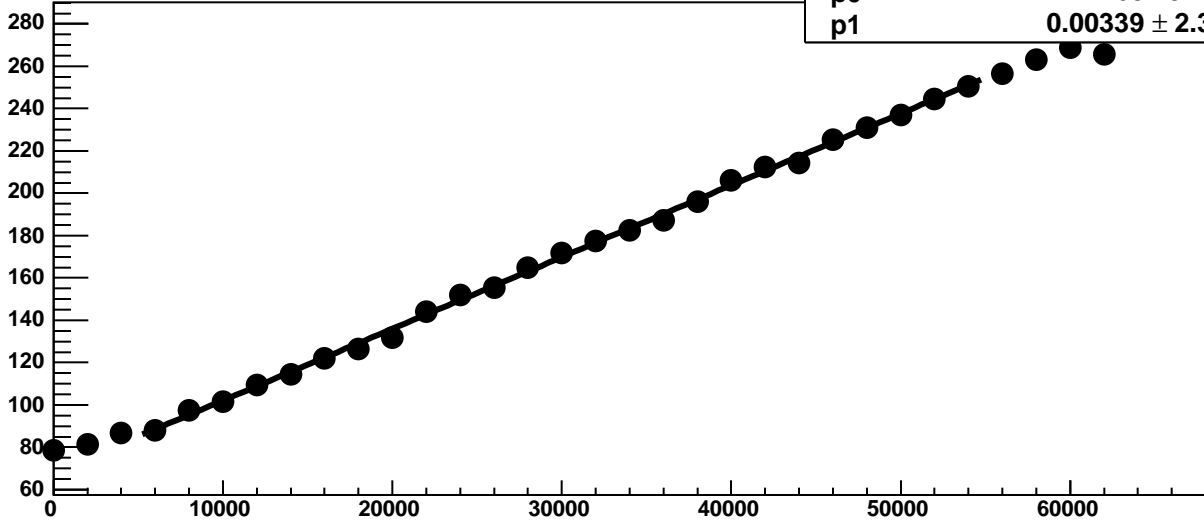
Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

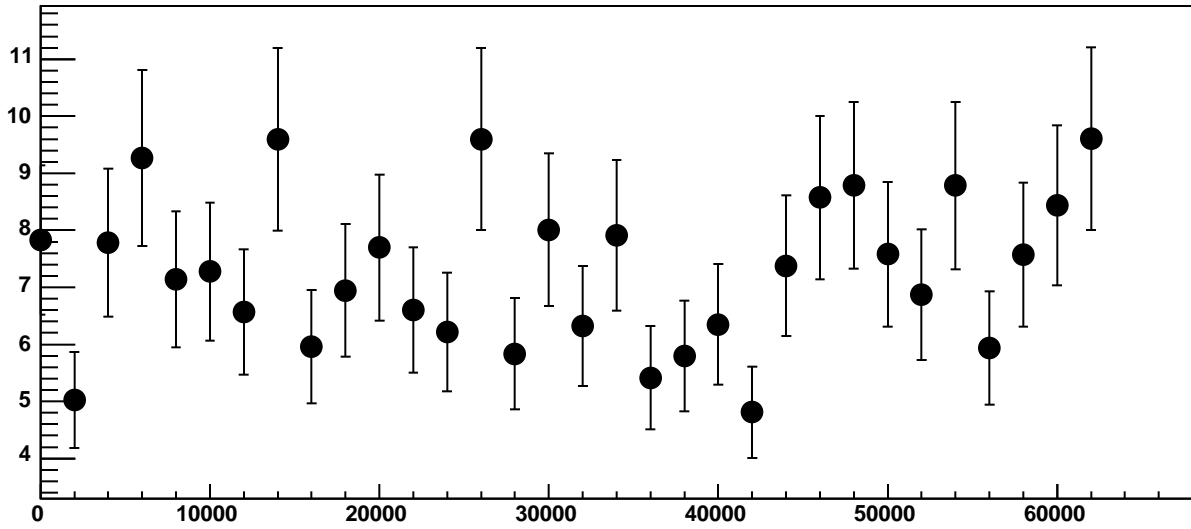


Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

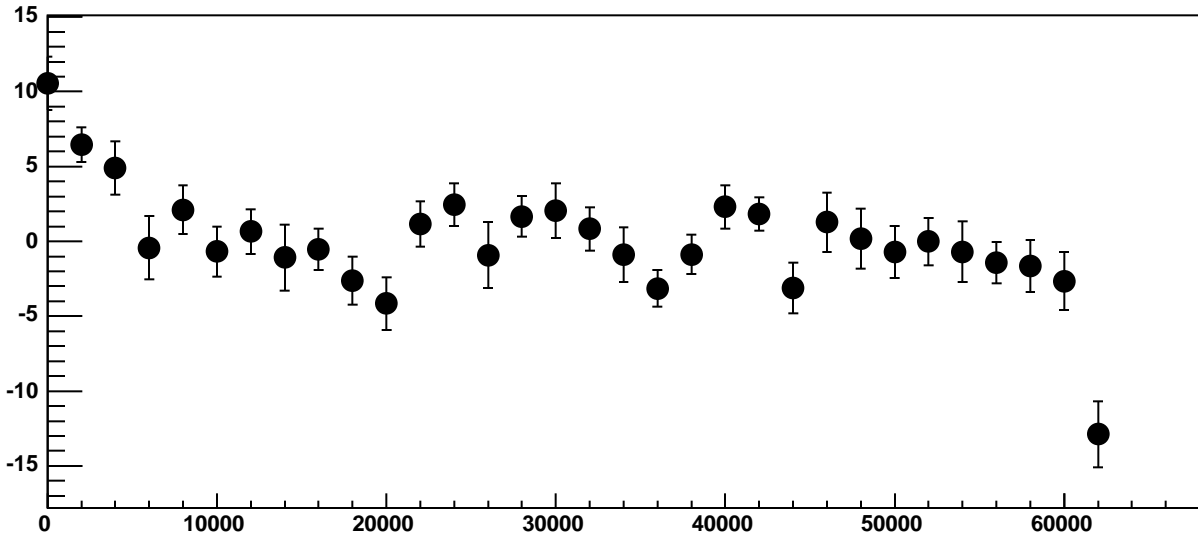


χ^2 / ndf 33.92 / 23
p0 68.19 ± 0.7834
p1 $0.00339 \pm 2.354e-05$

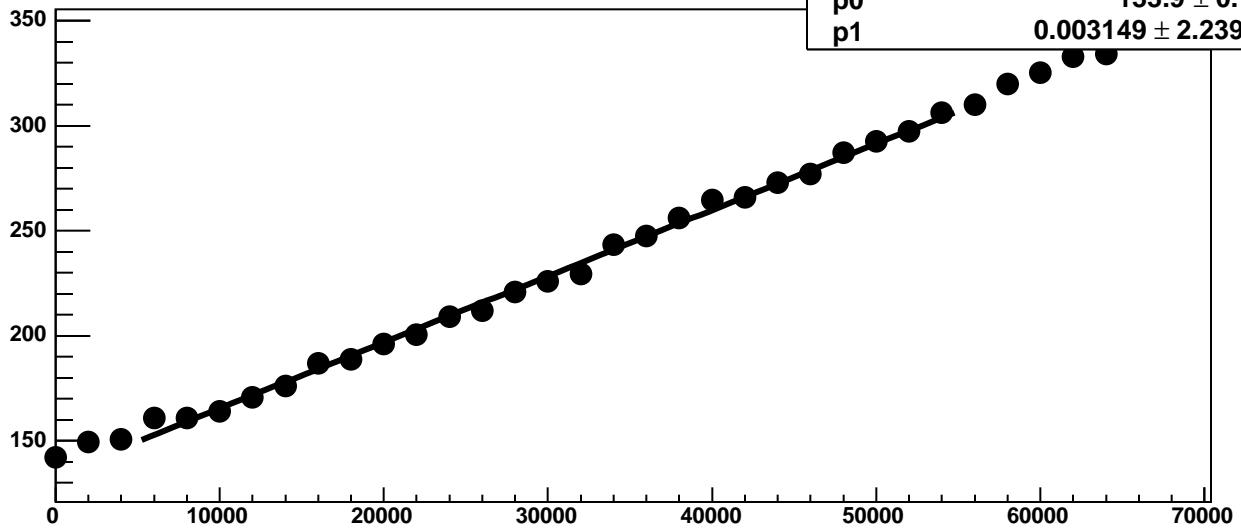
Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

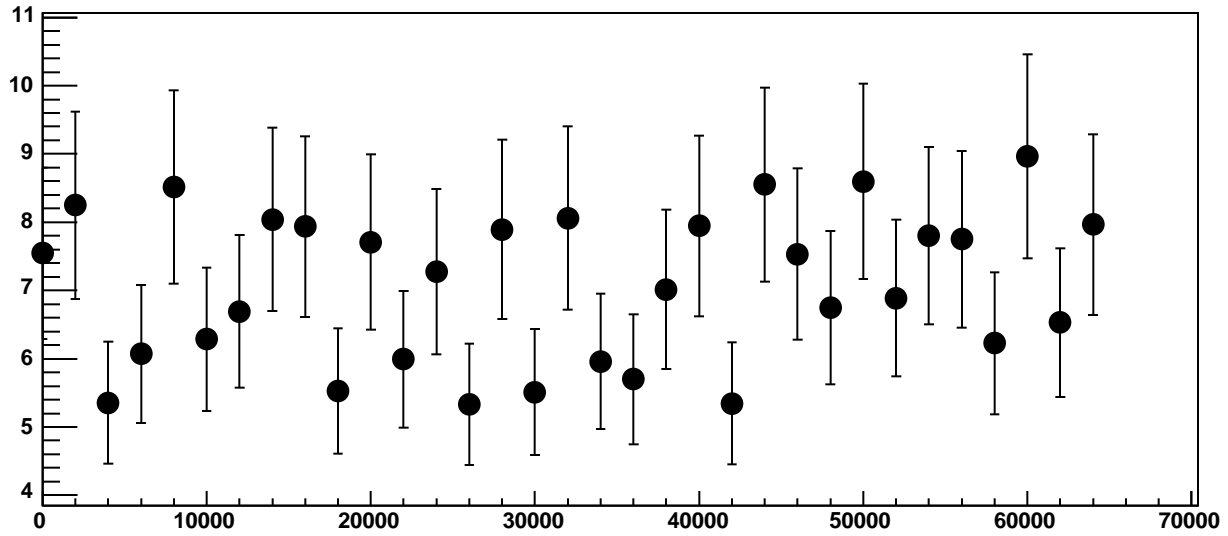


Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

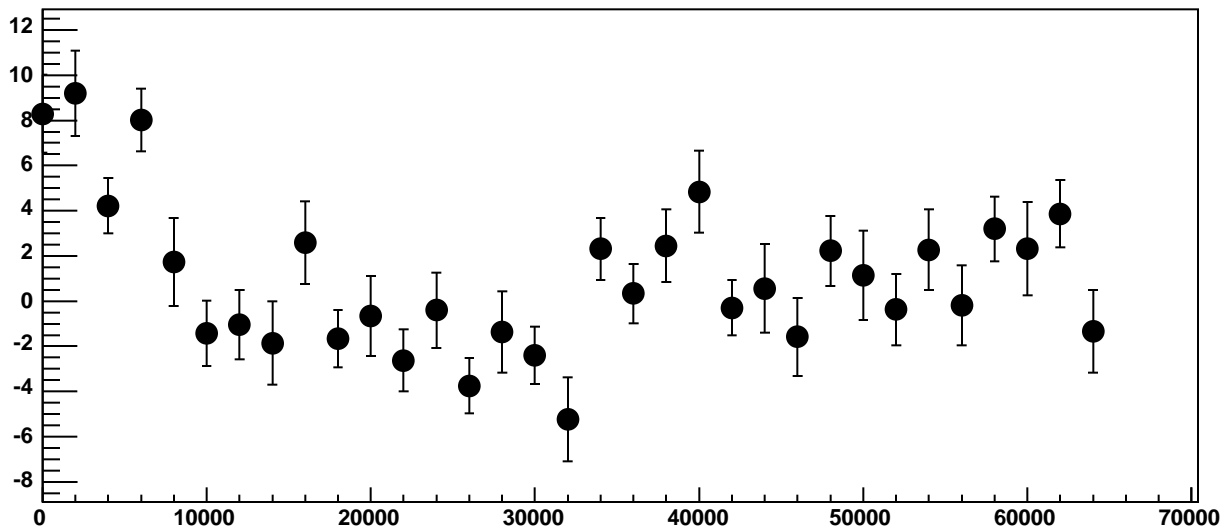


χ^2 / ndf 82.93 / 23
p0 133.9 ± 0.7272
p1 $0.003149 \pm 2.239\text{e-}05$

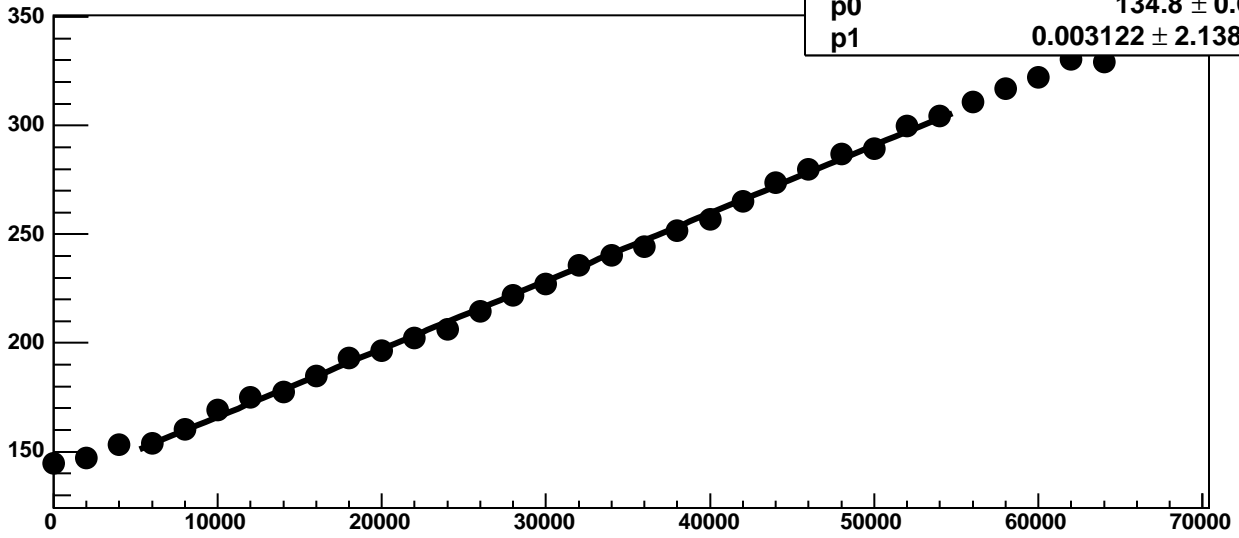
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.5 / 23

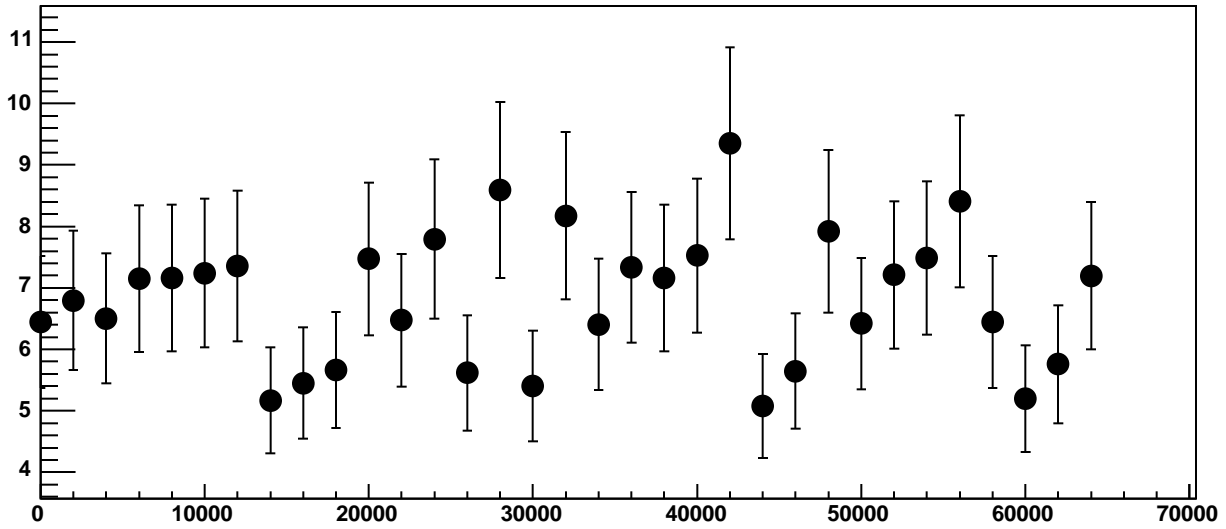
p0

134.8 ± 0.6993

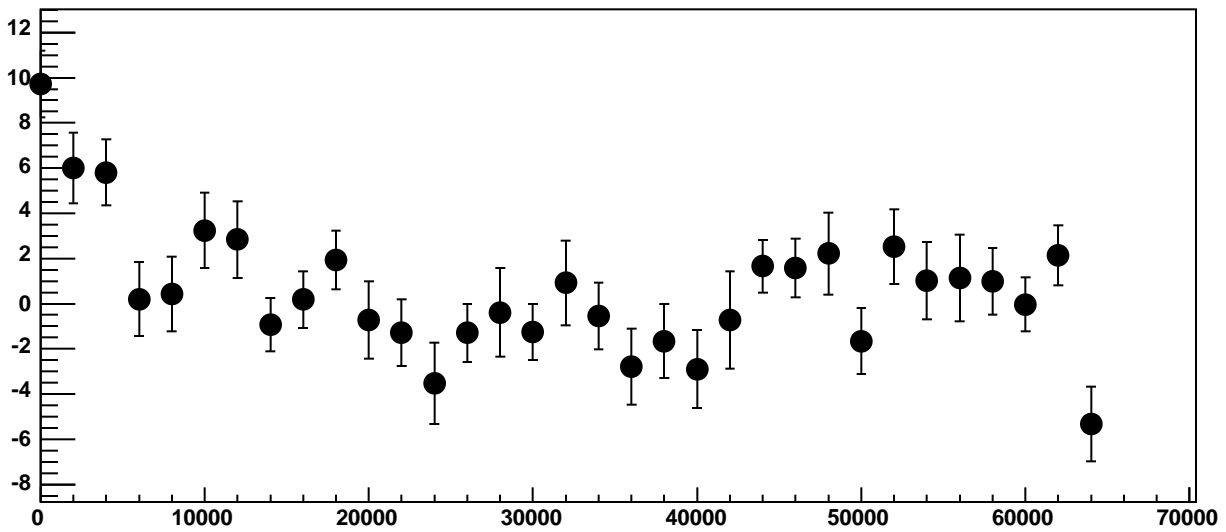
p1

$0.003122 \pm 2.138e-05$

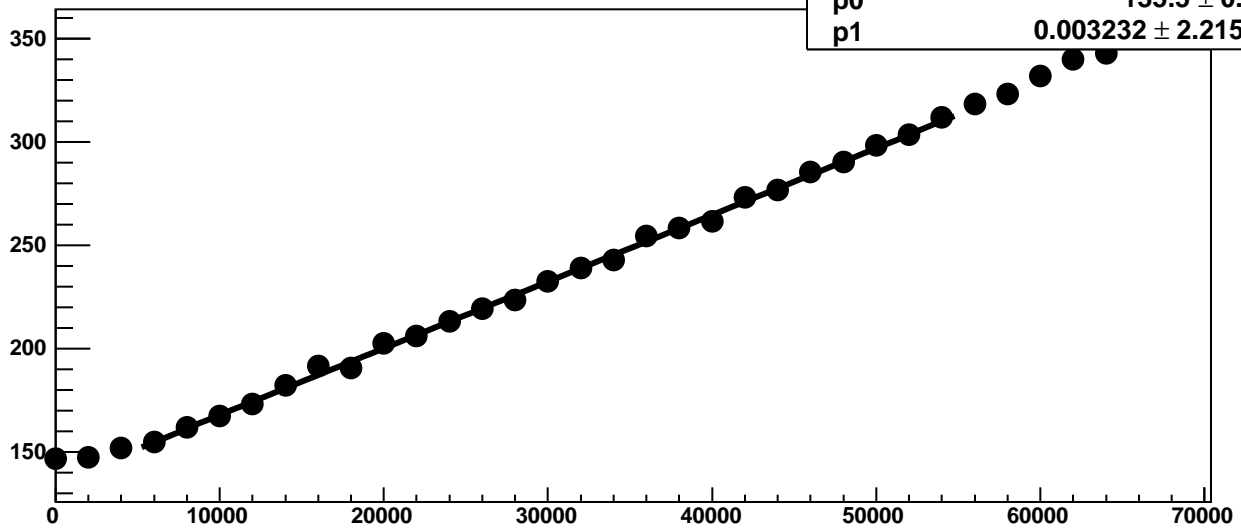
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



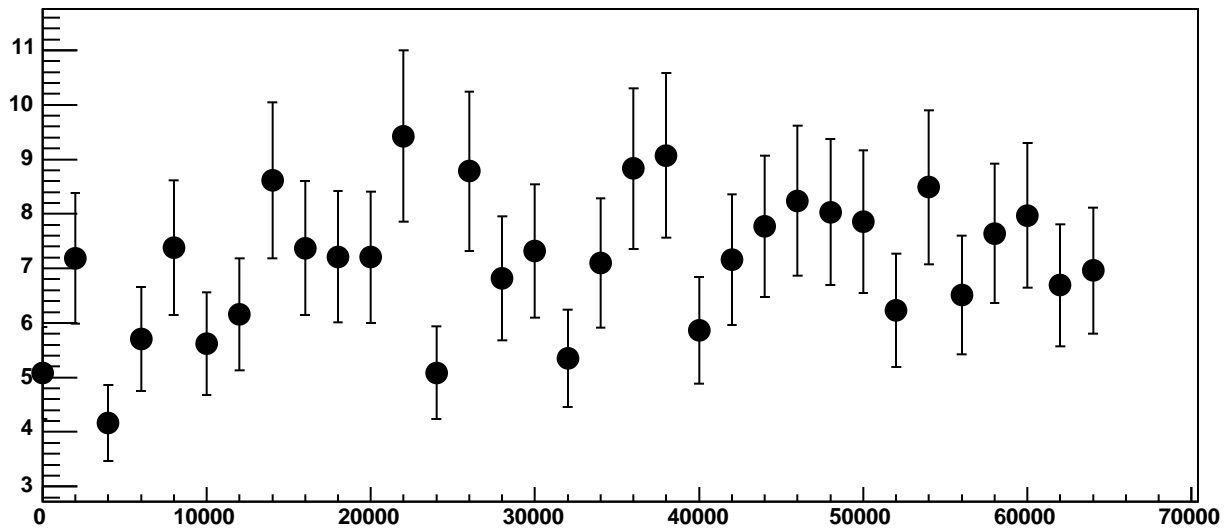
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



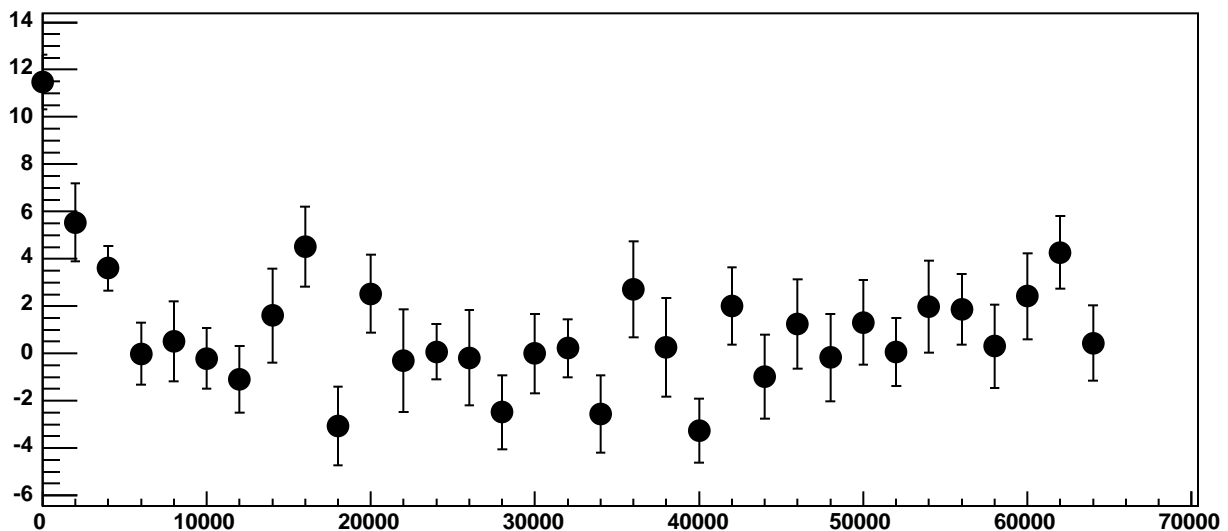
Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



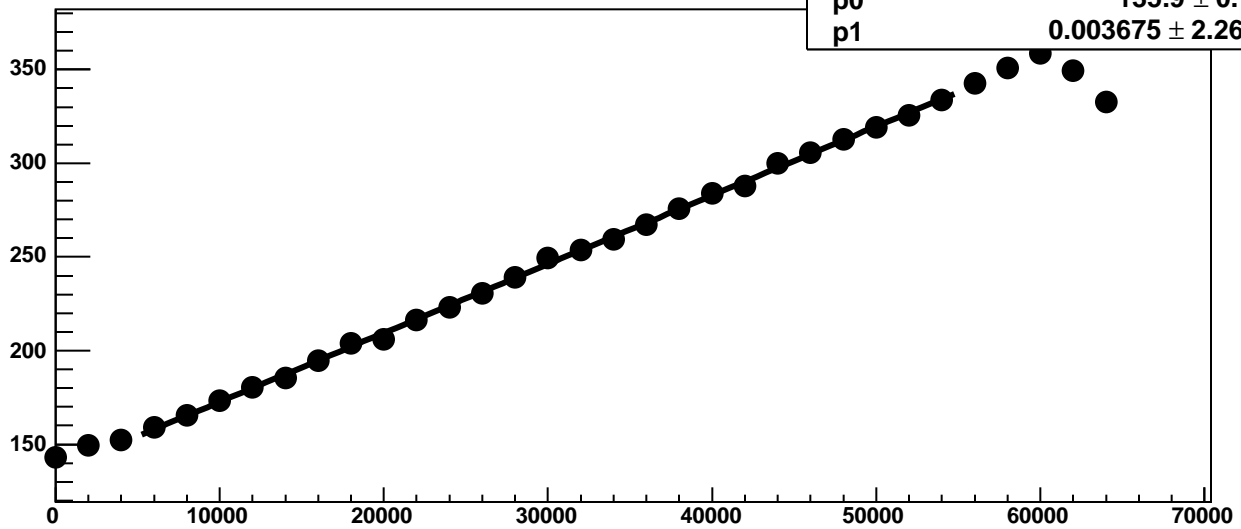
Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



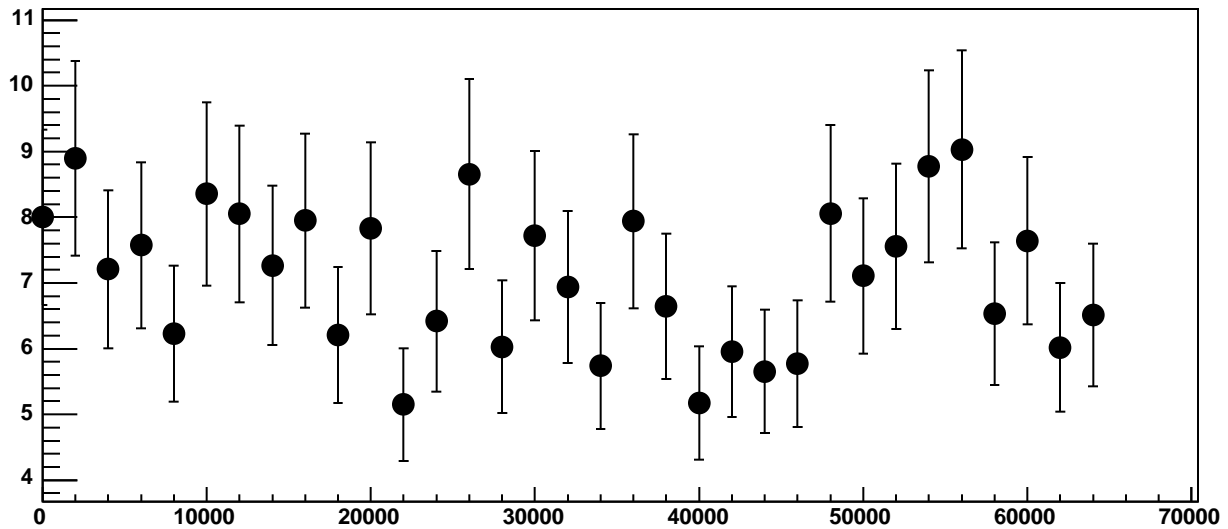
Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



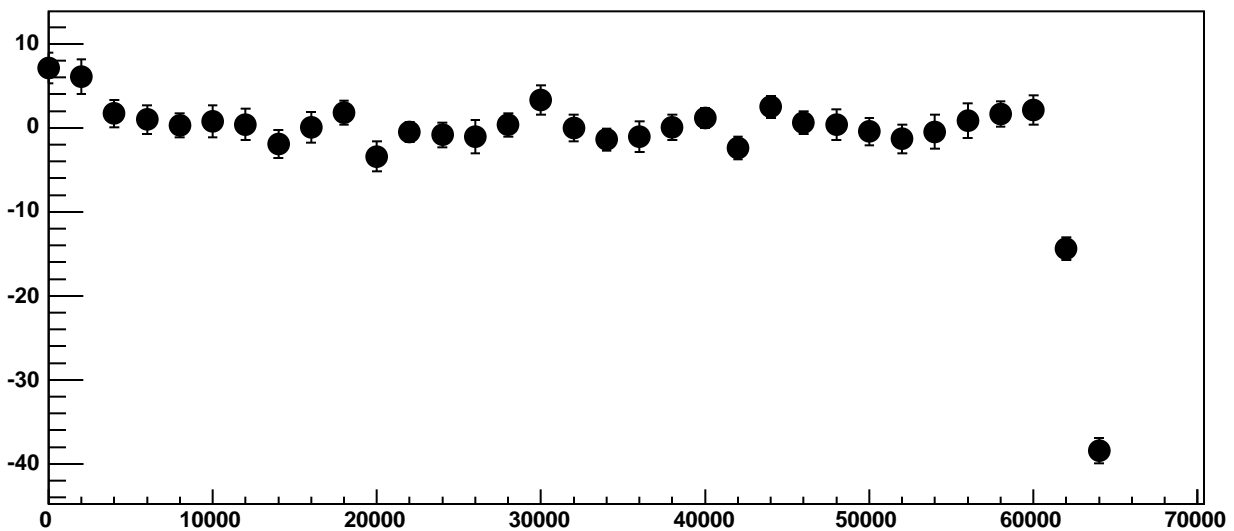
Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



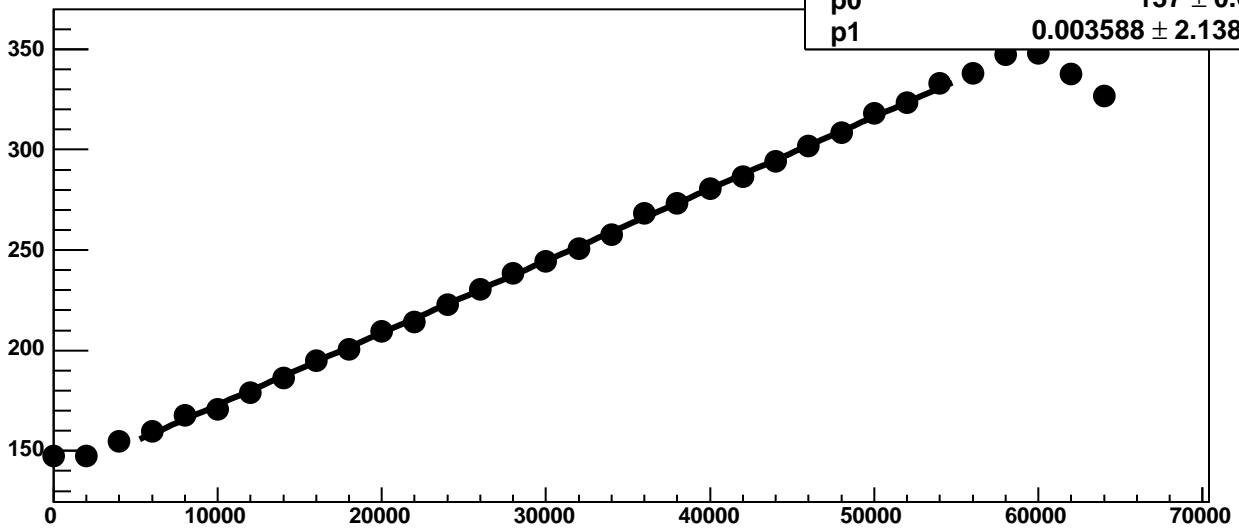
Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

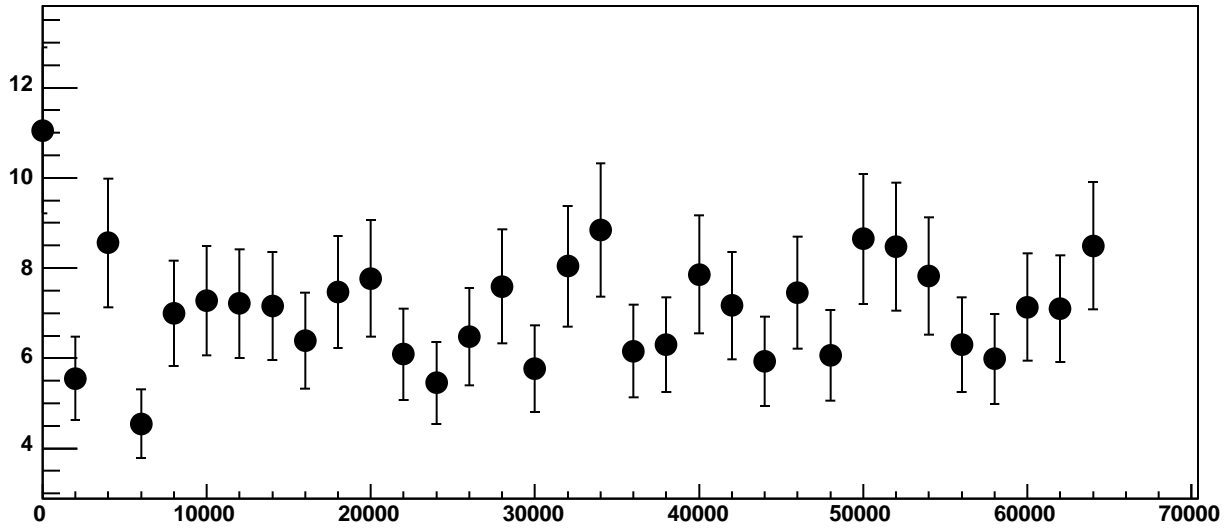


Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

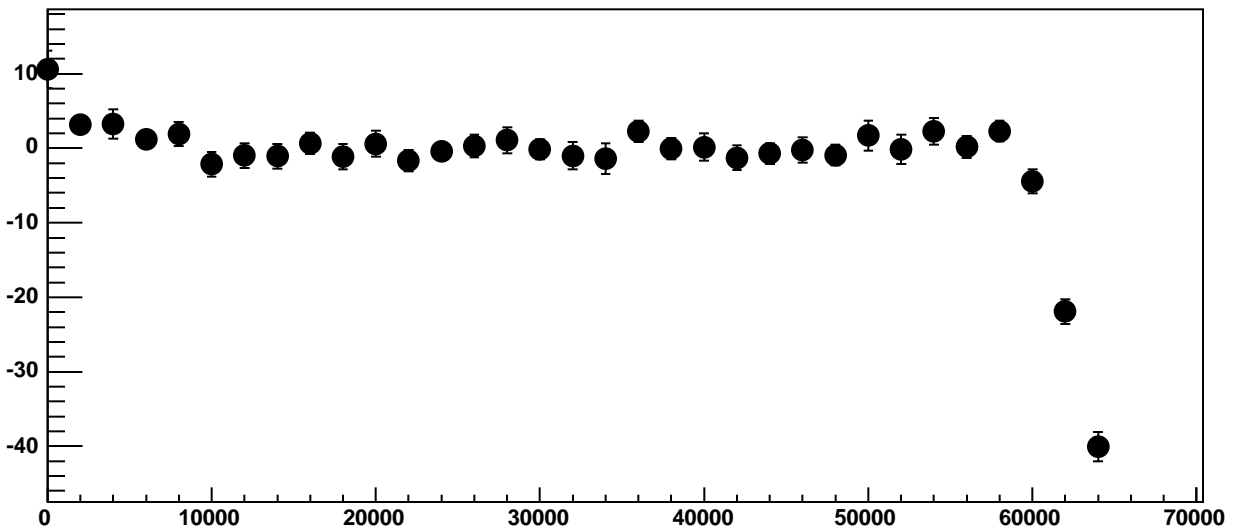


χ^2 / ndf 14.82 / 23
p0 137 ± 0.6795
p1 $0.003588 \pm 2.138e-05$

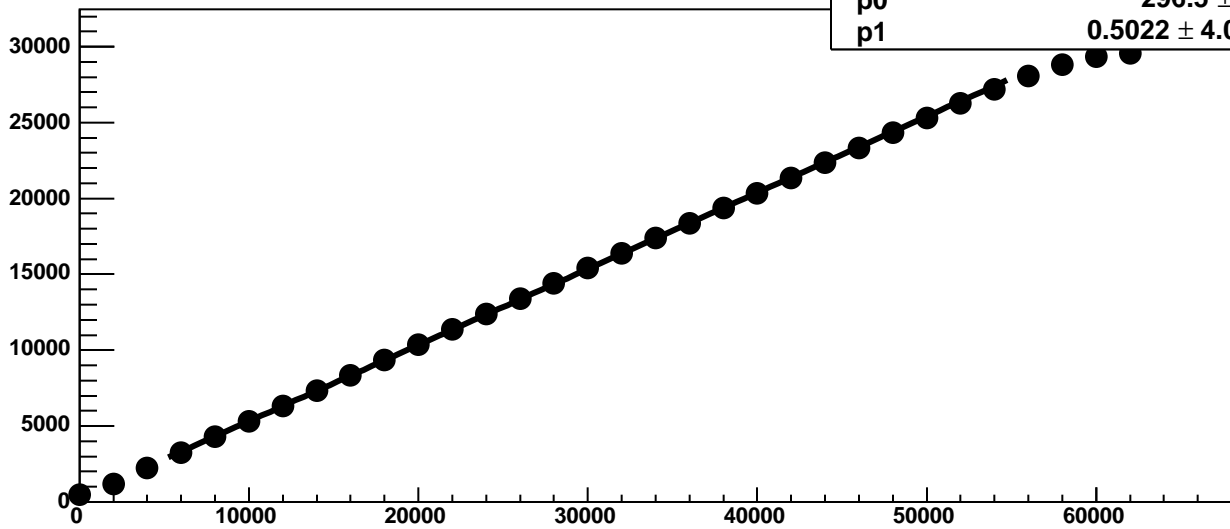
Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

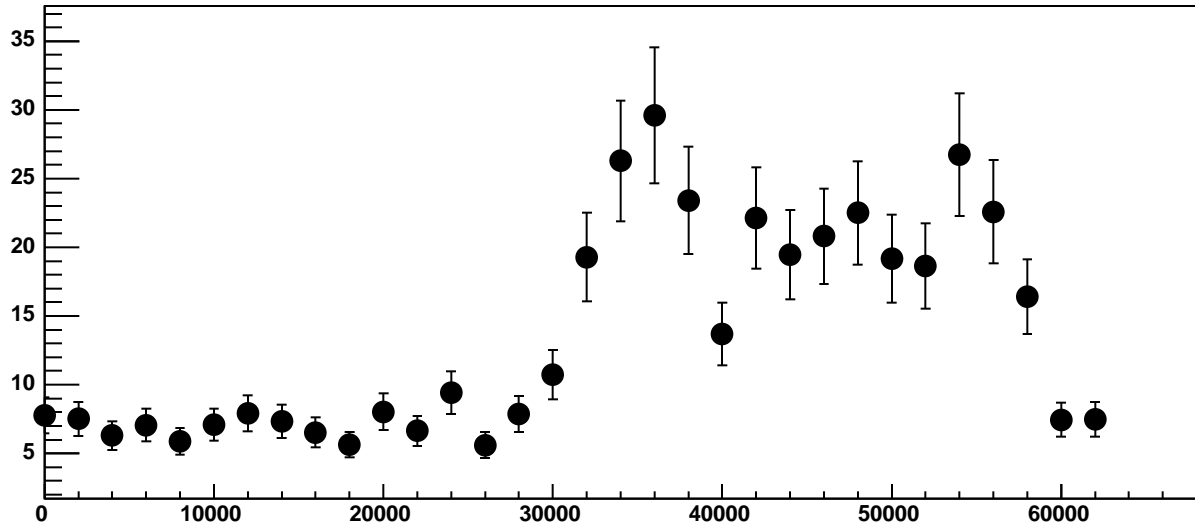


Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

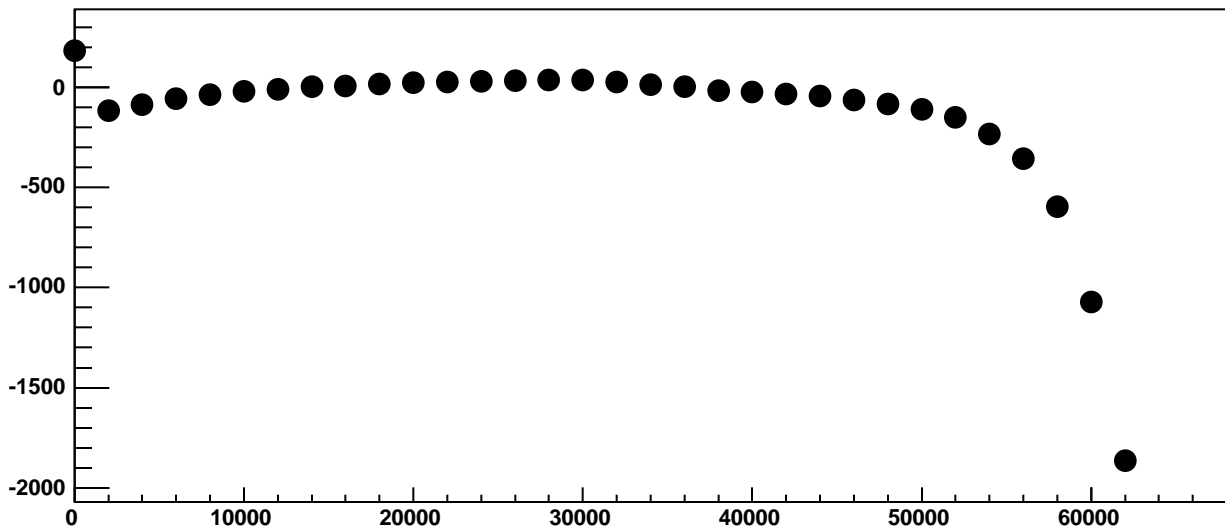


χ^2 / ndf 8248 / 23
p0 296.5 ± 0.9078
p1 $0.5022 \pm 4.063e-05$

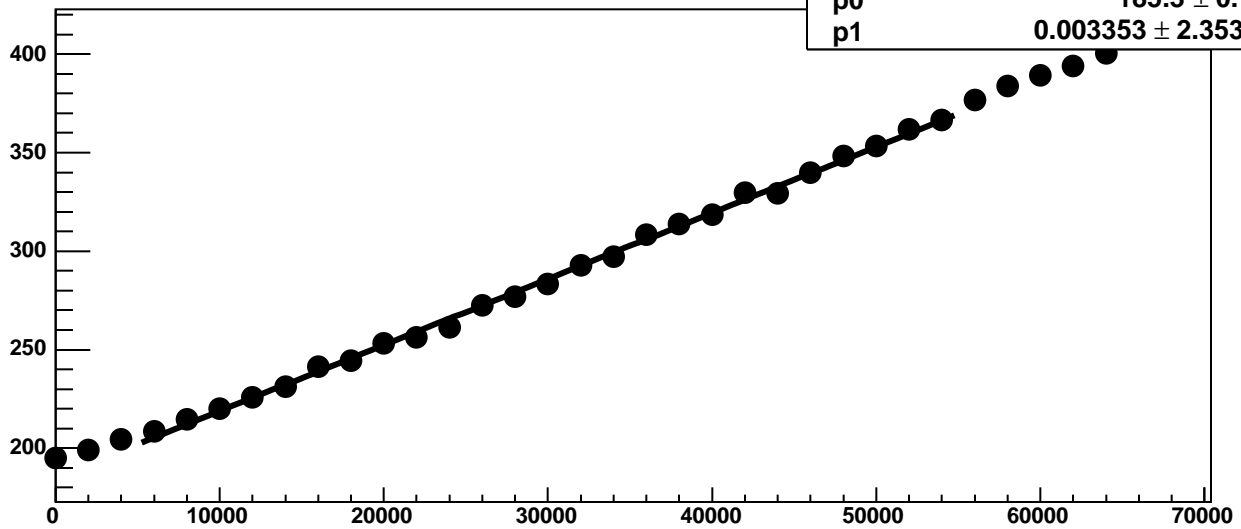
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

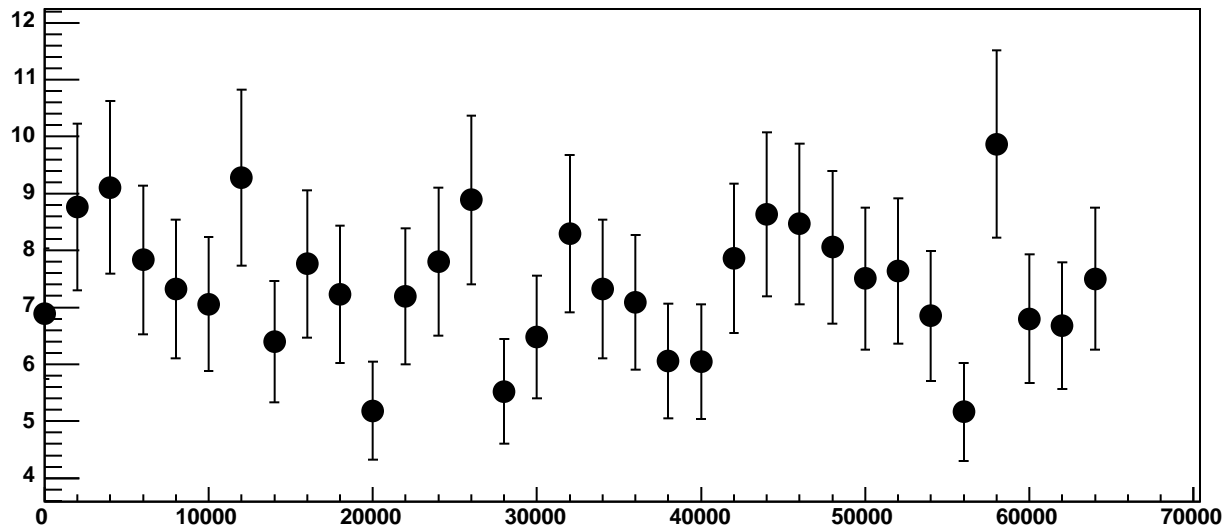


Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

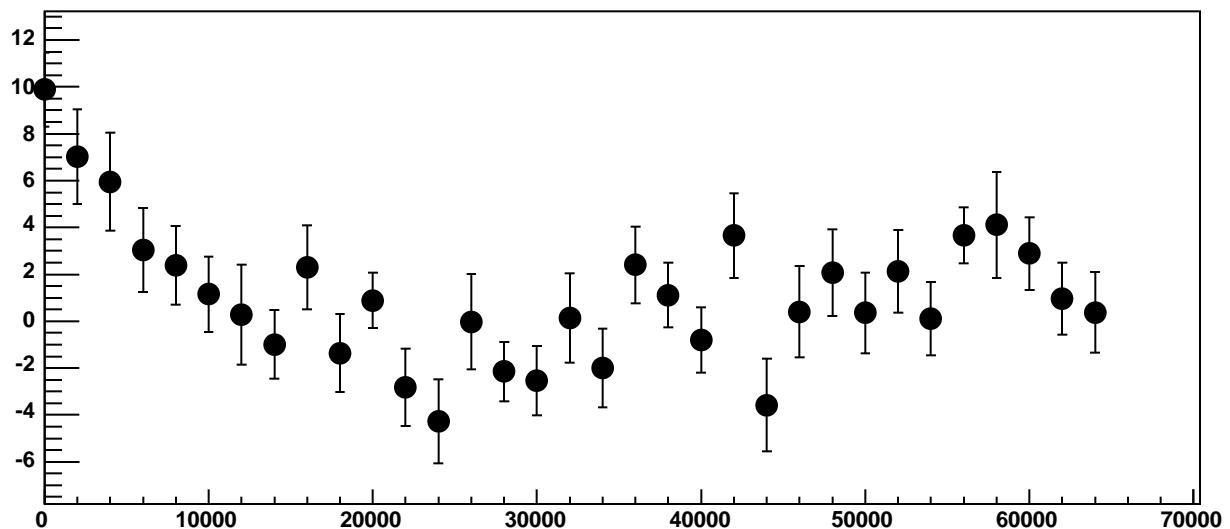


χ^2 / ndf 37.83 / 23
p0 185.3 ± 0.7708
p1 $0.003353 \pm 2.353e-05$

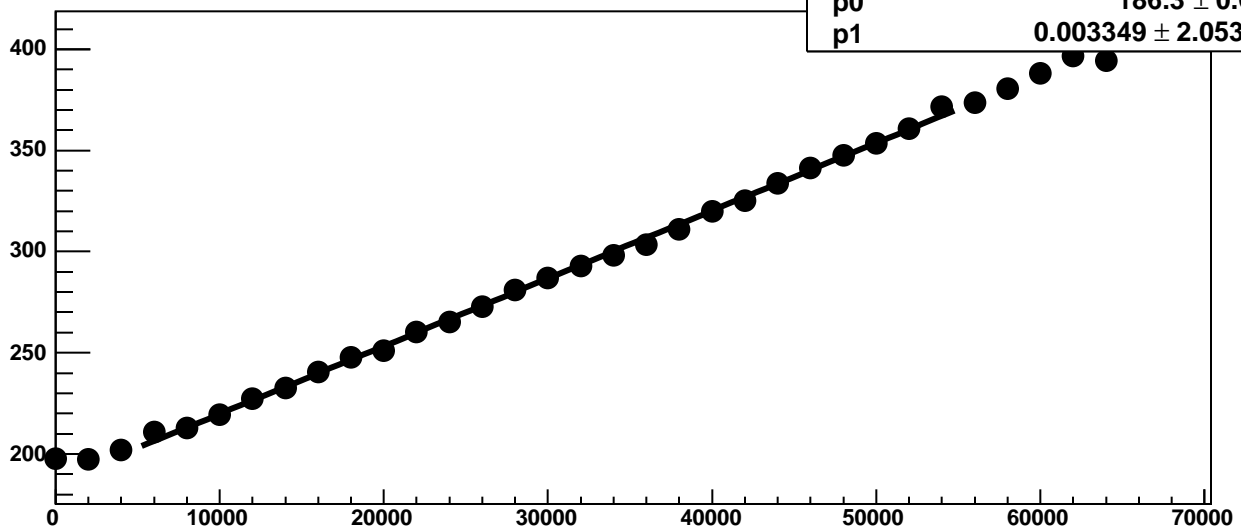
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

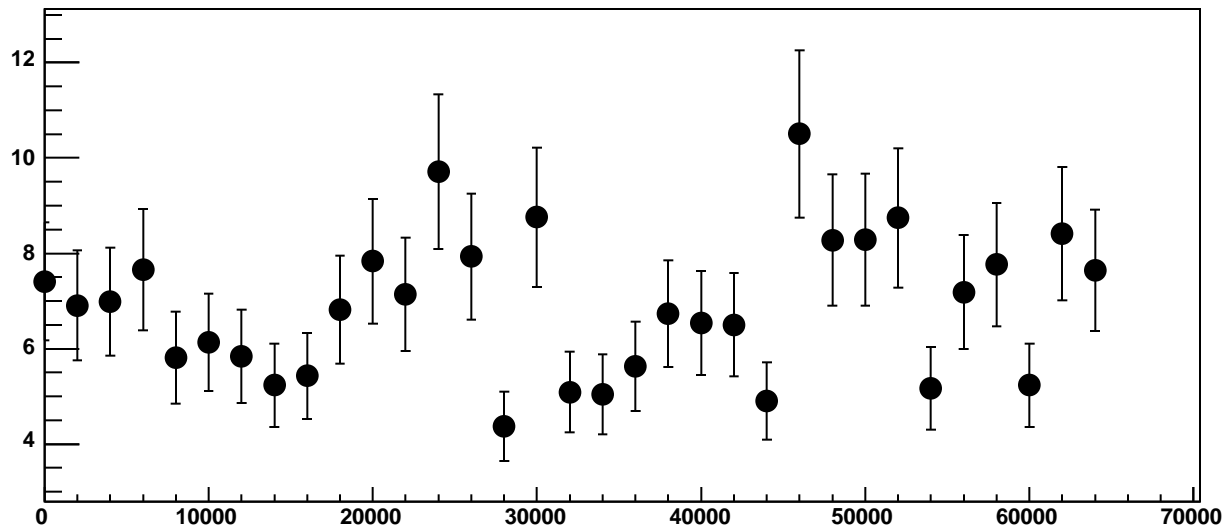


Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

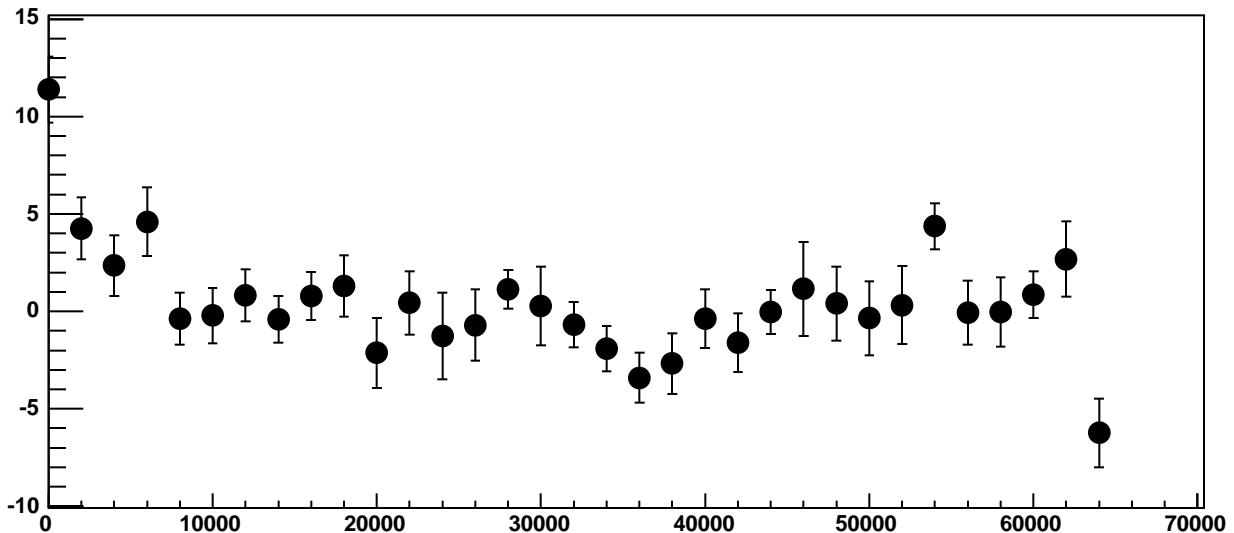


χ^2 / ndf 39.92 / 23
p0 186.3 ± 0.6684
p1 $0.003349 \pm 2.053e-05$

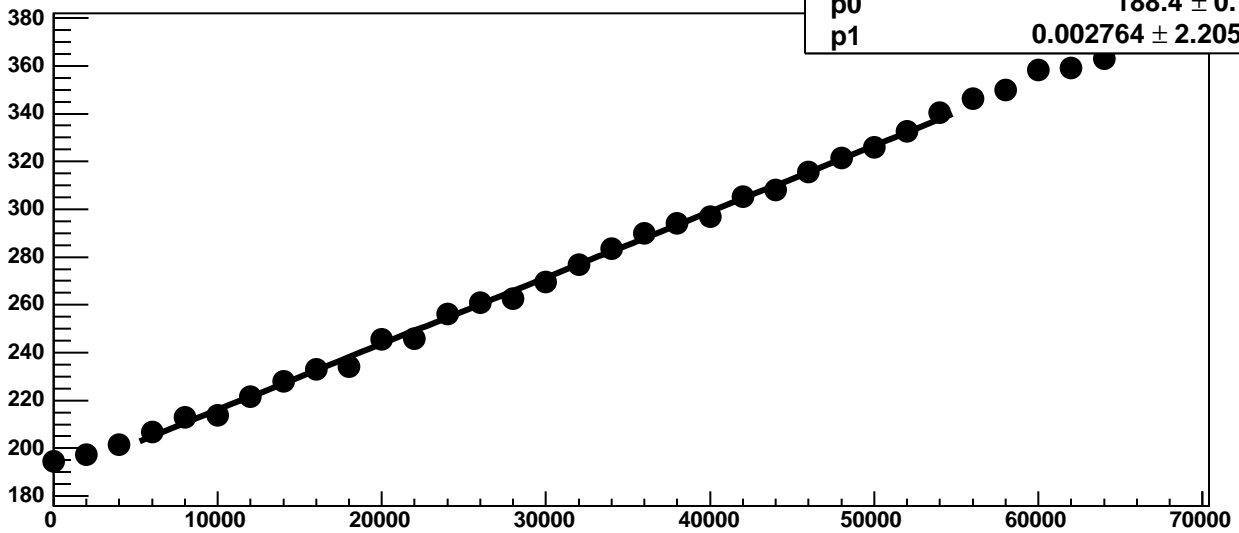
Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

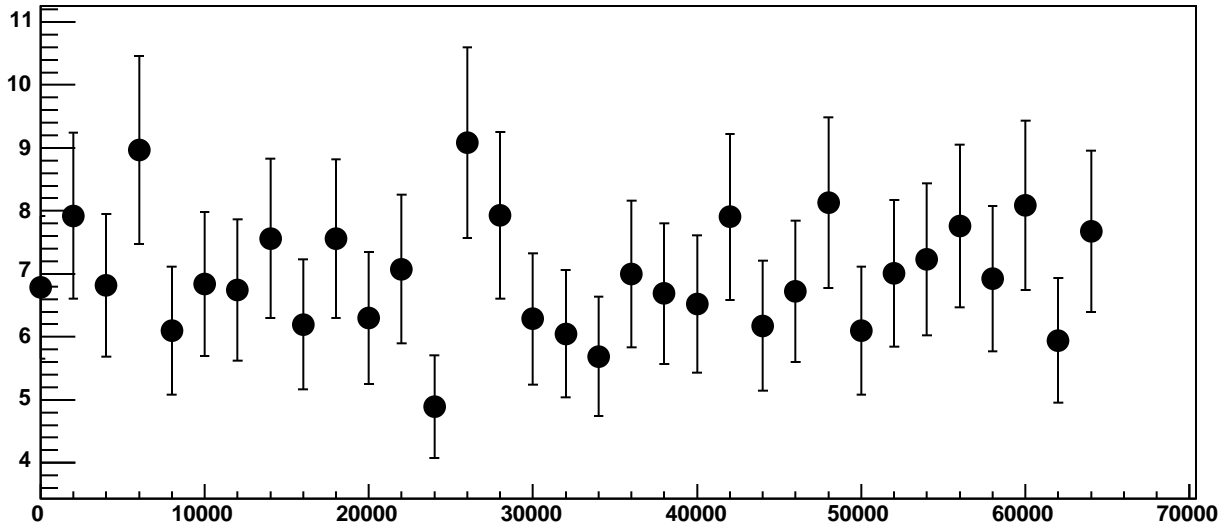


Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

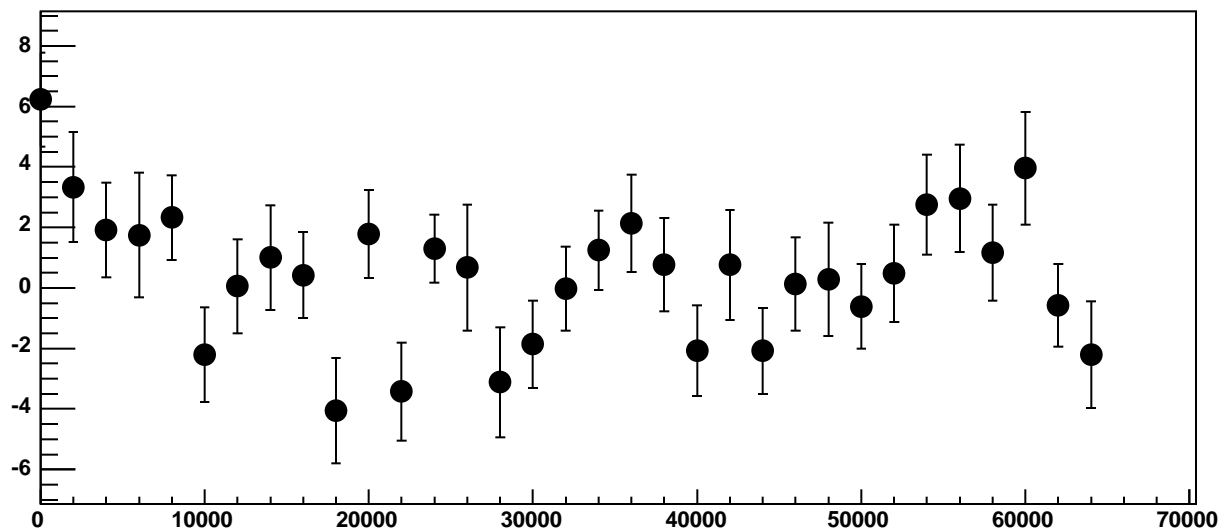


χ^2 / ndf 33.65 / 23
p0 188.4 ± 0.7309
p1 0.002764 ± 2.205e-05

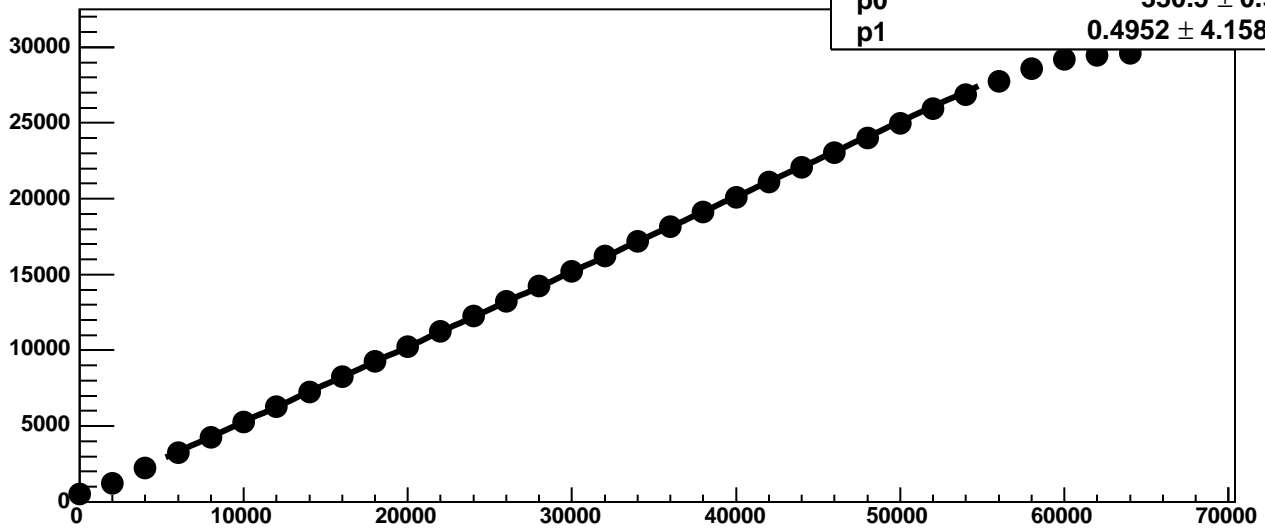
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



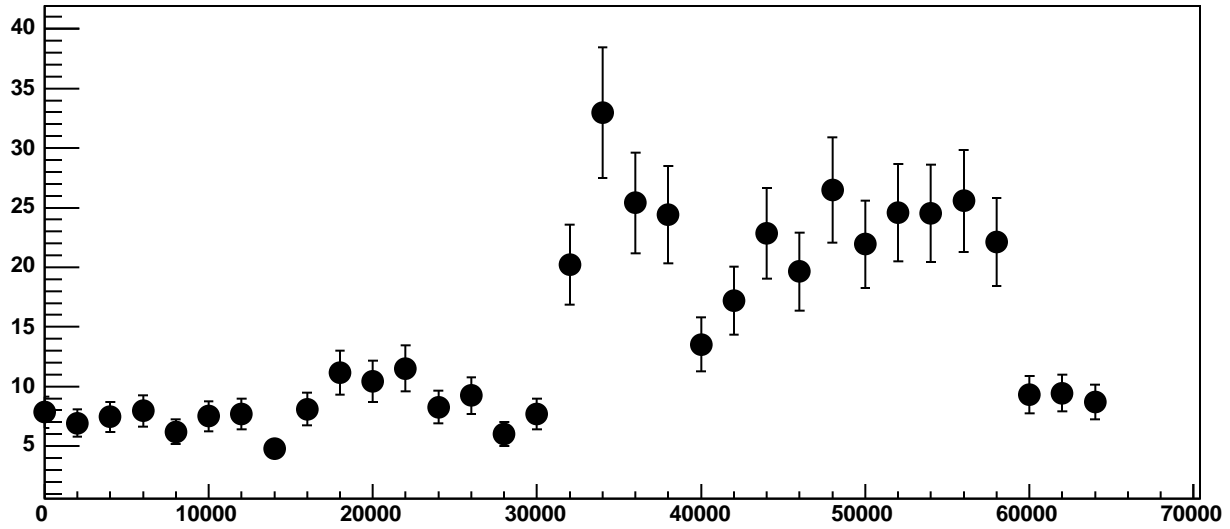
Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



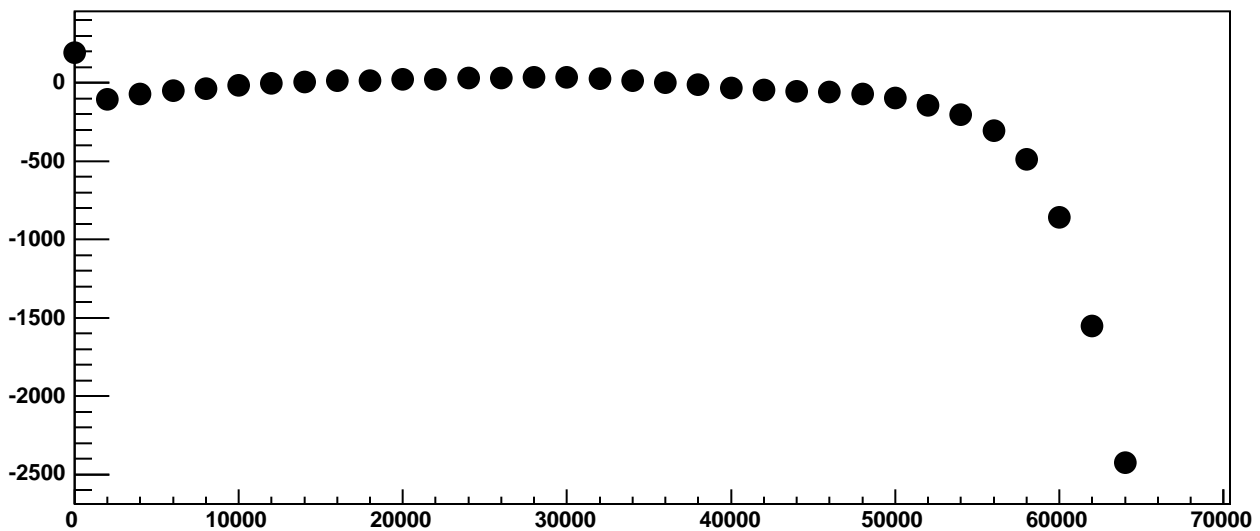
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



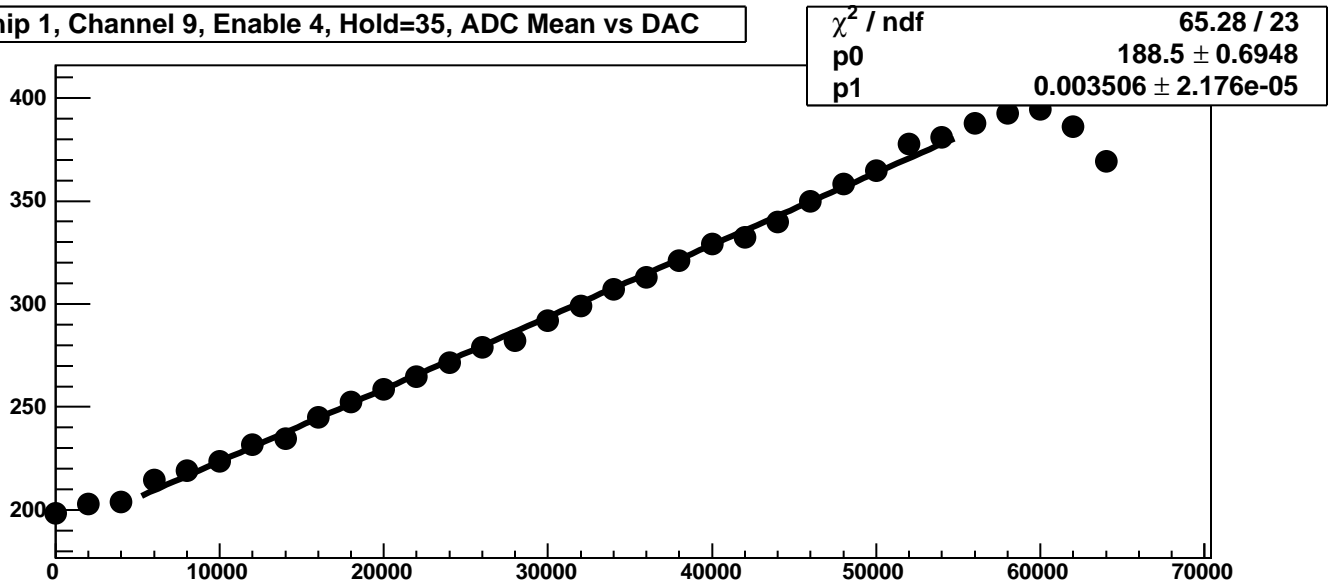
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



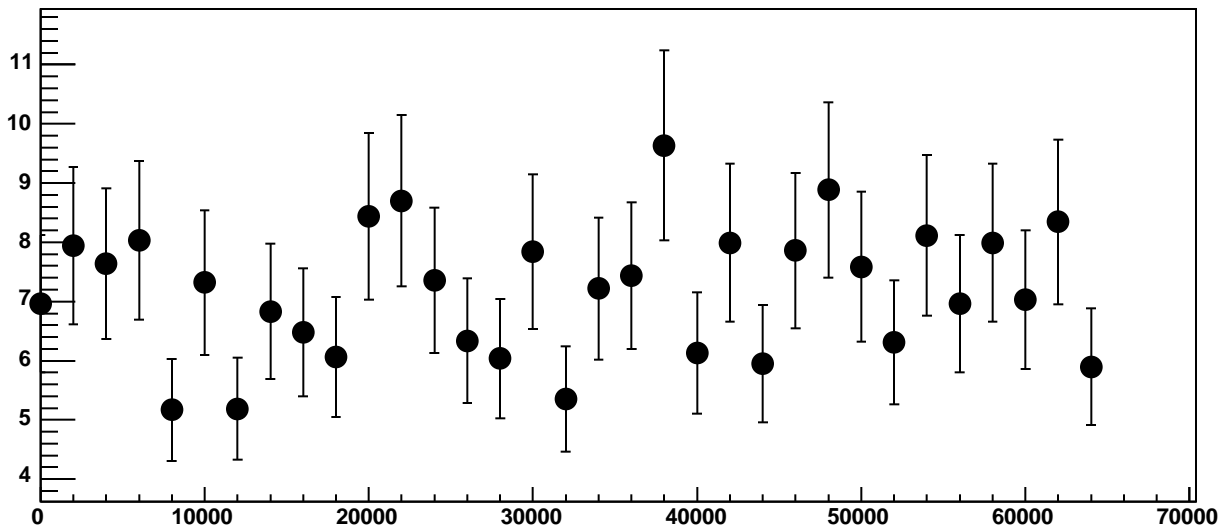
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



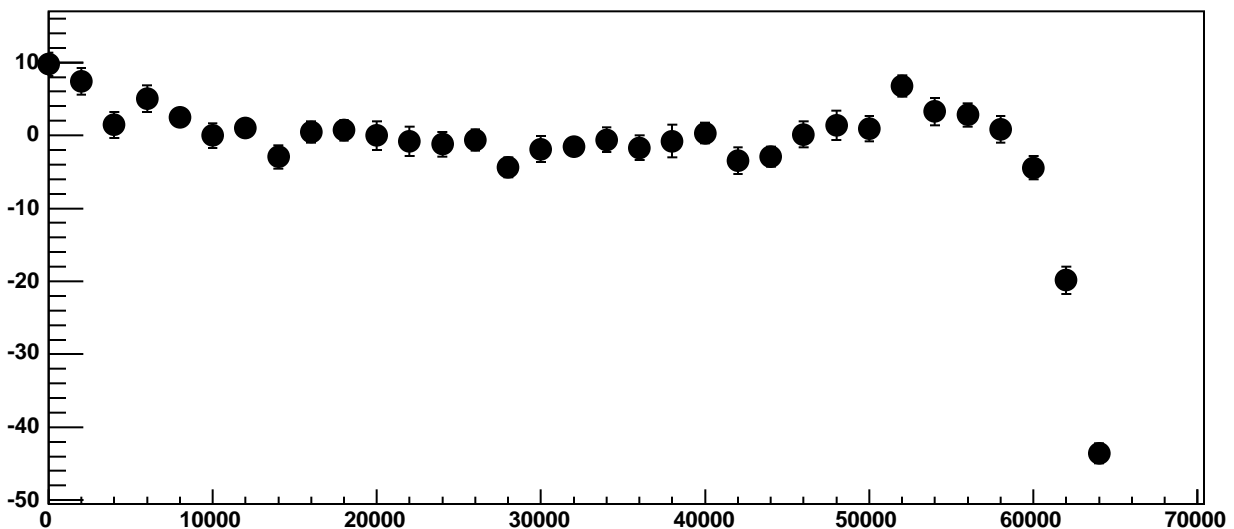
Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



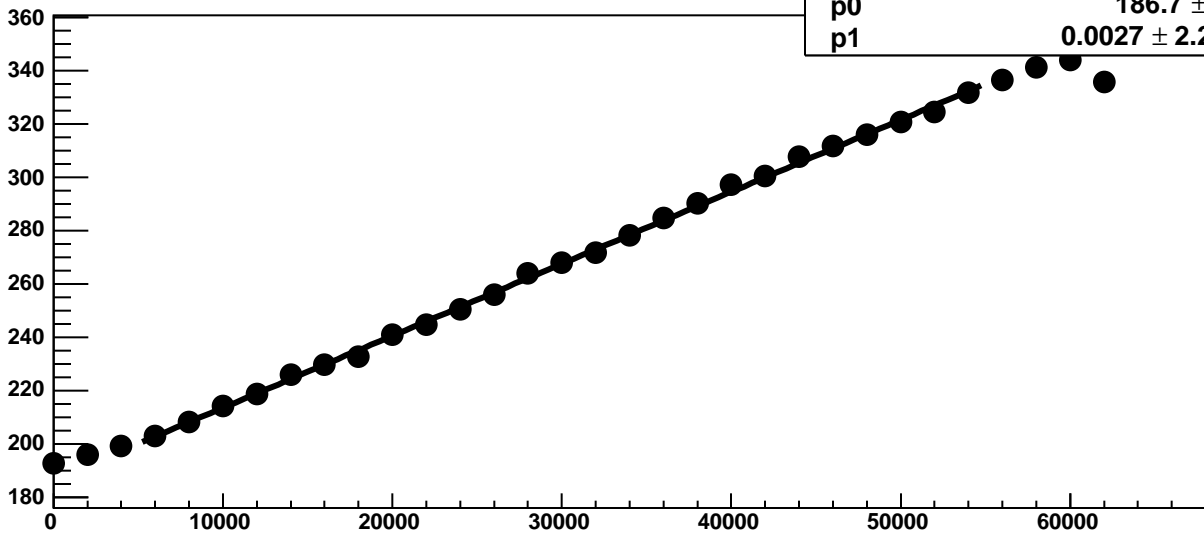
Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

13.84 / 23

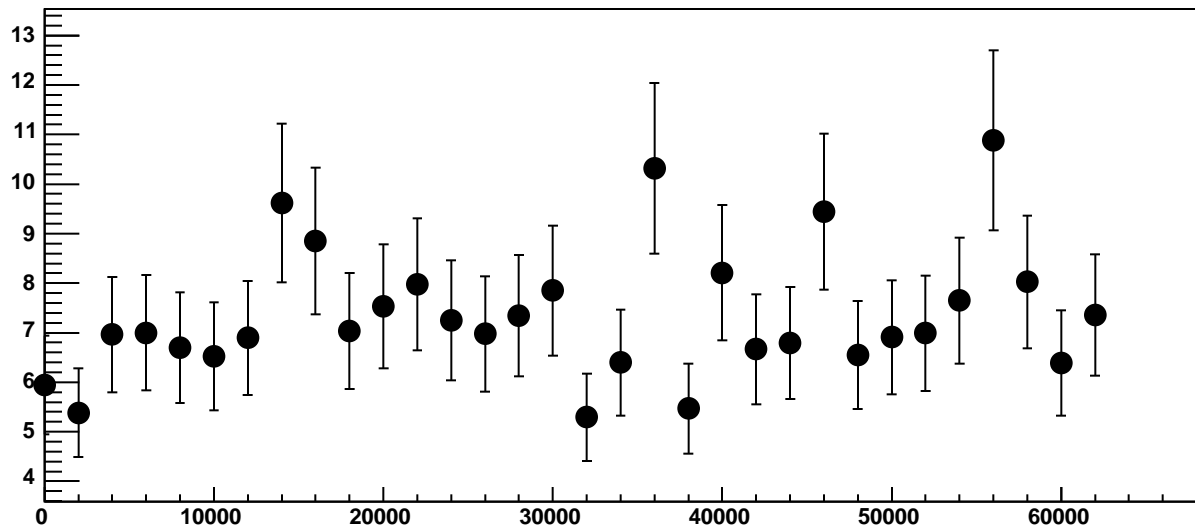
p0

186.7 ± 0.7634

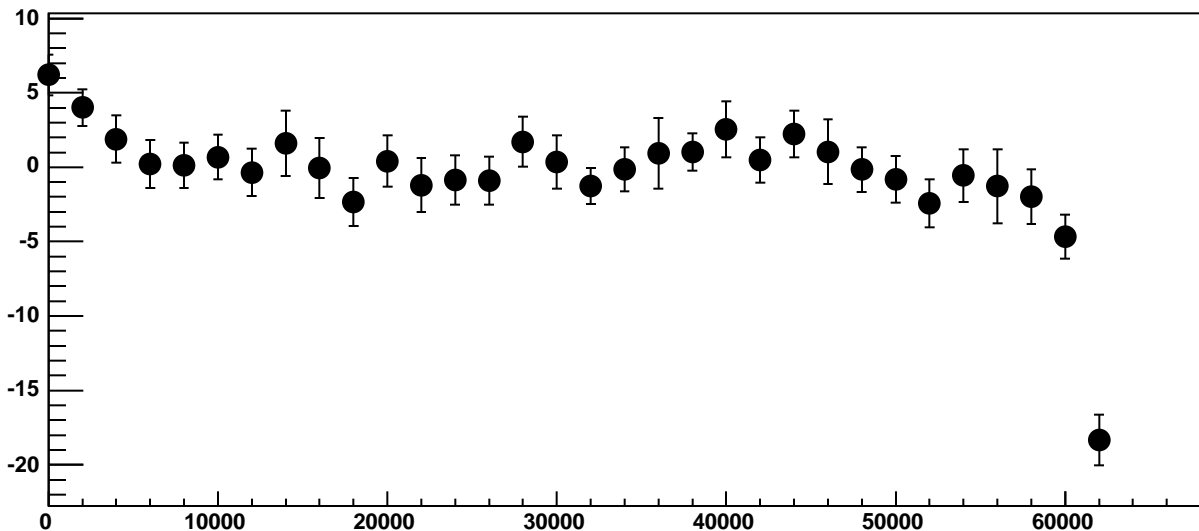
p1

$0.0027 \pm 2.279e-05$

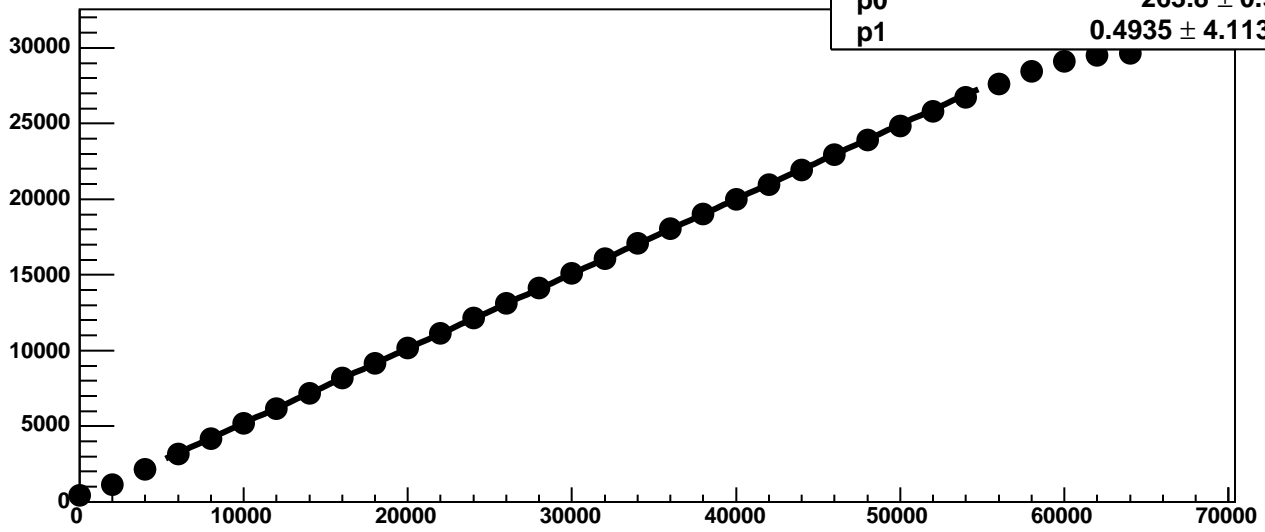
Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



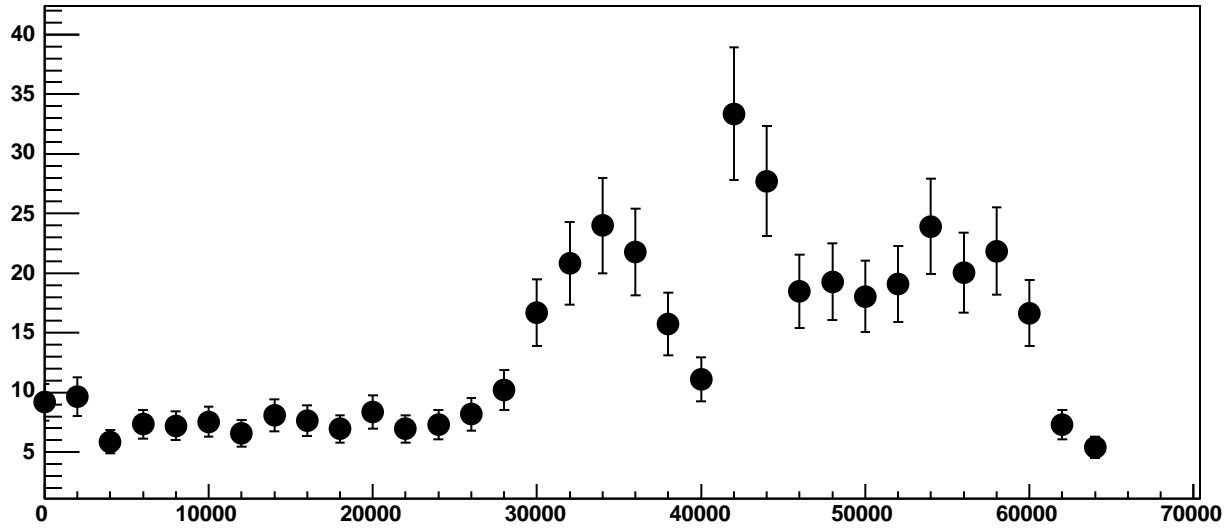
Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



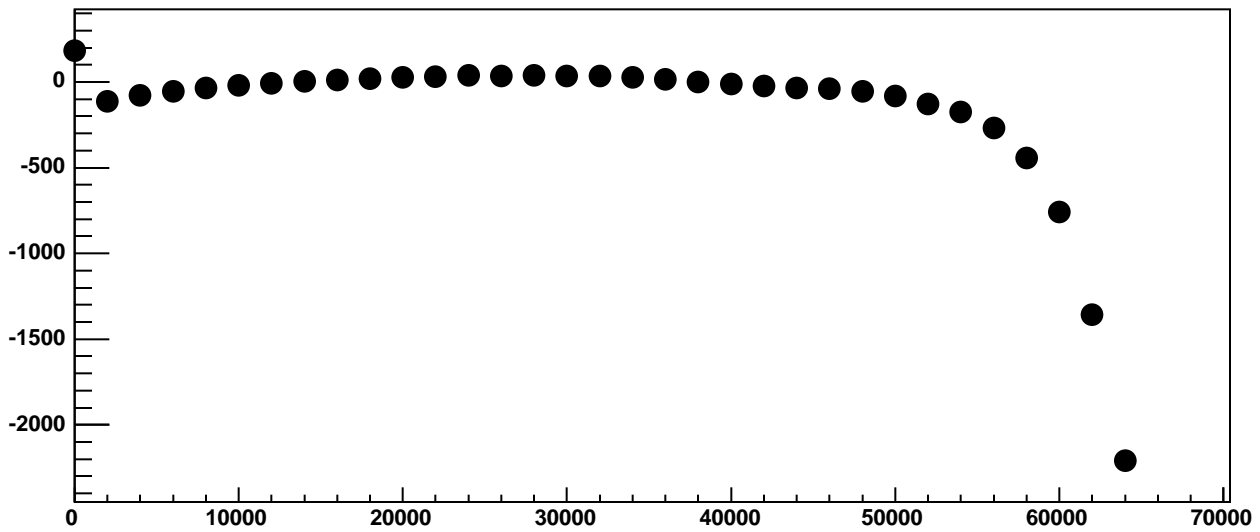
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



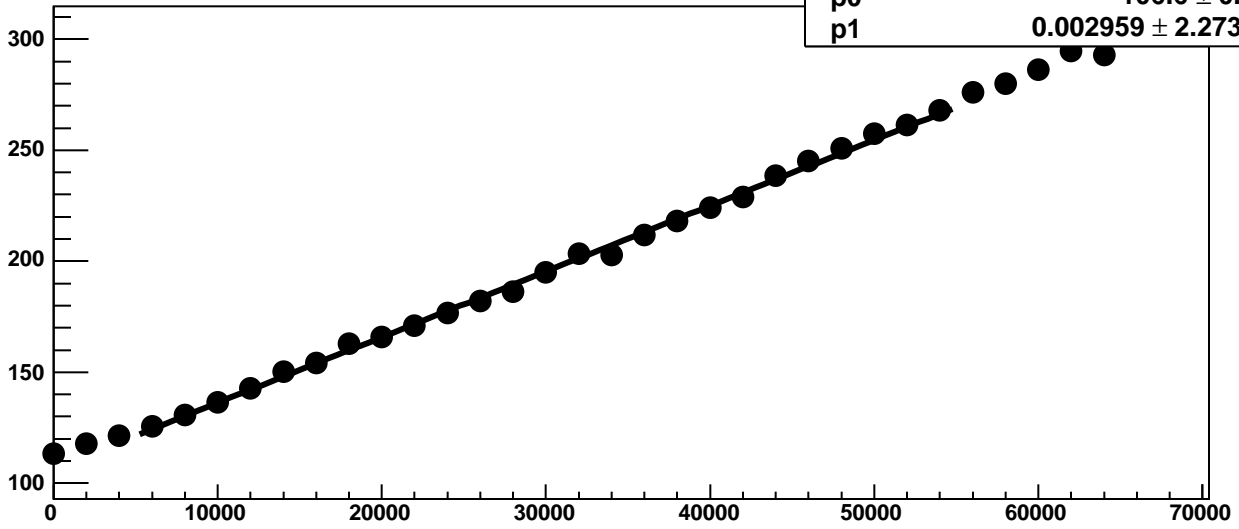
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.64 / 23

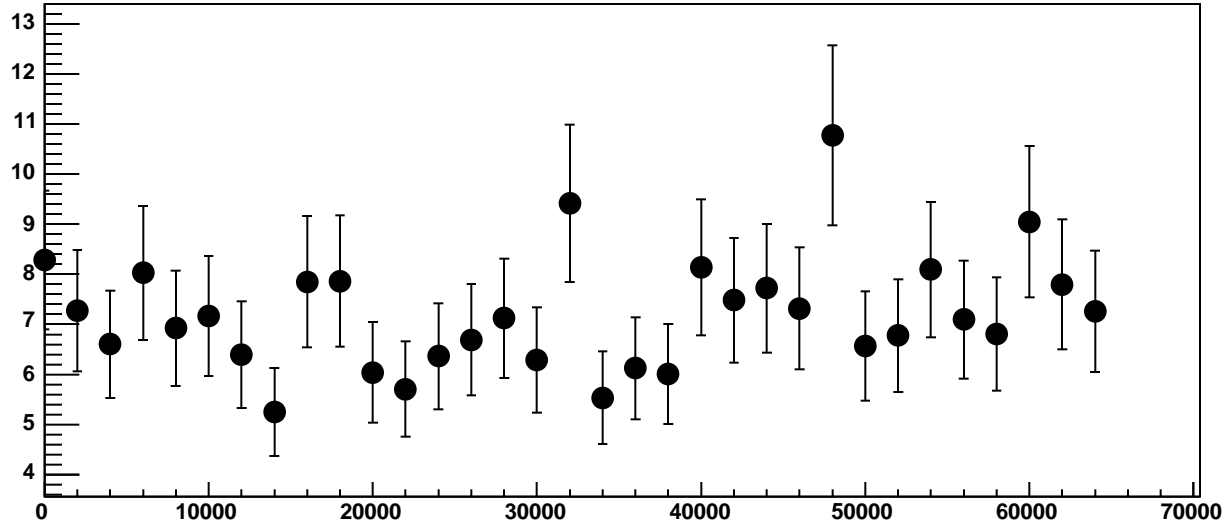
p0

106.6 ± 0.7291

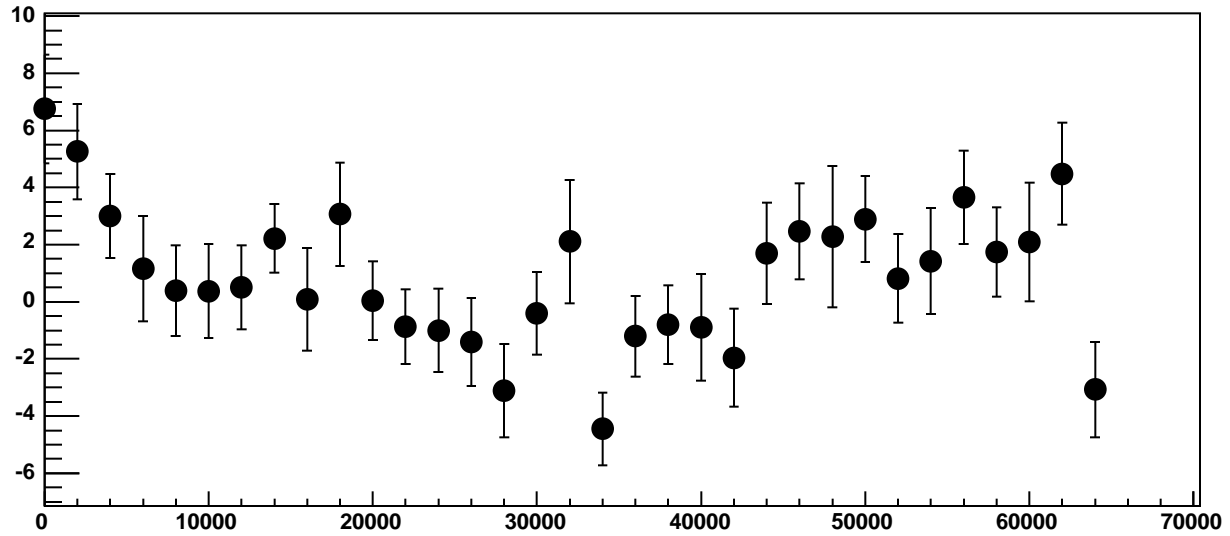
p1

$0.002959 \pm 2.273e-05$

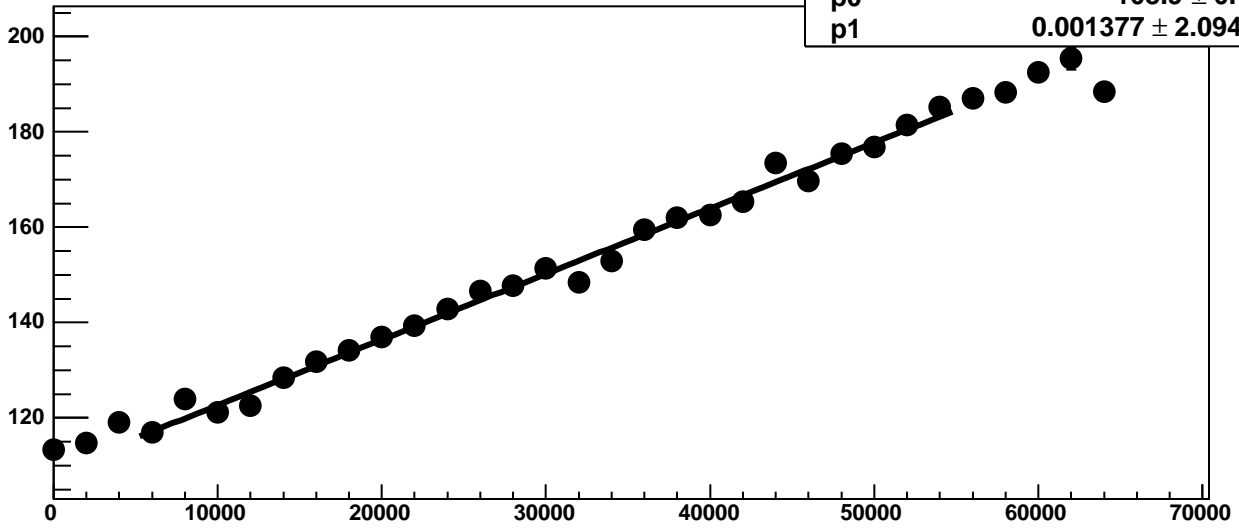
Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.81 / 23

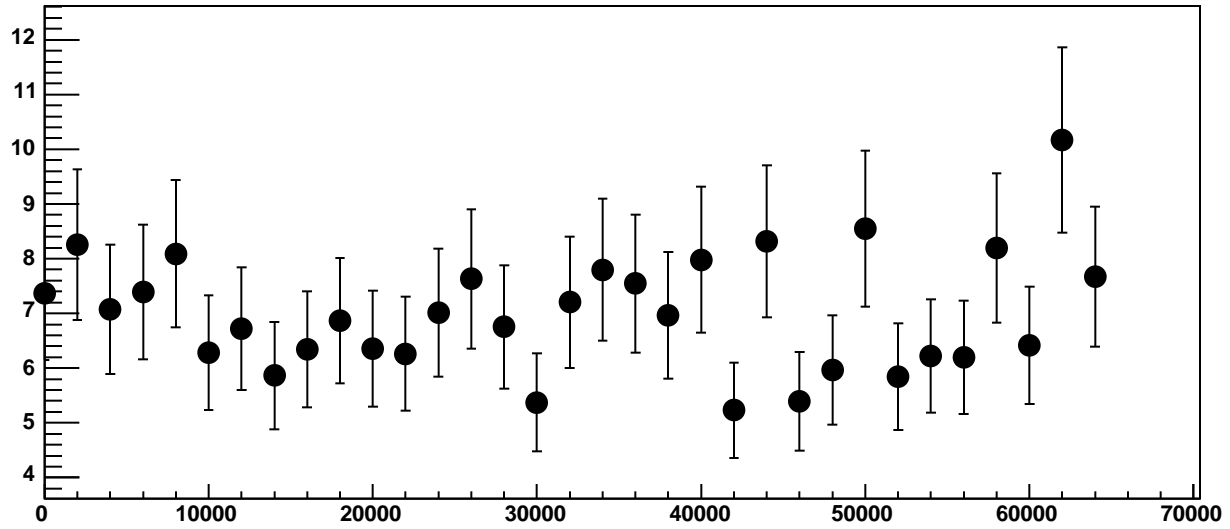
p0

108.9 ± 0.7085

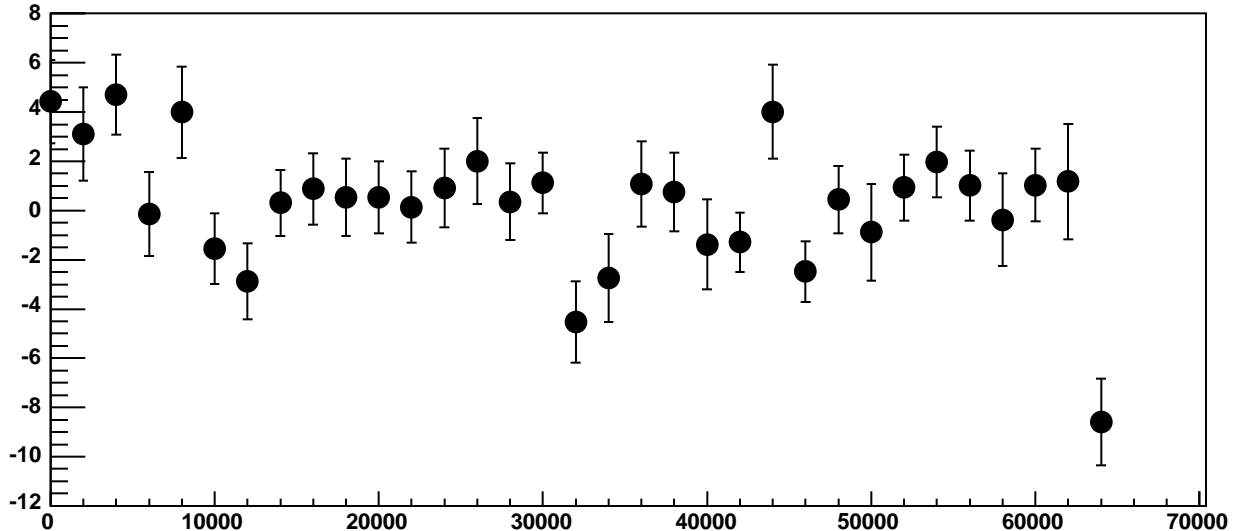
p1

$0.001377 \pm 2.094e-05$

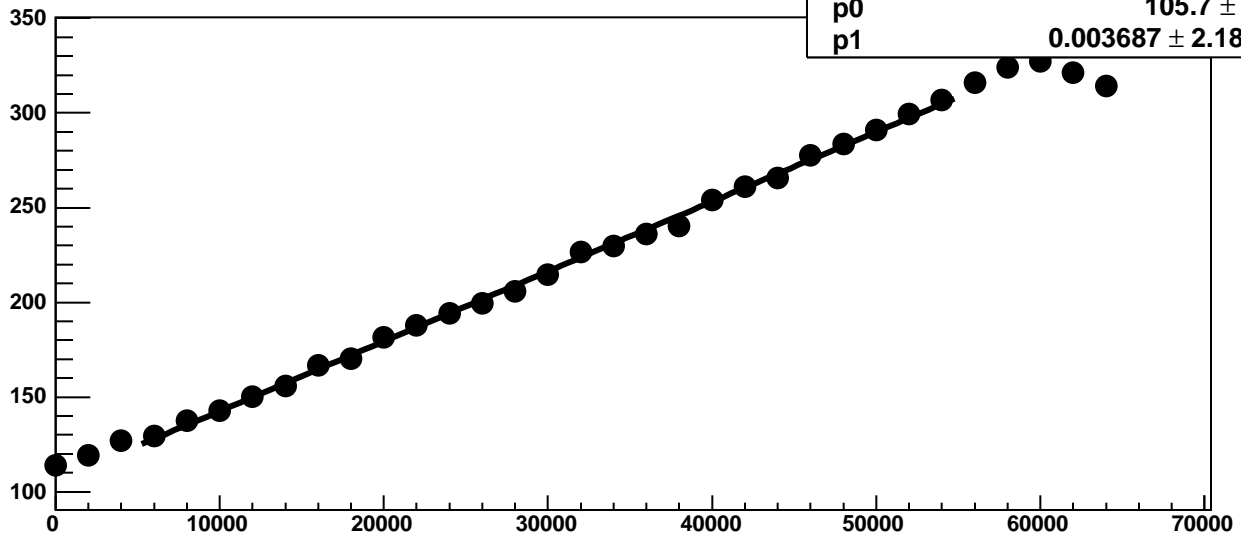
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.08 / 23

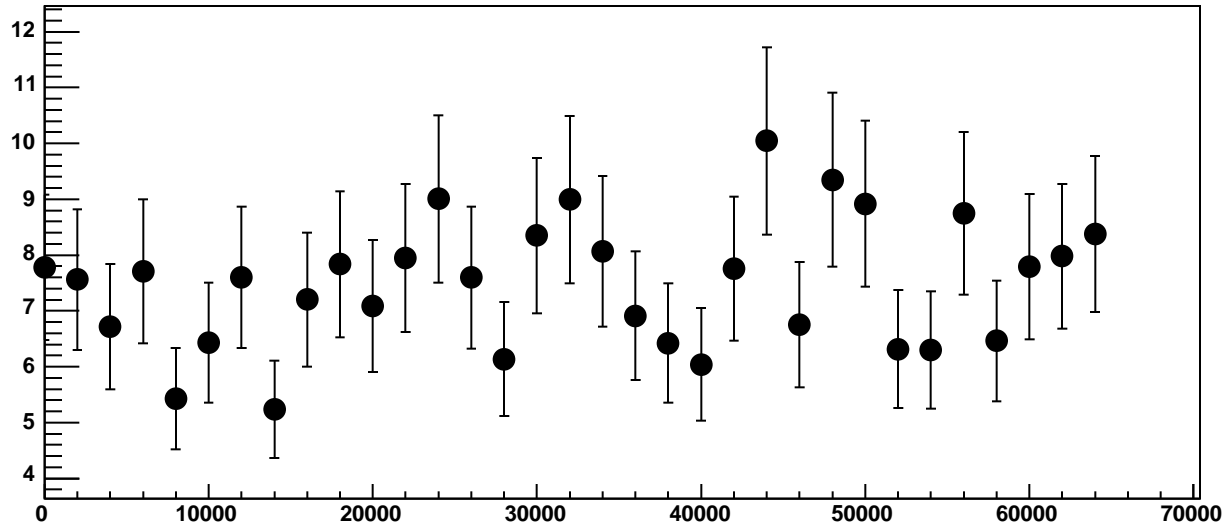
p0

105.7 ± 0.71

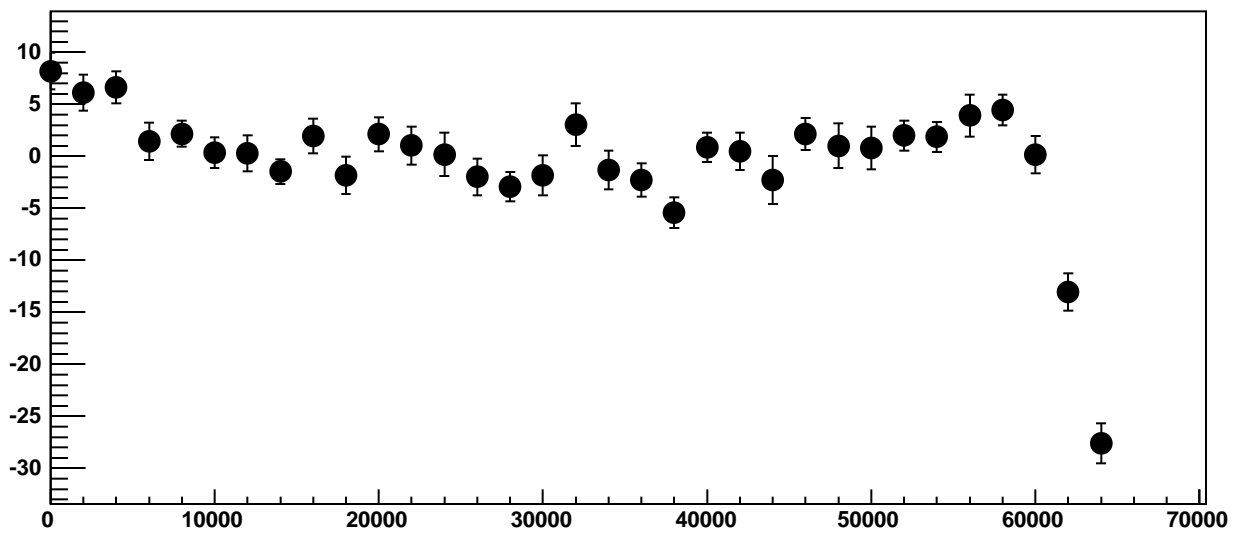
p1

$0.003687 \pm 2.18e-05$

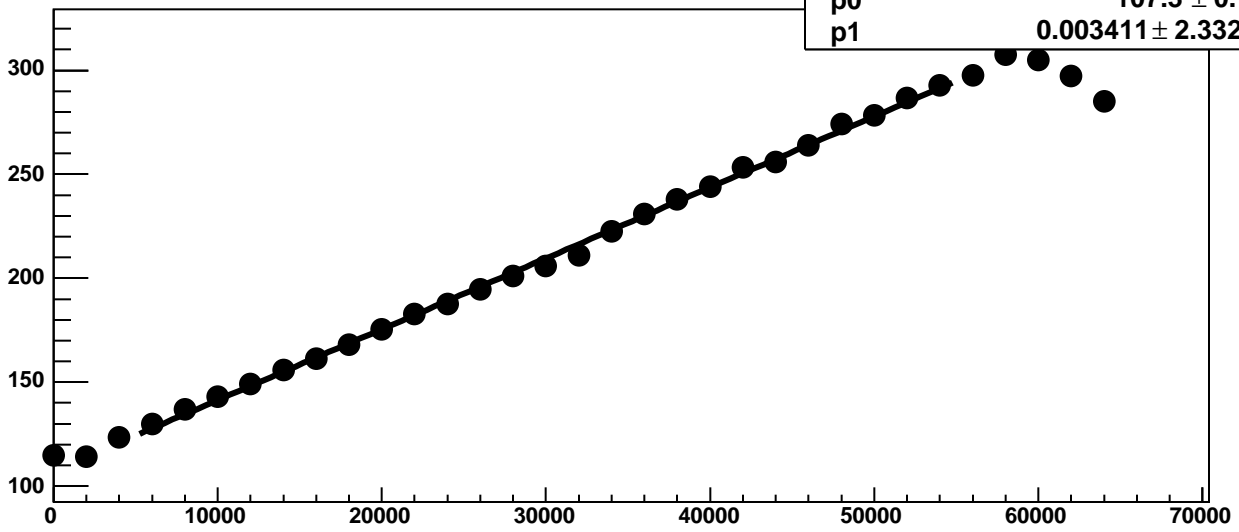
Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



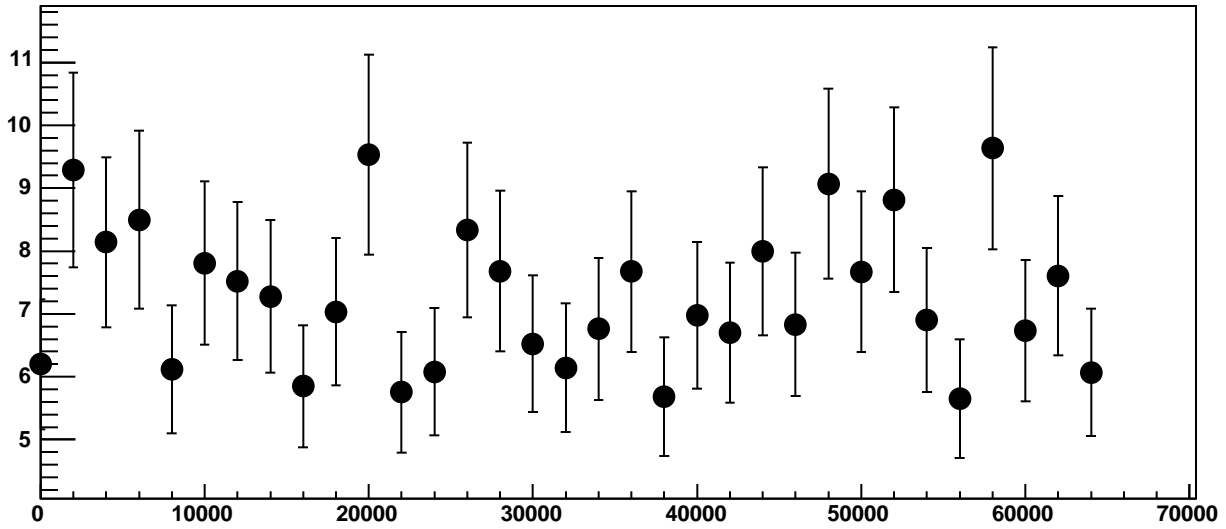
Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



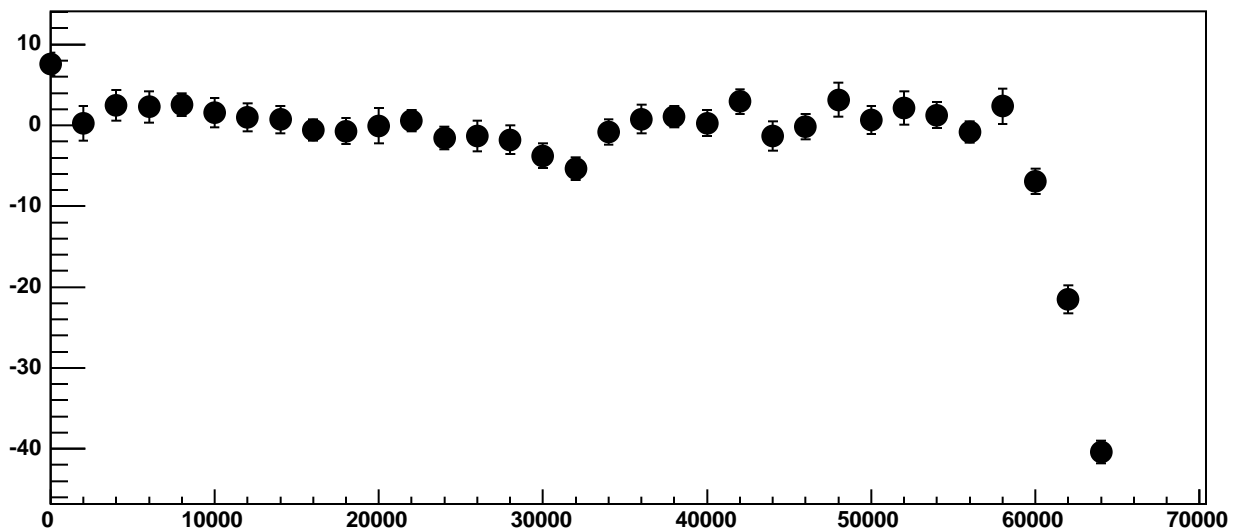
Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



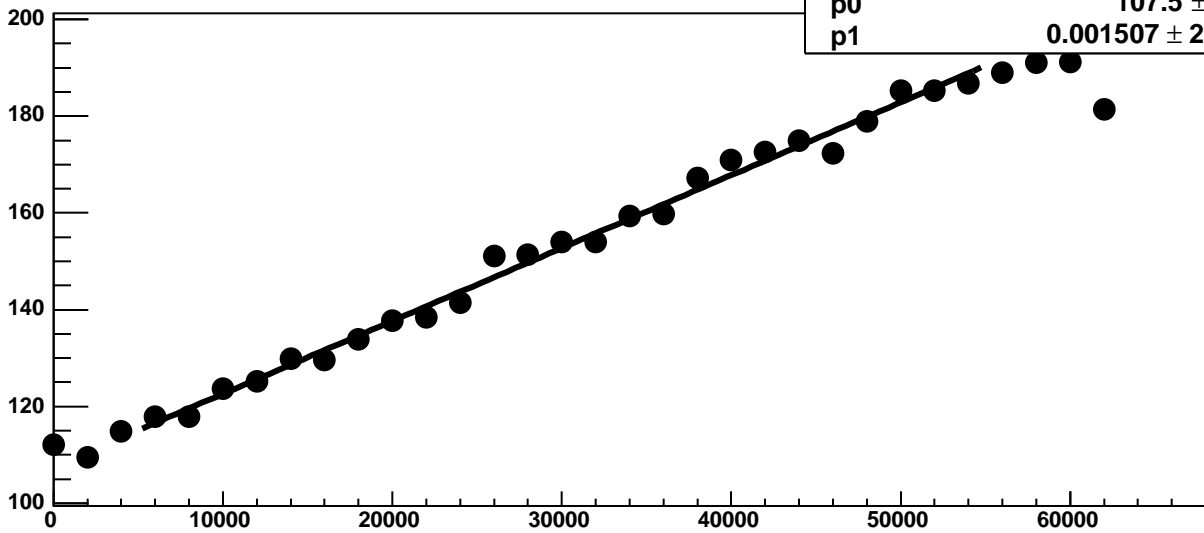
Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



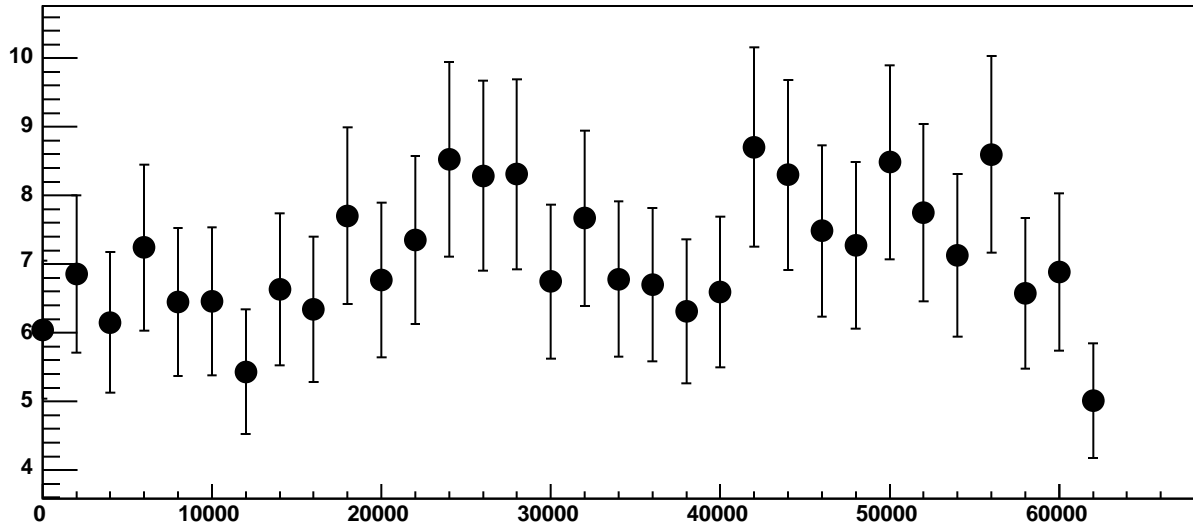
Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



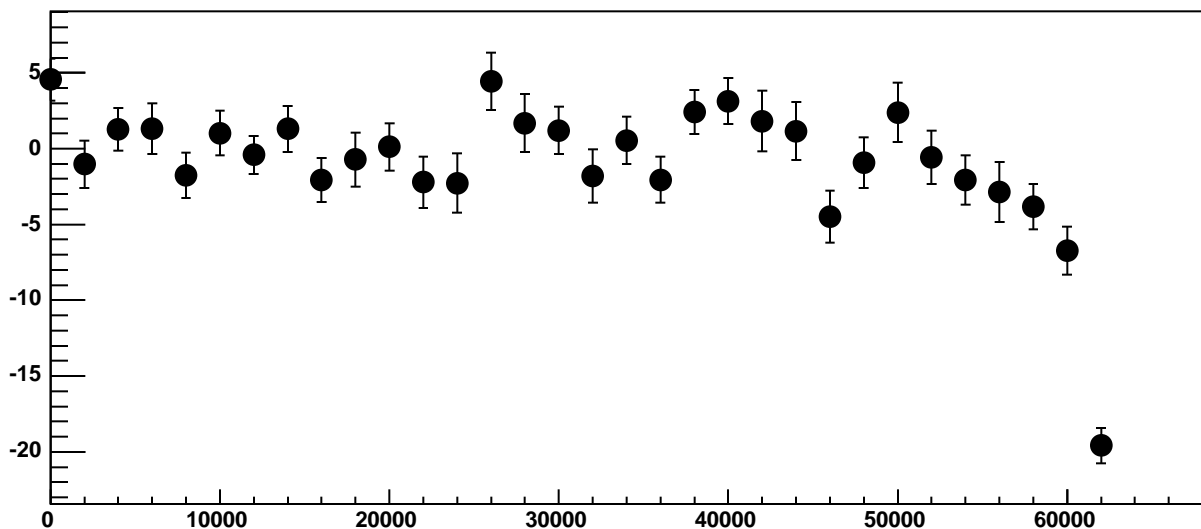
Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



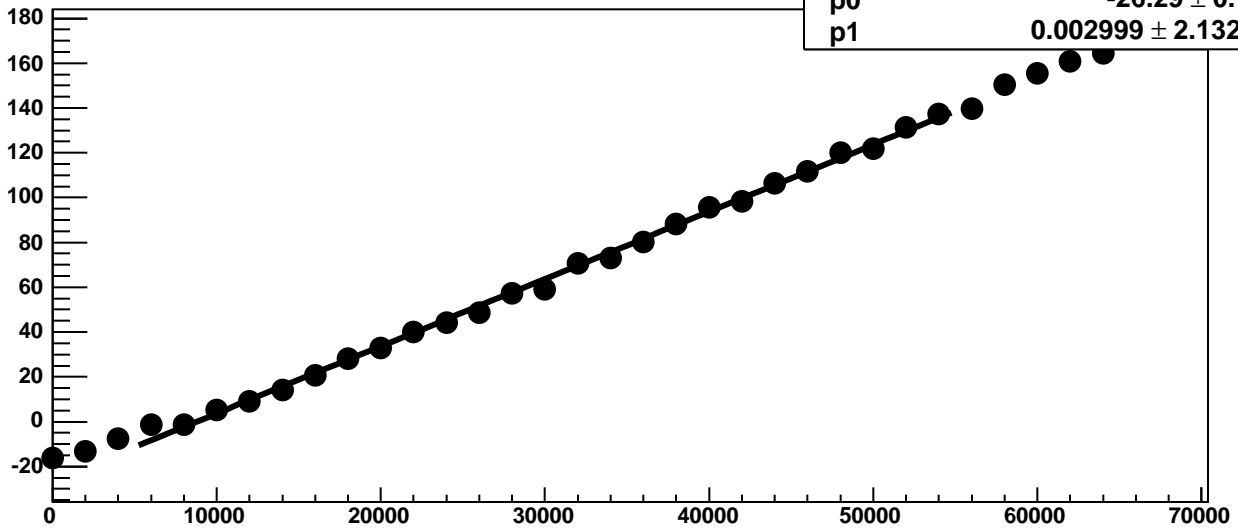
Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

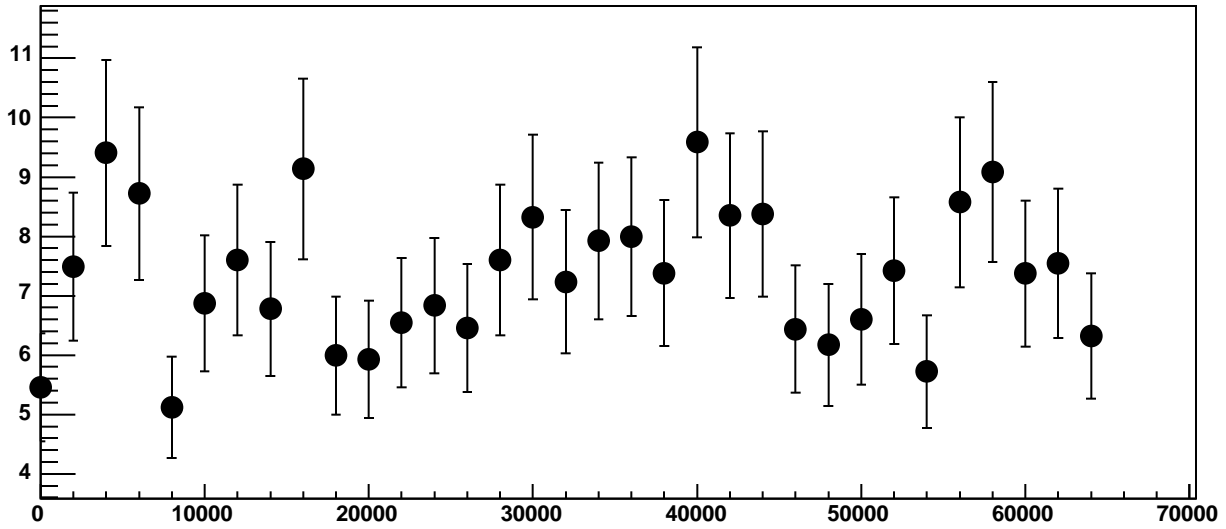


Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

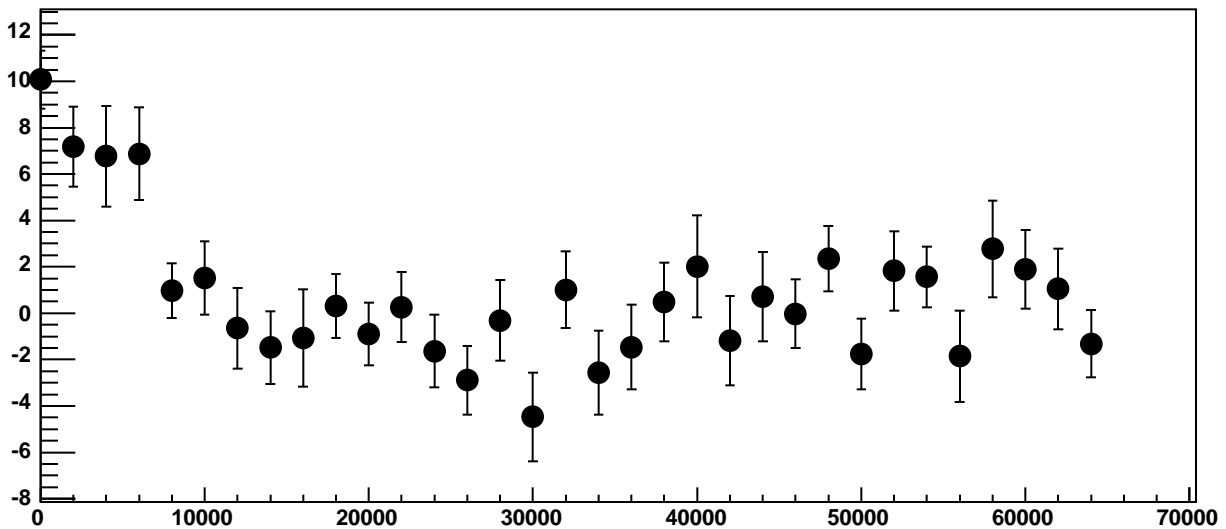


χ^2 / ndf 36.73 / 23
p0 -26.29 ± 0.7084
p1 $0.002999 \pm 2.132e-05$

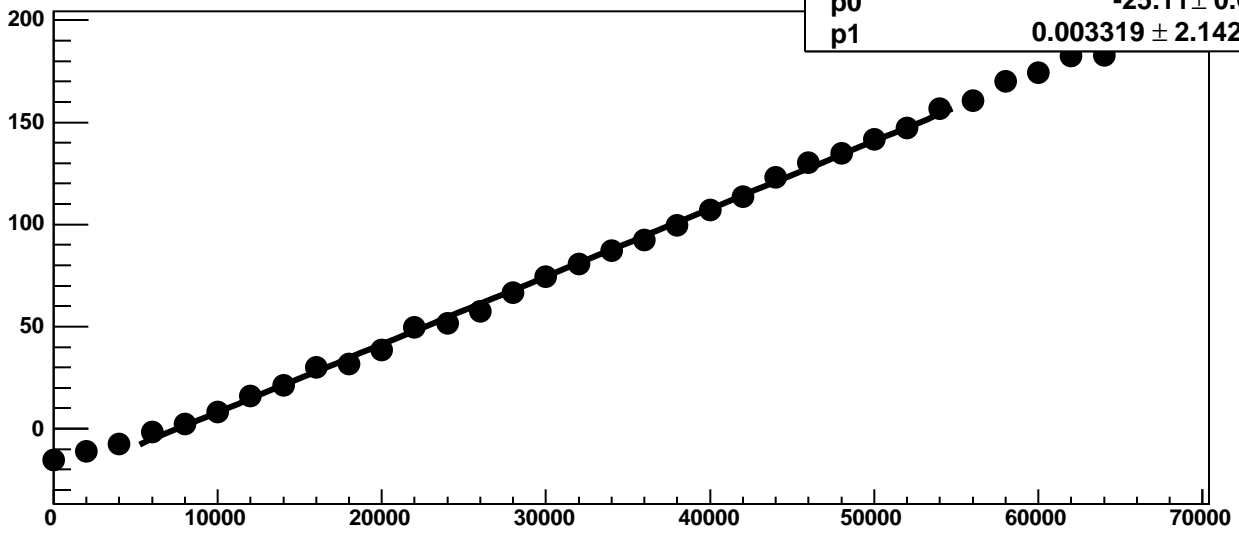
Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



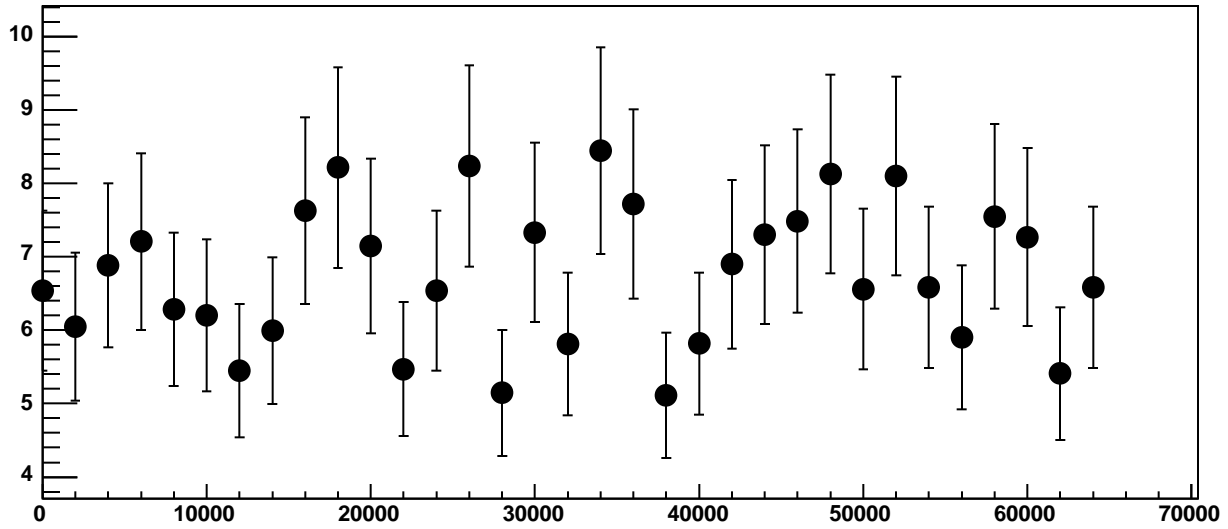
Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



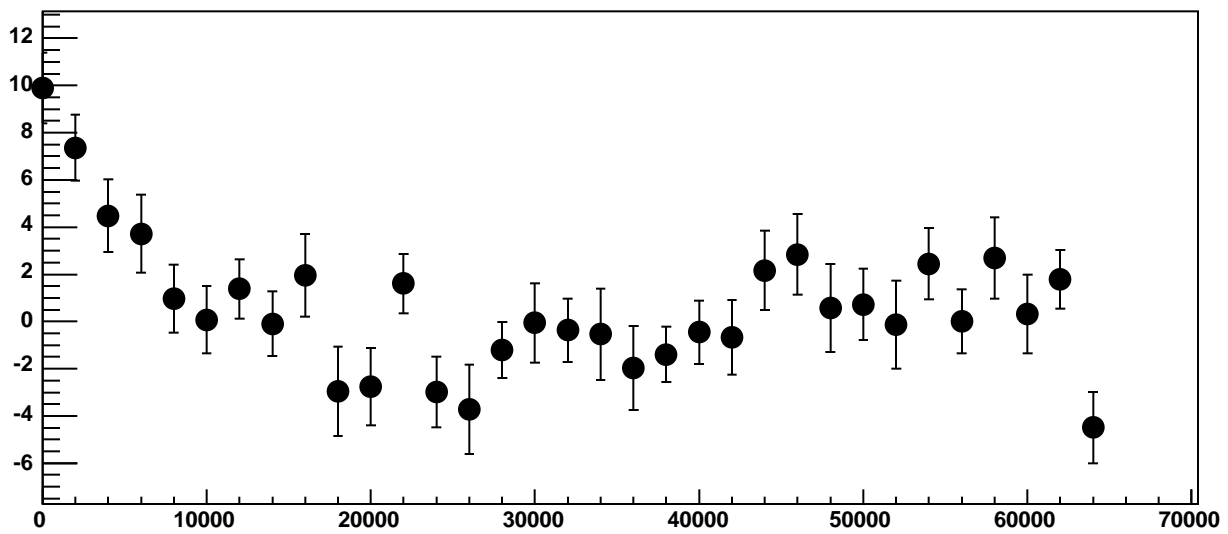
Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

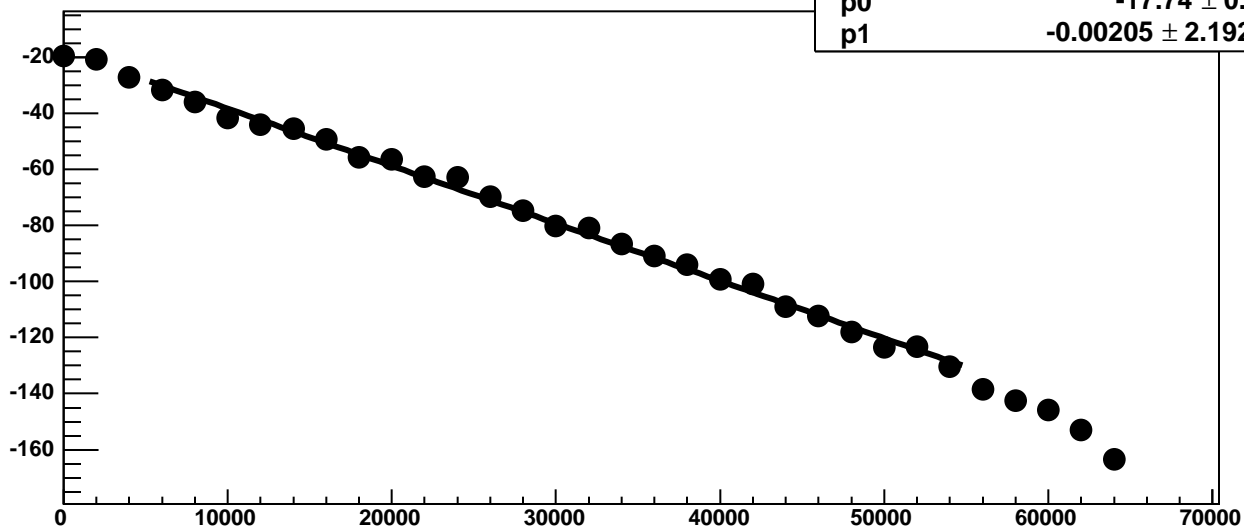


Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

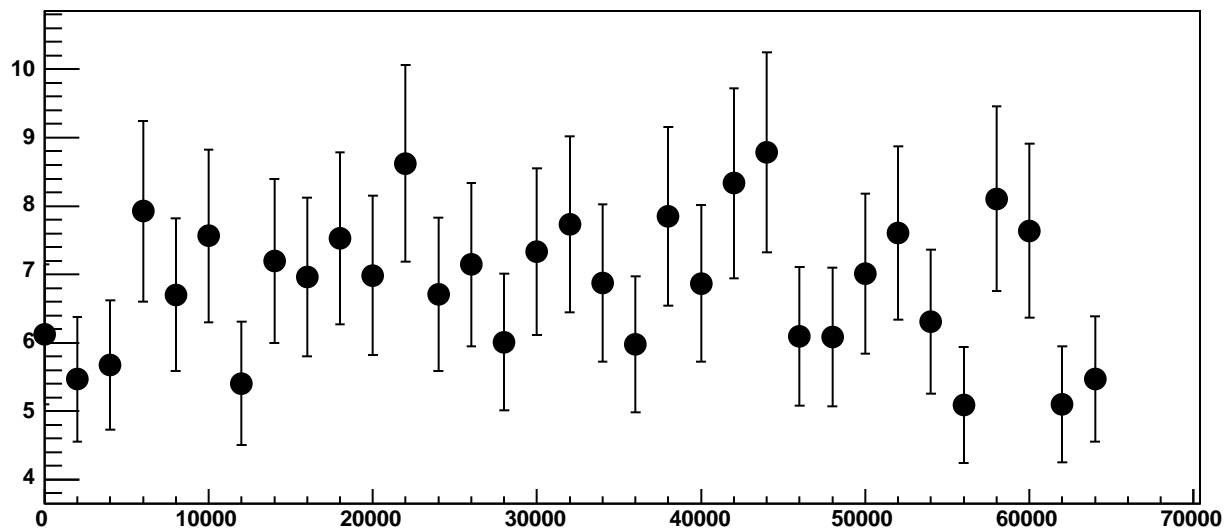


Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

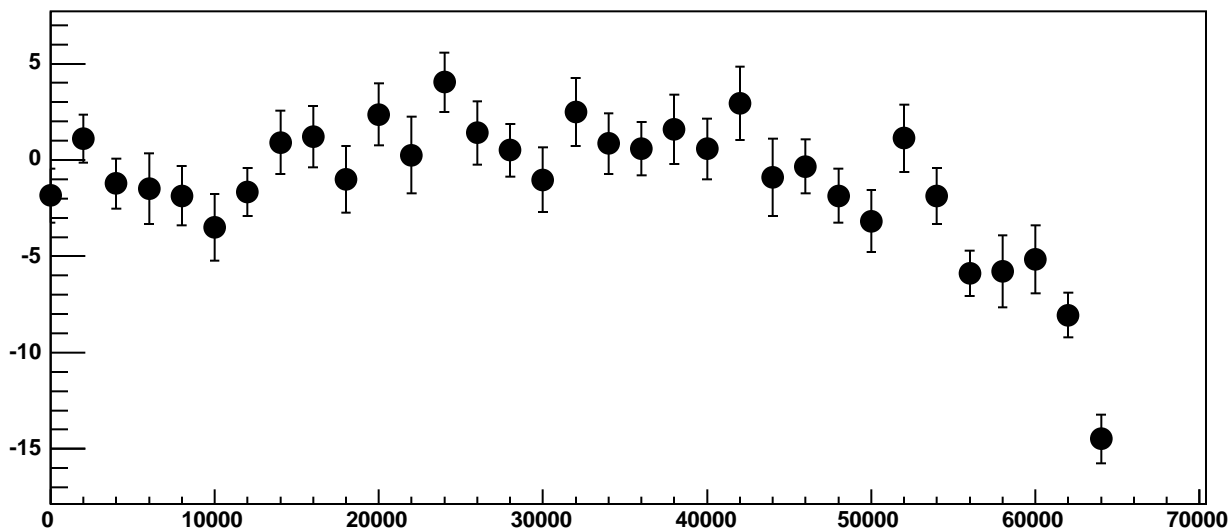
χ^2 / ndf 33.37 / 23
p0 -17.74 ± 0.7334
p1 $-0.00205 \pm 2.192e-05$



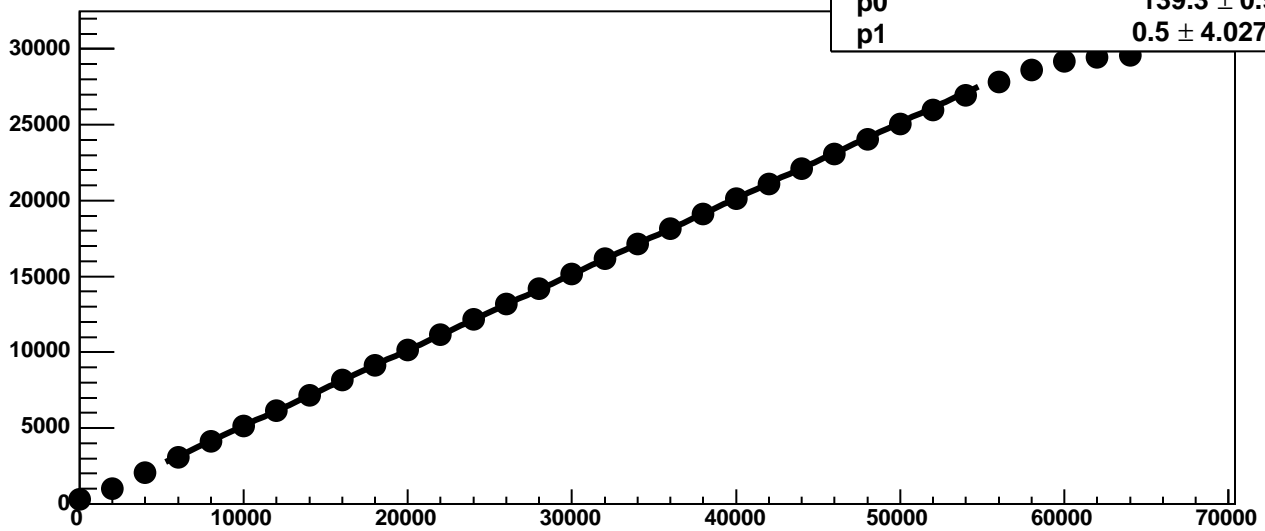
Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



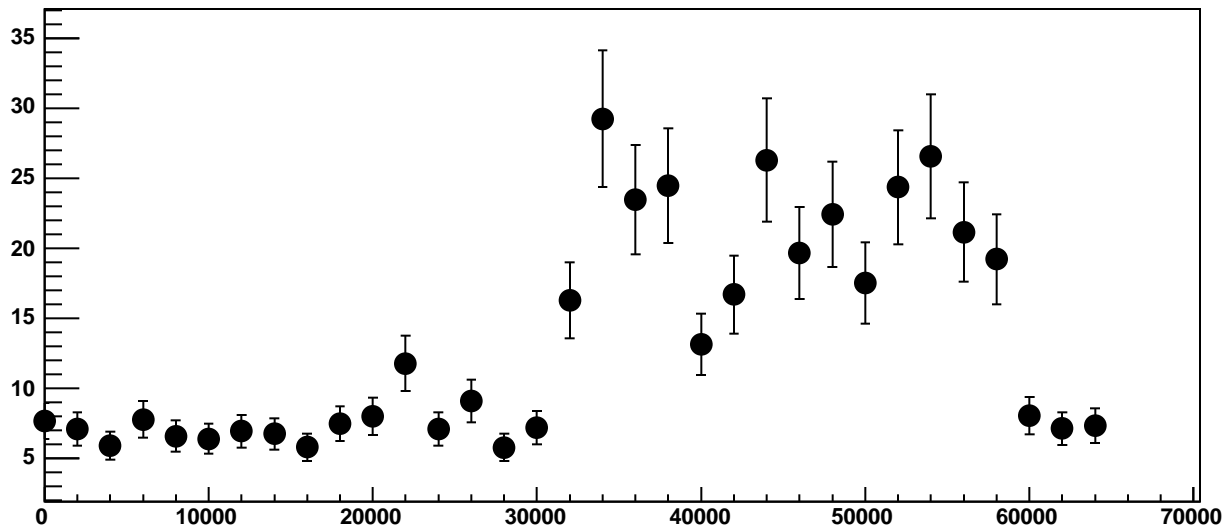
Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



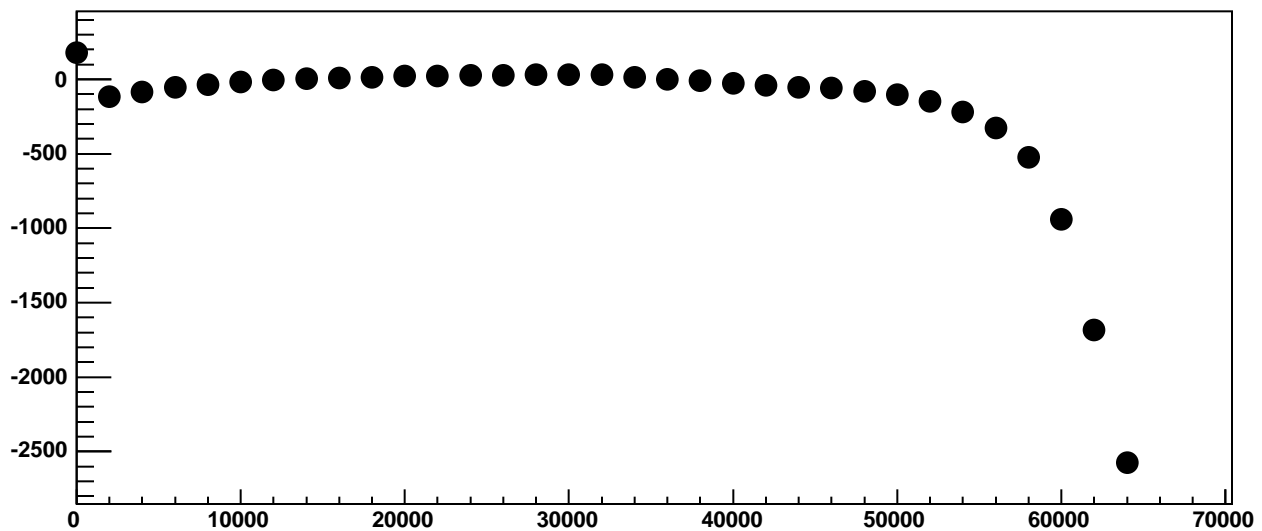
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



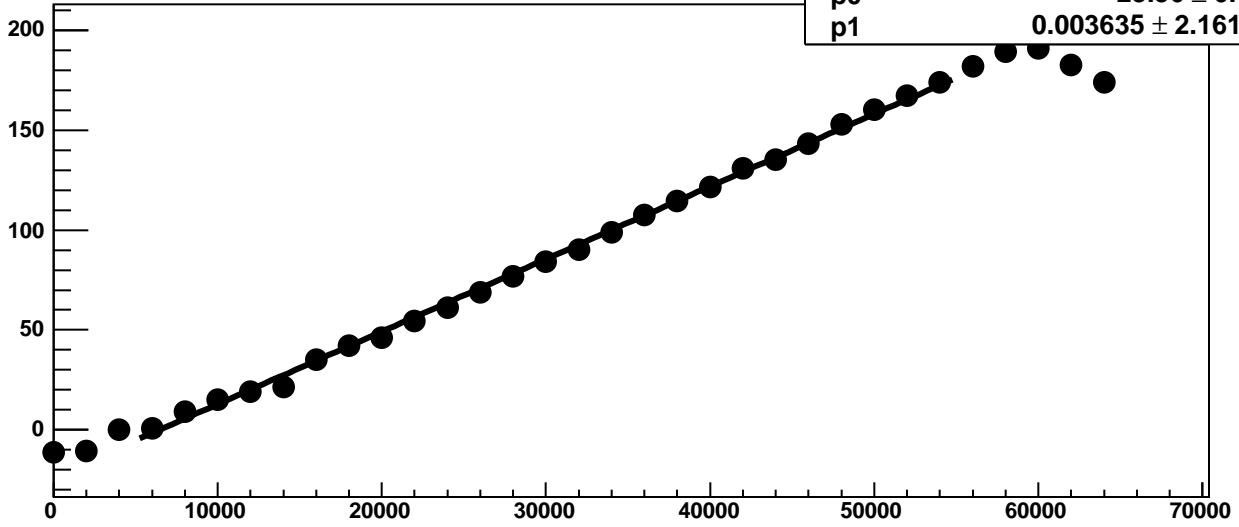
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



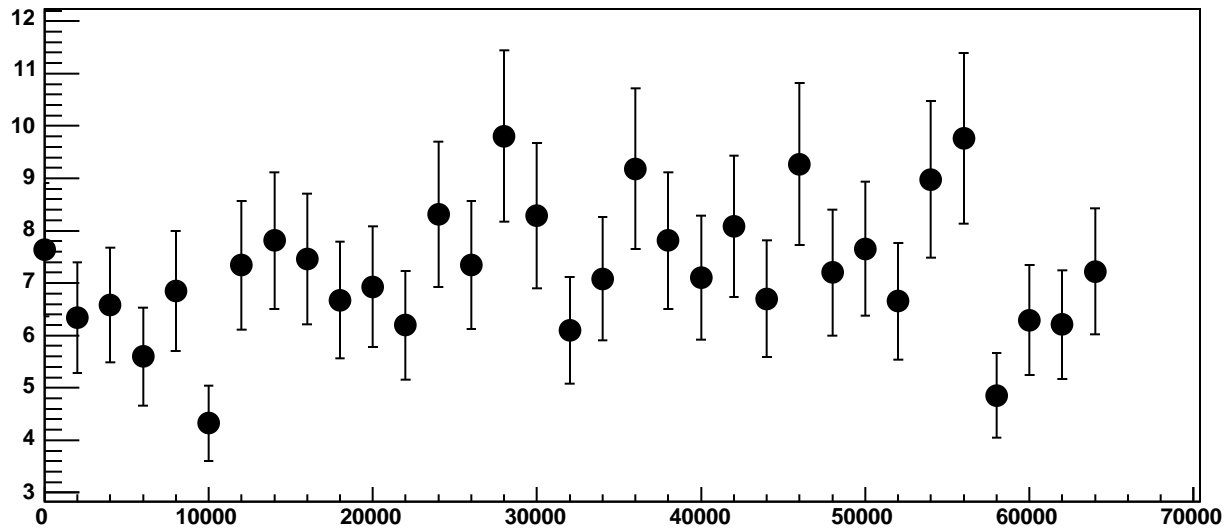
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



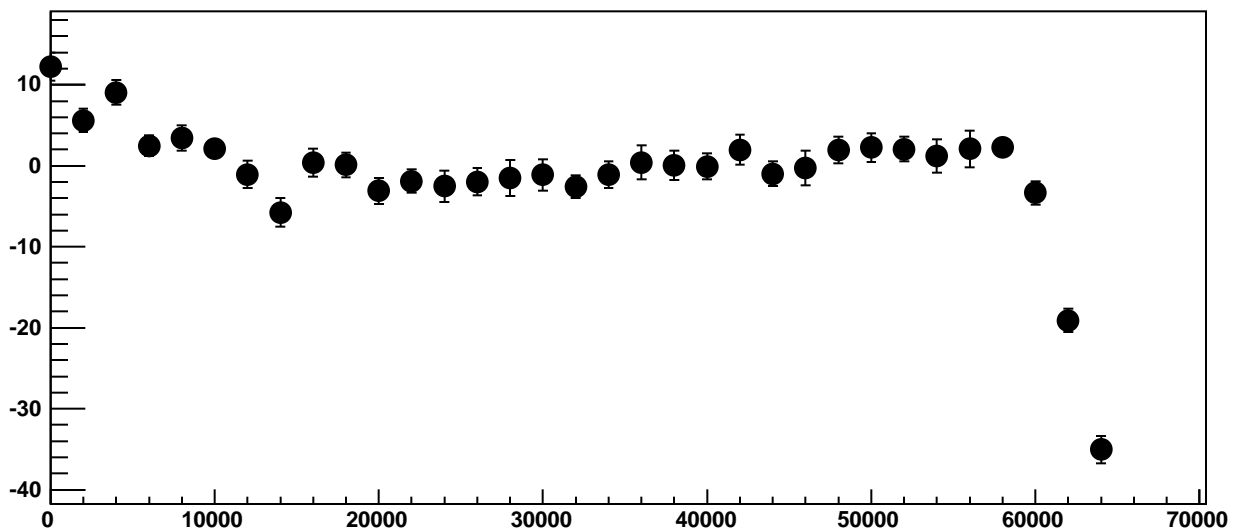
Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



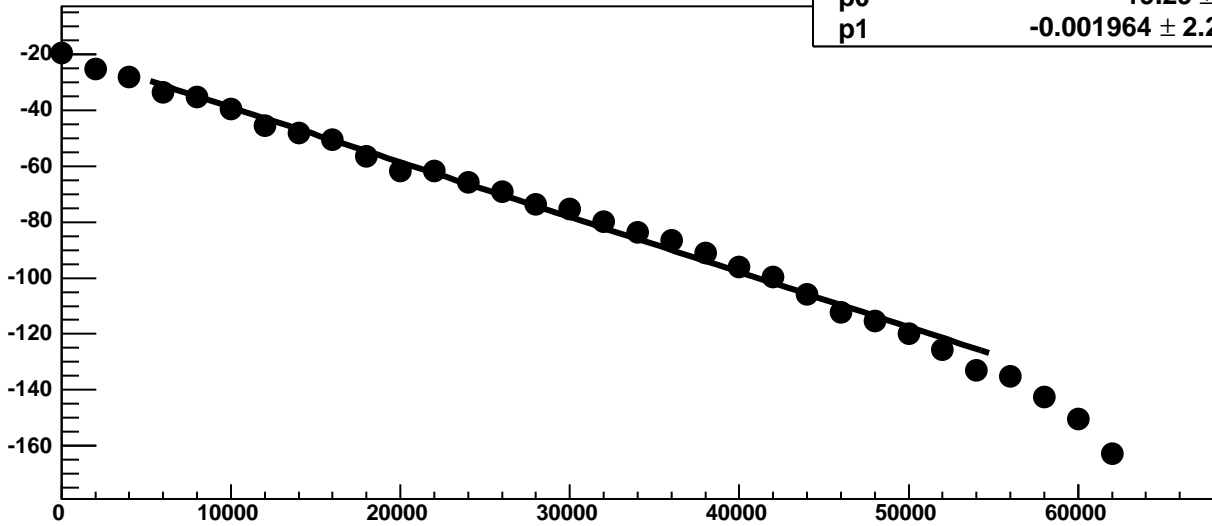
Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



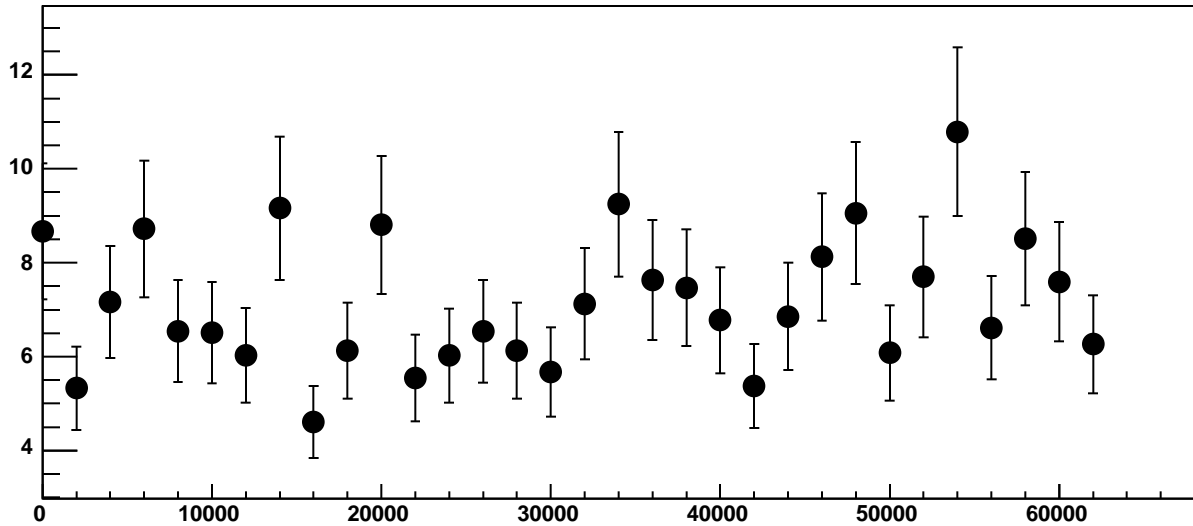
Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



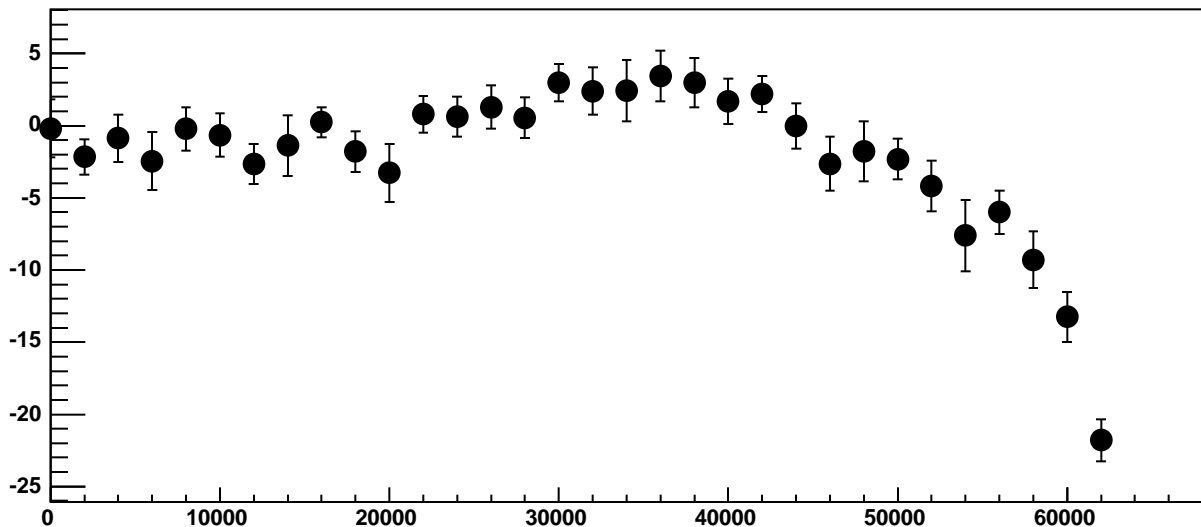
Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



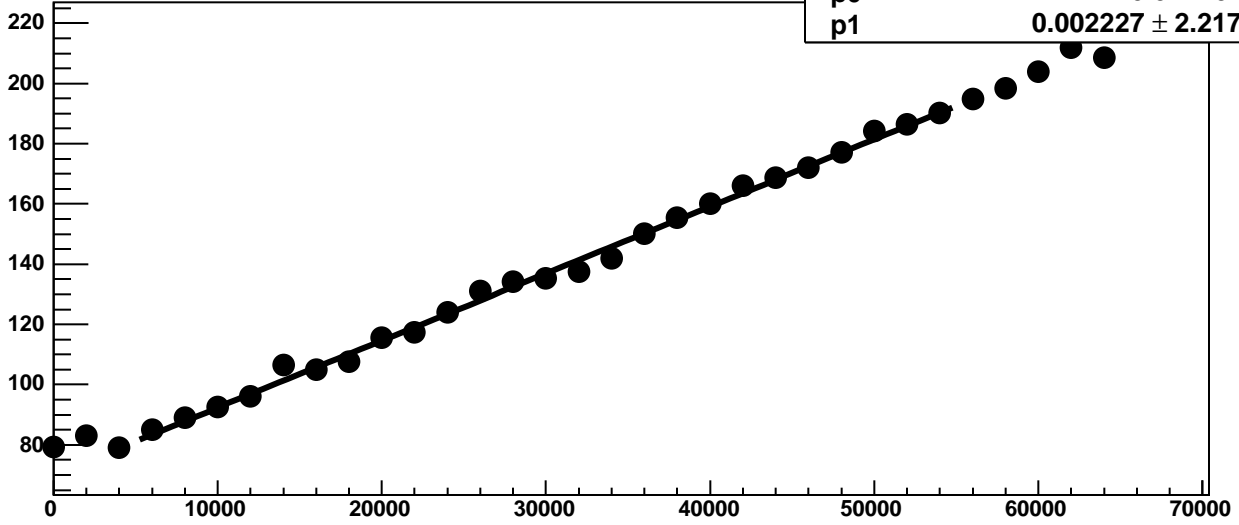
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.69 / 23

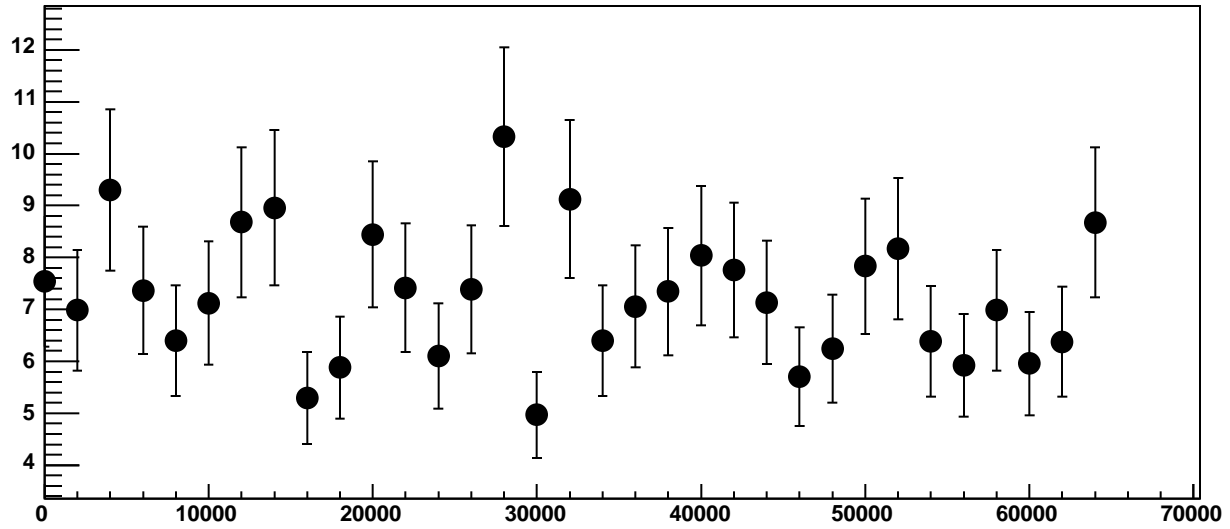
p0

70.07 ± 0.7383

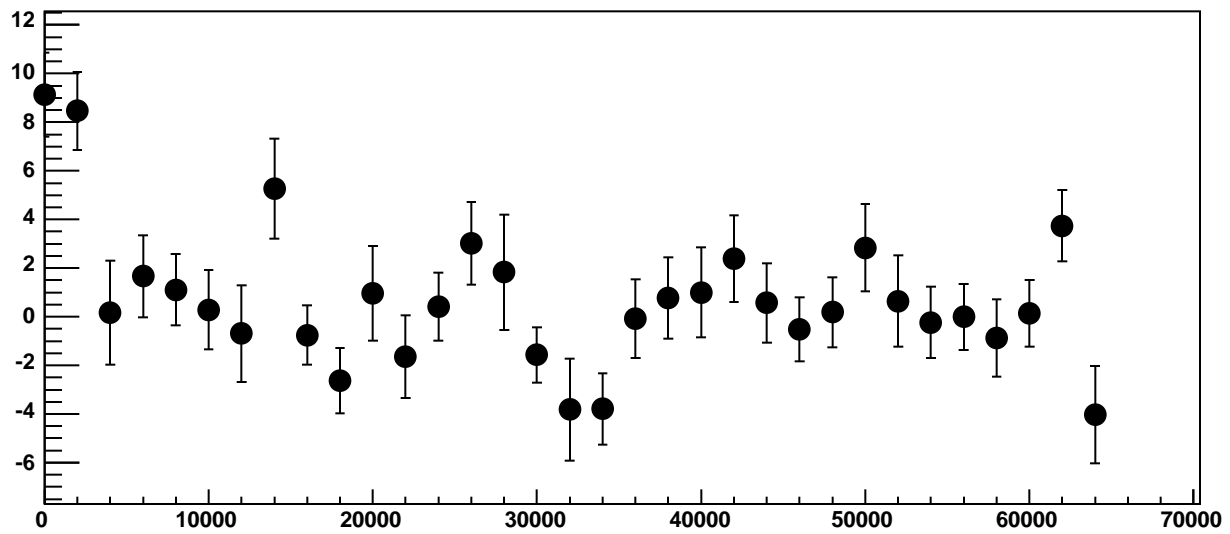
p1

$0.002227 \pm 2.217\text{e-}05$

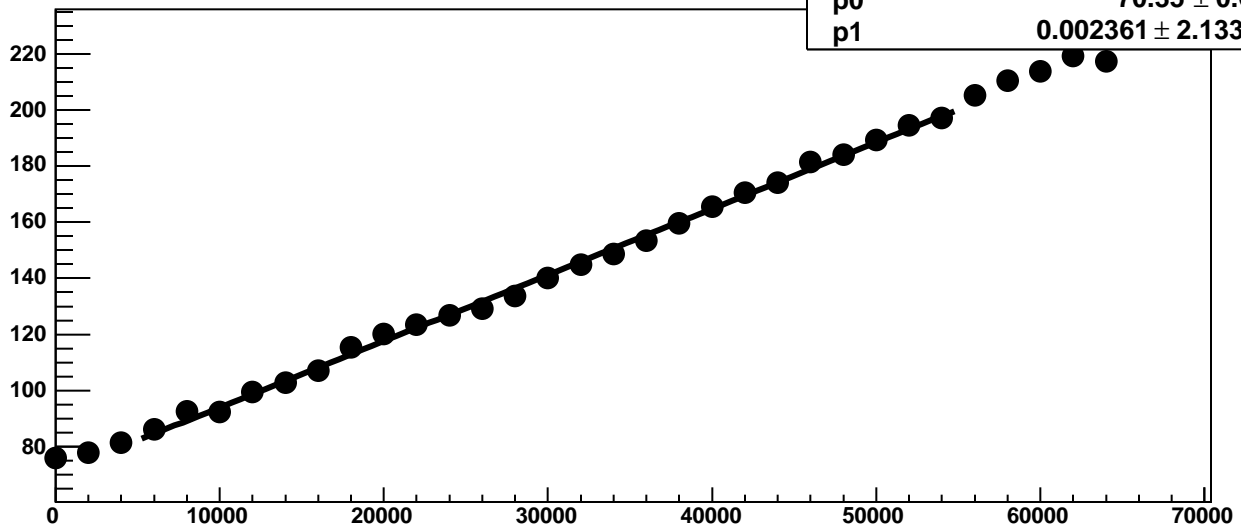
Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

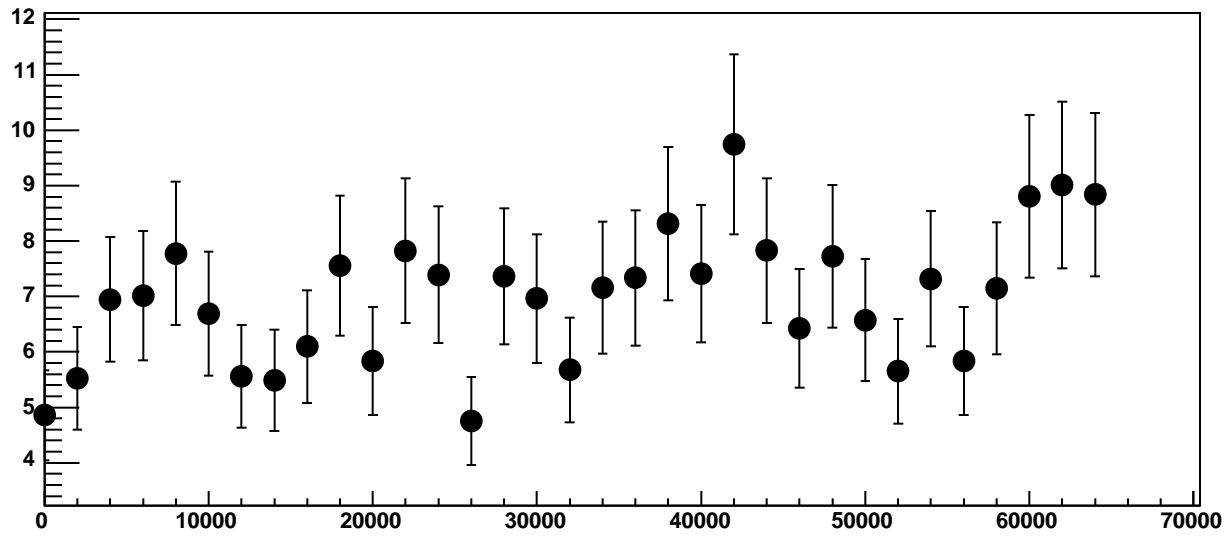


Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

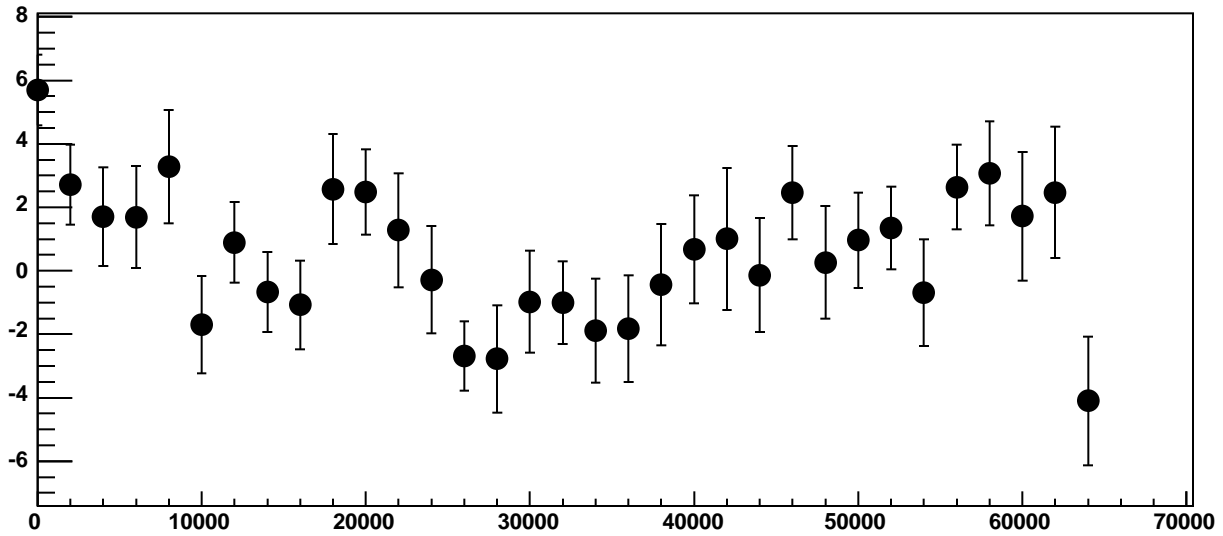


χ^2 / ndf 30.39 / 23
p0 70.35 ± 0.6907
p1 $0.002361 \pm 2.133e-05$

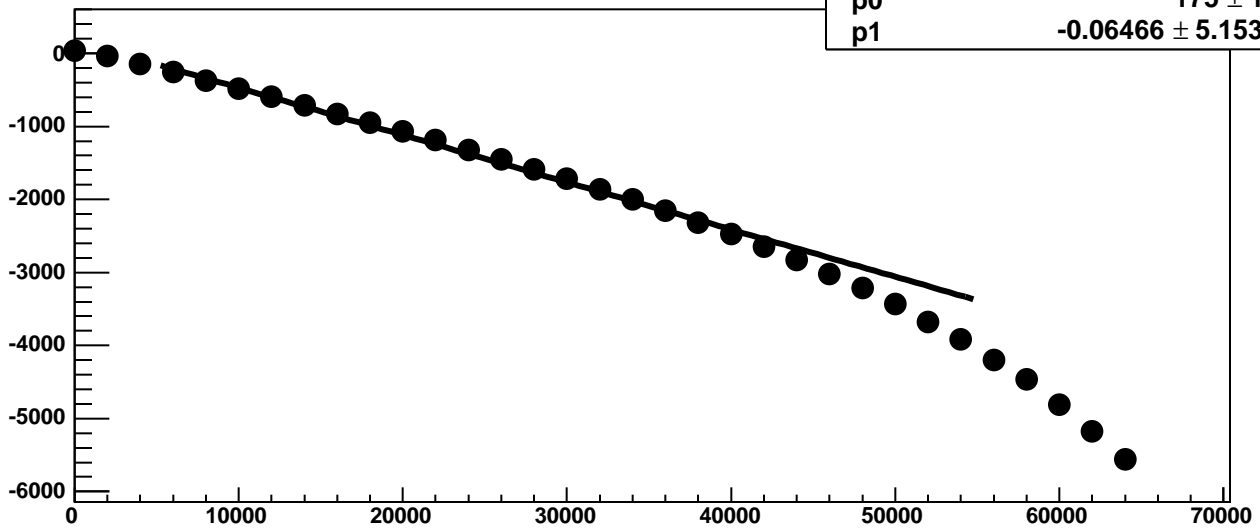
Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



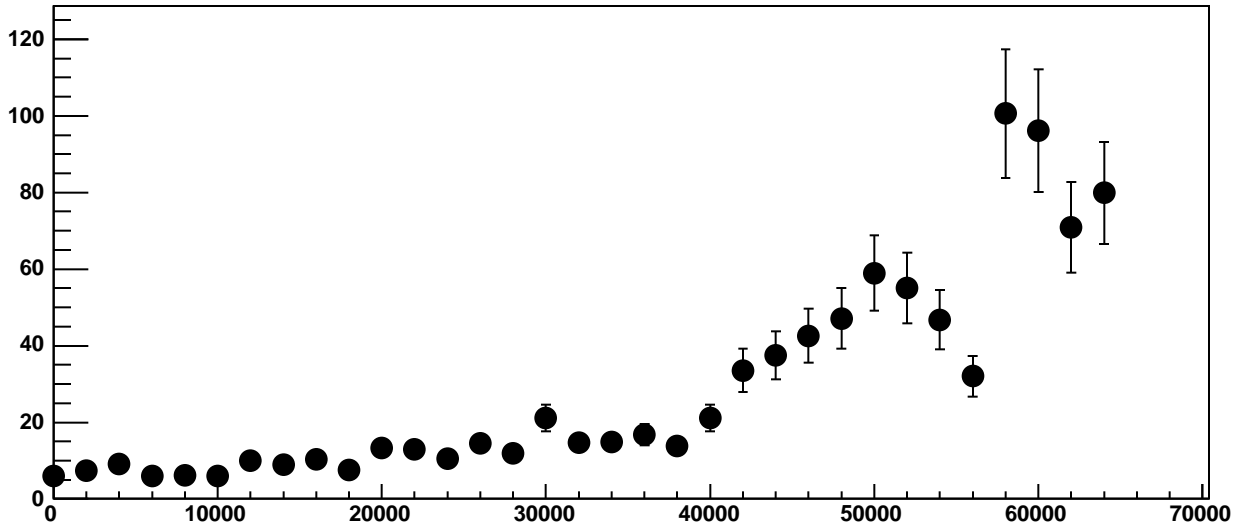
Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



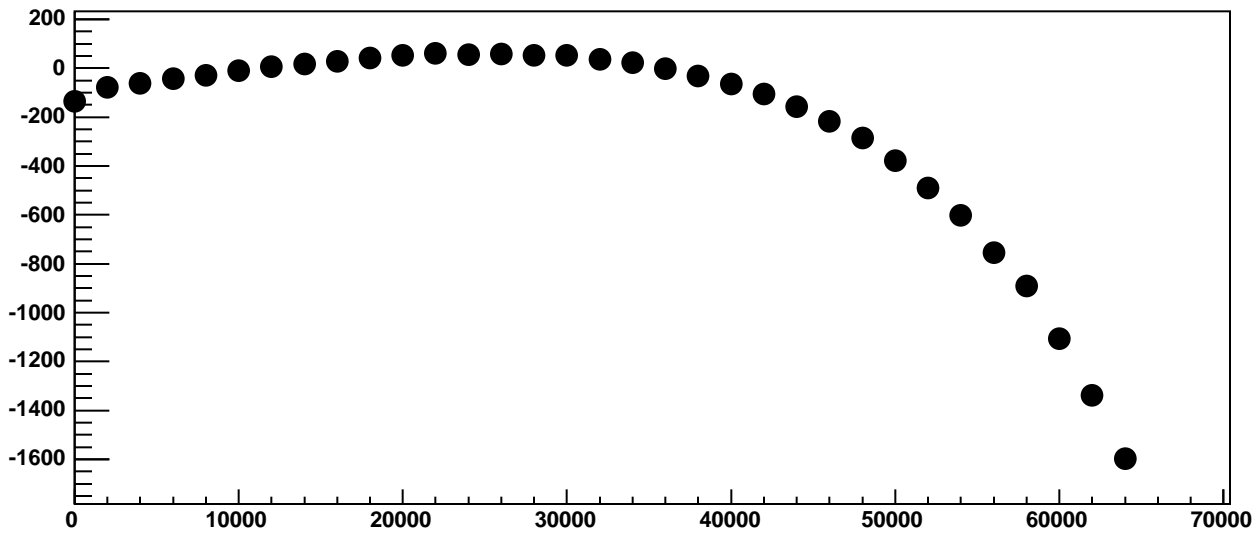
Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



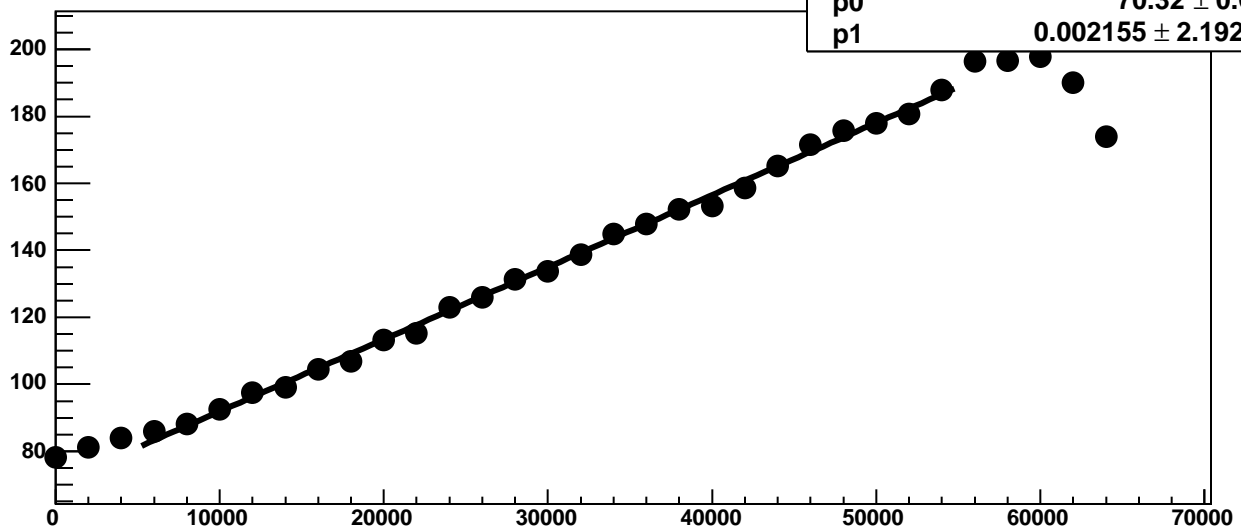
Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

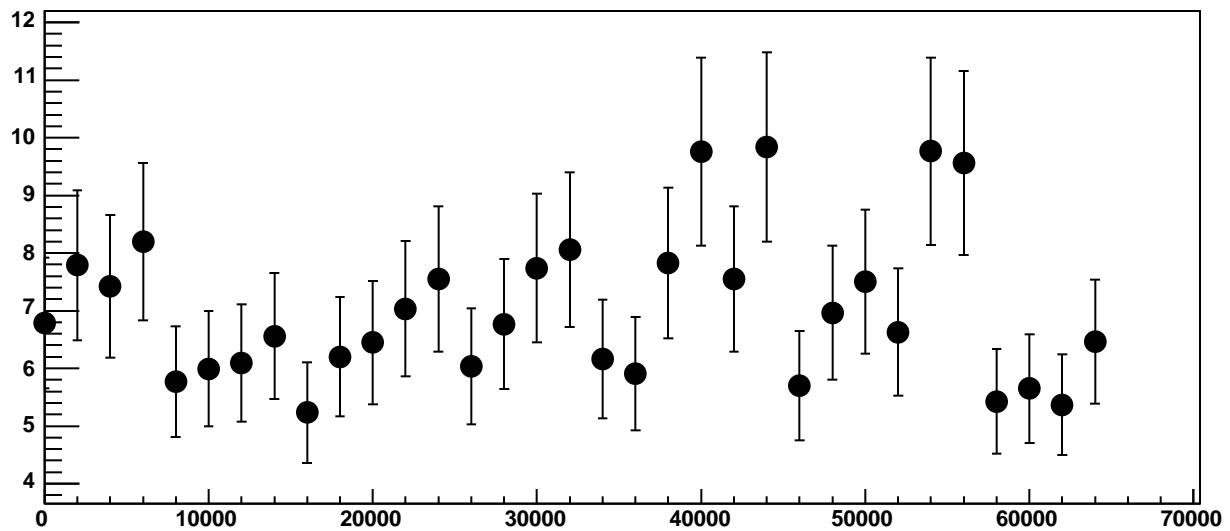


Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

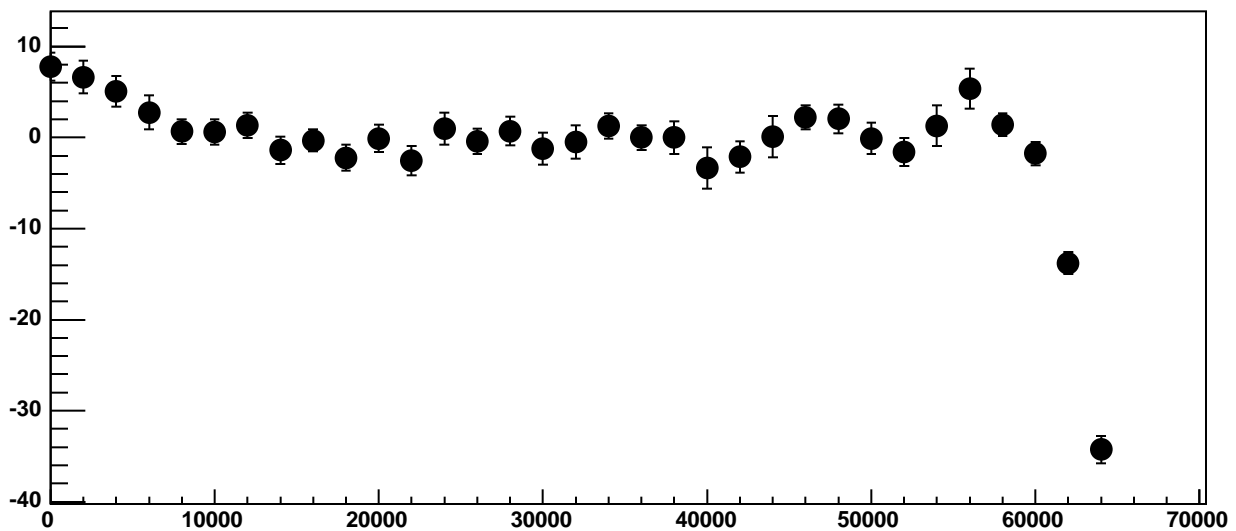


χ^2 / ndf 20.95 / 23
p0 70.32 ± 0.6898
p1 $0.002155 \pm 2.192e-05$

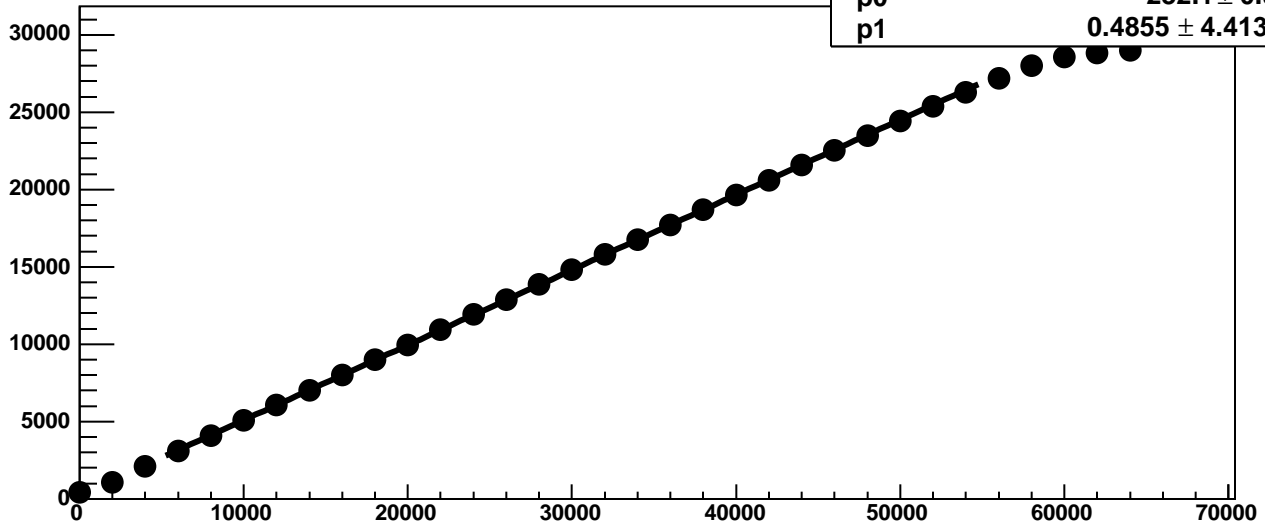
Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5375 / 23

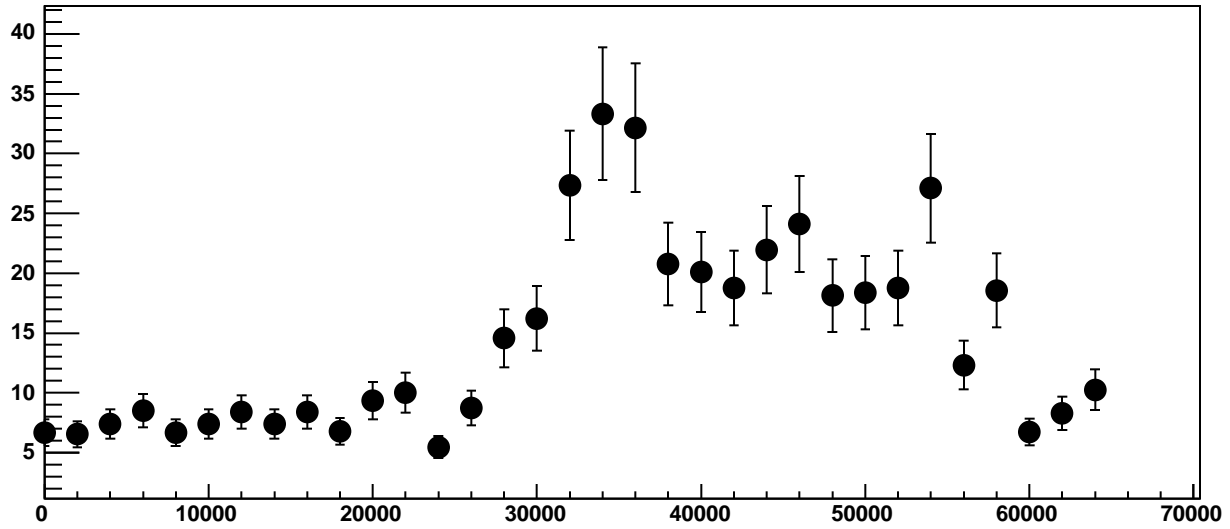
p0

232.1 ± 0.9966

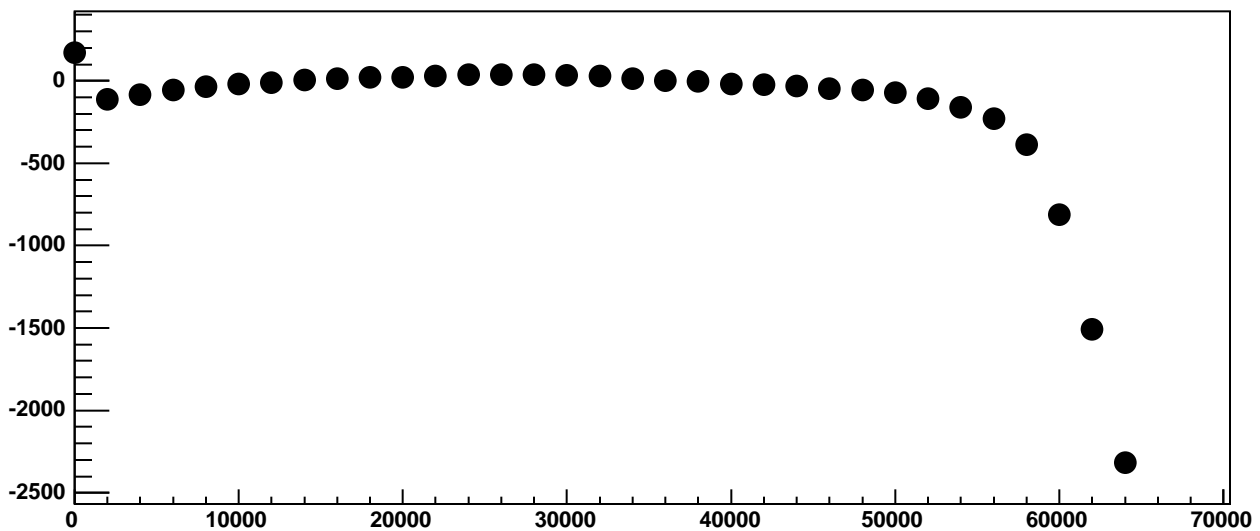
p1

$0.4855 \pm 4.413e-05$

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

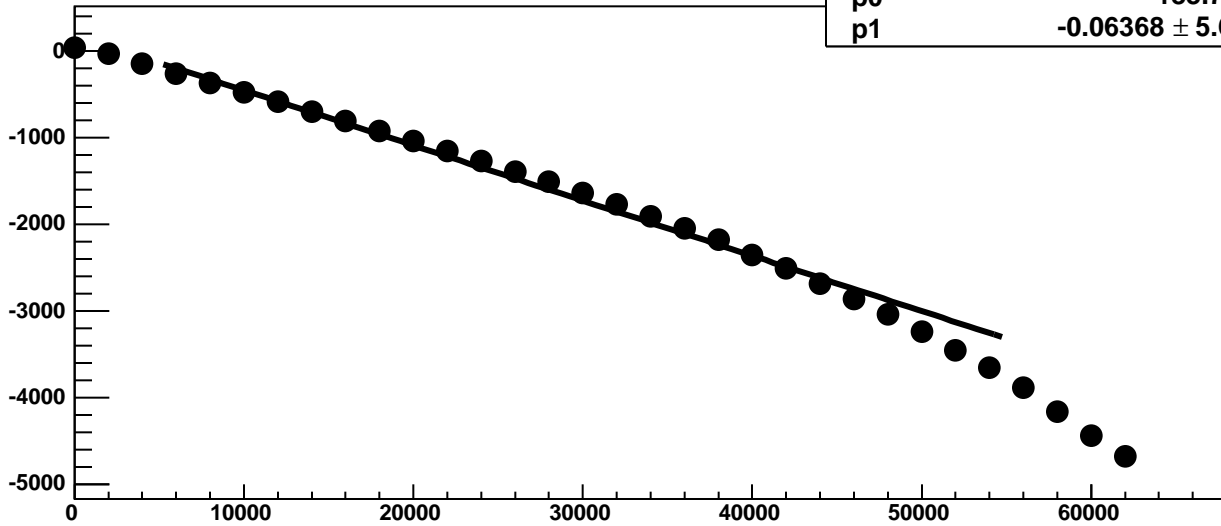


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

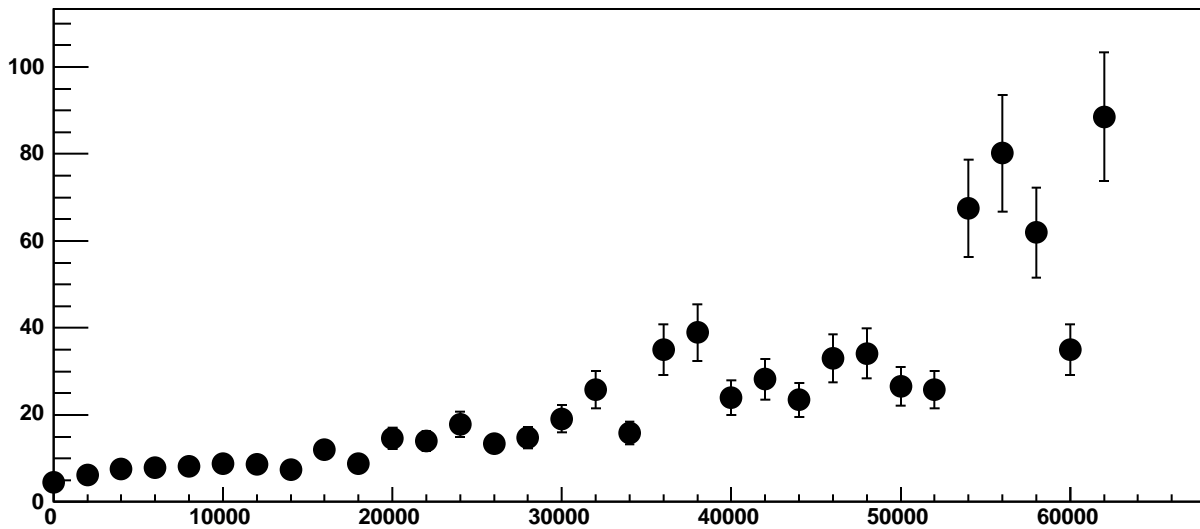


Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

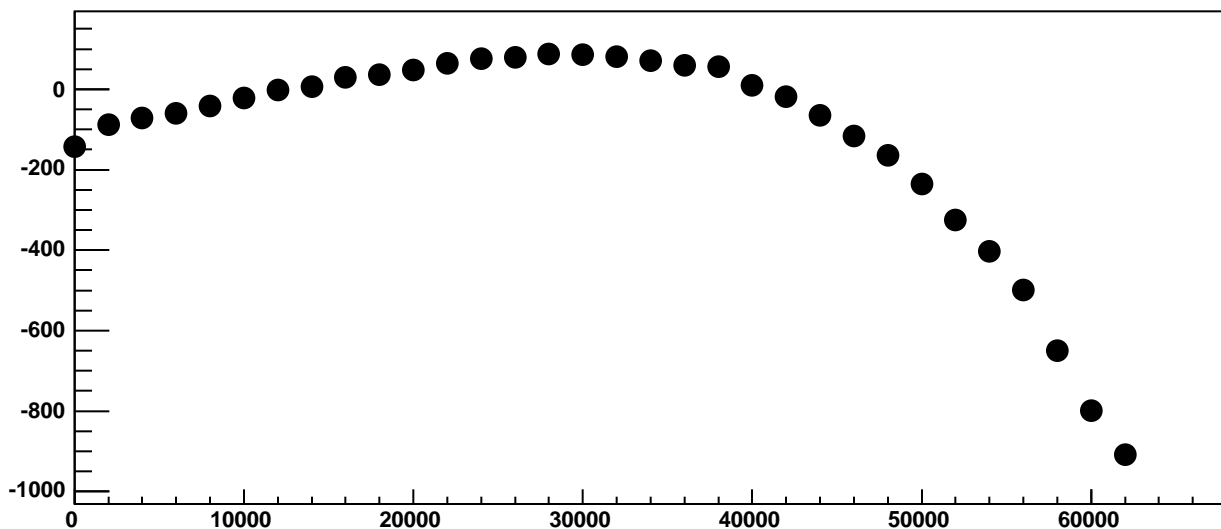
χ^2 / ndf 1.153e+04 / 23
p0 183.7 ± 1.171
p1 -0.06368 ± 5.659e-05



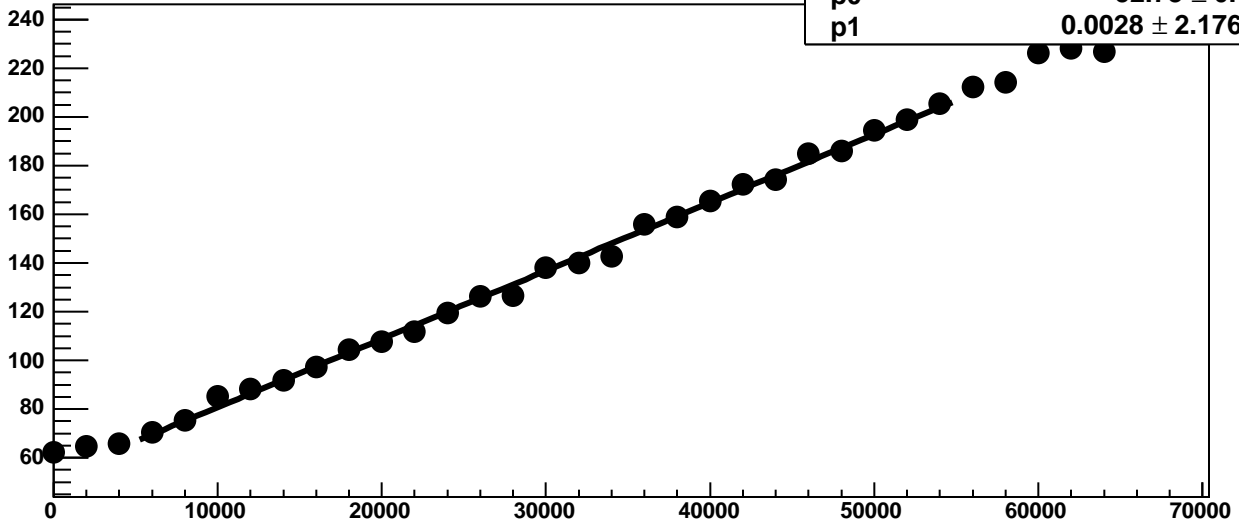
Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

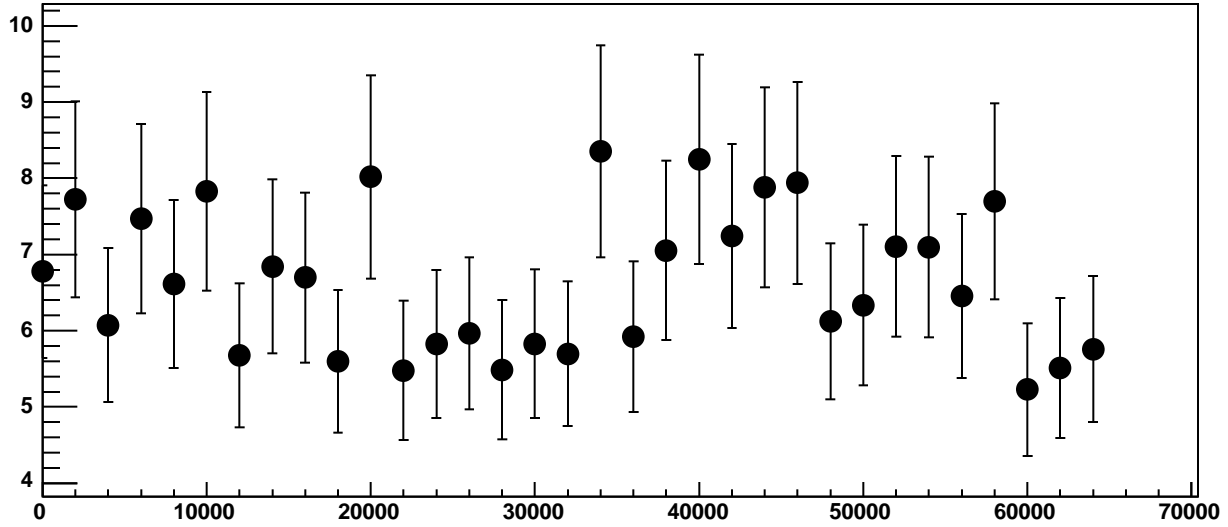


Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

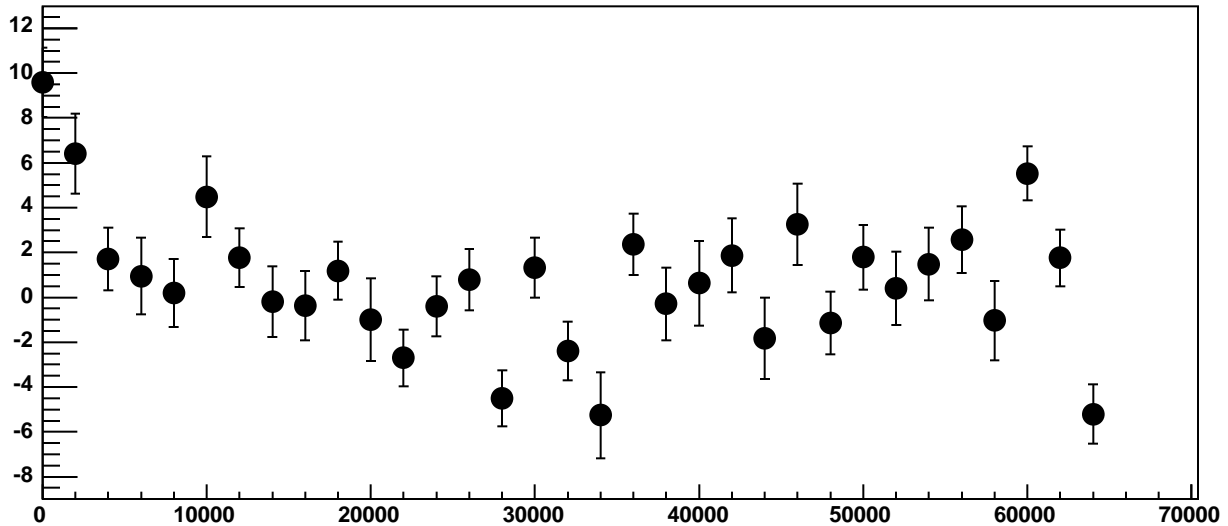


χ^2 / ndf 50.96 / 23
p0 52.78 ± 0.7039
p1 $0.0028 \pm 2.176e-05$

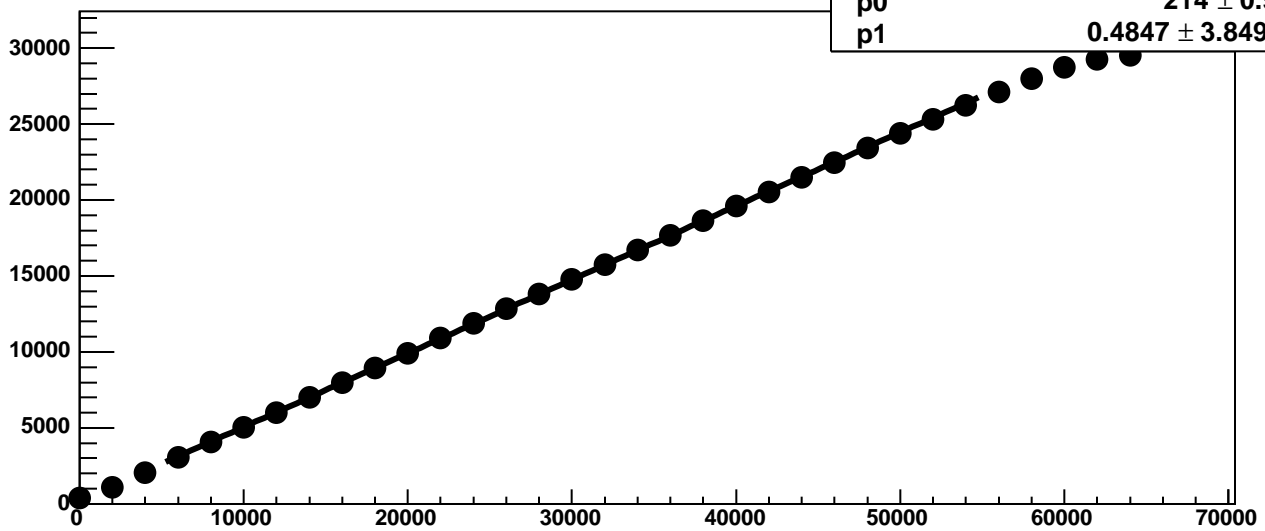
Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



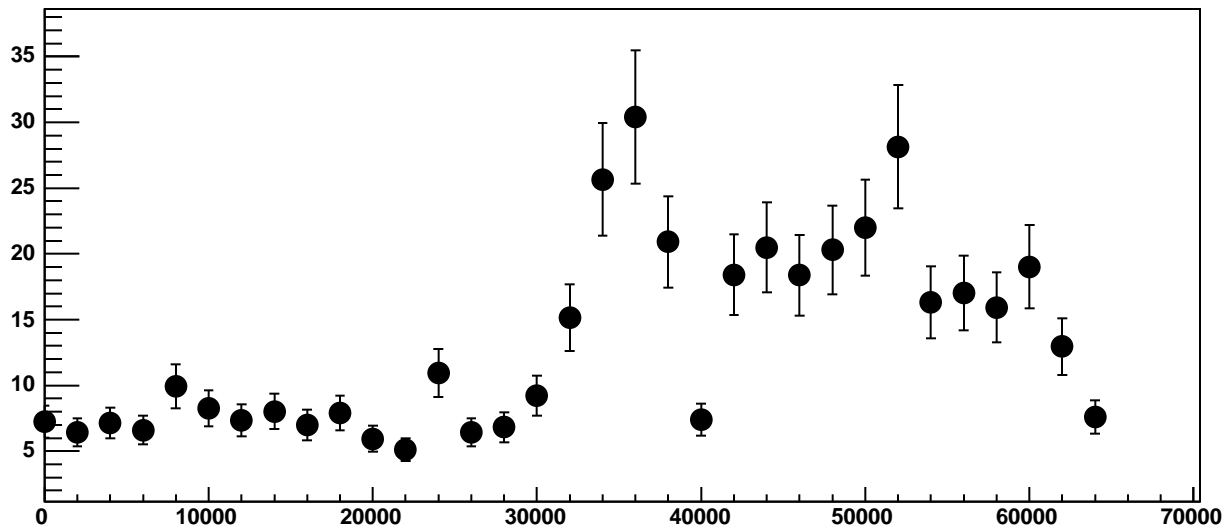
Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



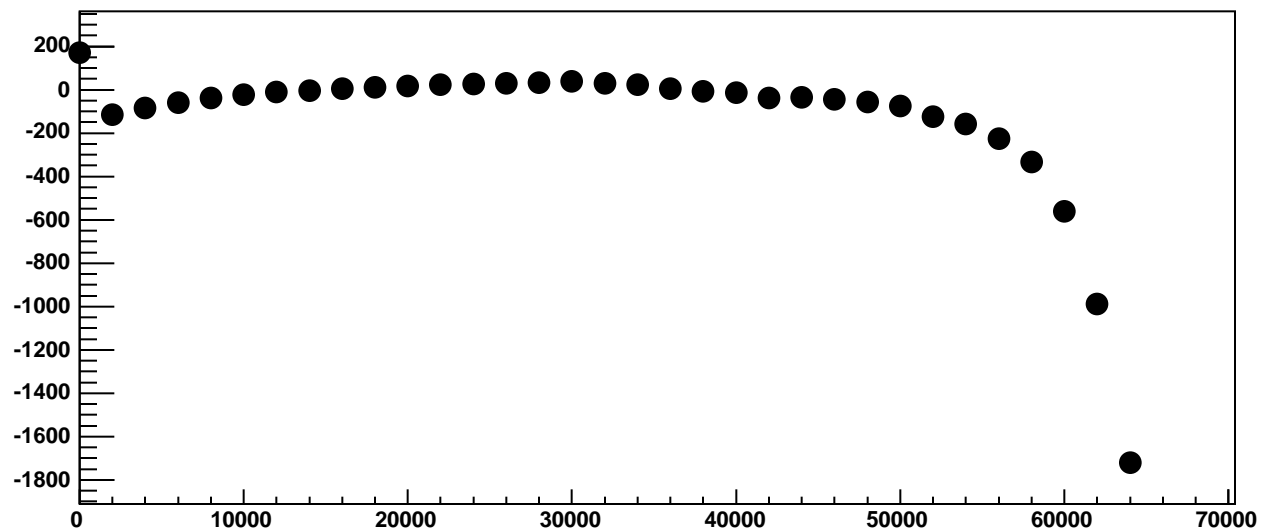
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



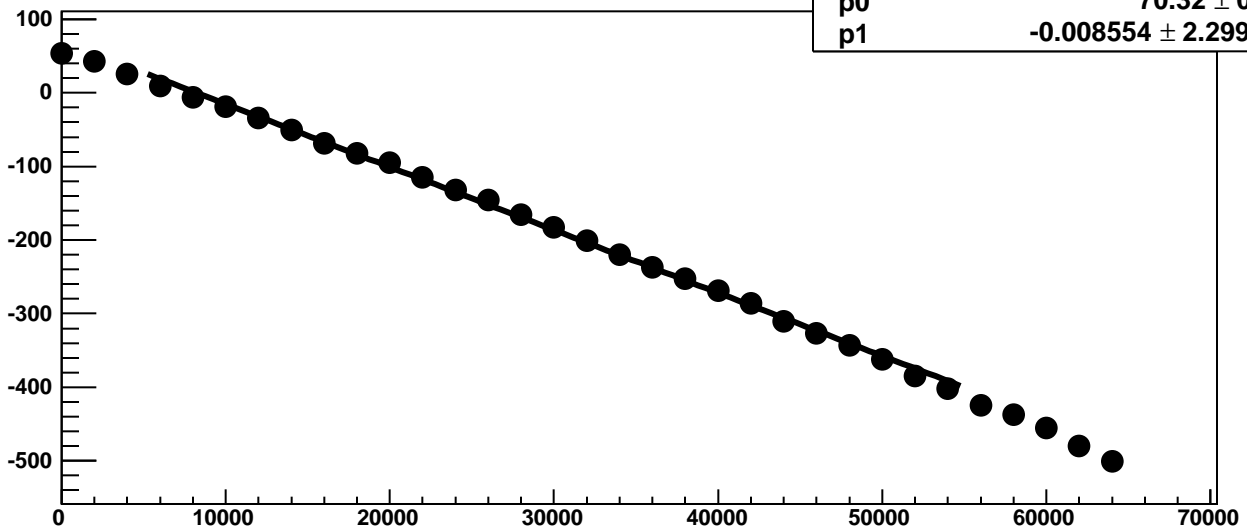
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



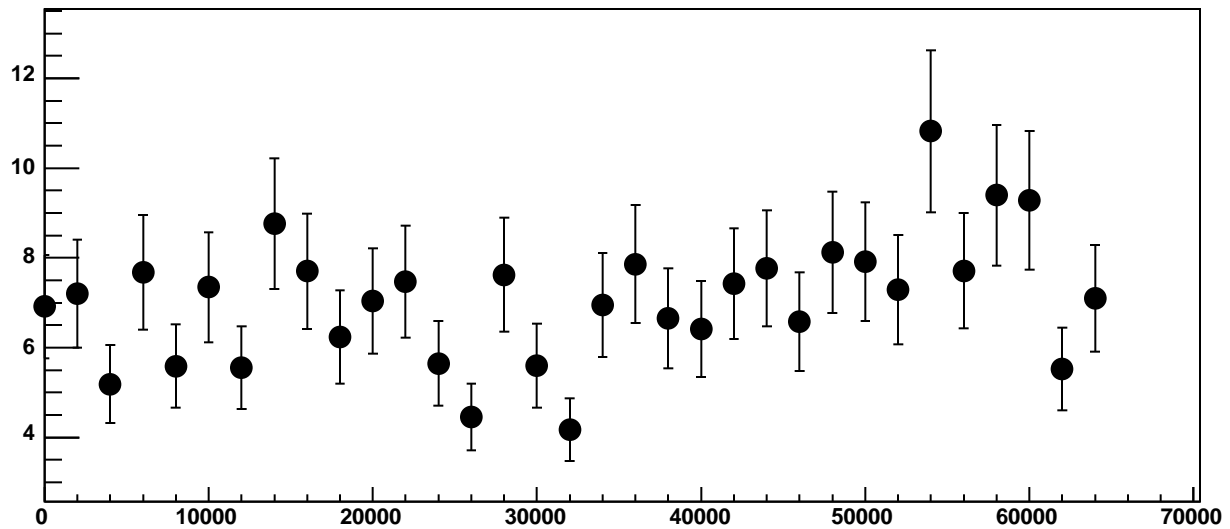
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



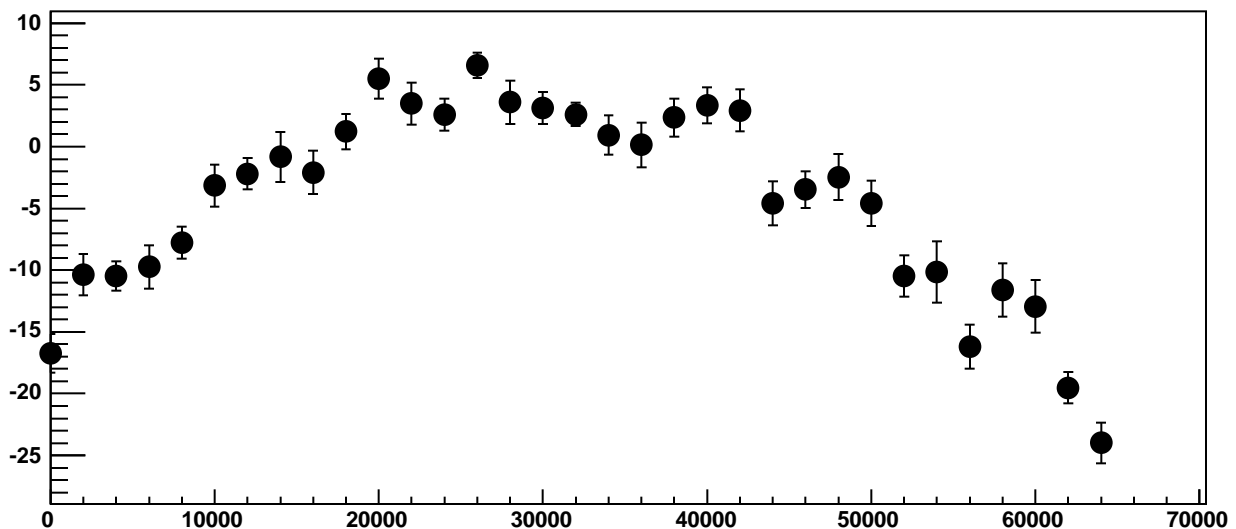
Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



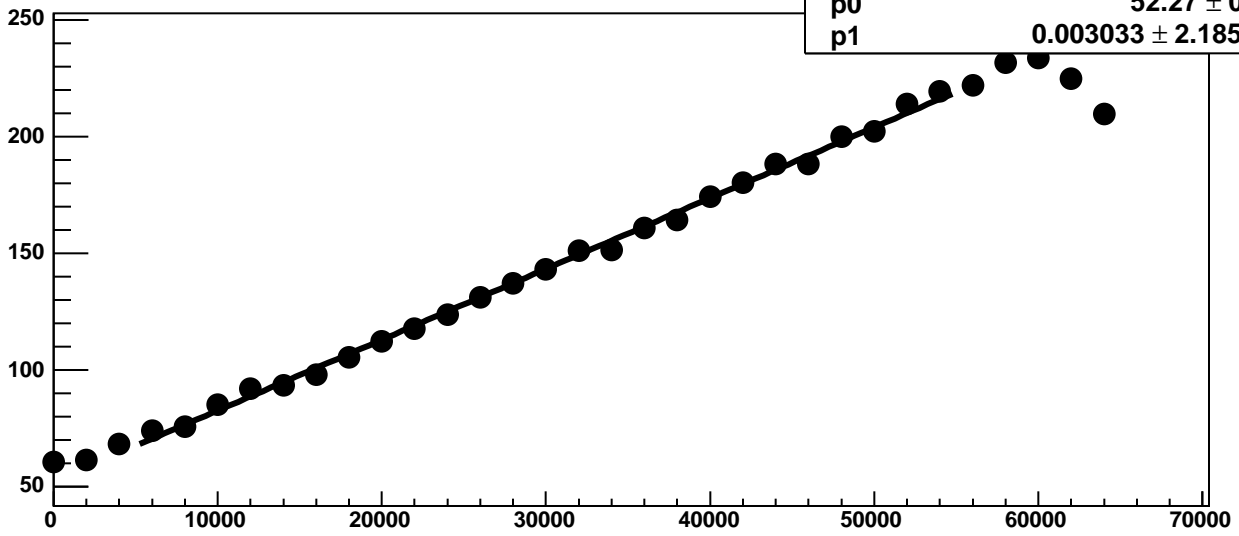
Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

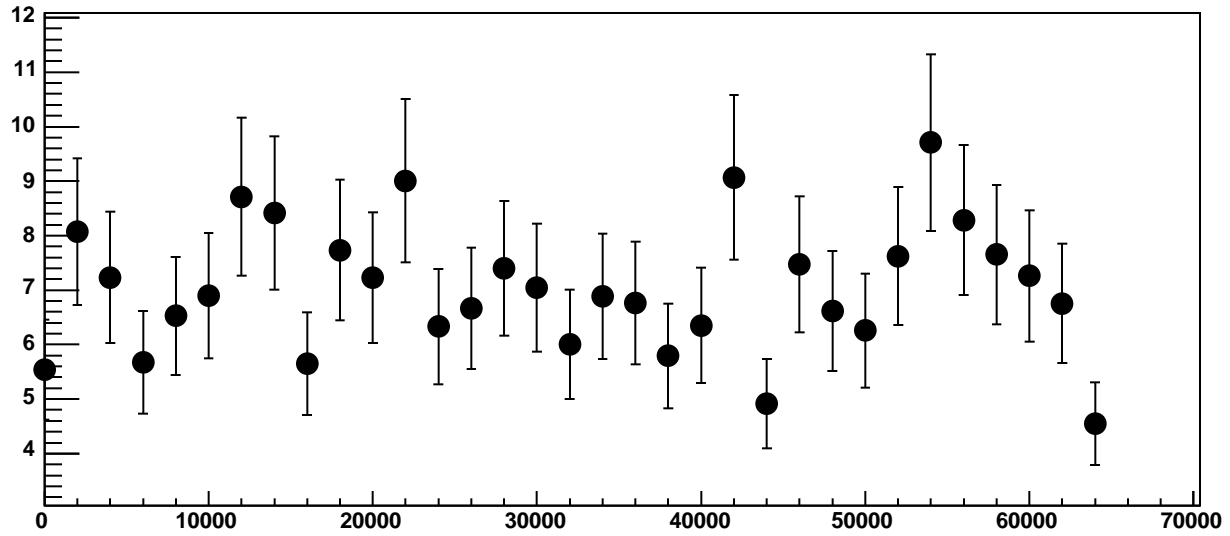


Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

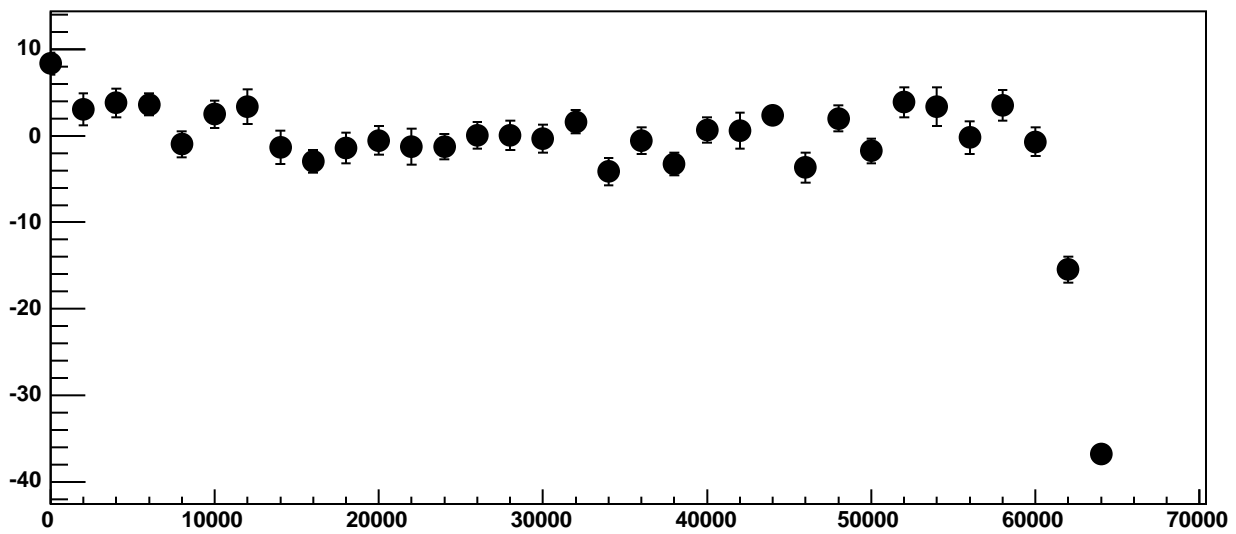


χ^2 / ndf 55.26 / 23
p0 52.27 ± 0.725
p1 $0.003033 \pm 2.185e-05$

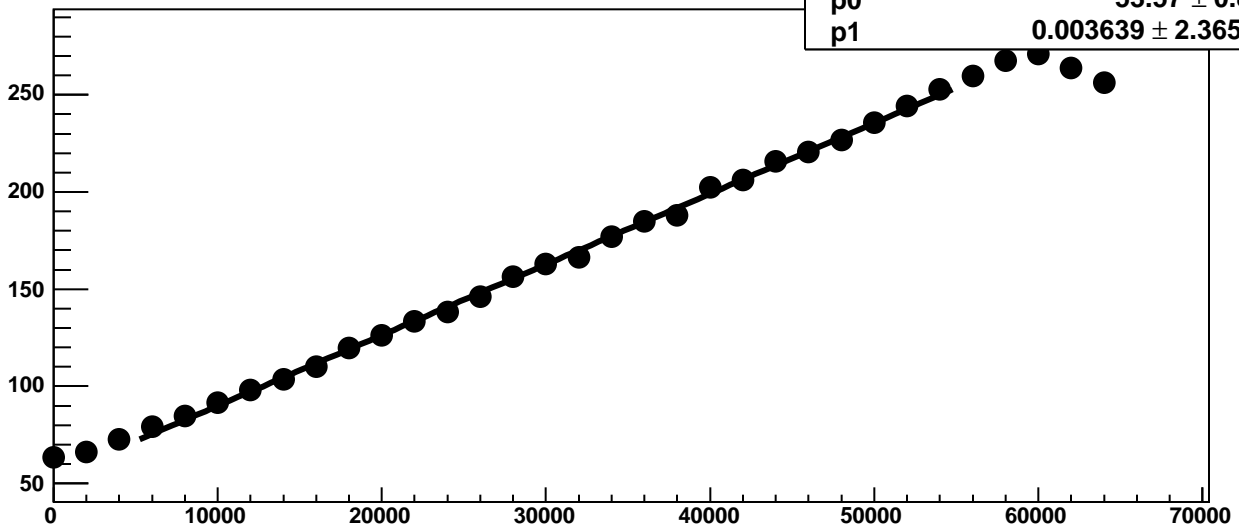
Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

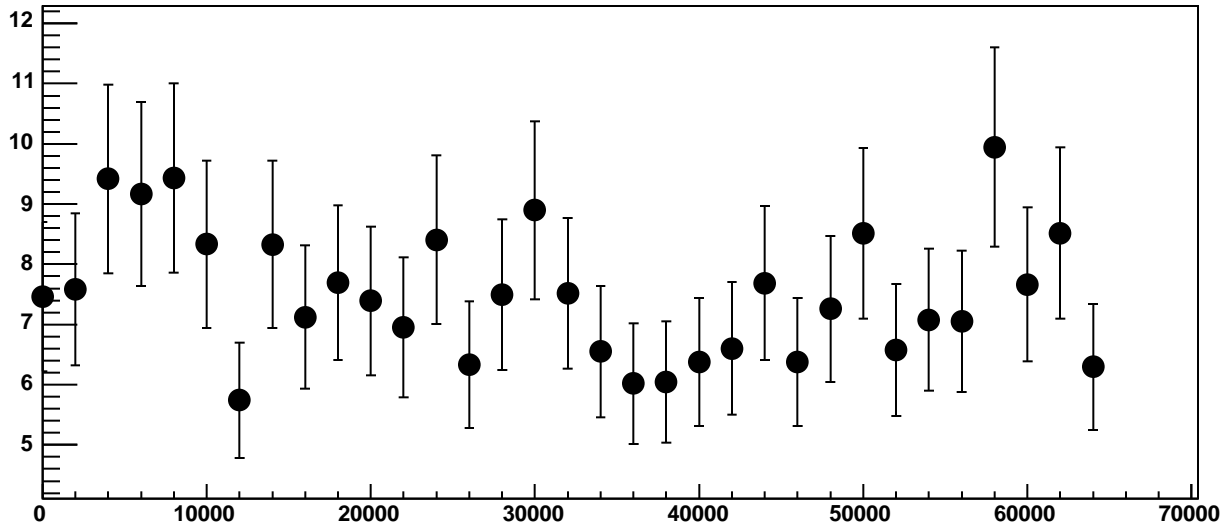


Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

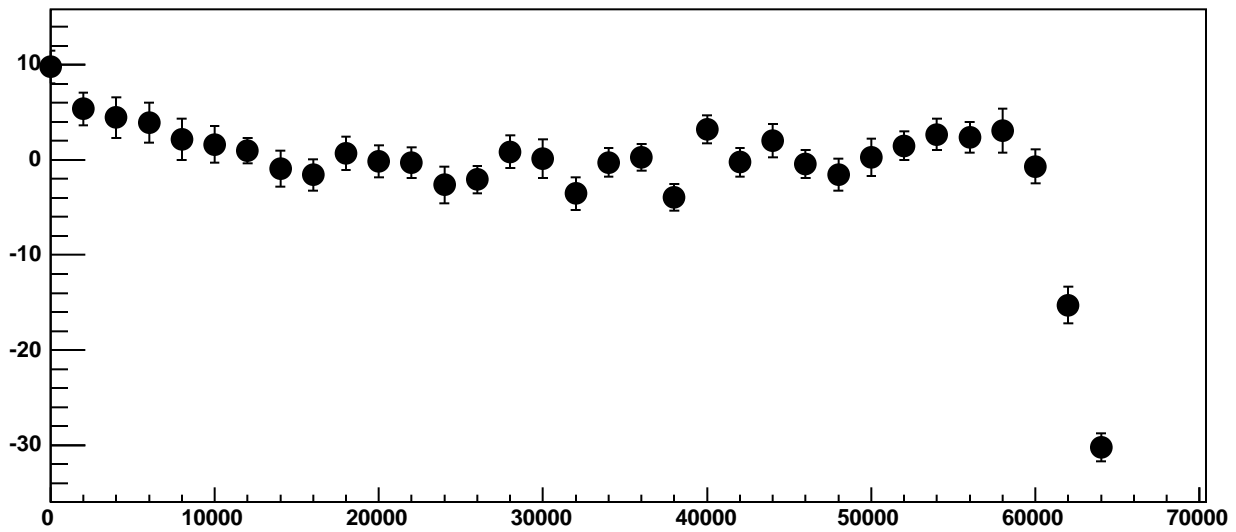


χ^2 / ndf 34.37 / 23
p0 53.57 ± 0.8092
p1 $0.003639 \pm 2.365e-05$

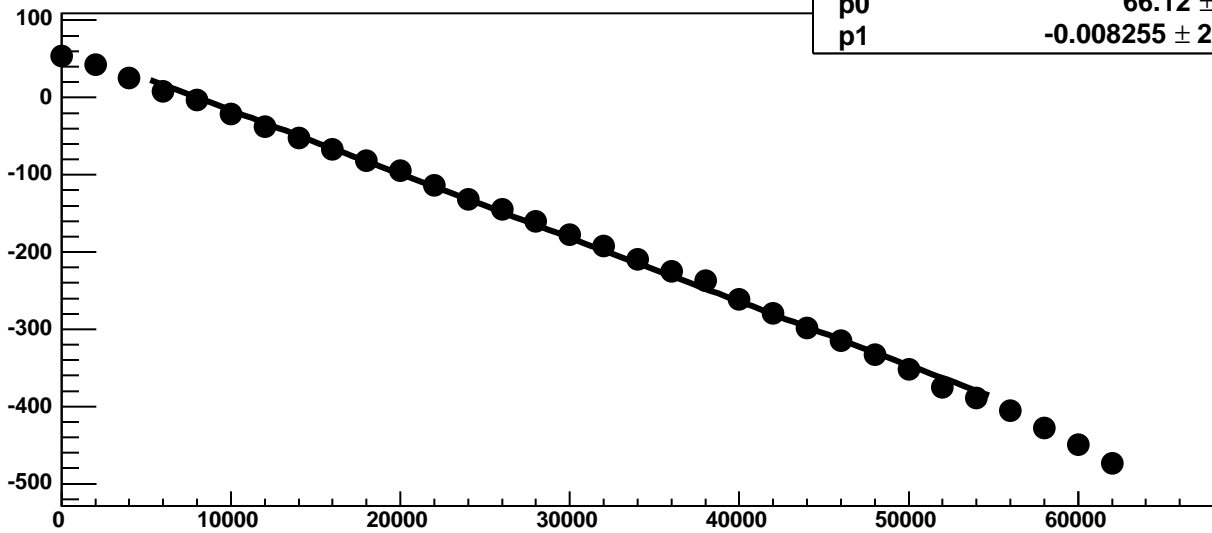
Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

226.6 / 23

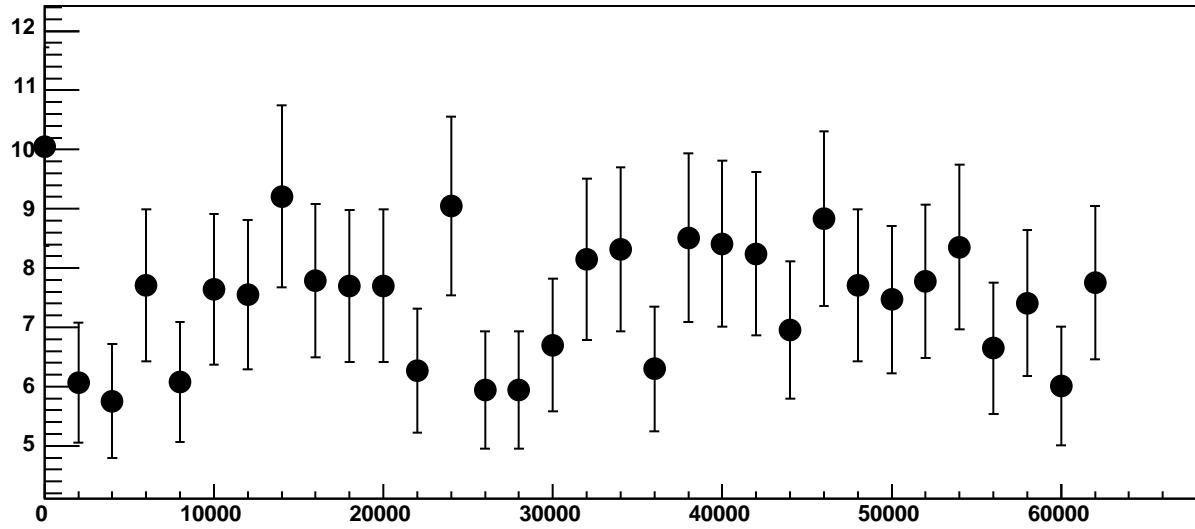
p0

66.12 ± 0.7843

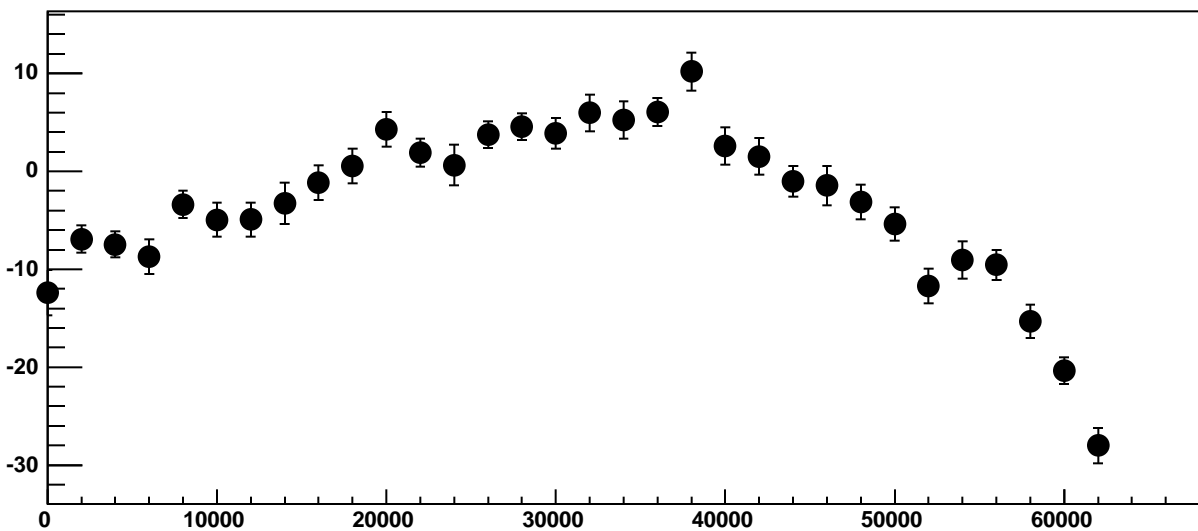
p1

$-0.008255 \pm 2.42e-05$

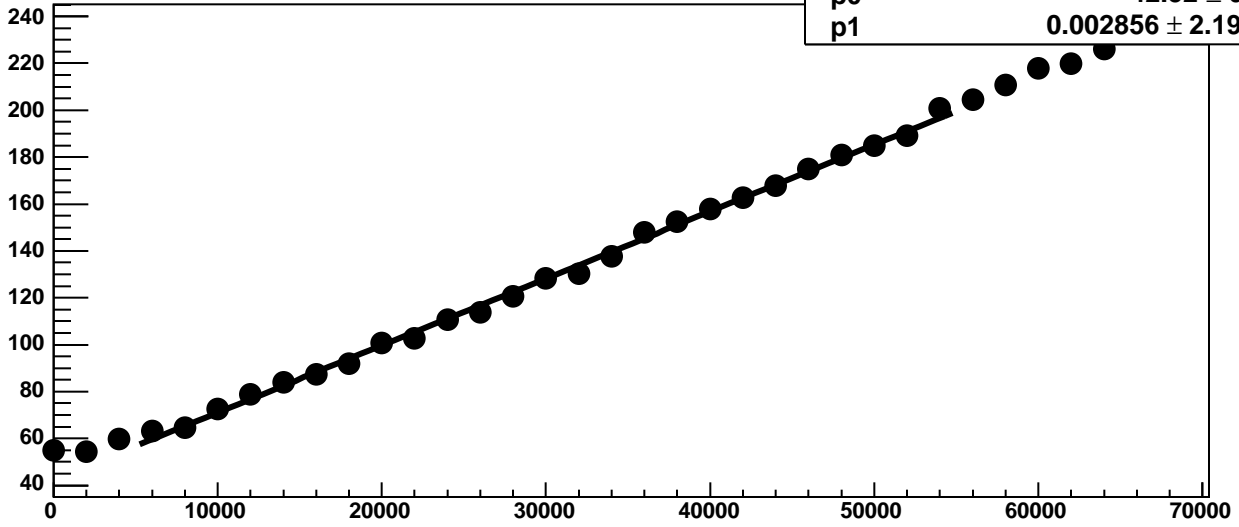
Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.49 / 23

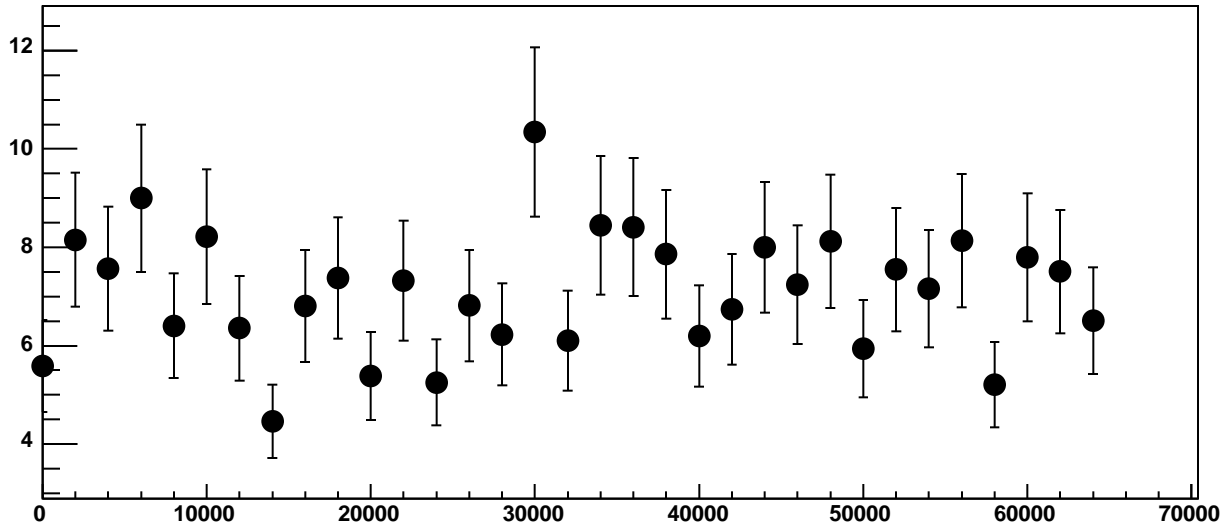
p0

42.52 ± 0.702

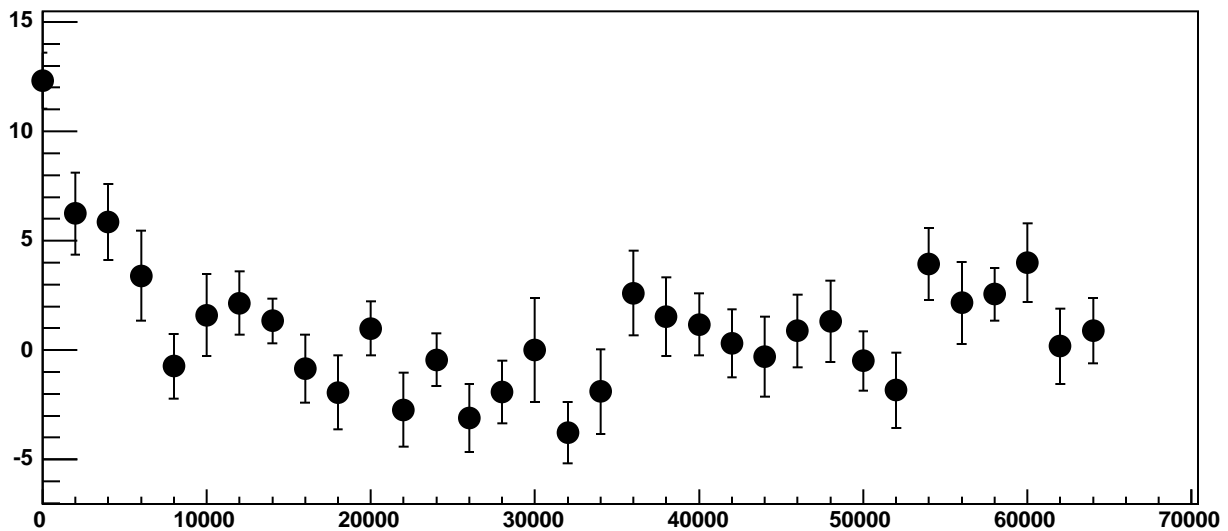
p1

$0.002856 \pm 2.19e-05$

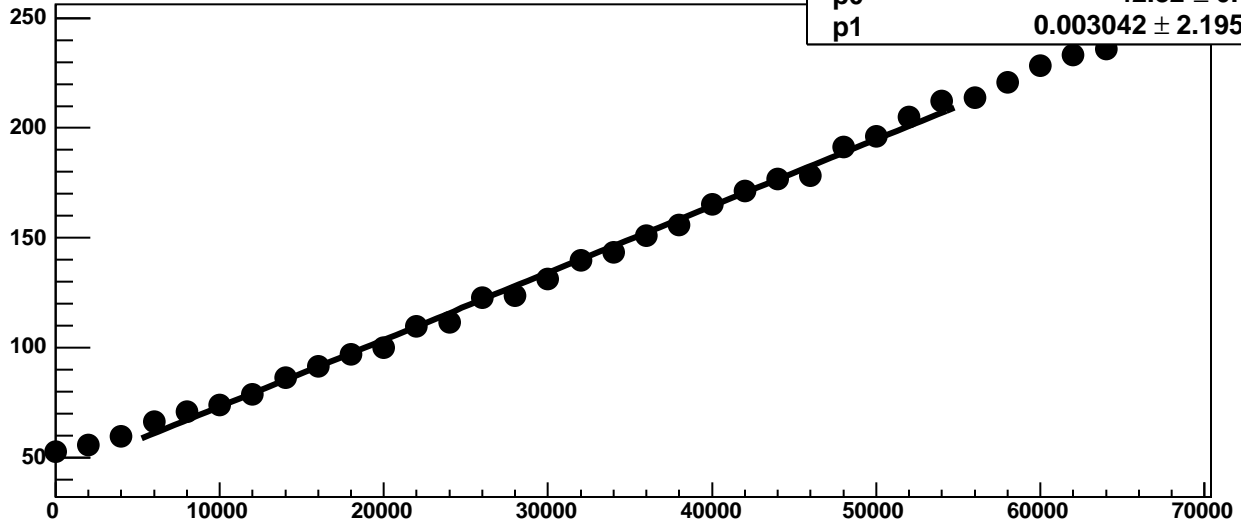
Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

70.41 / 23

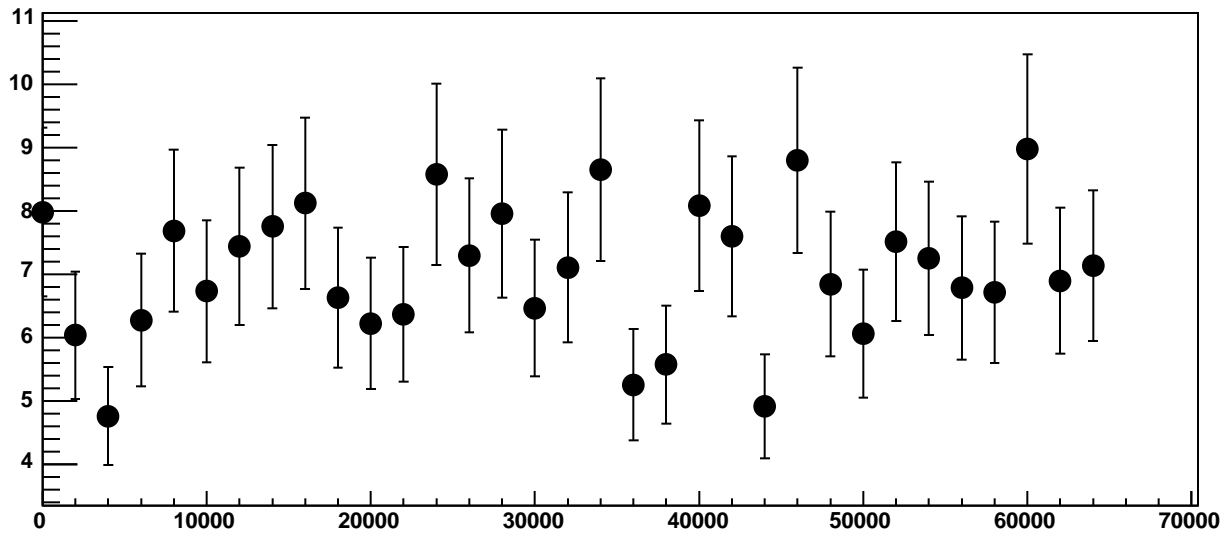
p0

42.82 ± 0.7425

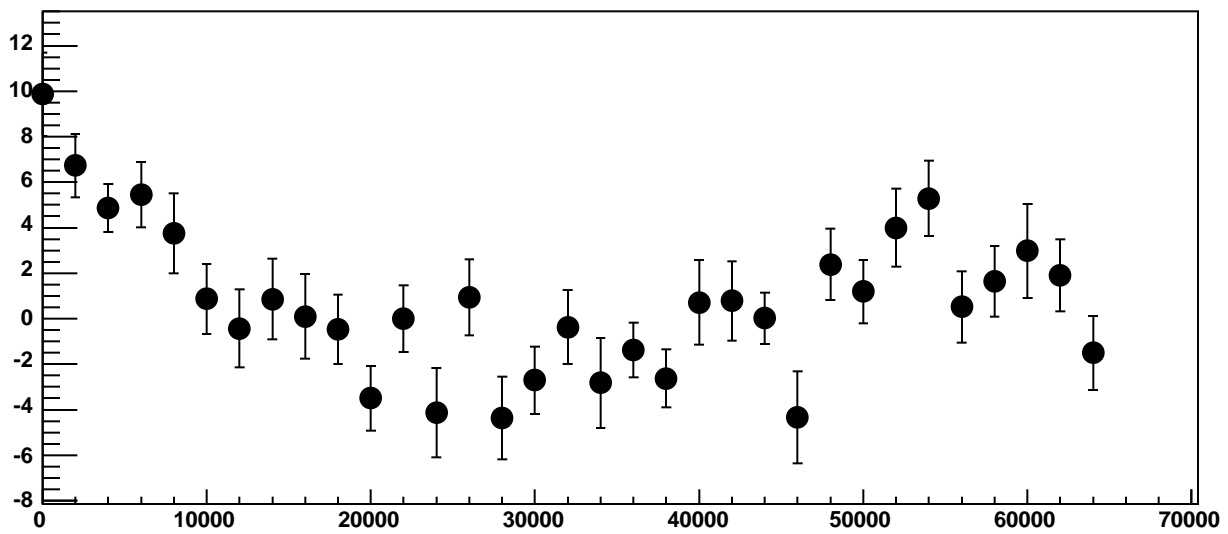
p1

$0.003042 \pm 2.195e-05$

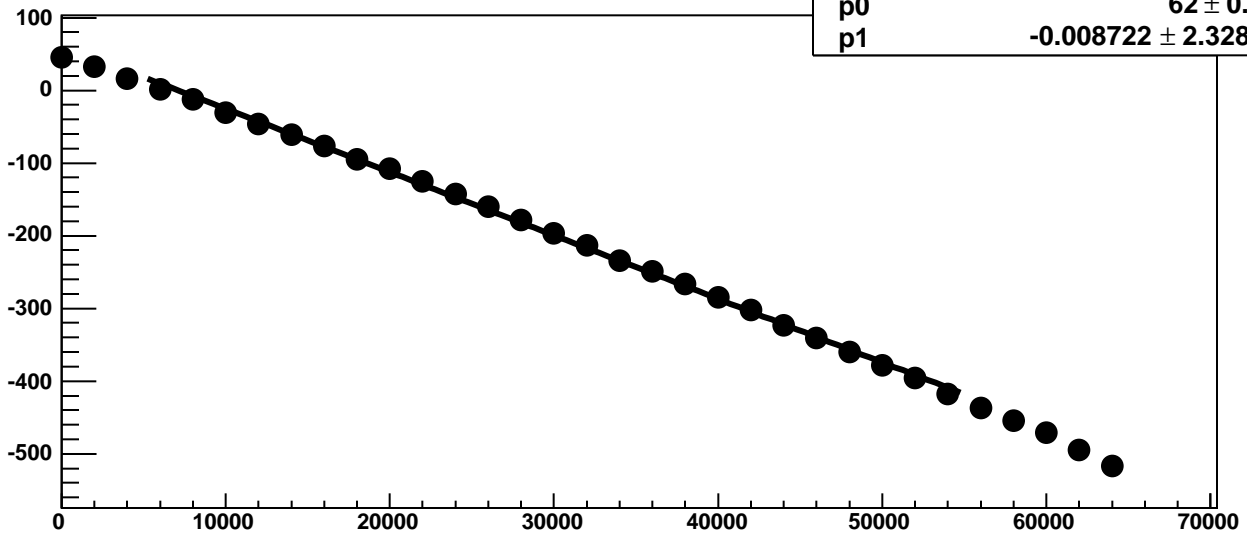
Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



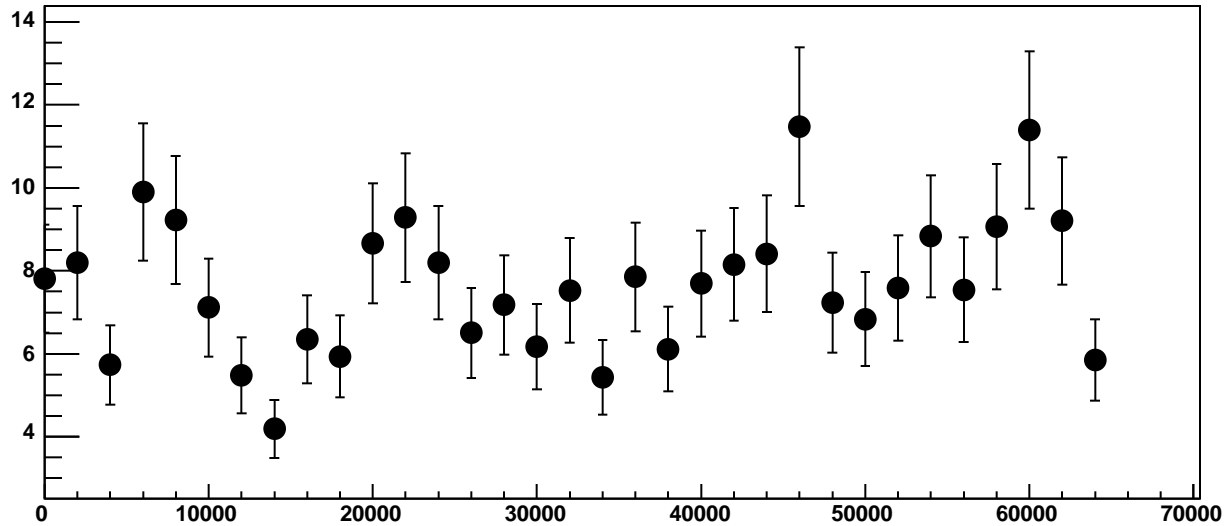
Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



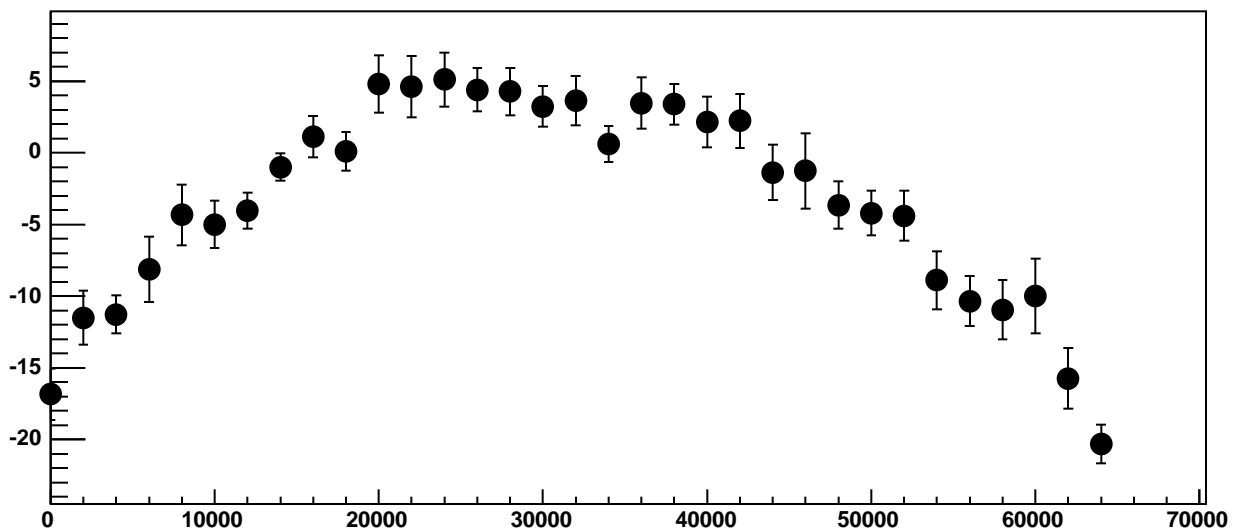
Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



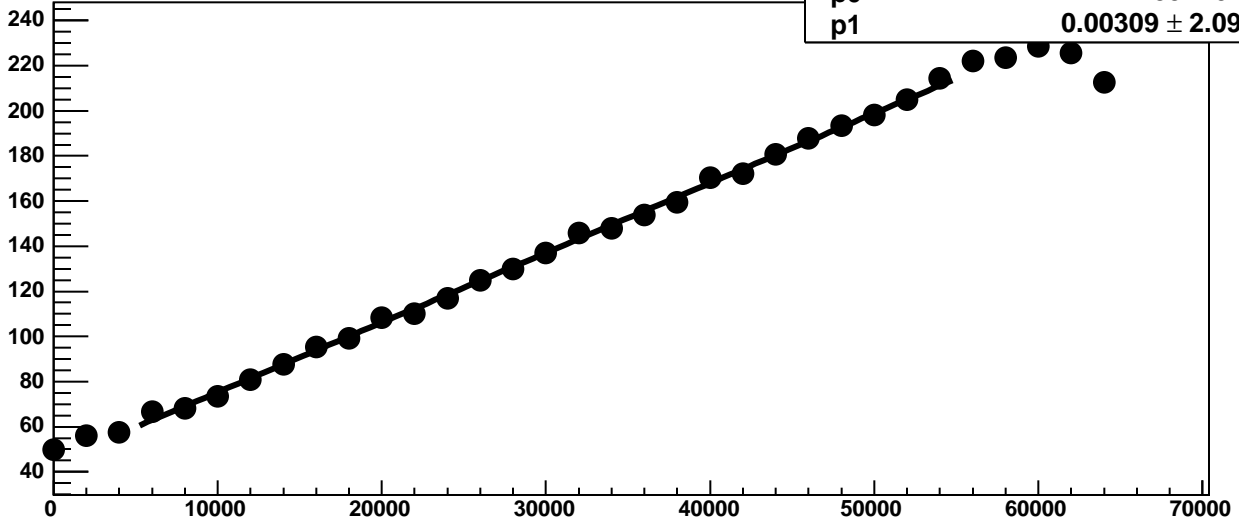
Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



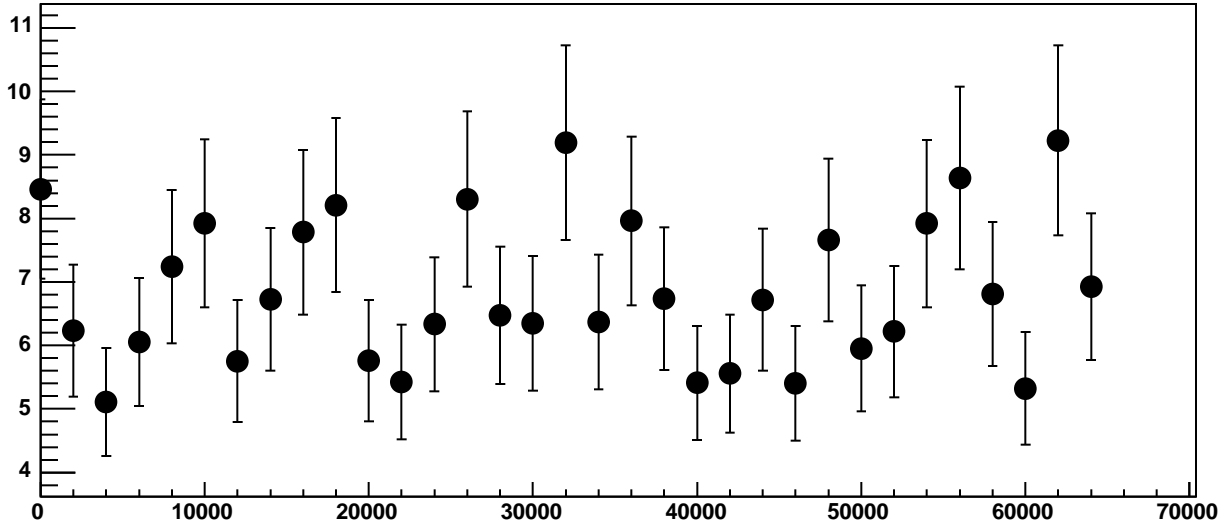
Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



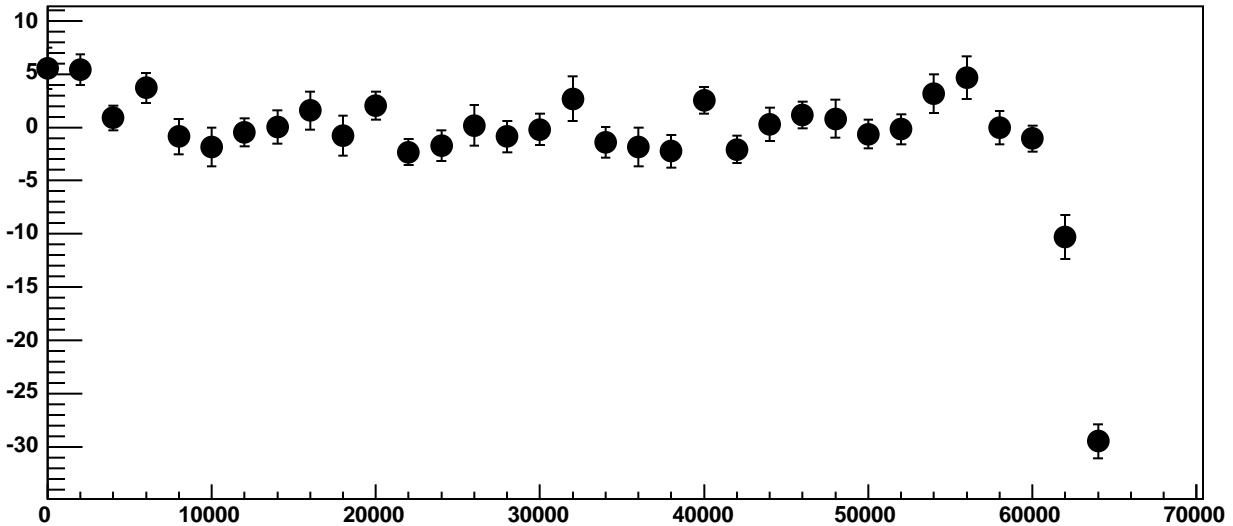
Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



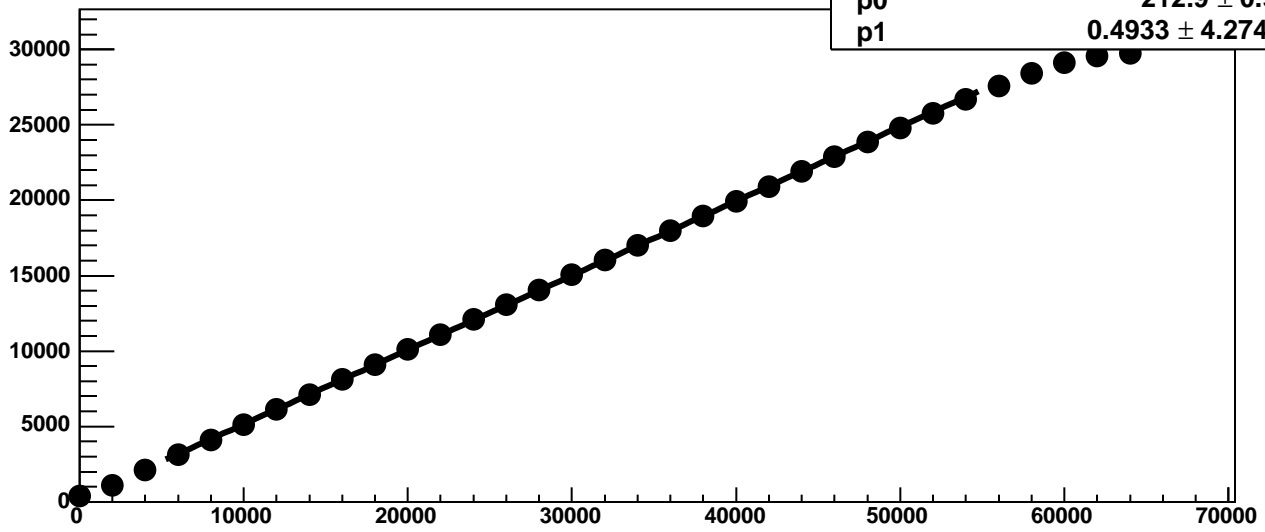
Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



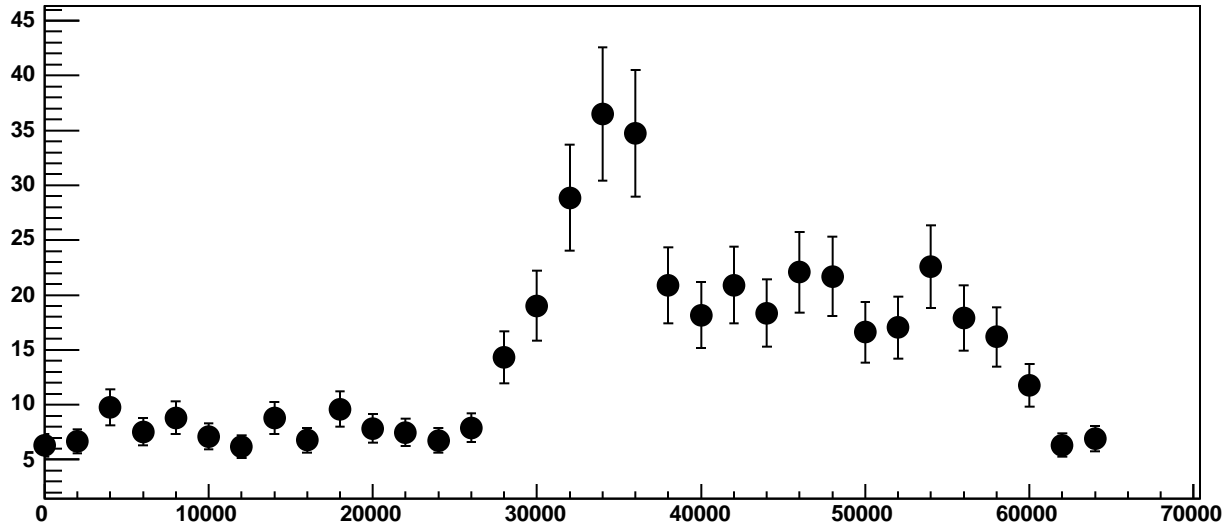
Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



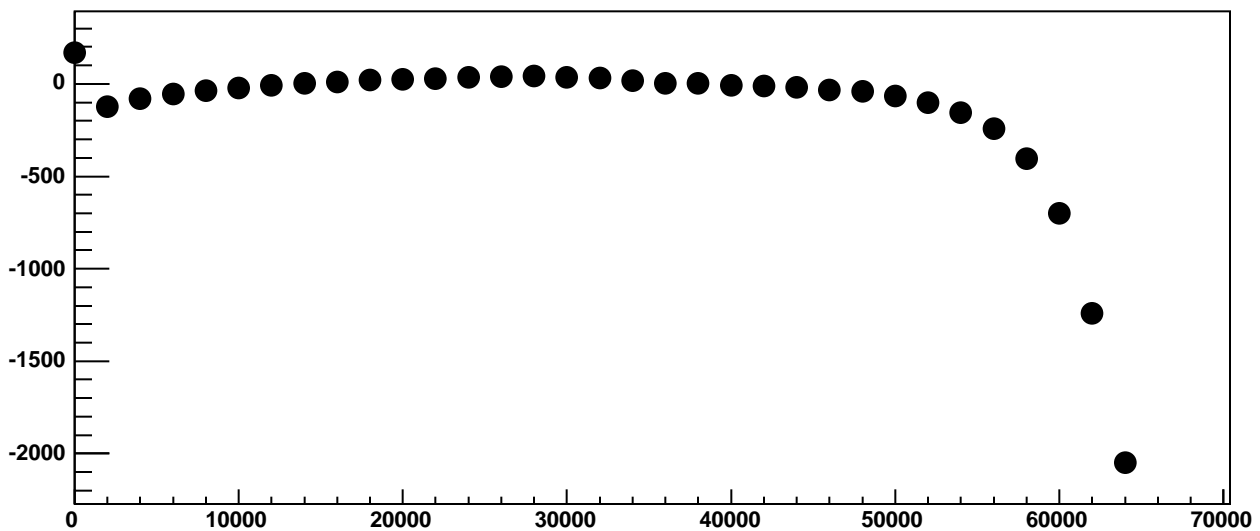
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



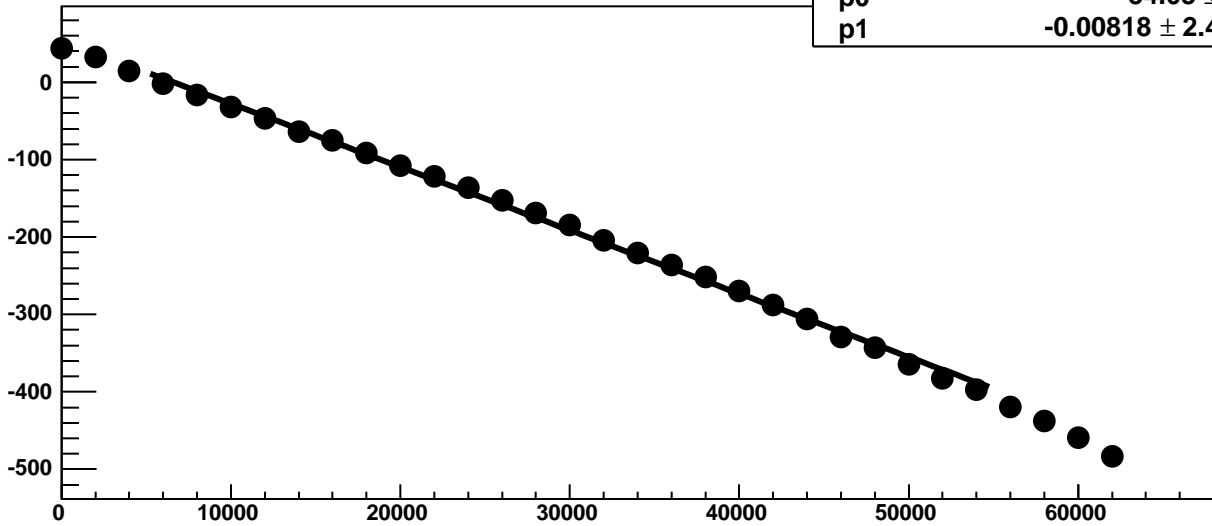
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

206.5 / 23

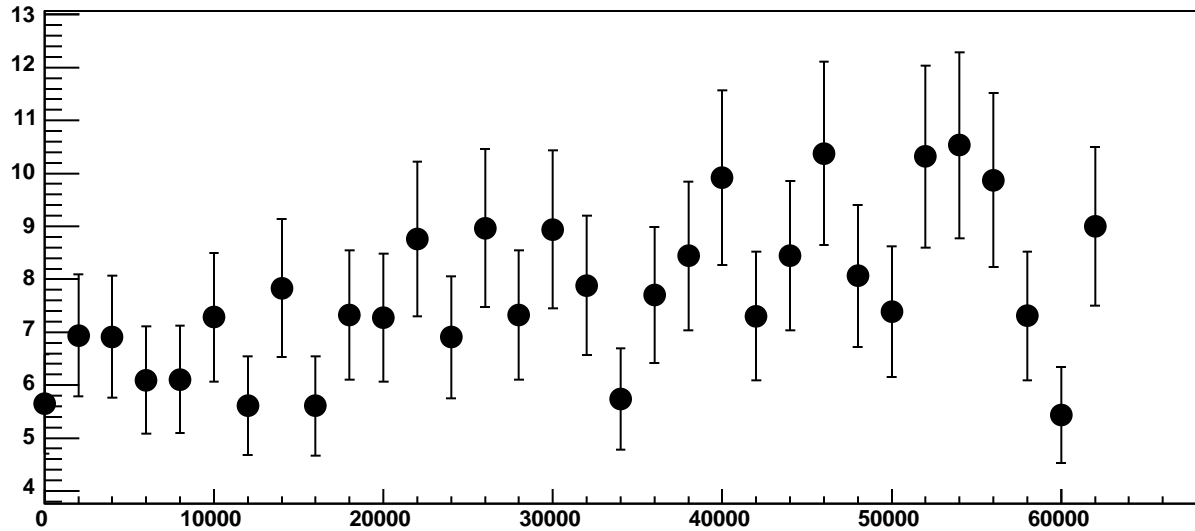
p0

54.03 ± 0.7261

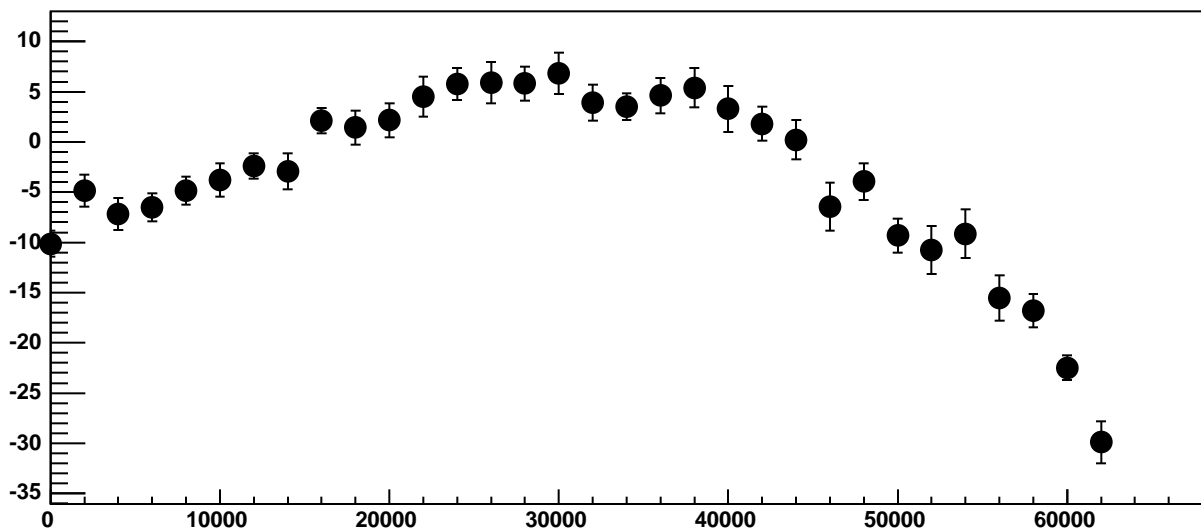
p1

$-0.00818 \pm 2.405e-05$

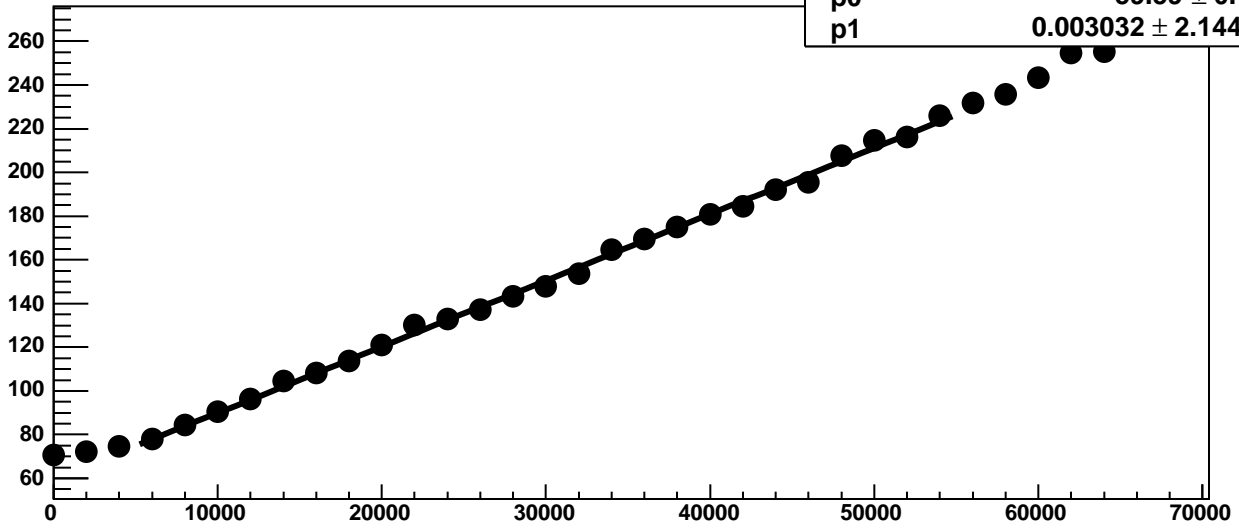
Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

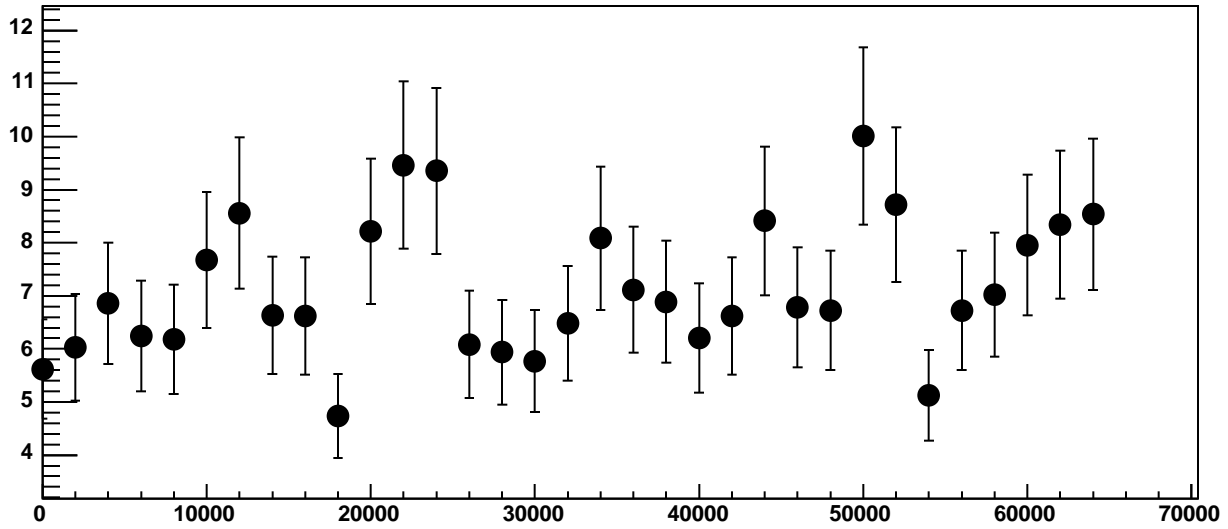


Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

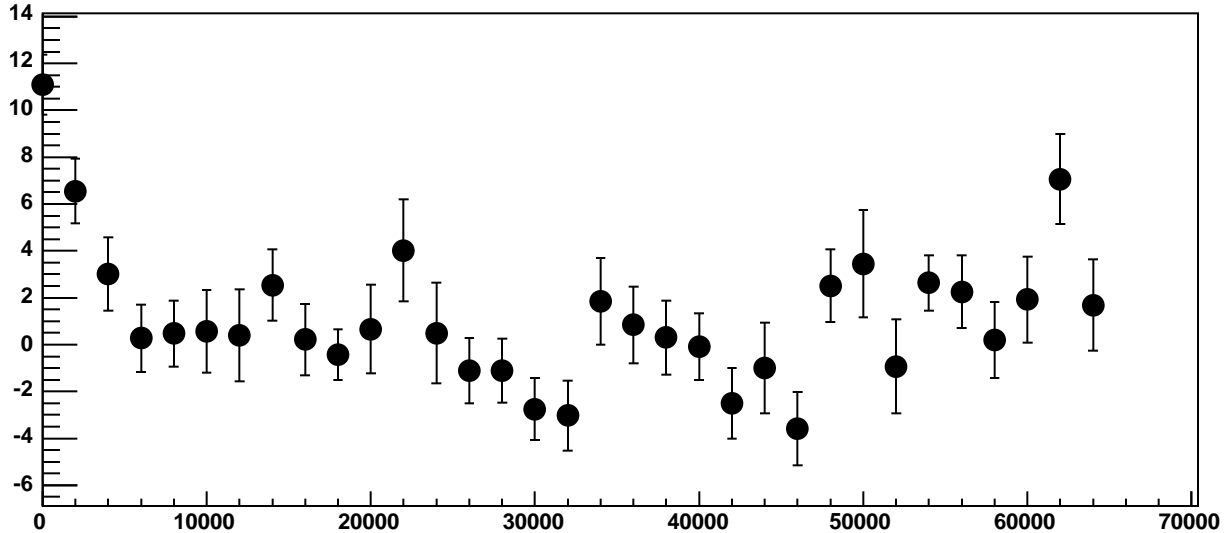


χ^2 / ndf 36.39 / 23
p0 59.59 ± 0.7077
p1 $0.003032 \pm 2.144e-05$

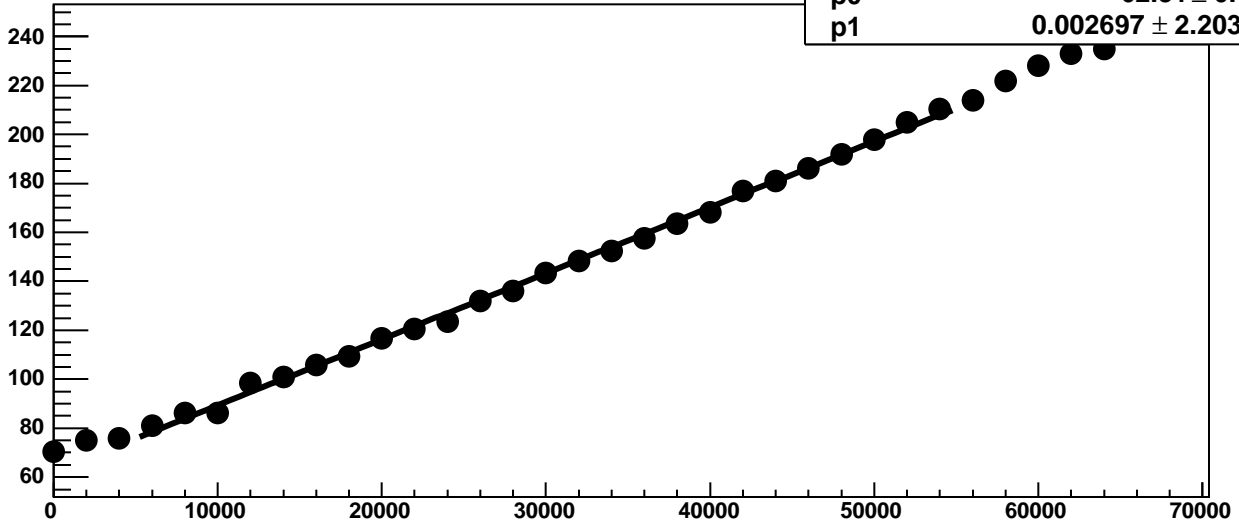
Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



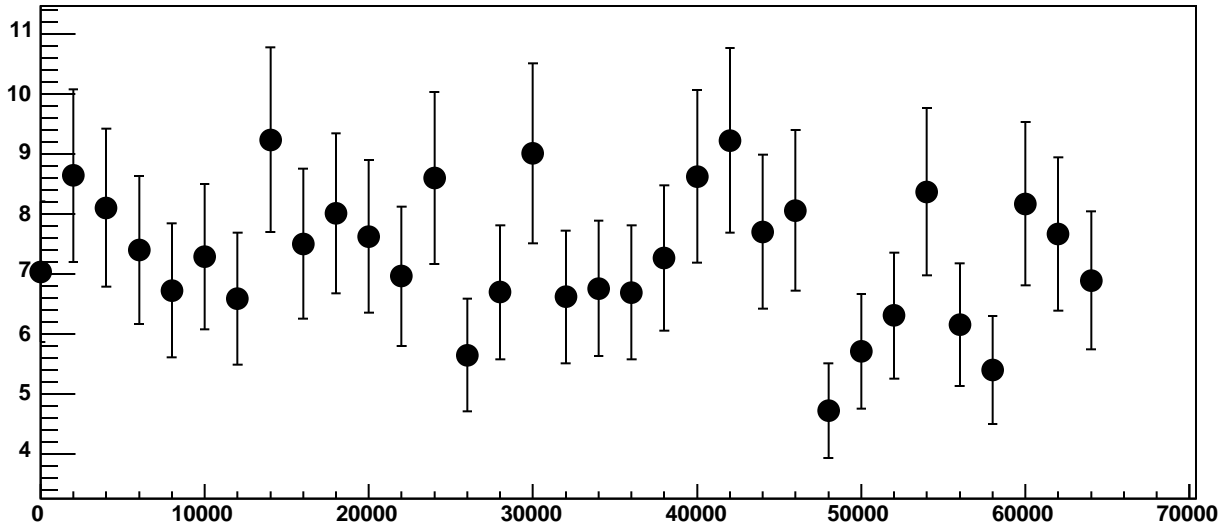
Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



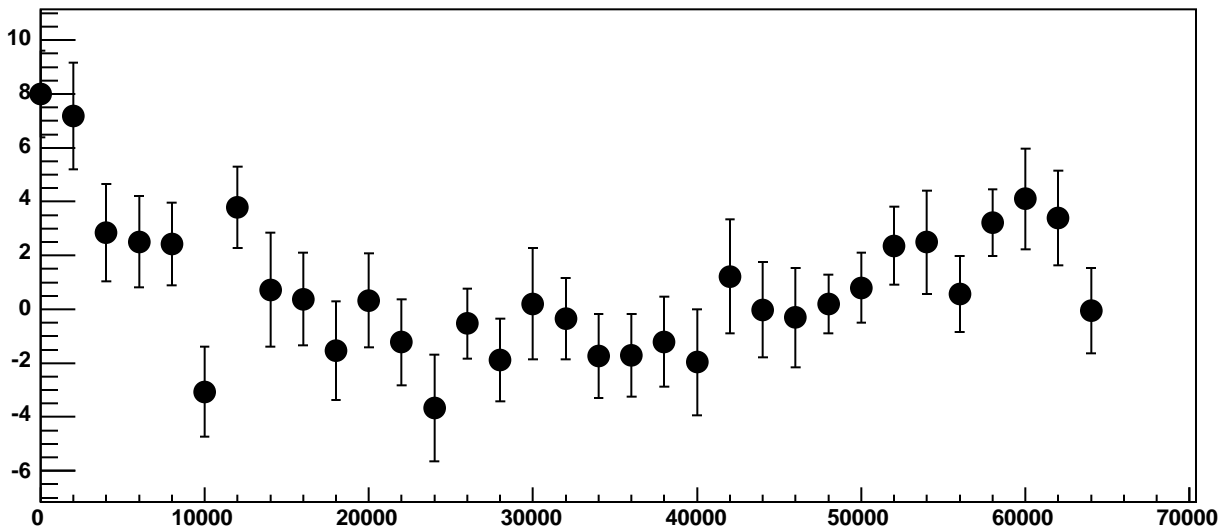
Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



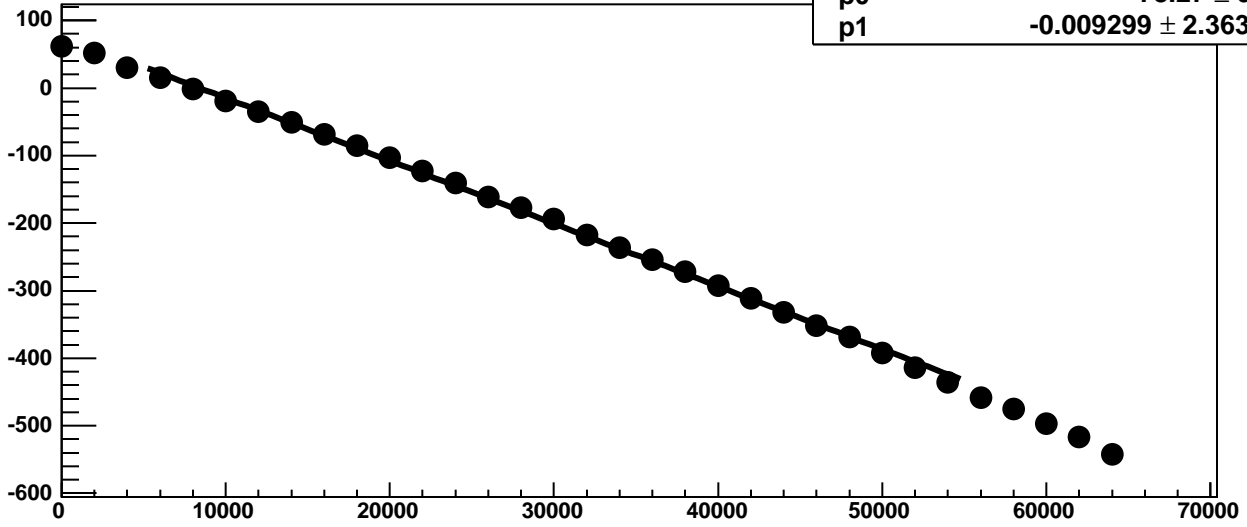
Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



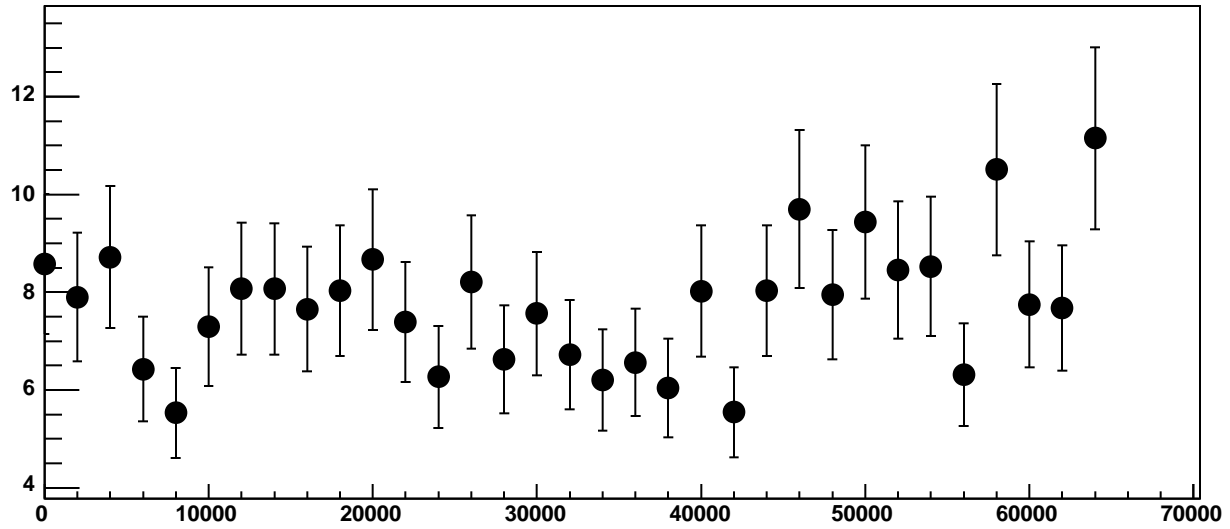
Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



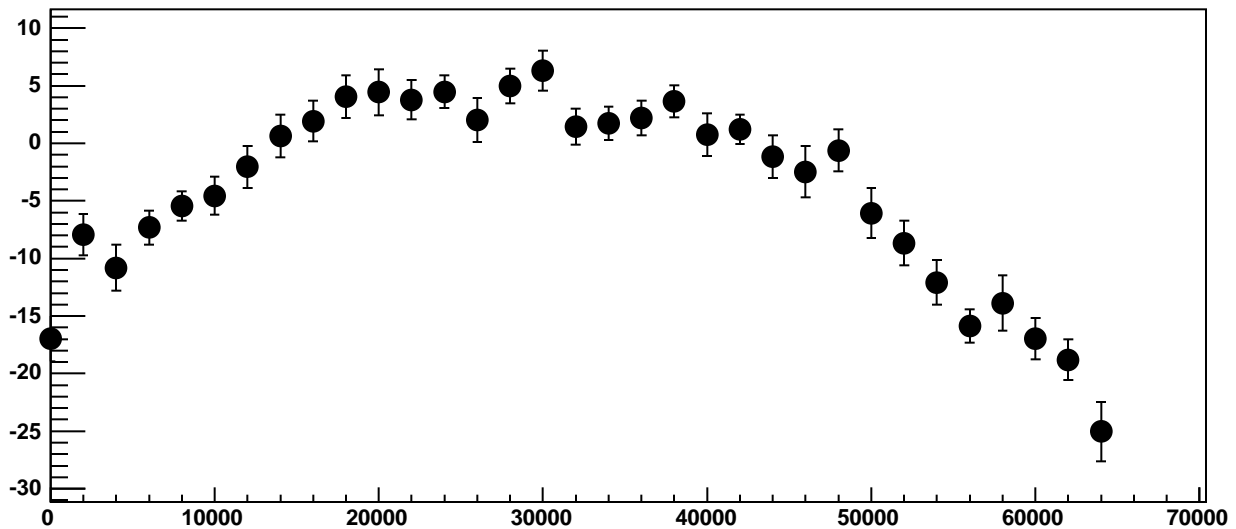
Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



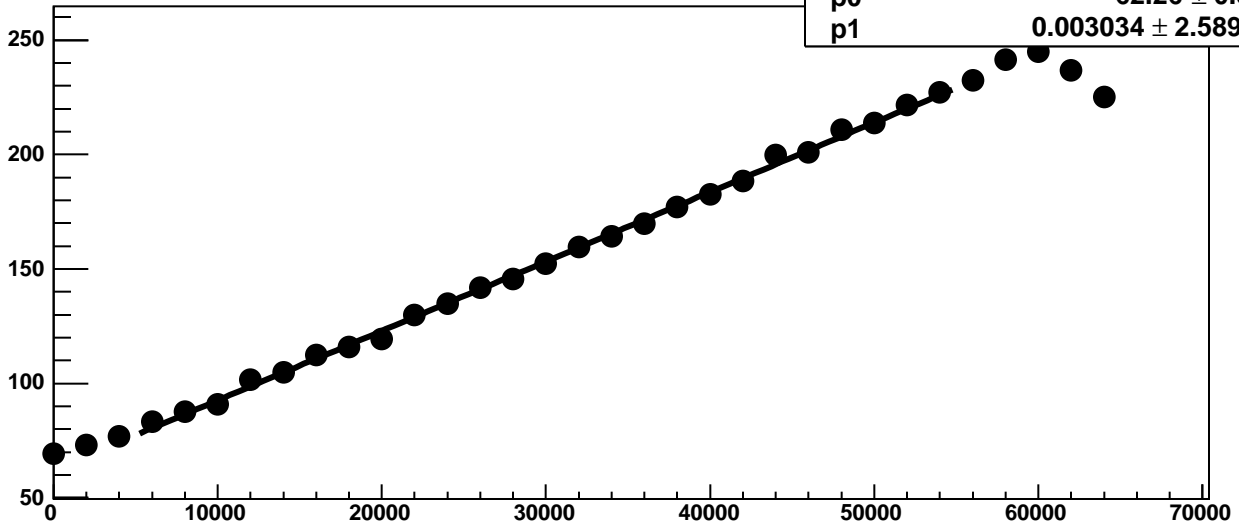
Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

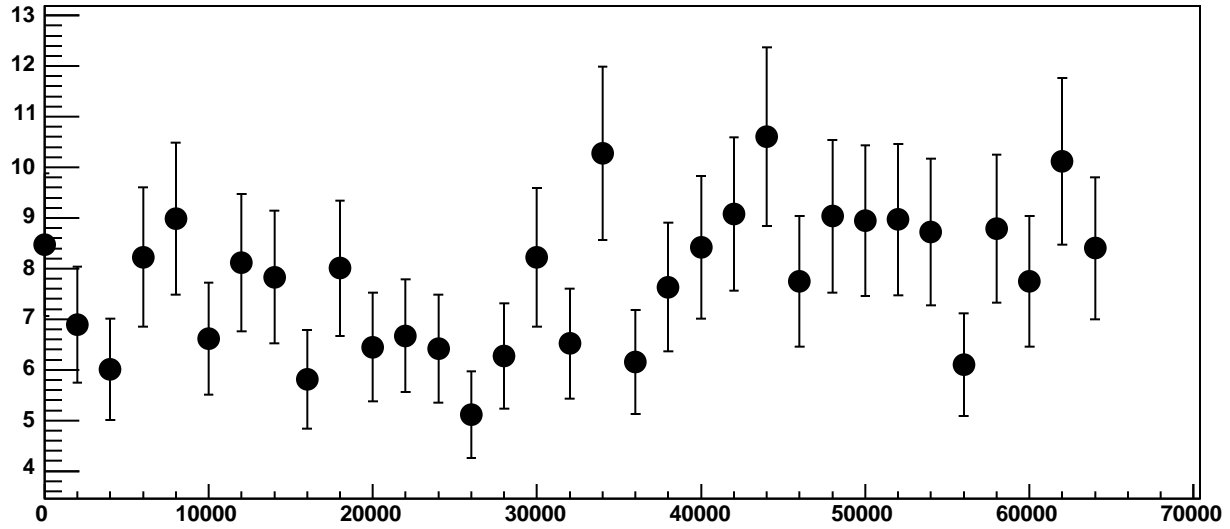


Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

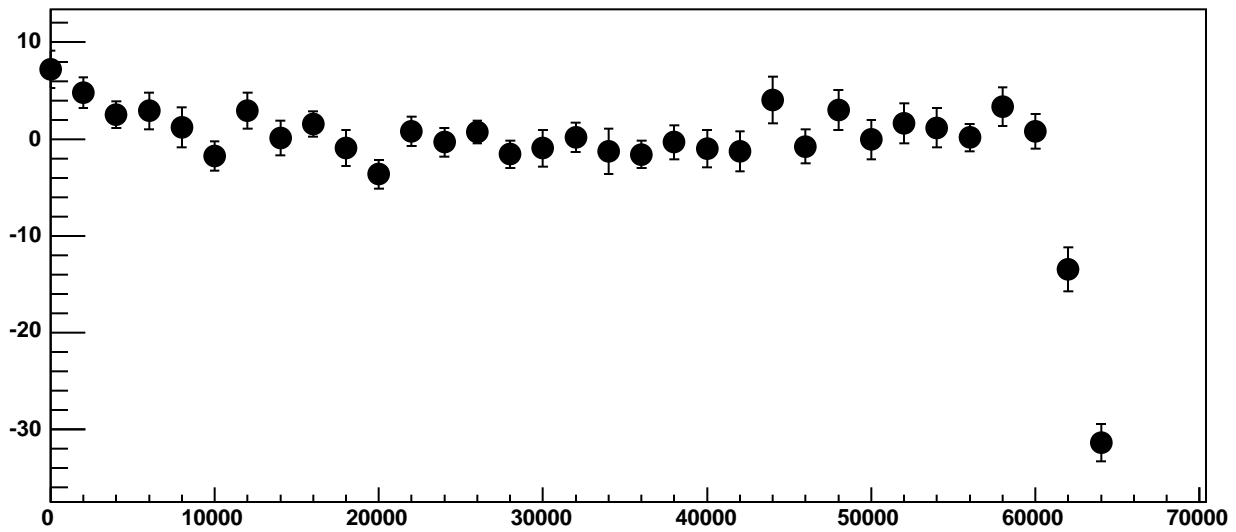


χ^2 / ndf 24.48 / 23
p0 62.26 ± 0.8027
p1 $0.003034 \pm 2.589e-05$

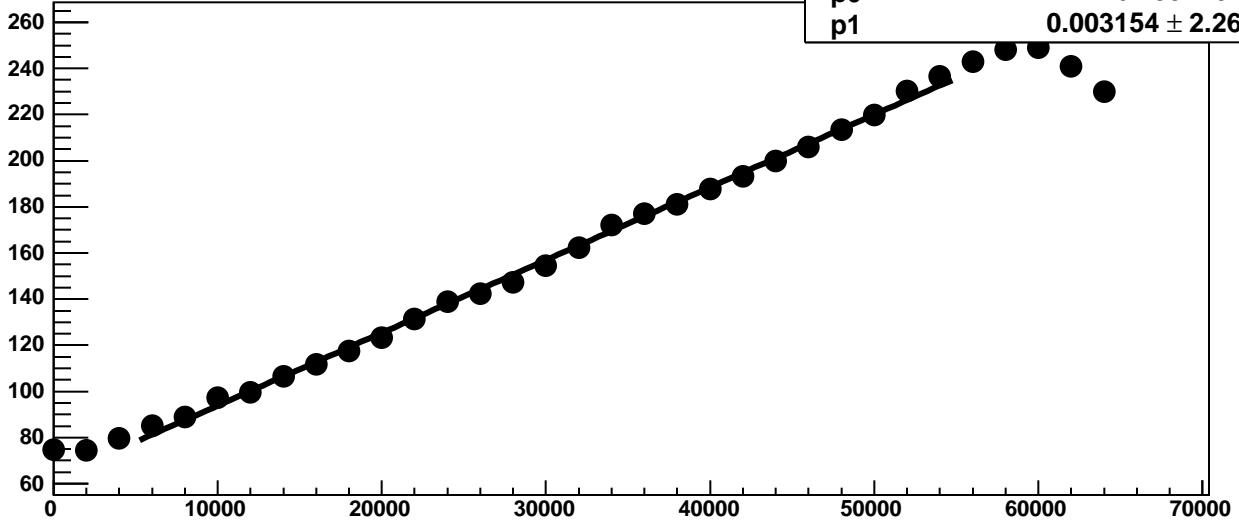
Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



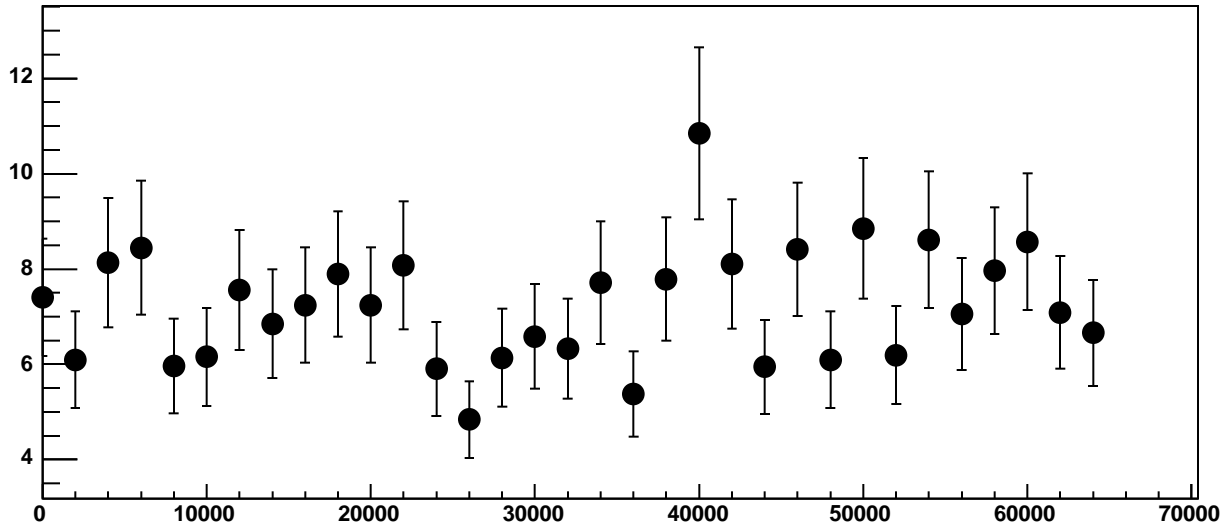
Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



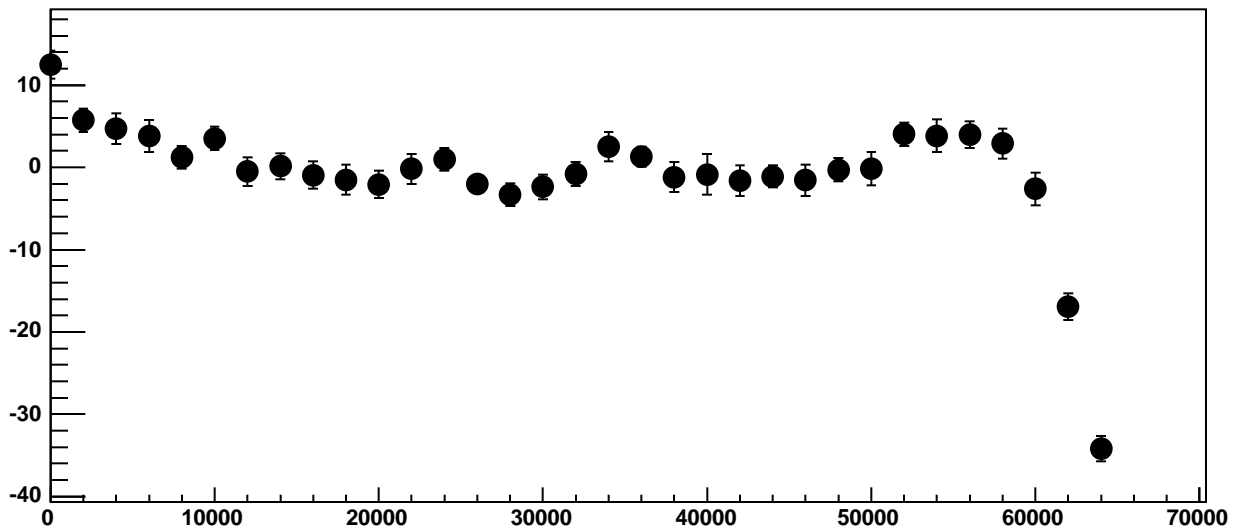
Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



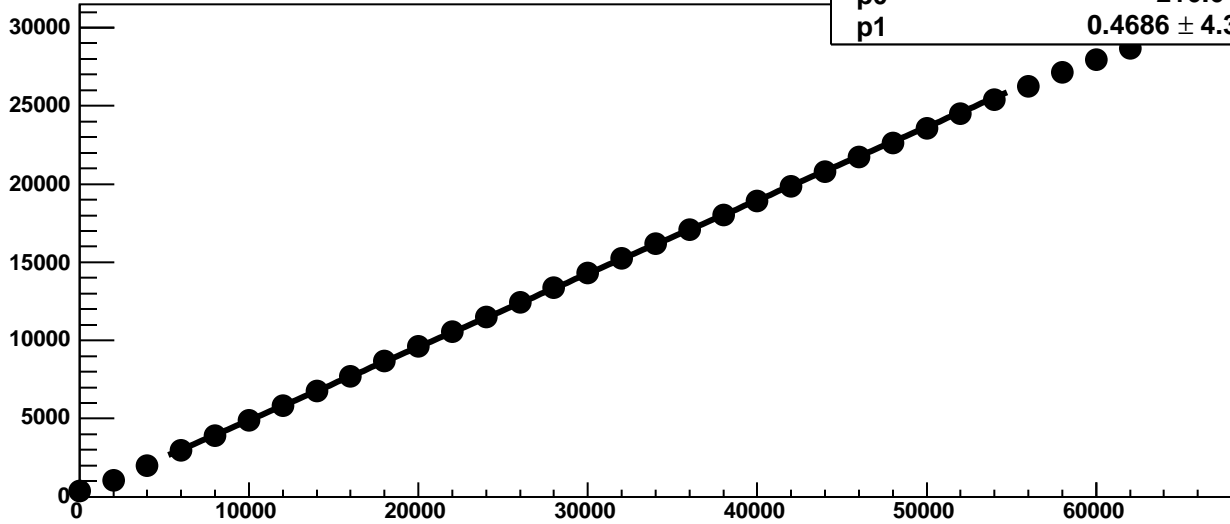
Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

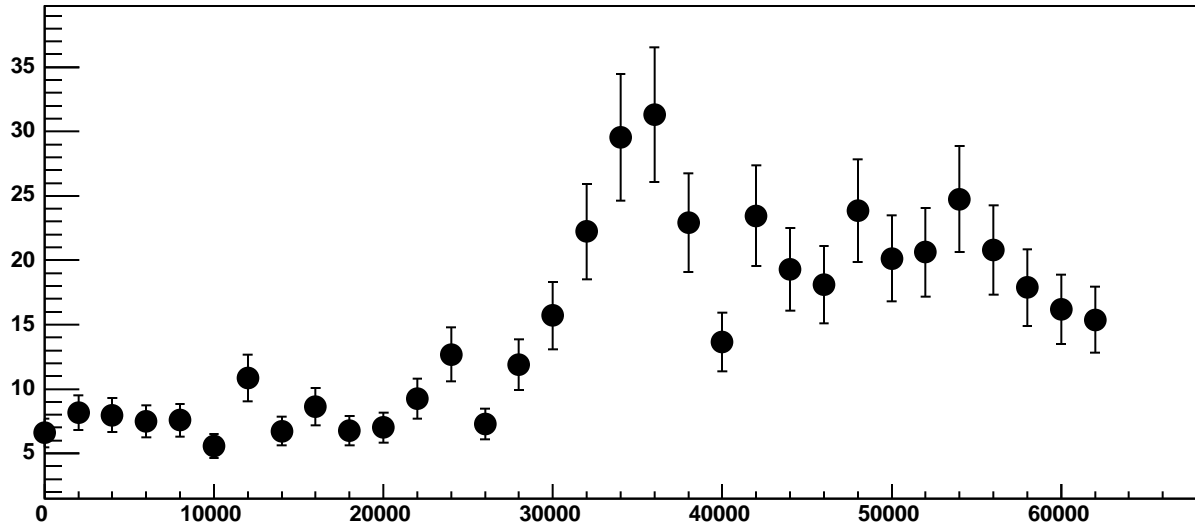


Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

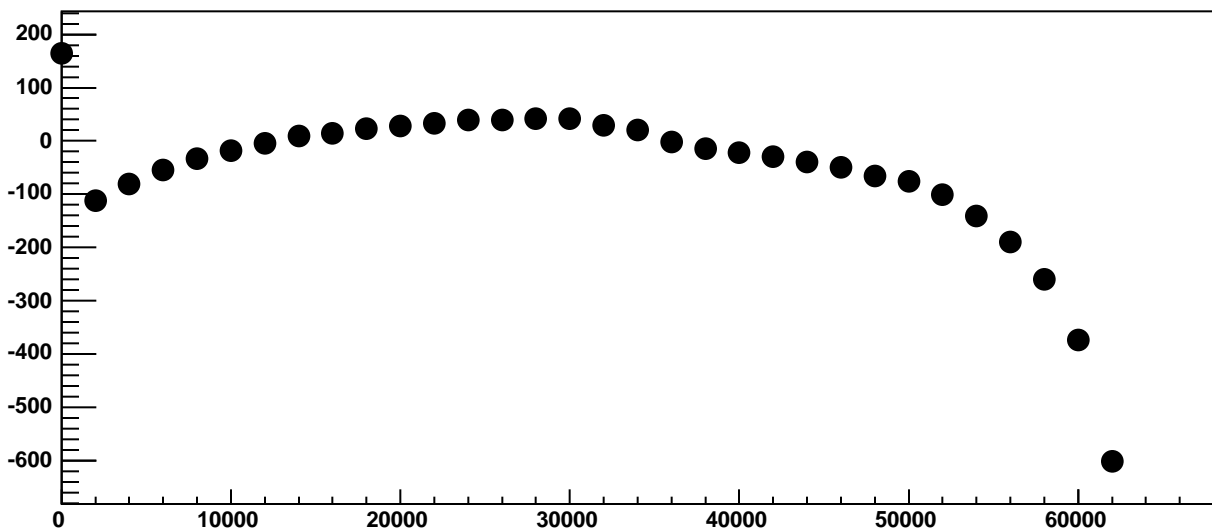


χ^2 / ndf 5362 / 23
p0 216.6 ± 0.957
p1 $0.4686 \pm 4.312e-05$

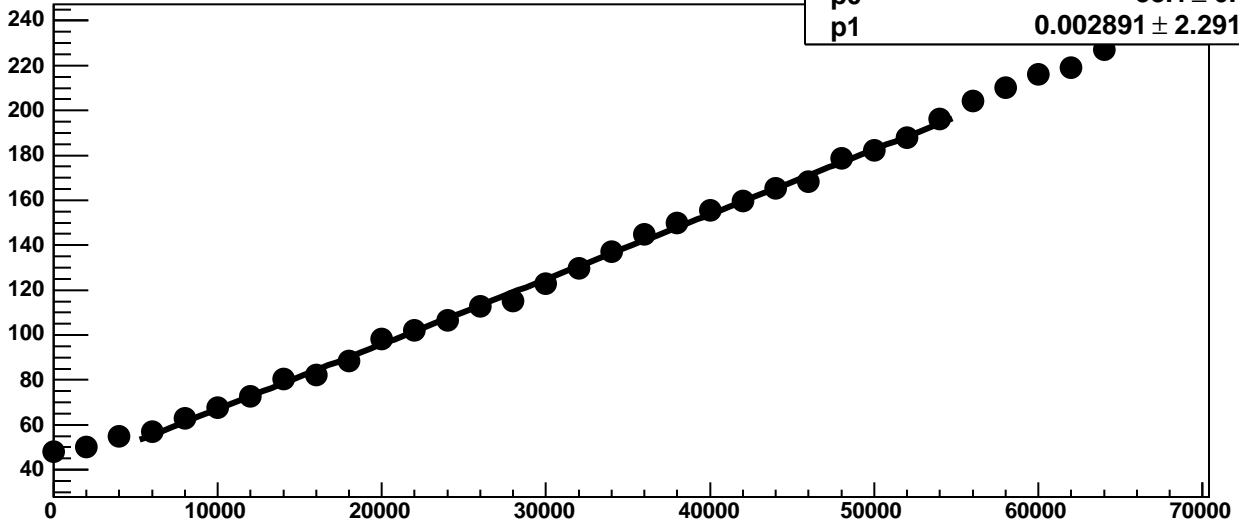
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25.31 / 23

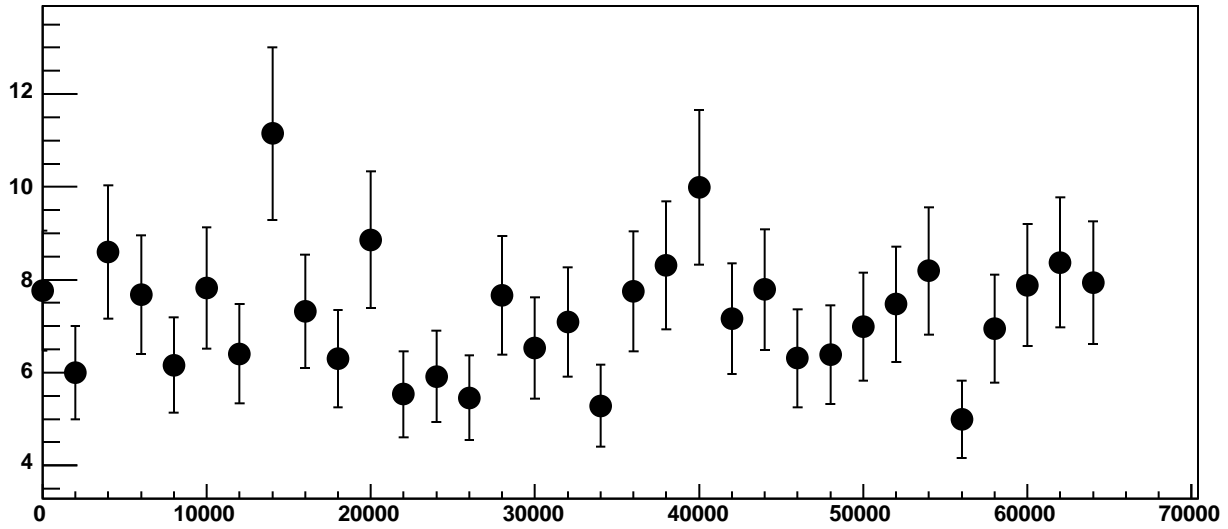
p0

38.1 ± 0.7494

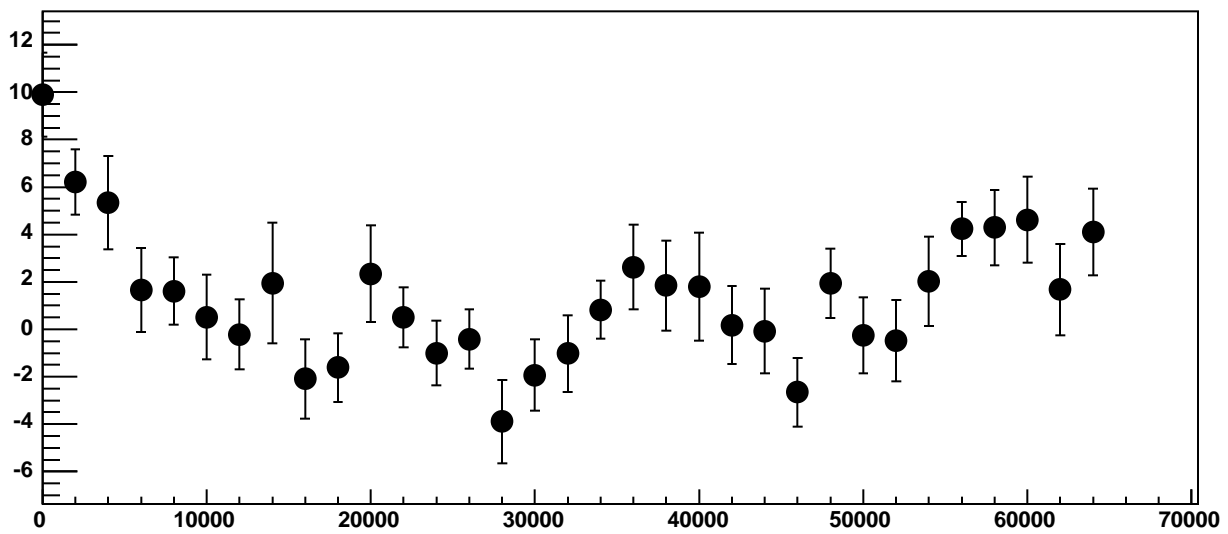
p1

$0.002891 \pm 2.291e-05$

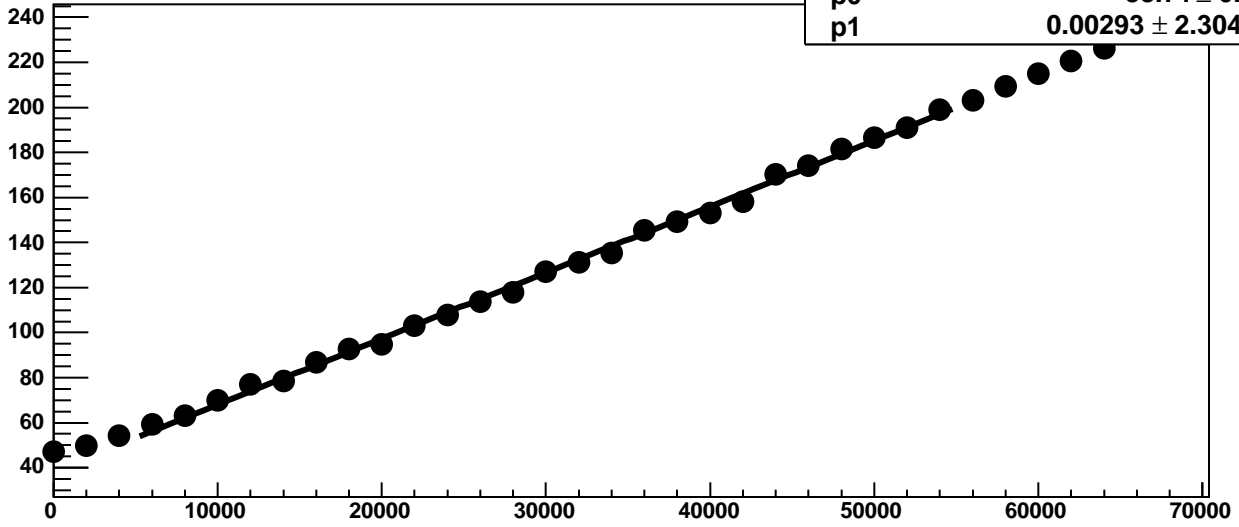
Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.15 / 23

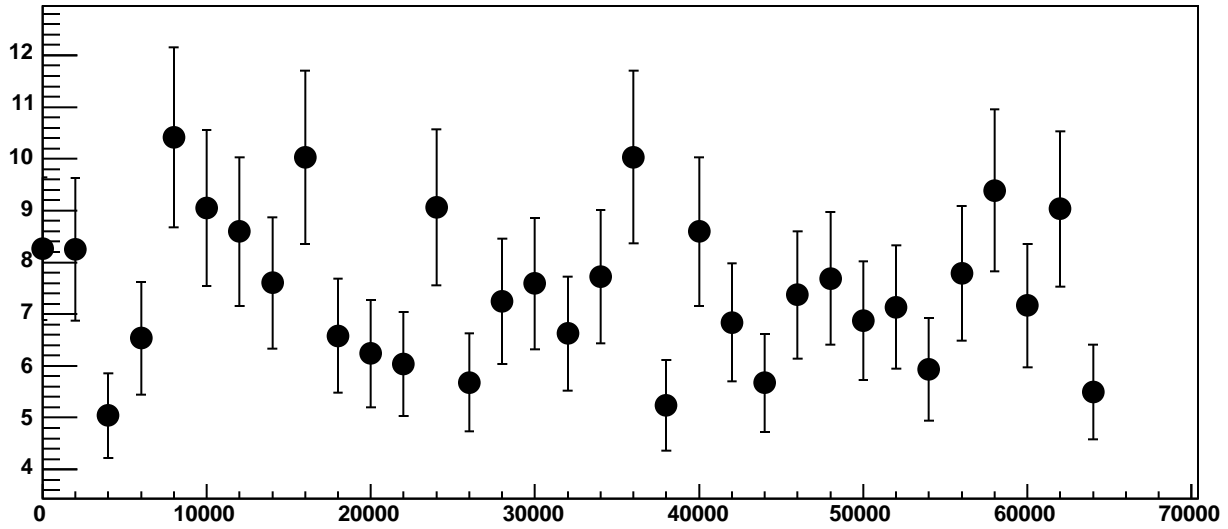
p0

38.71 ± 0.7961

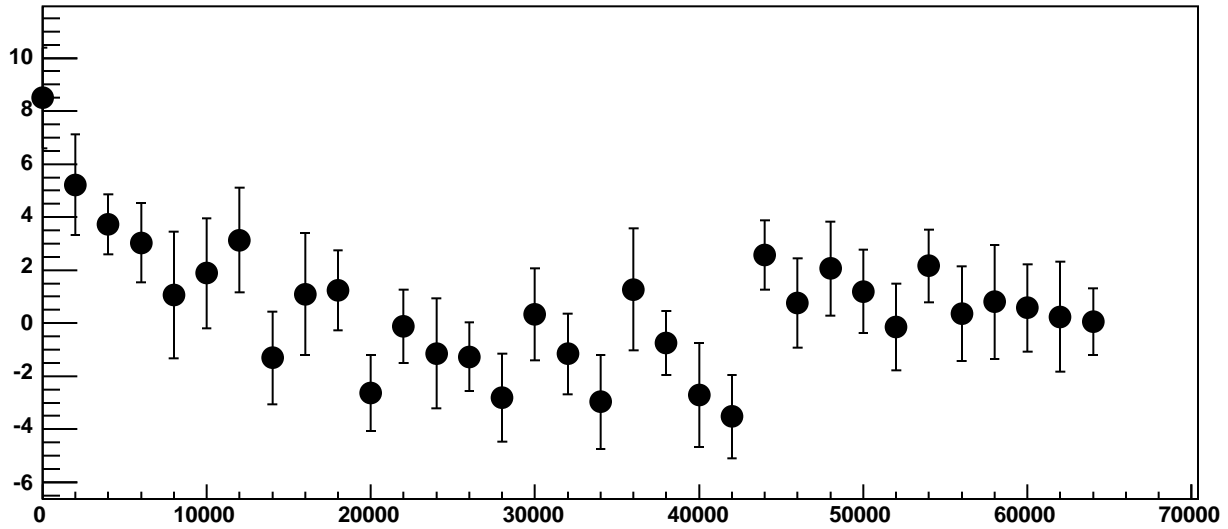
p1

$0.00293 \pm 2.304e-05$

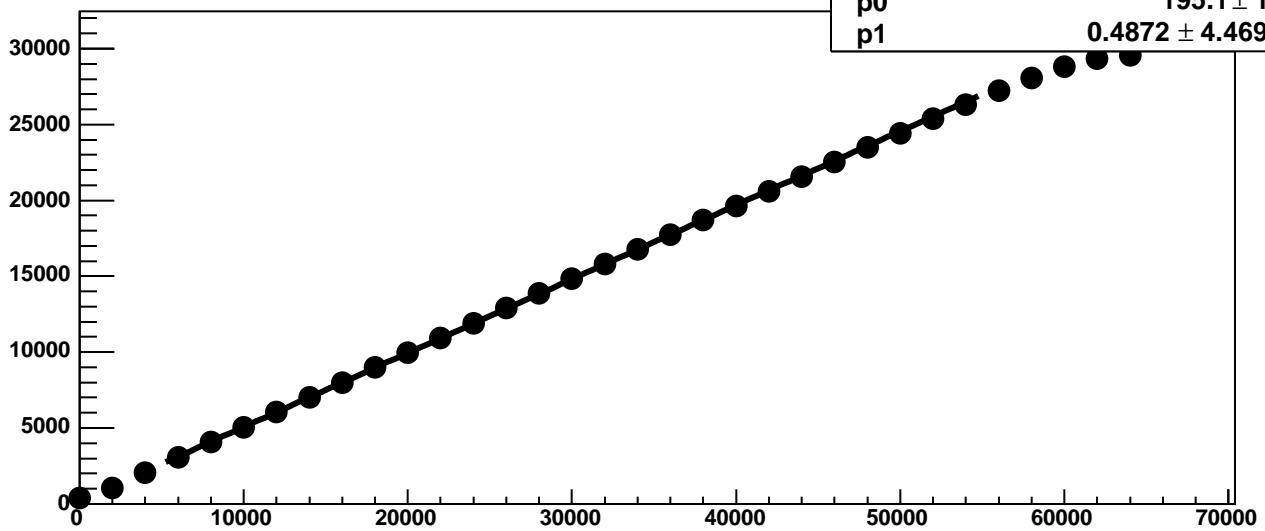
Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



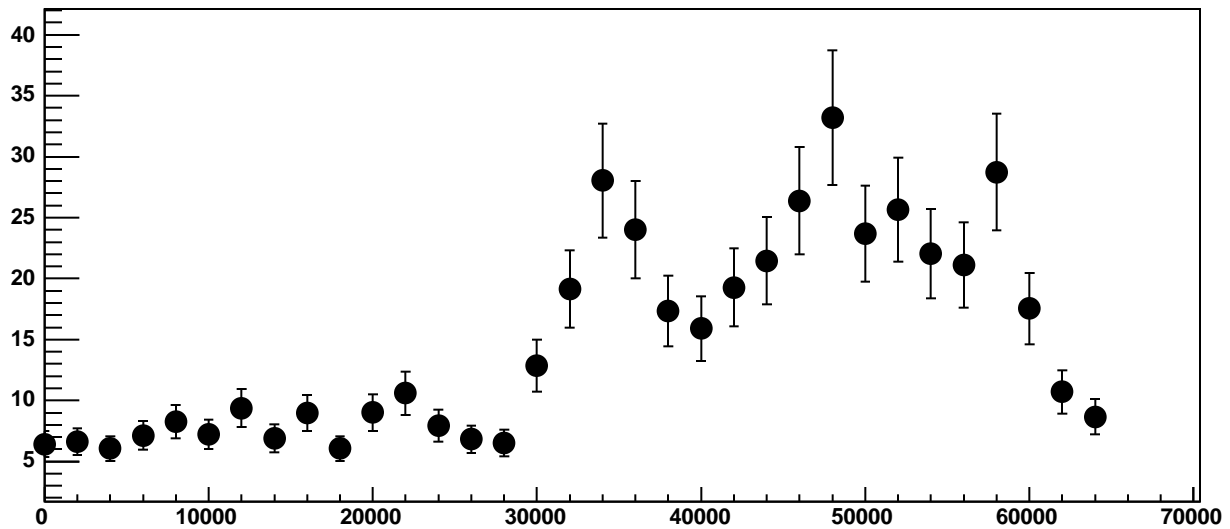
Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



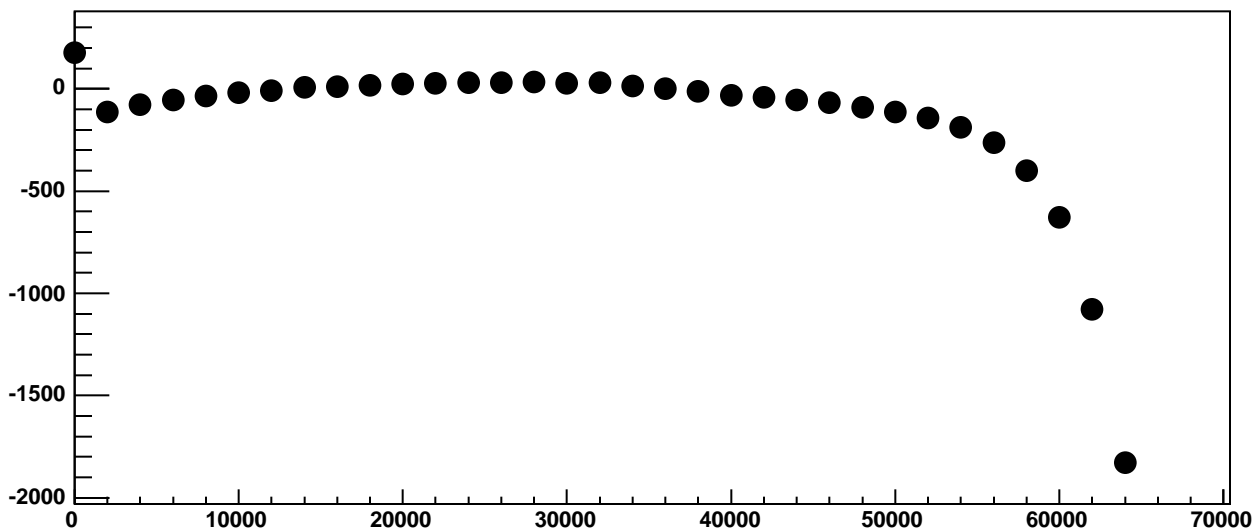
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



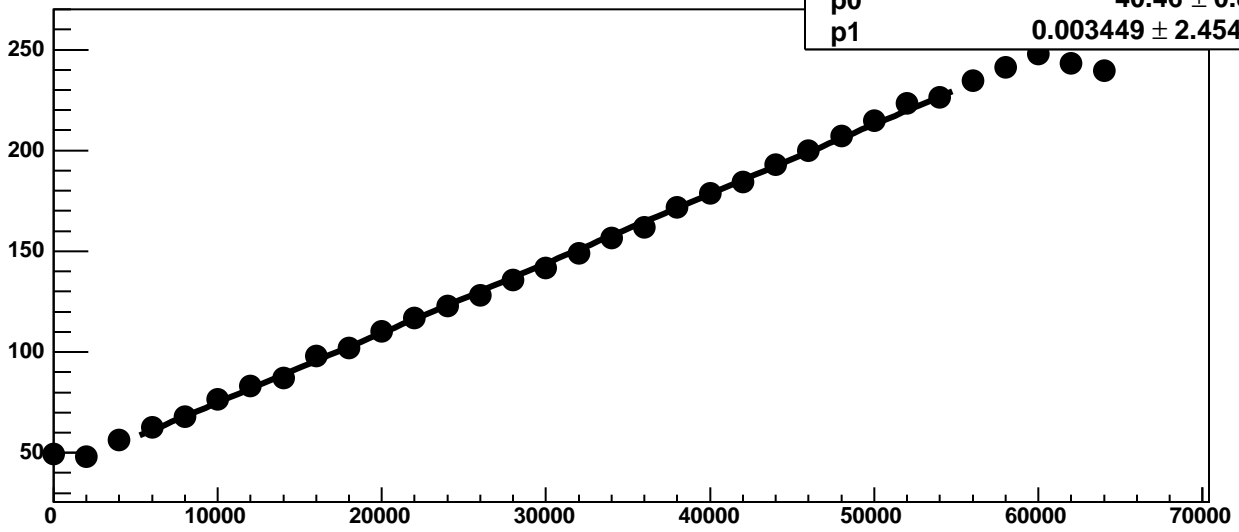
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



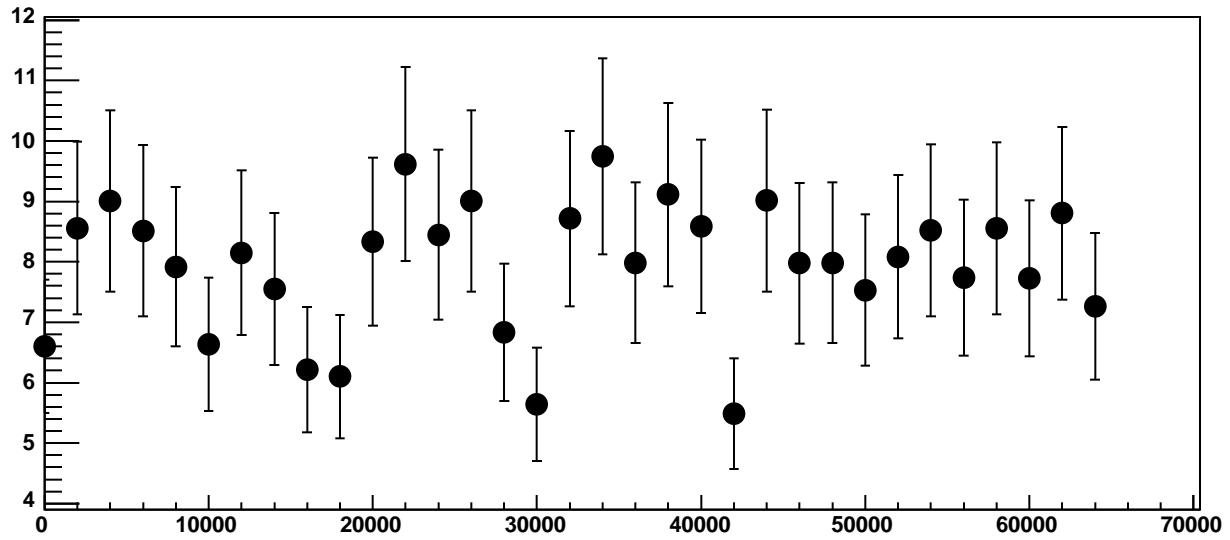
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



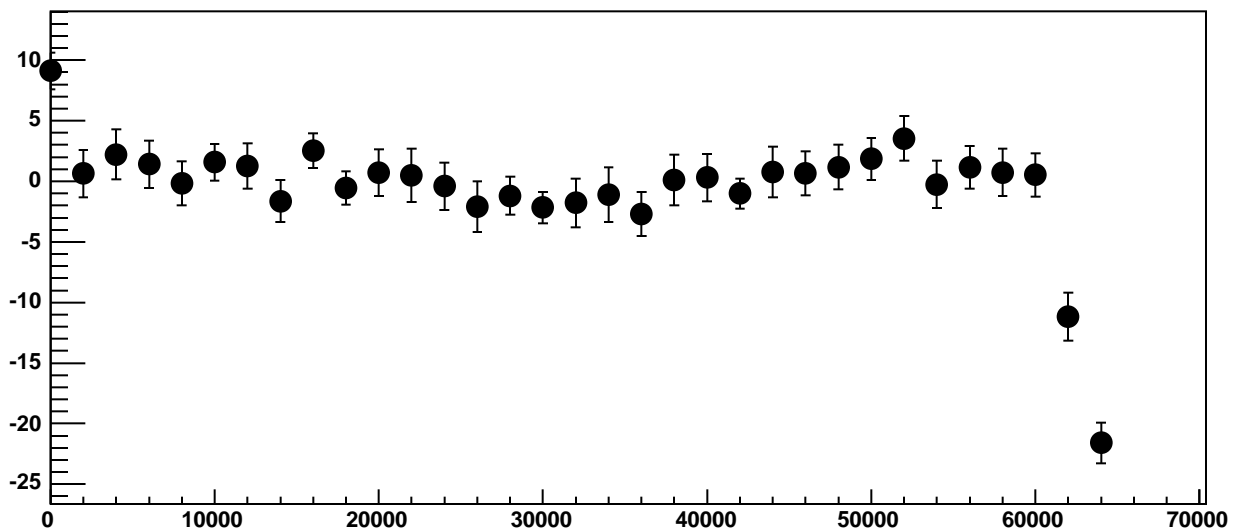
Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



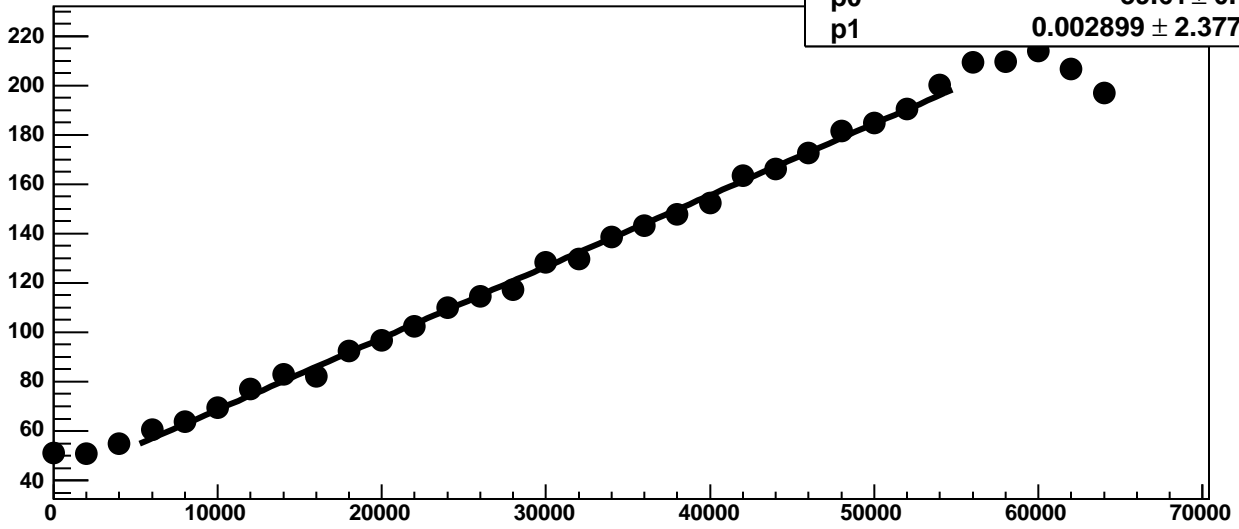
Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



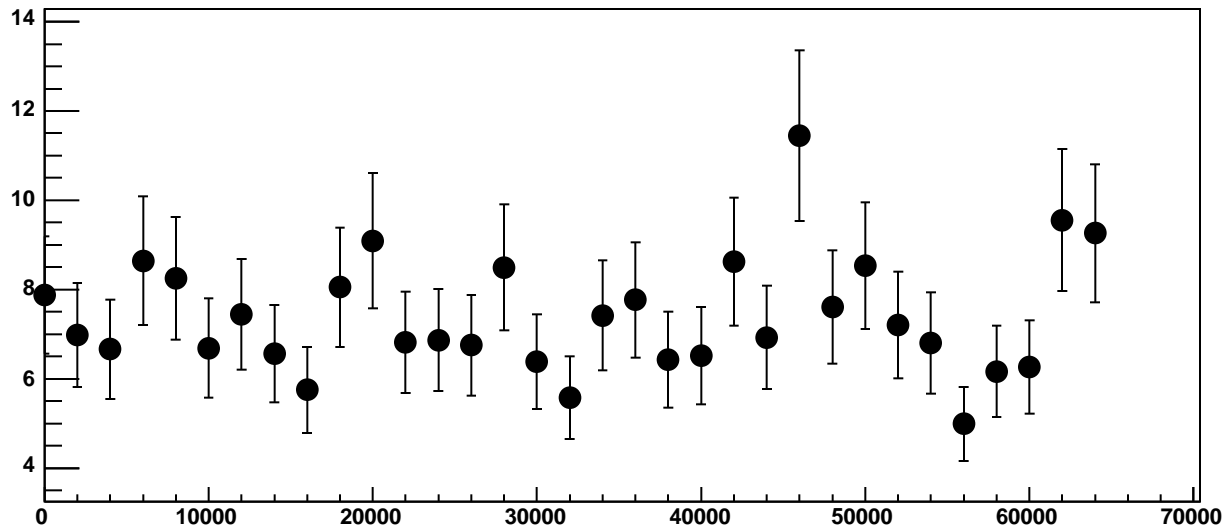
Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



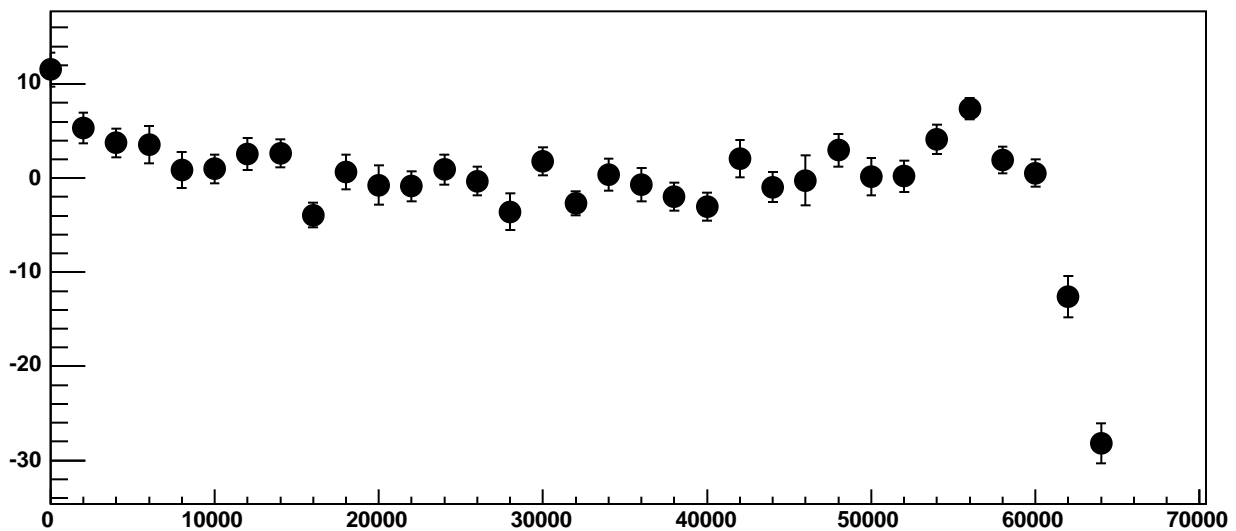
Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



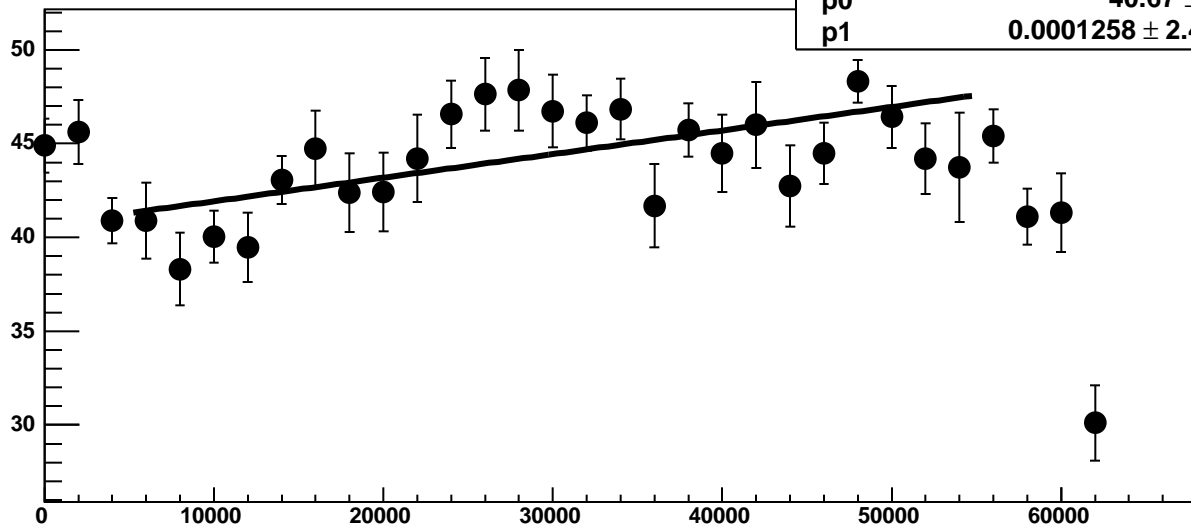
Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



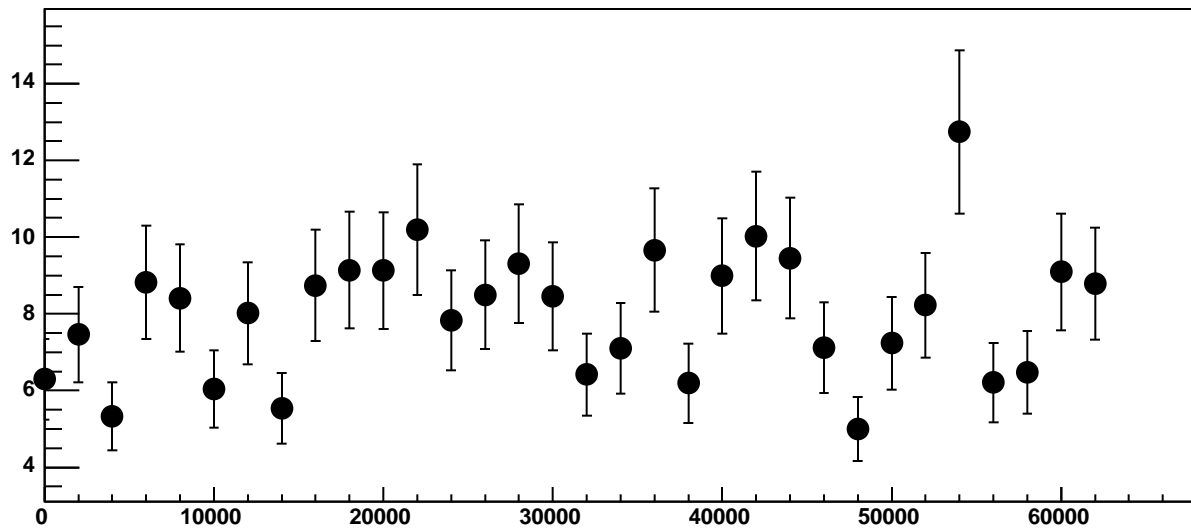
Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



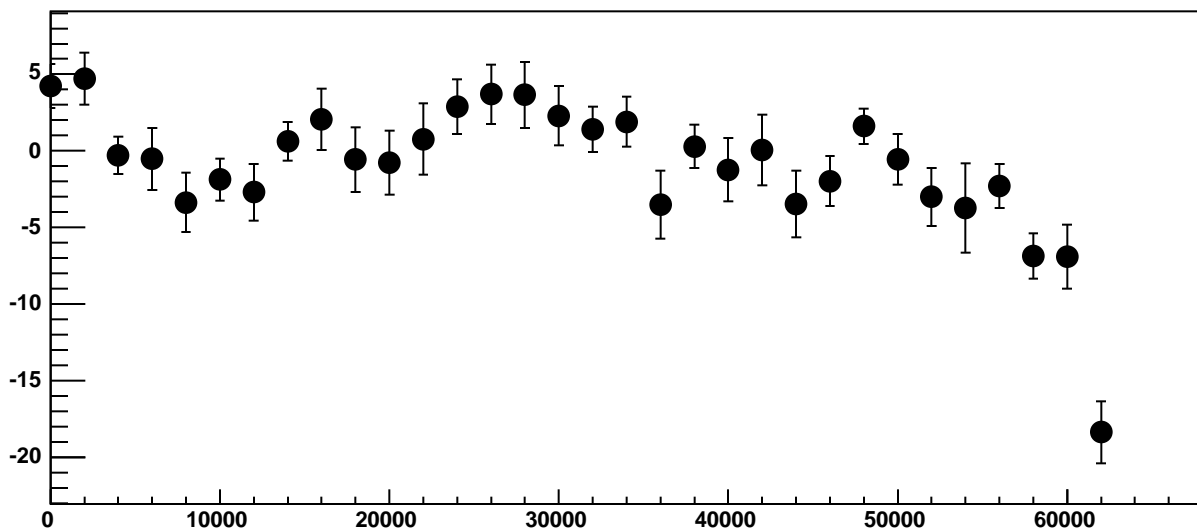
Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



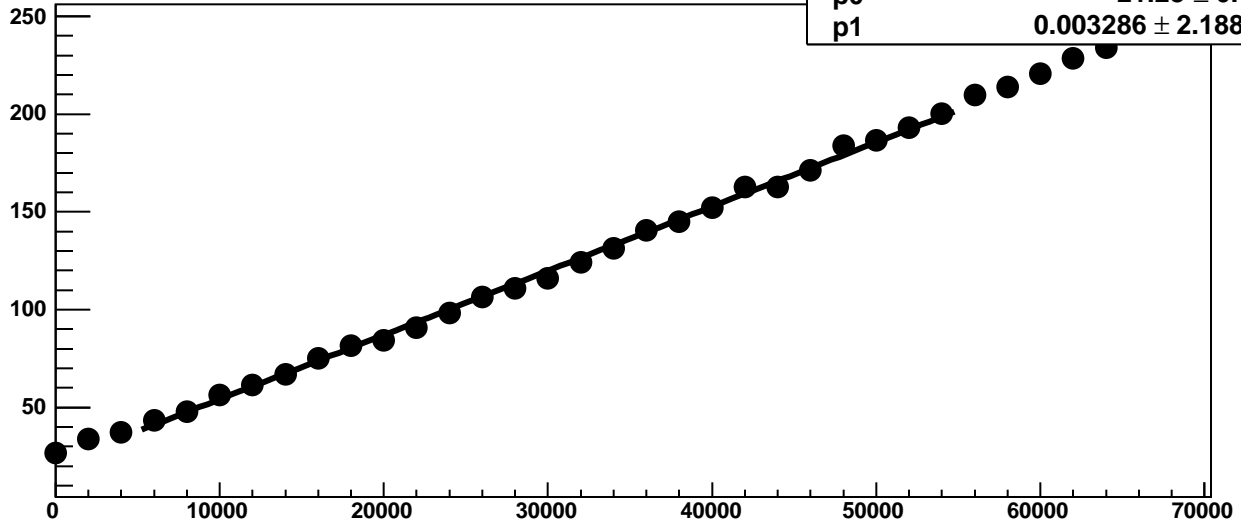
Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.12 / 23

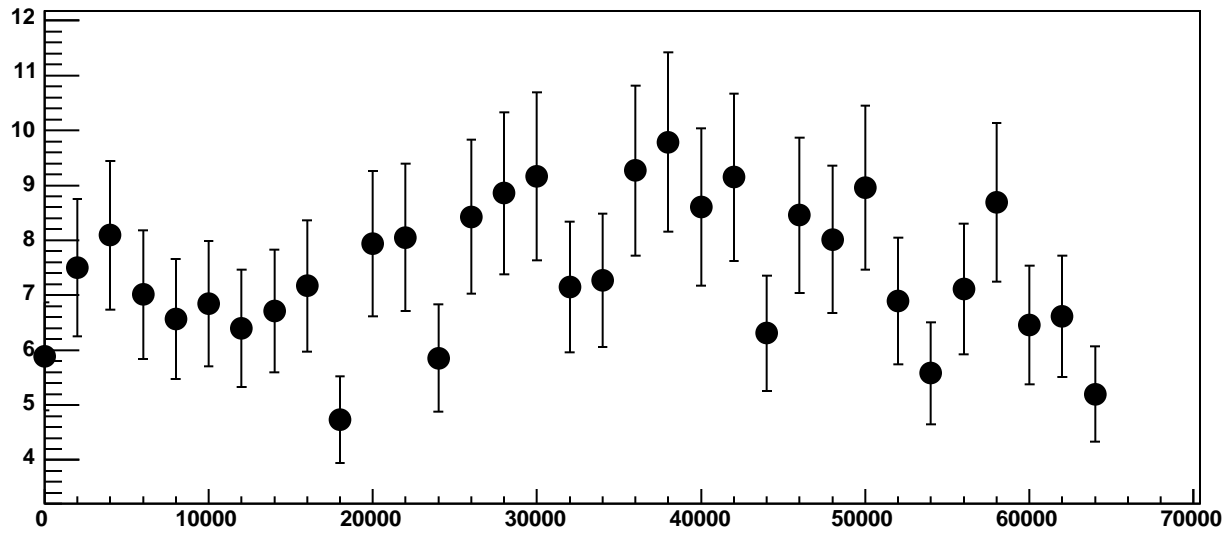
p0

21.28 ± 0.7074

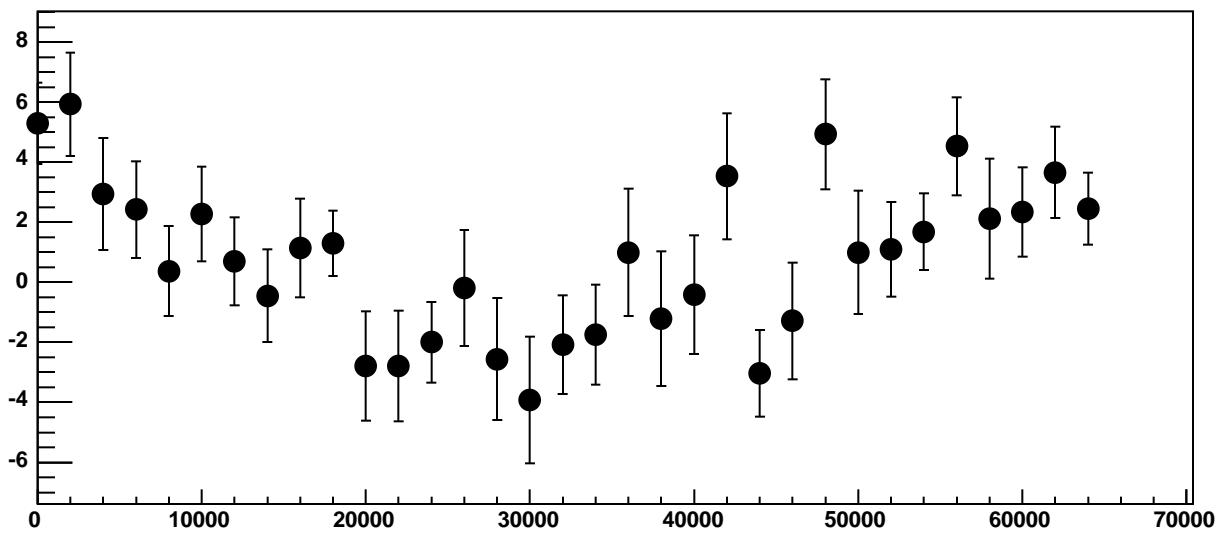
p1

$0.003286 \pm 2.188e-05$

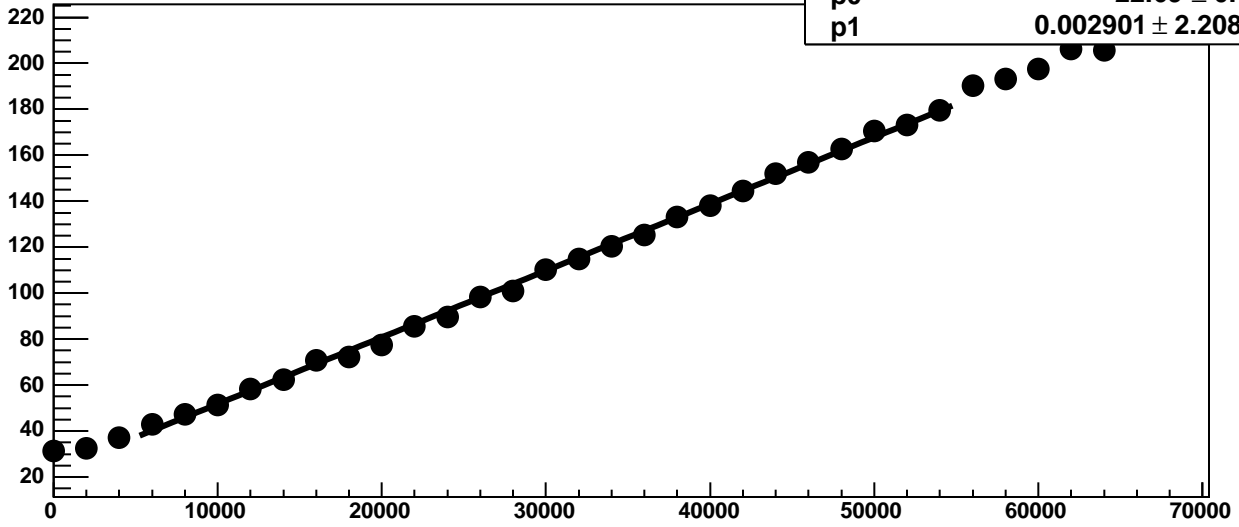
Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

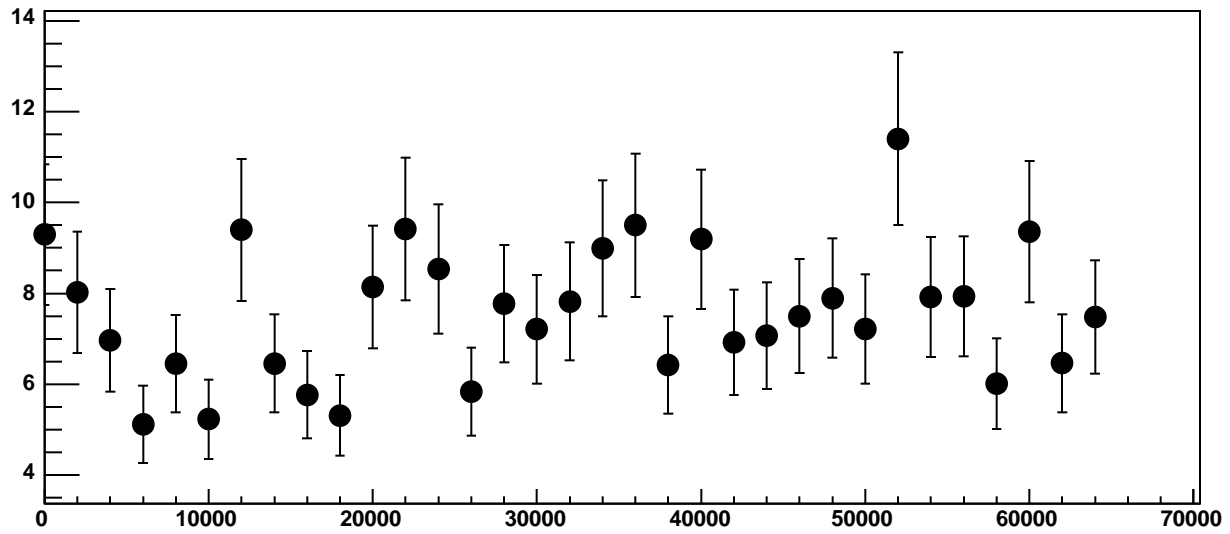


Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

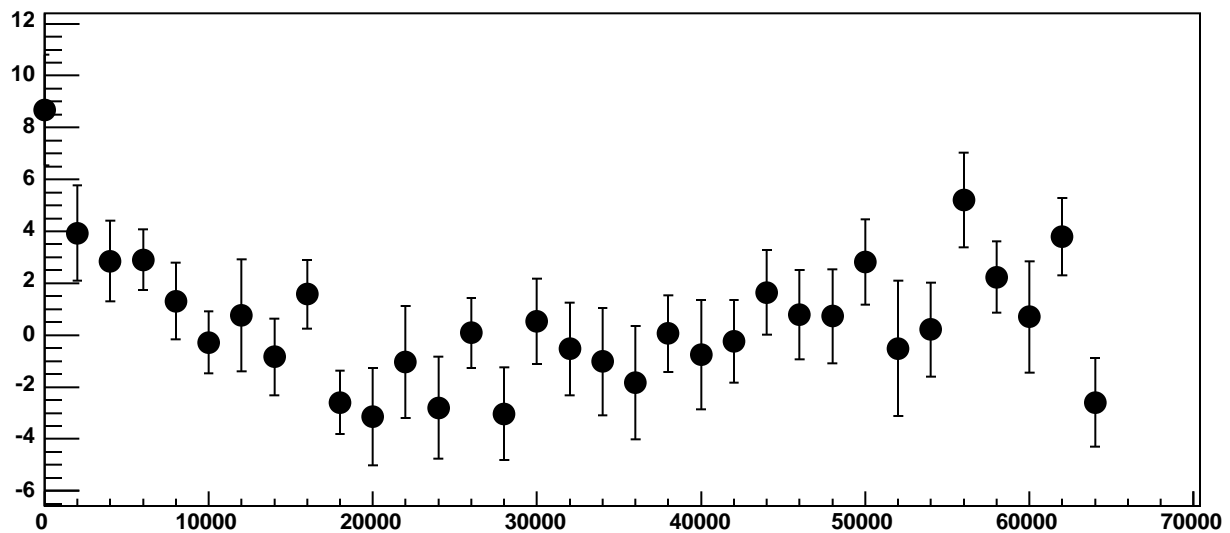


χ^2 / ndf 26.99 / 23
p0 22.69 ± 0.6757
p1 $0.002901 \pm 2.208e-05$

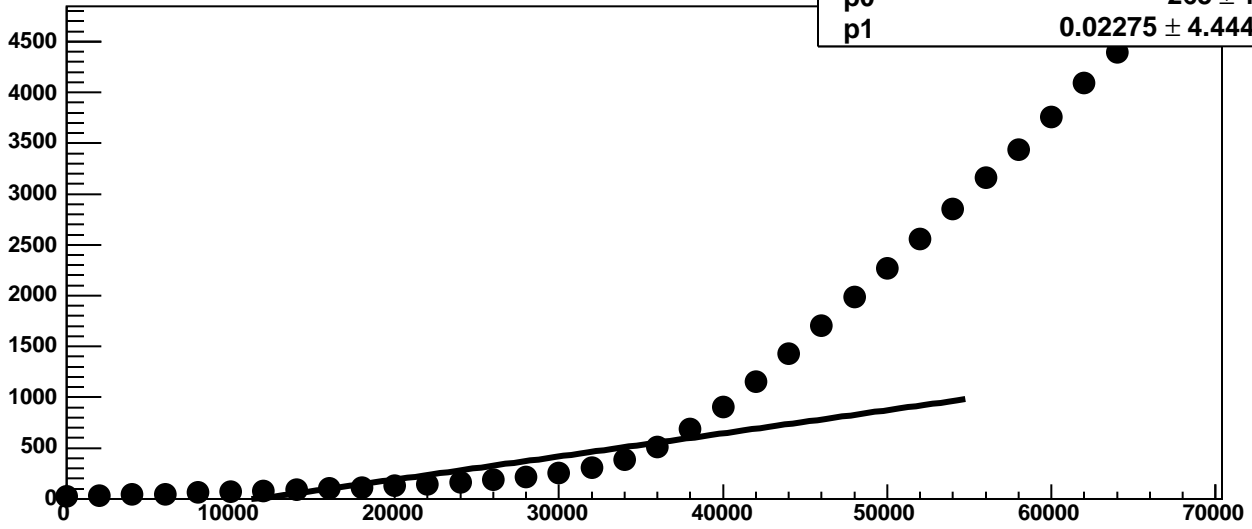
Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



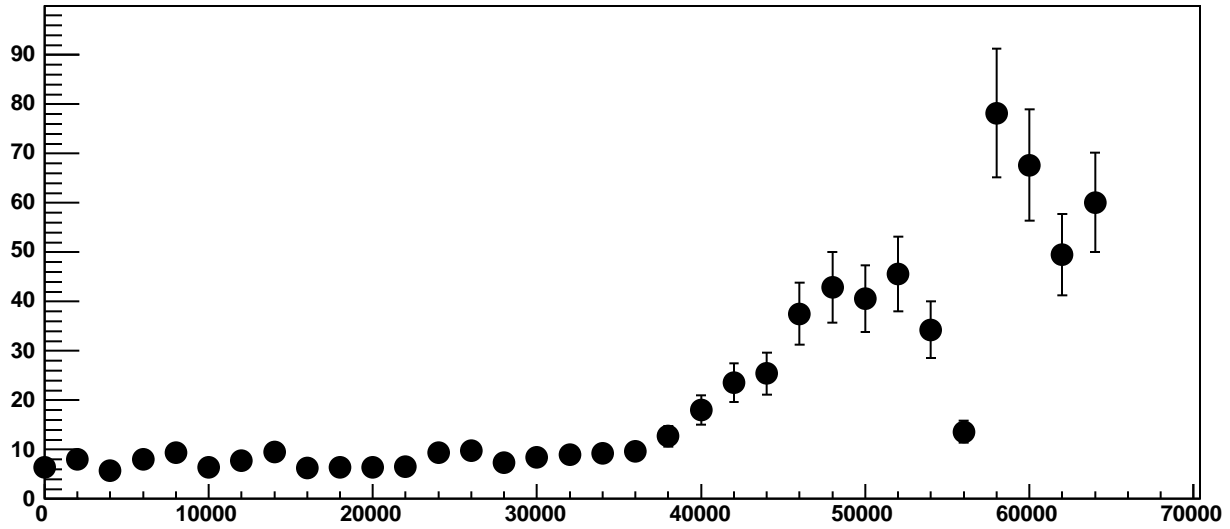
Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



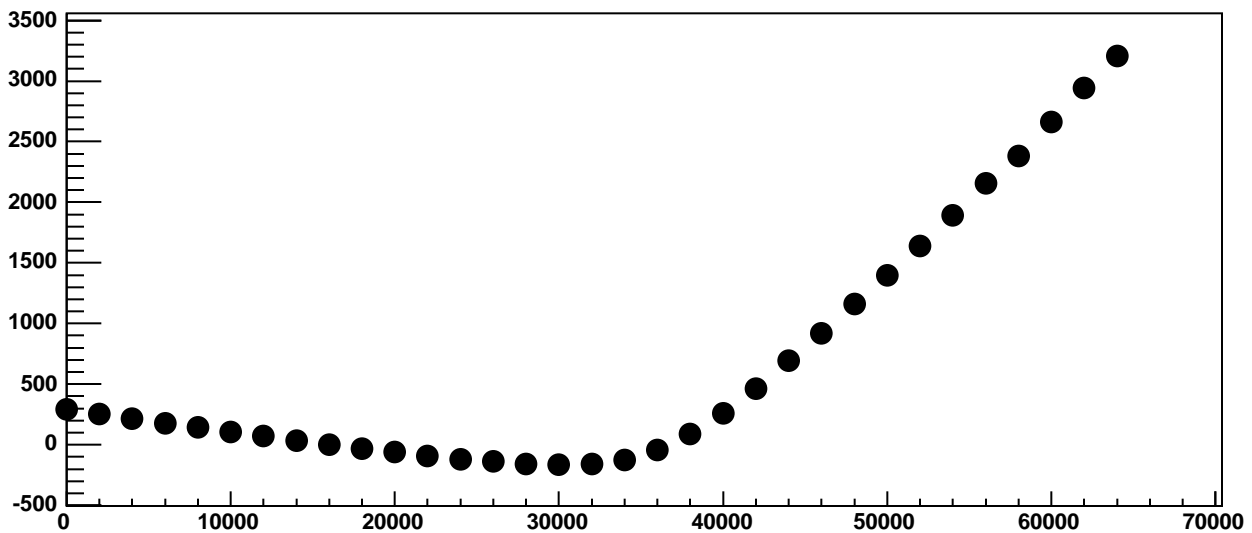
Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



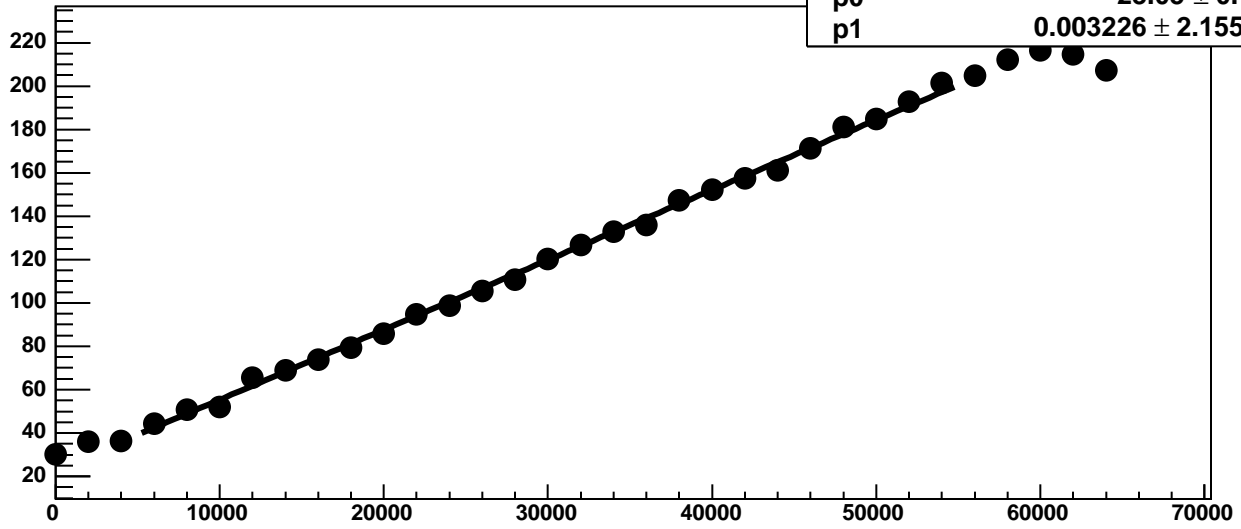
Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

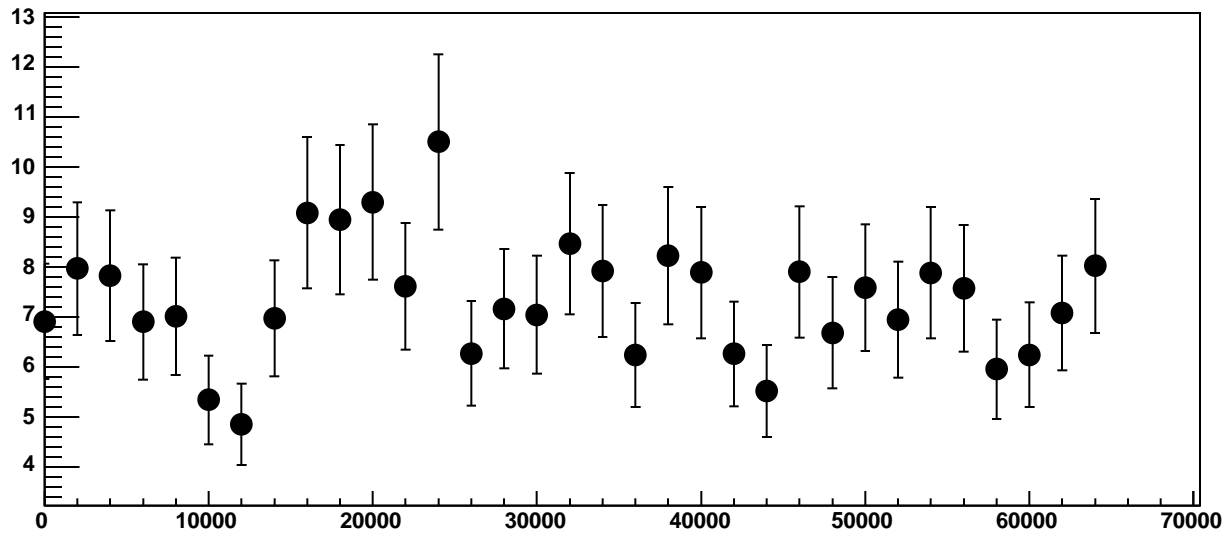


Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

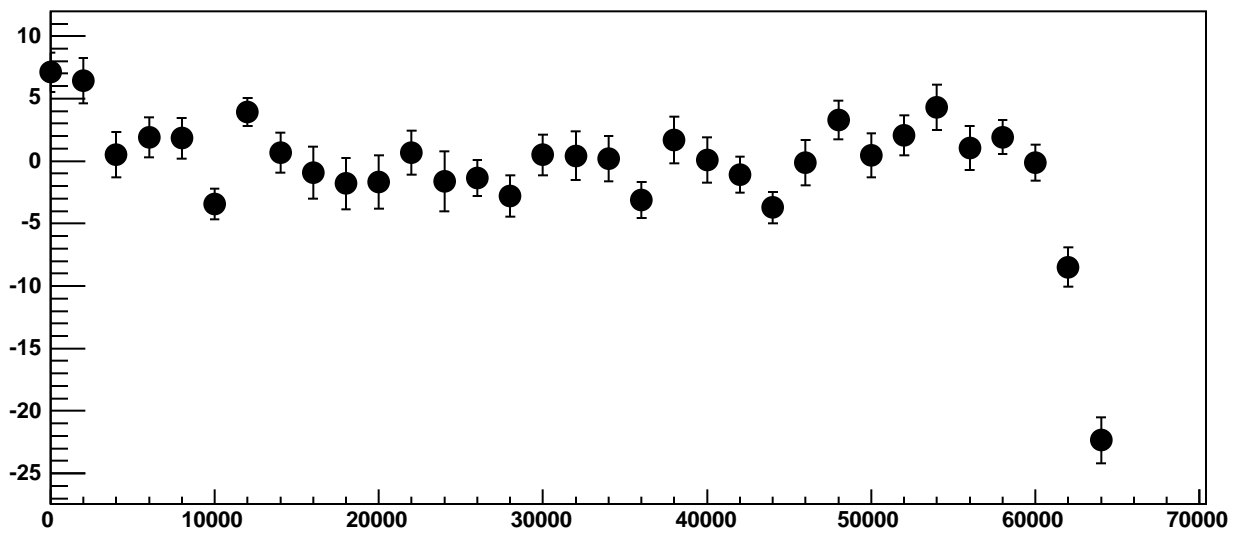


χ^2 / ndf 55.88 / 23
p0 23.05 ± 0.7084
p1 $0.003226 \pm 2.155e-05$

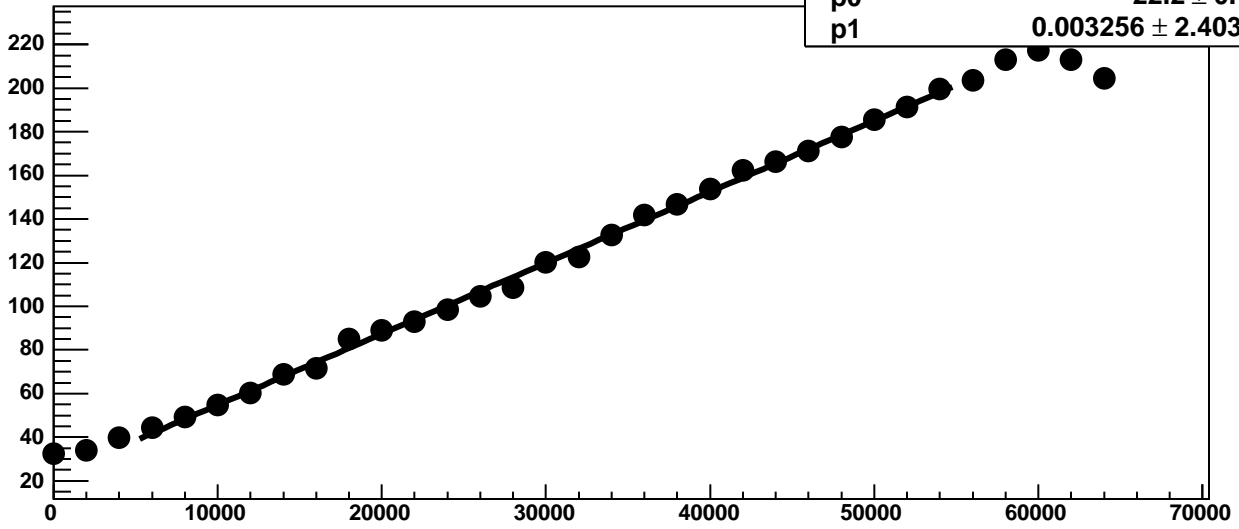
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



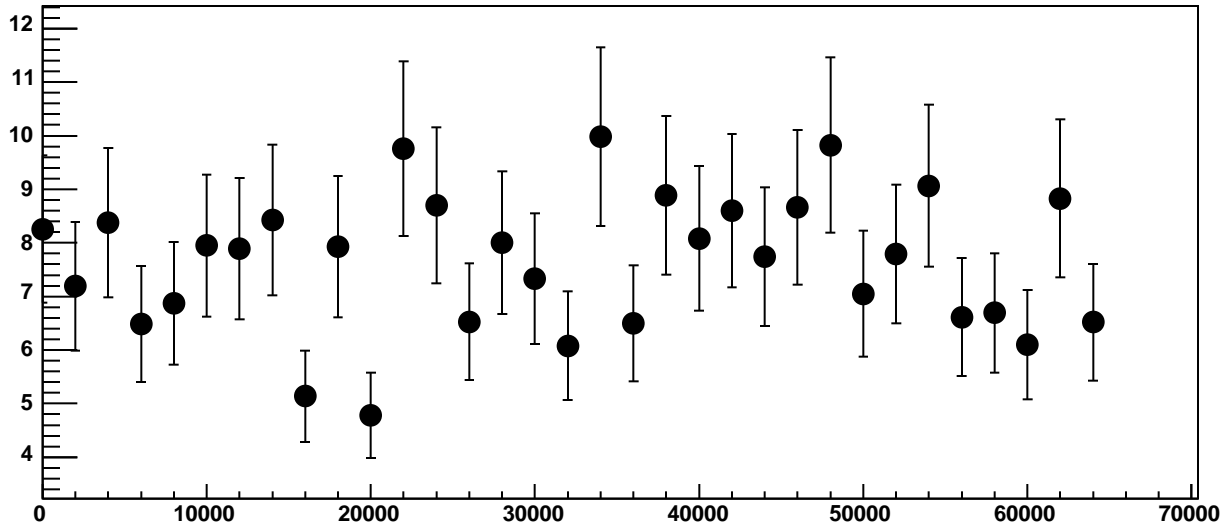
Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



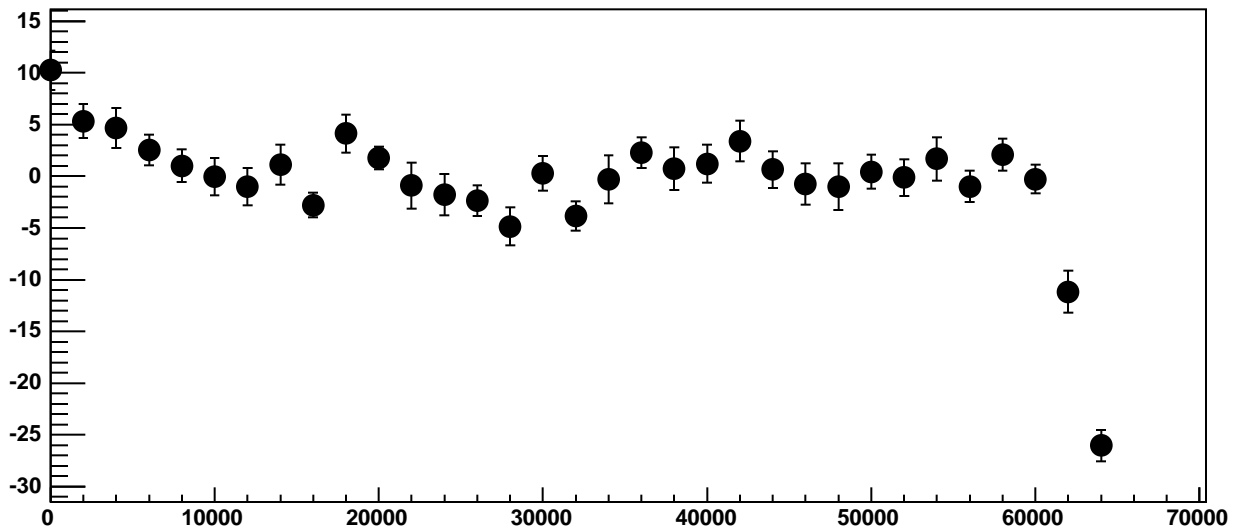
Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



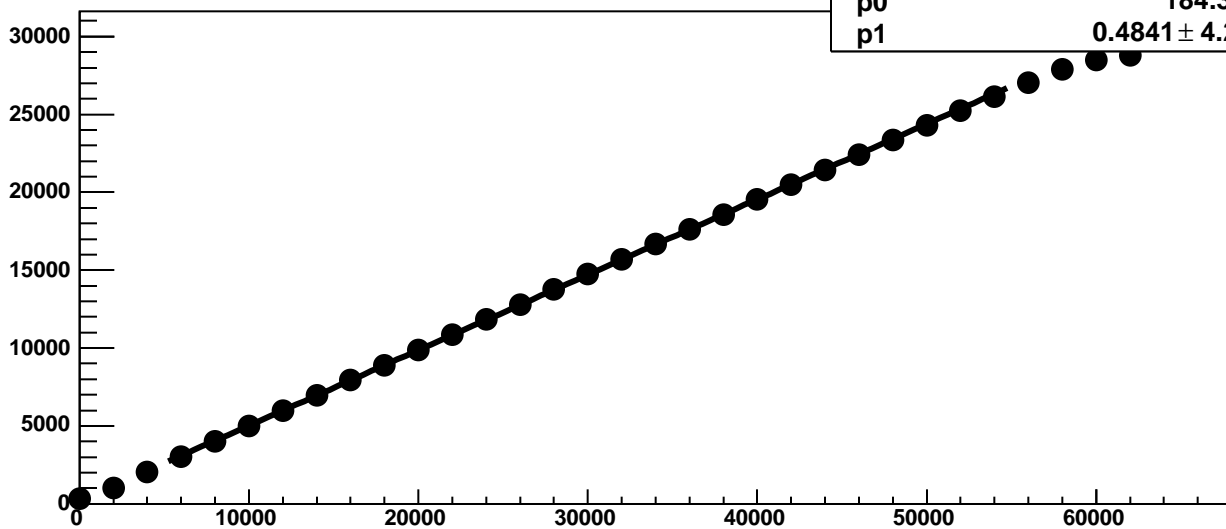
Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



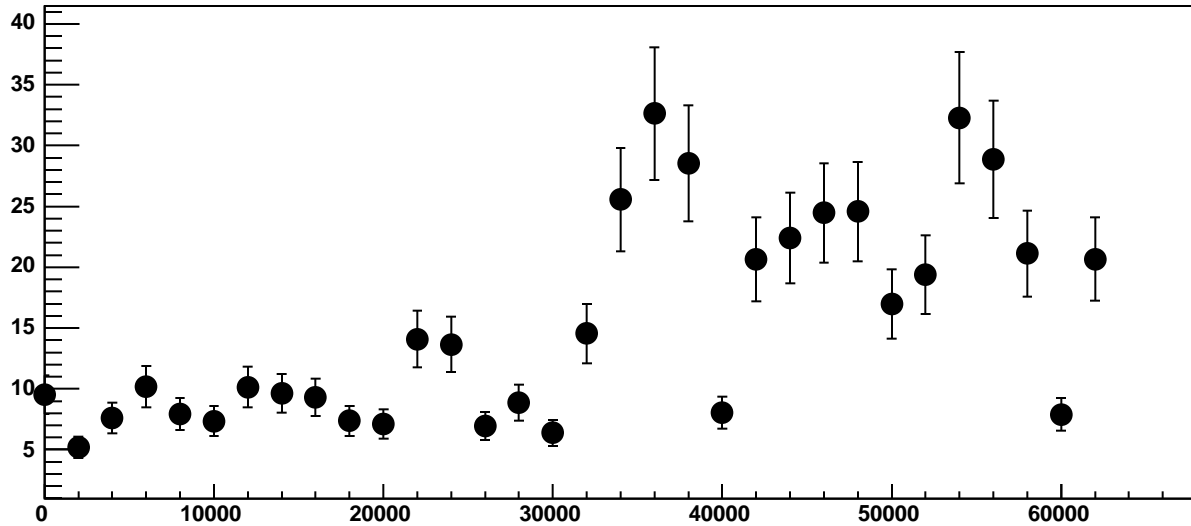
Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



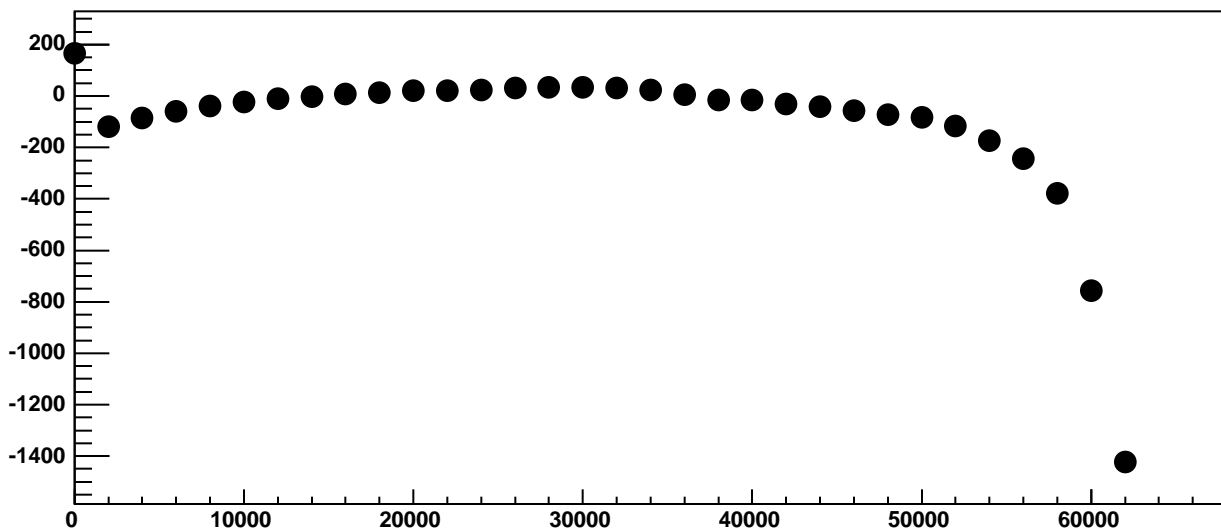
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



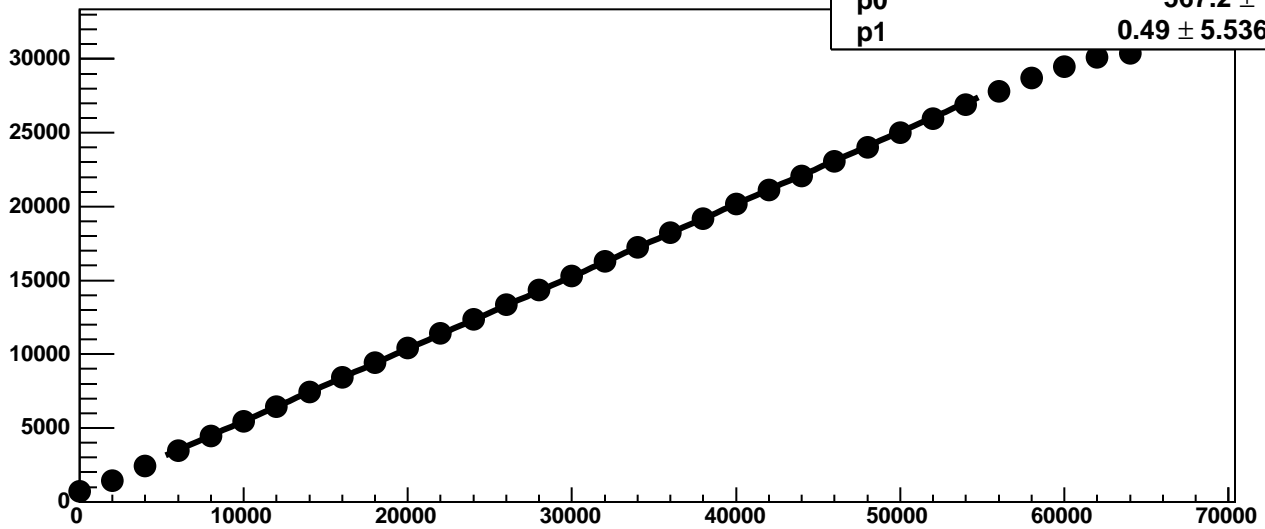
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



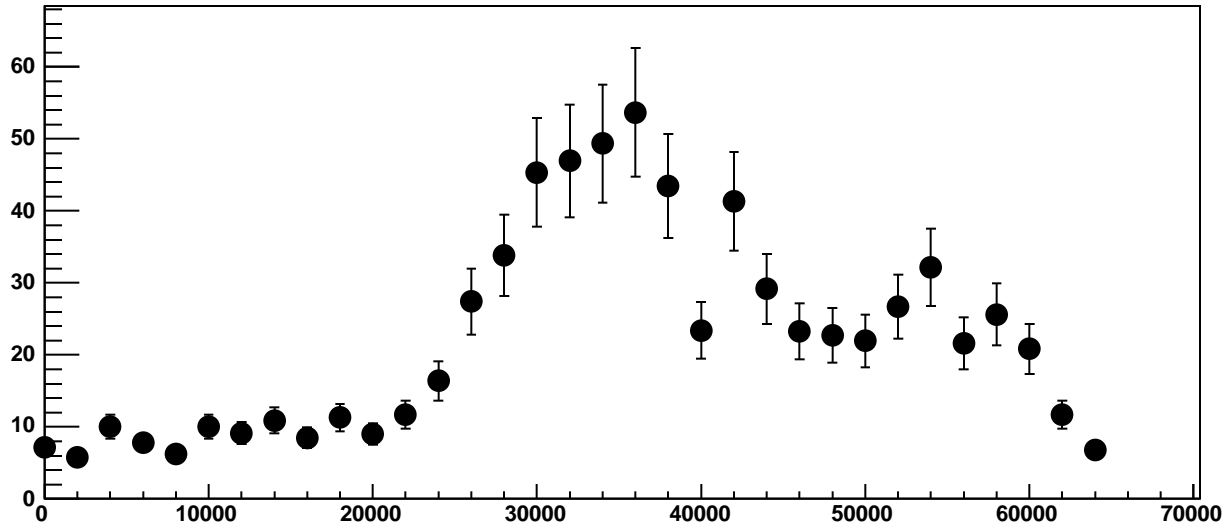
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



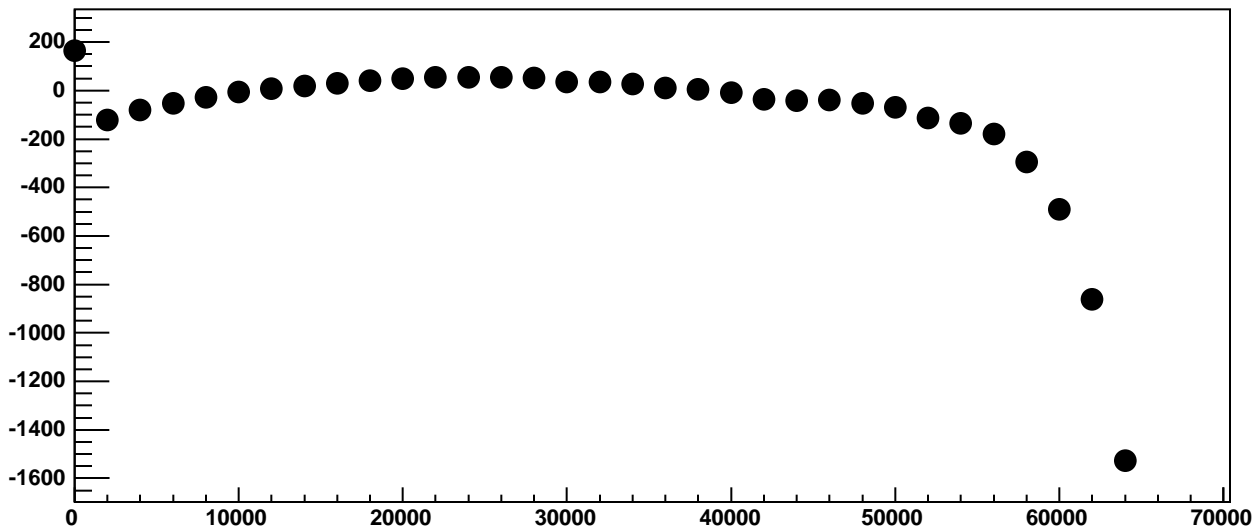
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



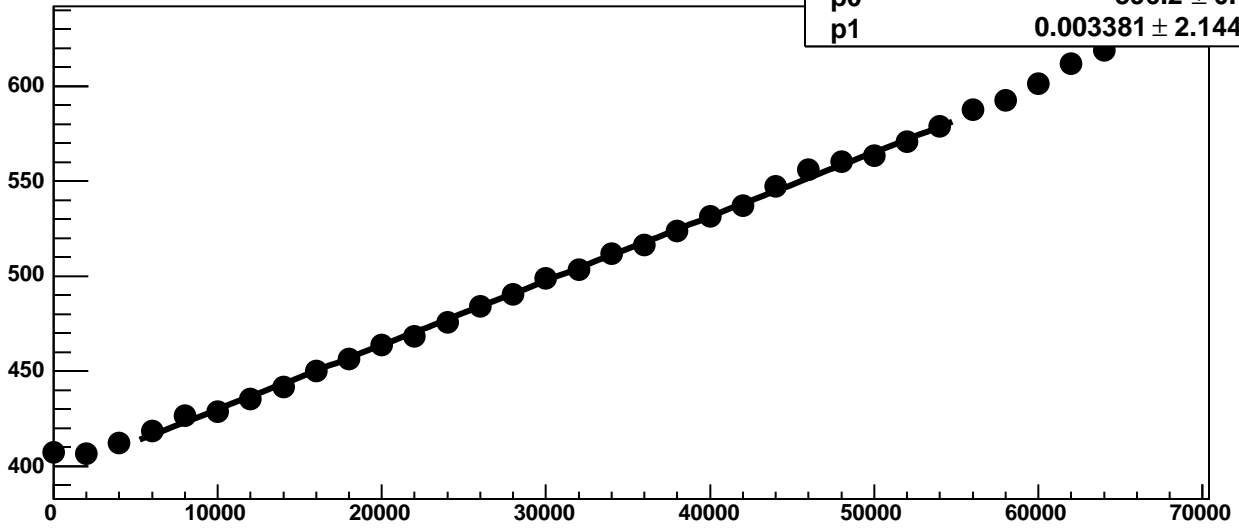
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

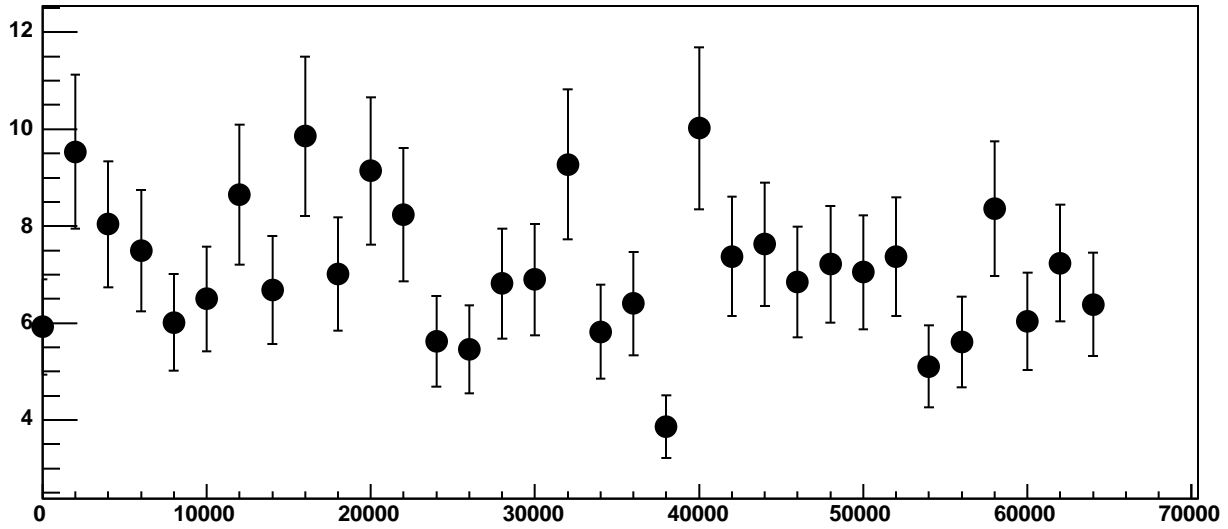


Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

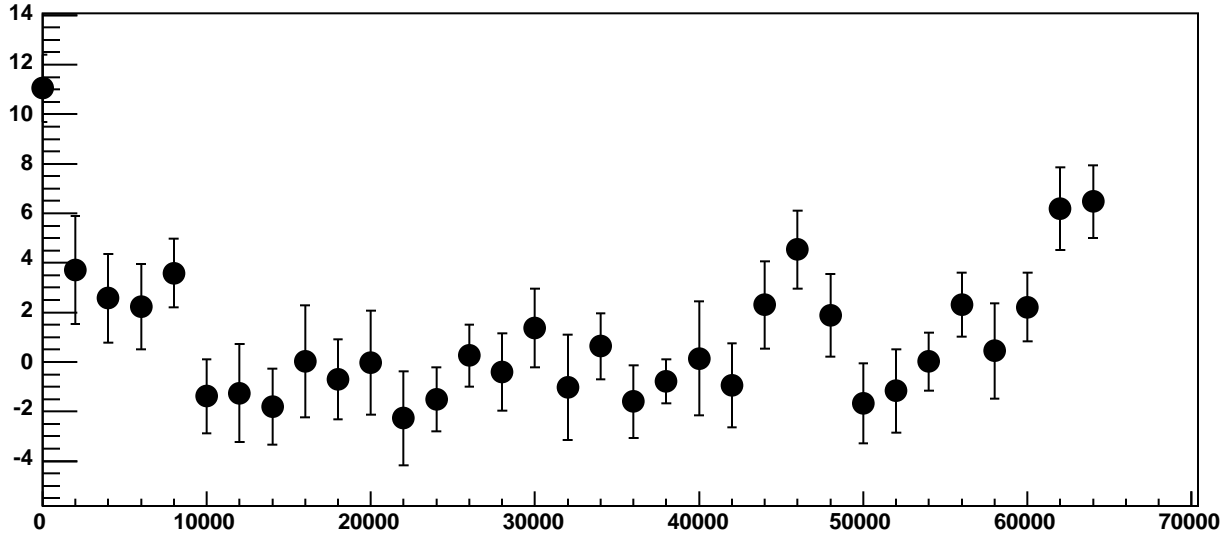


χ^2 / ndf 30.58 / 23
p0 396.2 ± 0.7358
p1 $0.003381 \pm 2.144\text{e-}05$

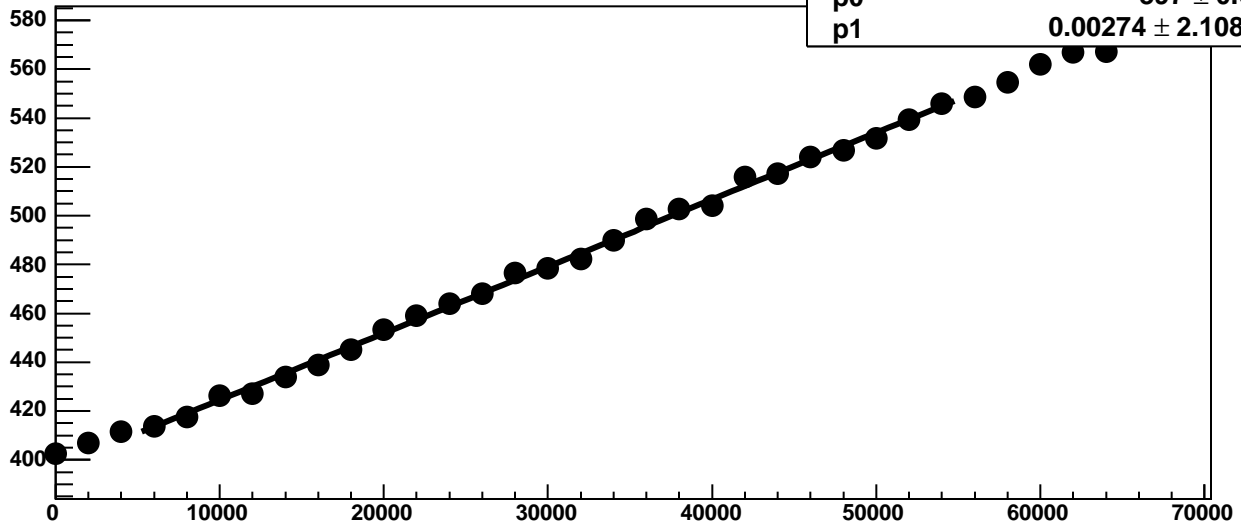
Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

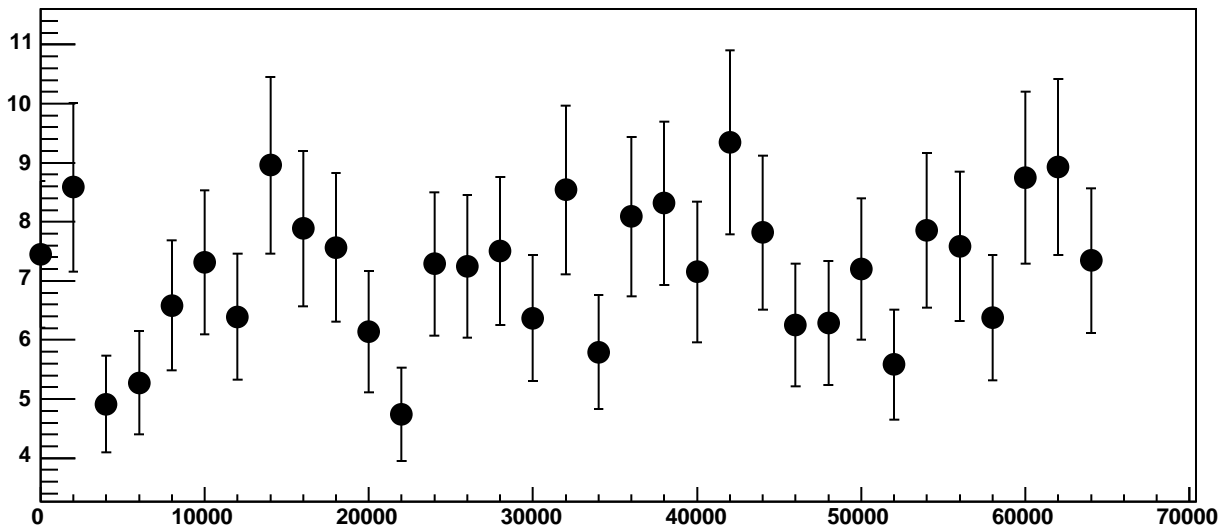


Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

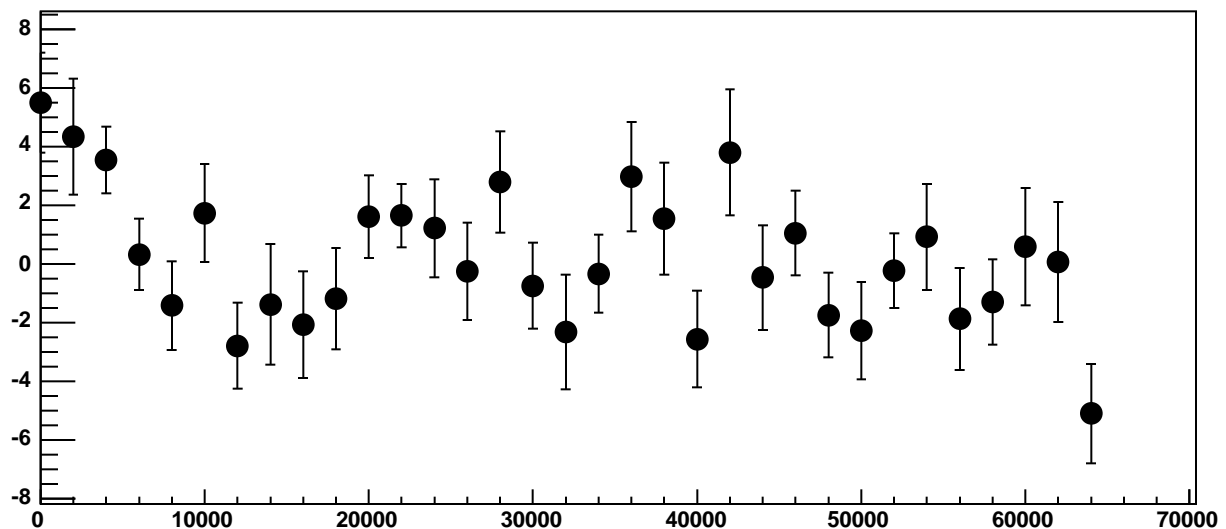


χ^2 / ndf 29.45 / 23
p0 397 ± 0.6913
p1 $0.00274 \pm 2.108e-05$

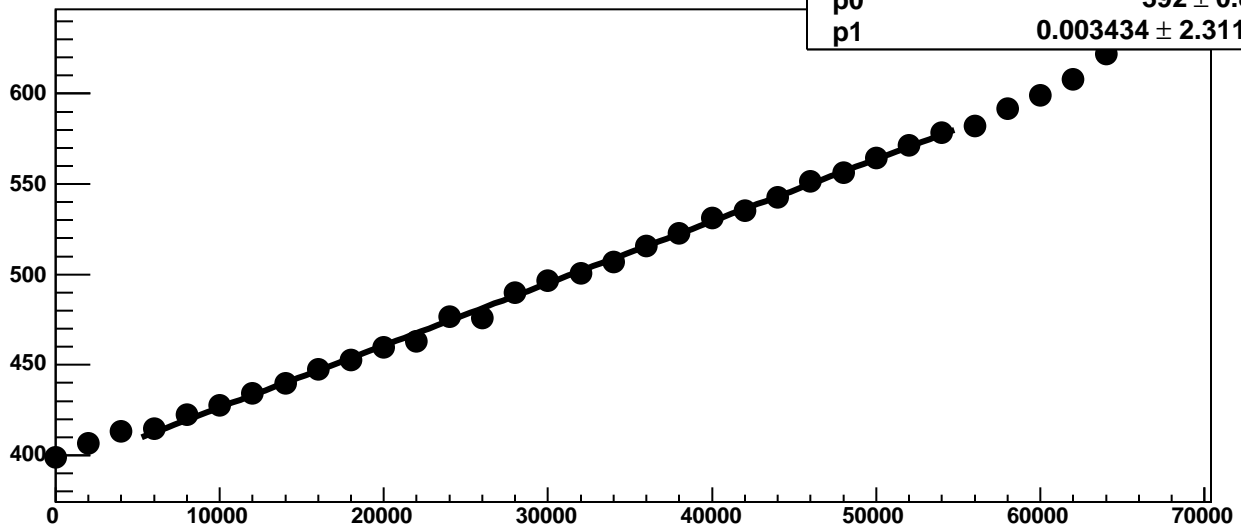
Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

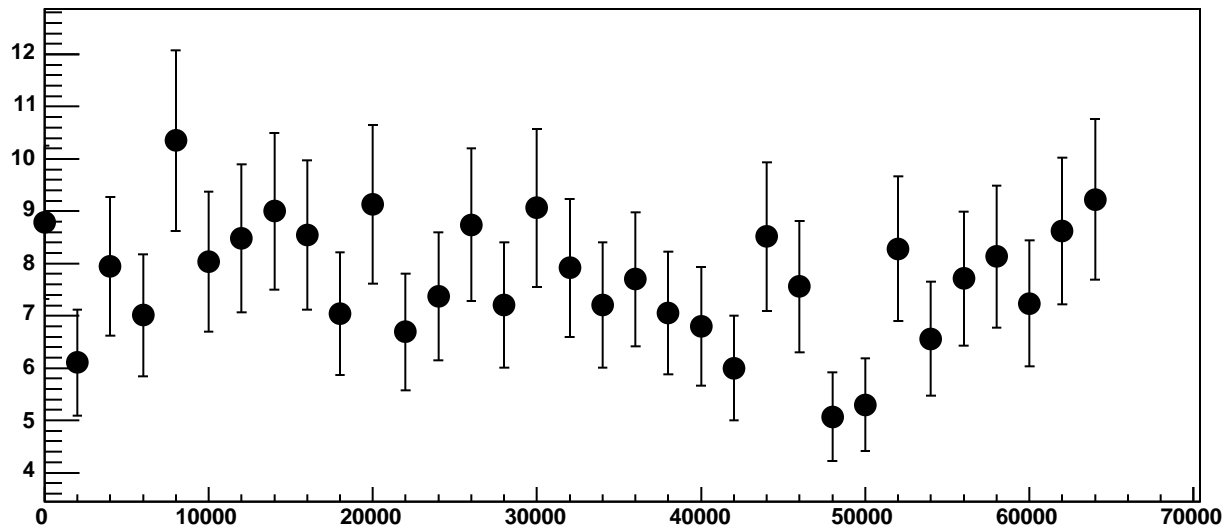


Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

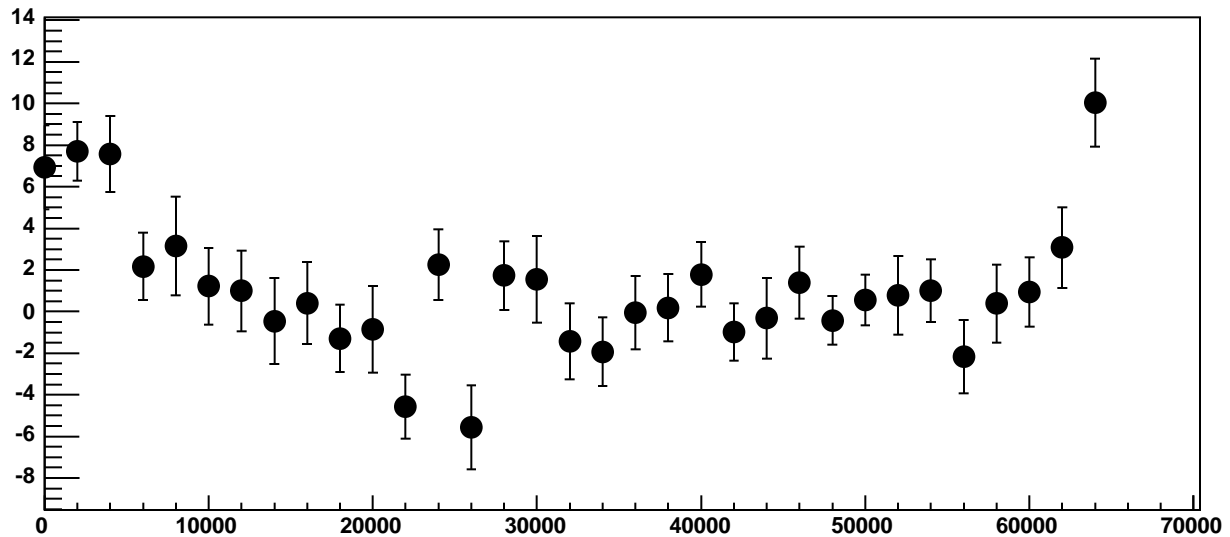


χ^2 / ndf 30.57 / 23
p0 392 ± 0.8285
p1 $0.003434 \pm 2.311e-05$

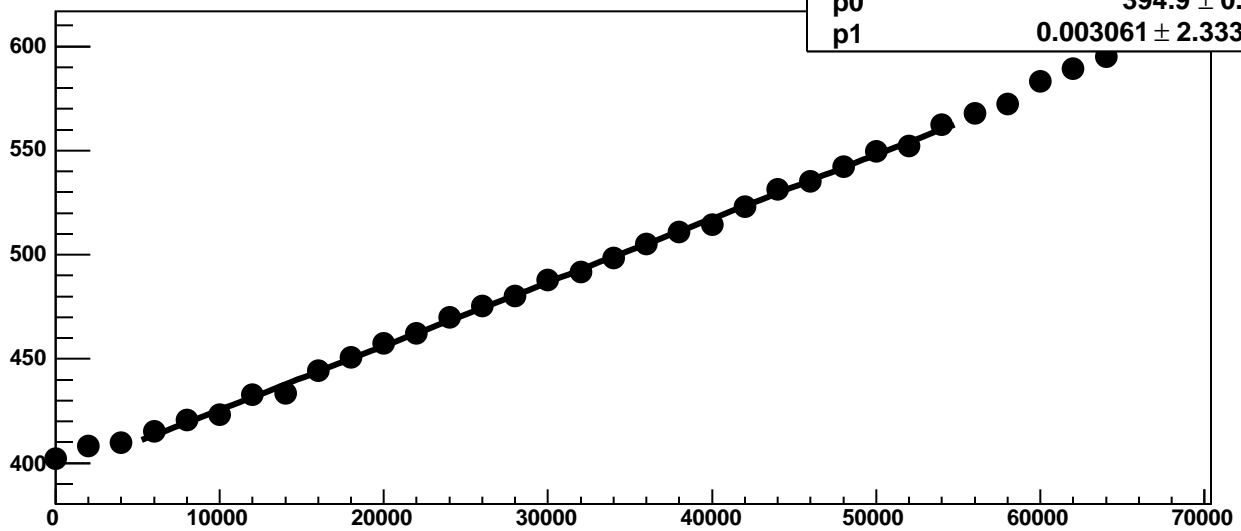
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.23 / 23

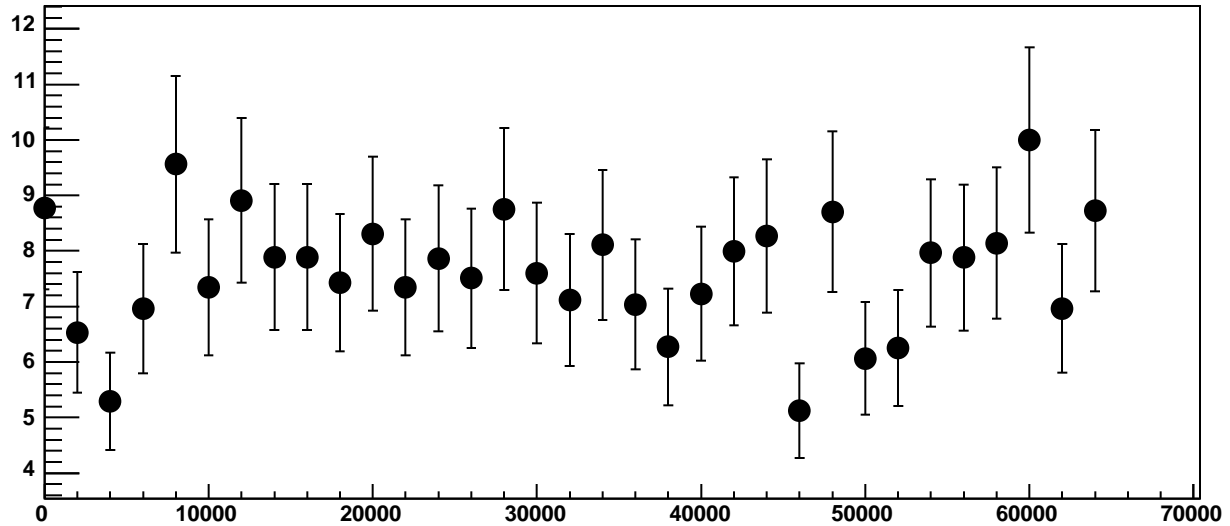
p0

394.9 ± 0.8151

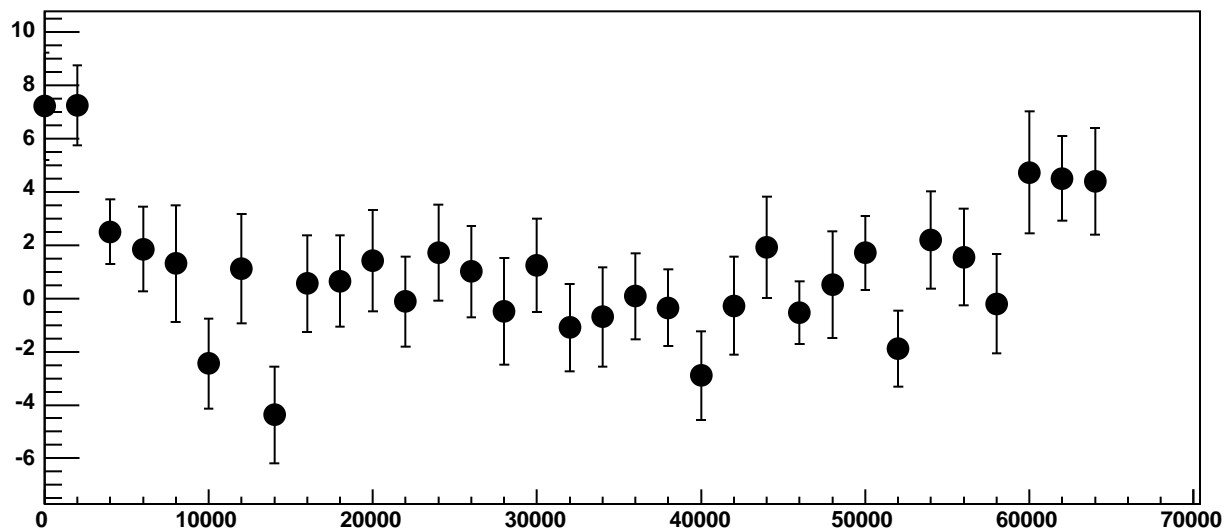
p1

$0.003061 \pm 2.333e-05$

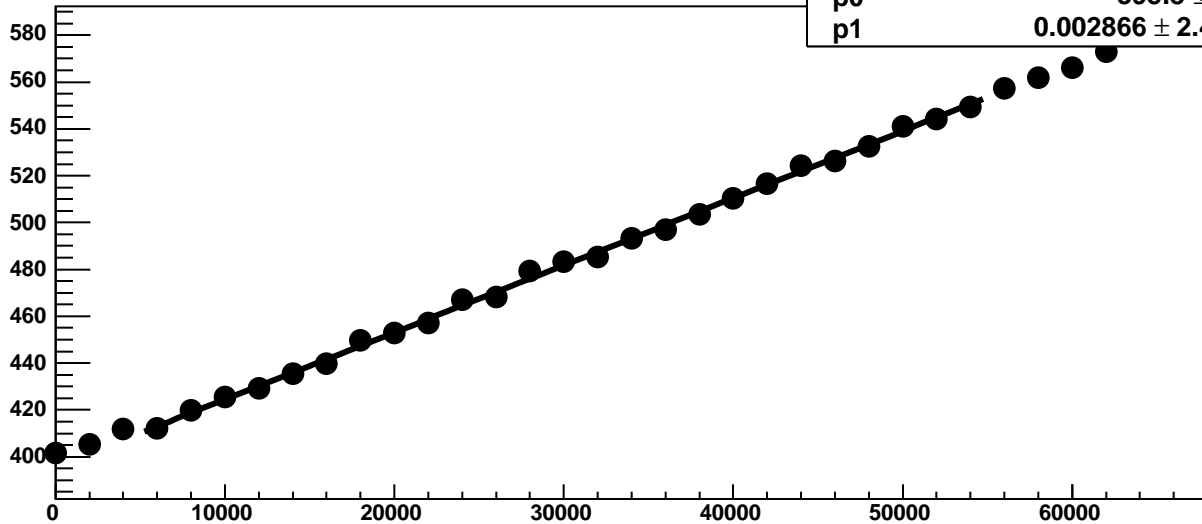
Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

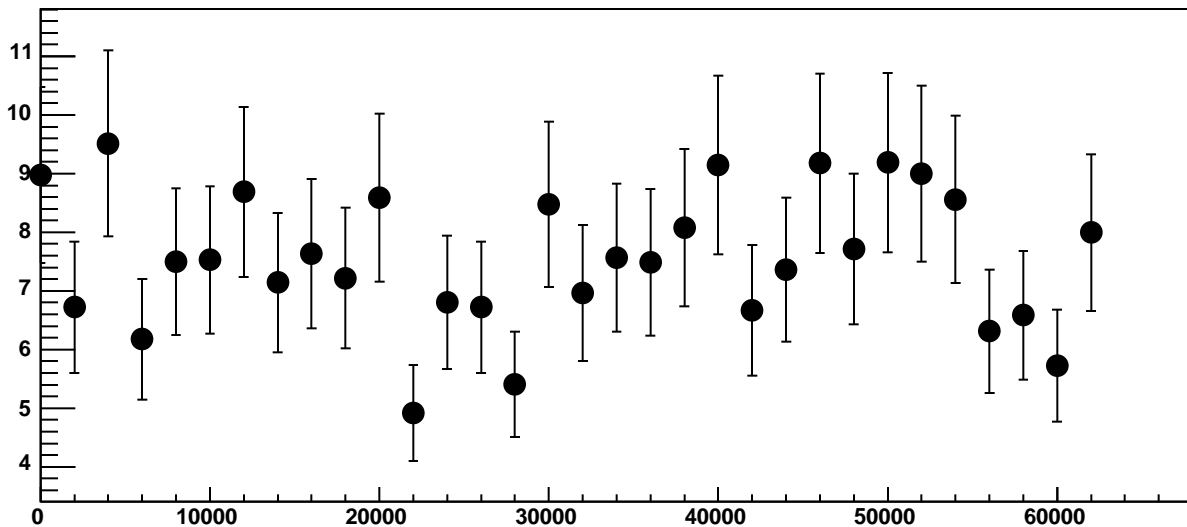


Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

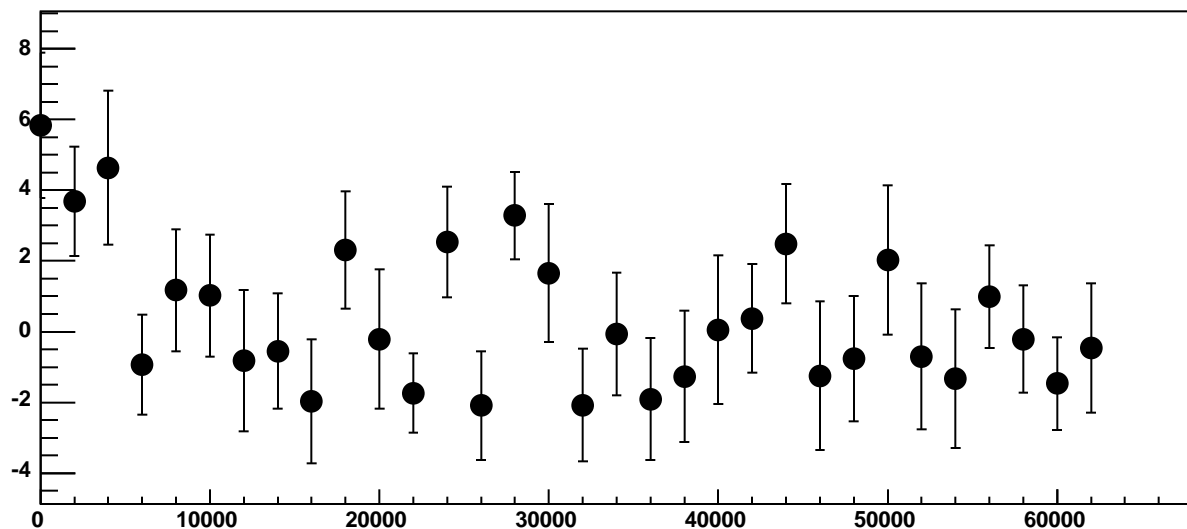


χ^2 / ndf 26.95 / 23
p0 395.8 ± 0.7707
p1 $0.002866 \pm 2.459e-05$

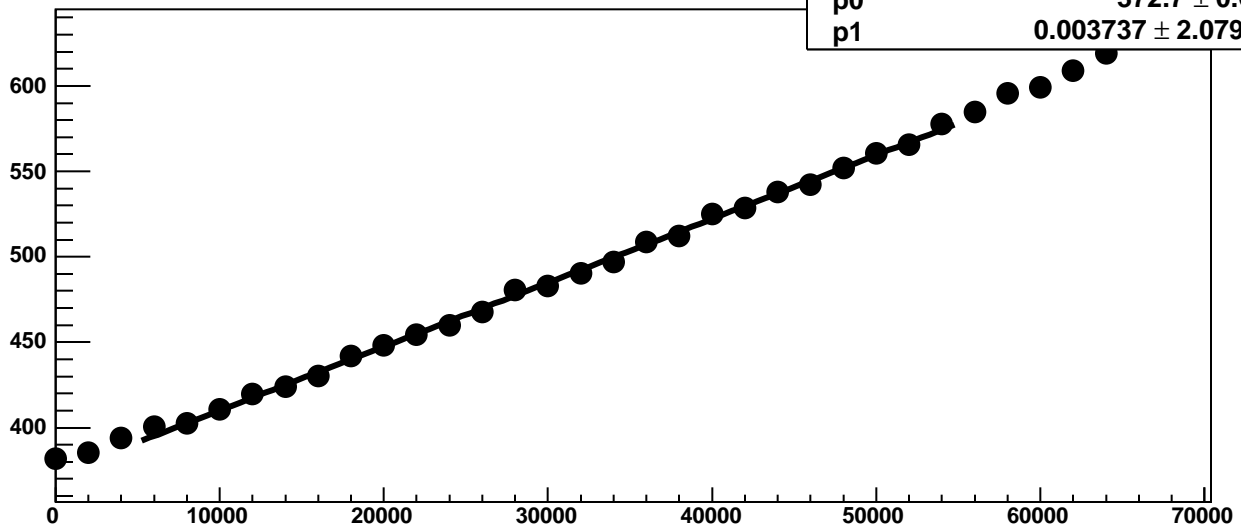
Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



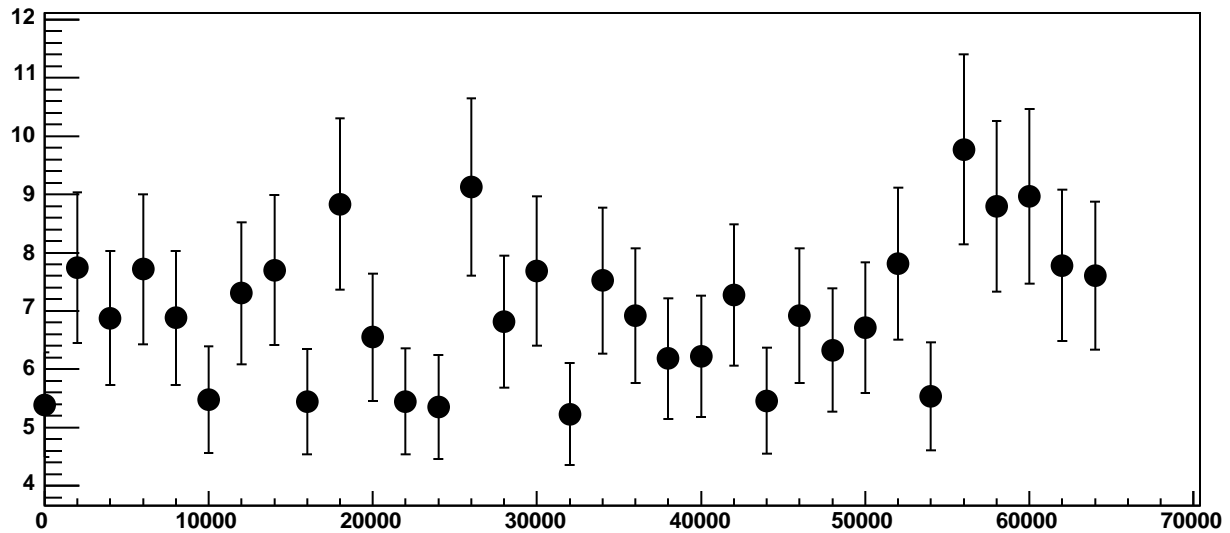
Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



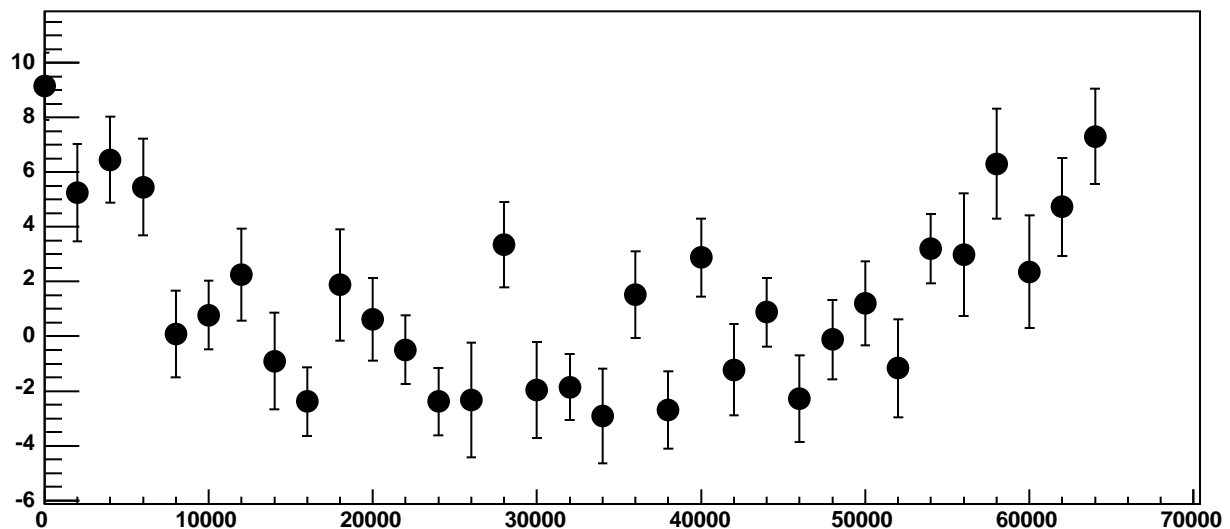
Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



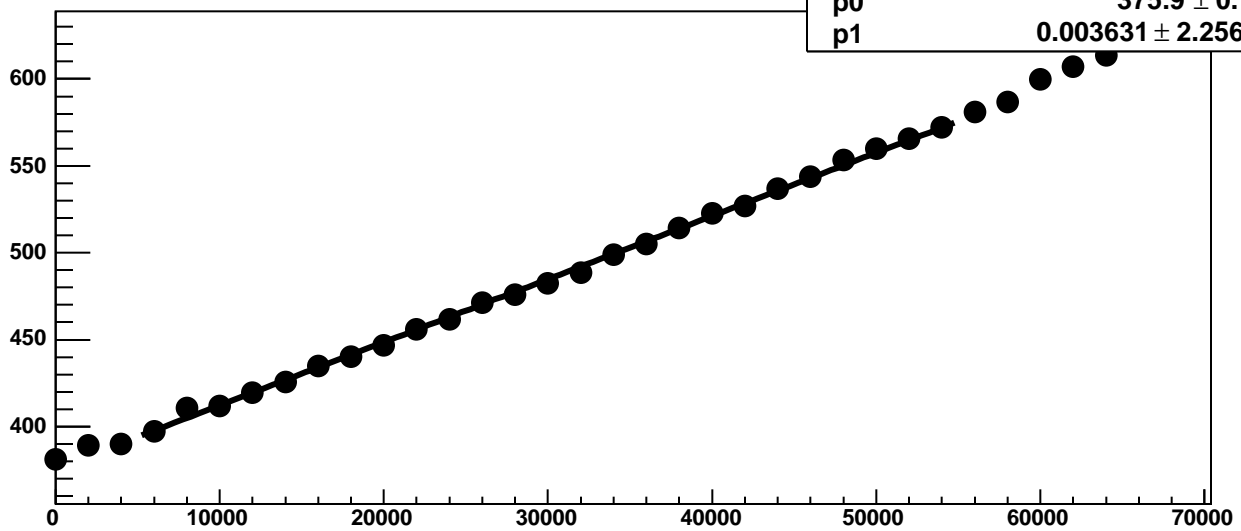
Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

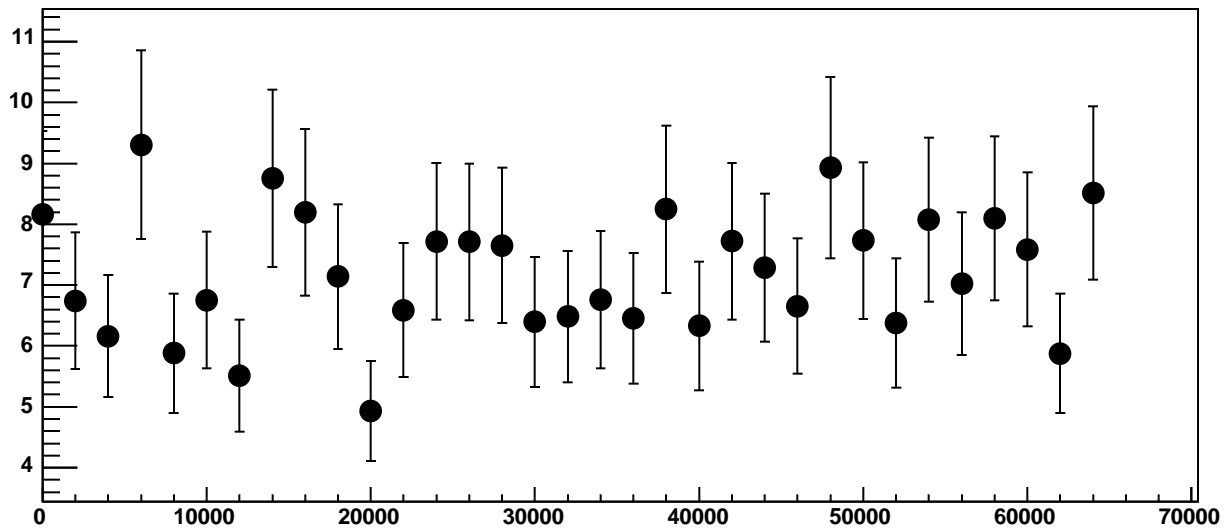


Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

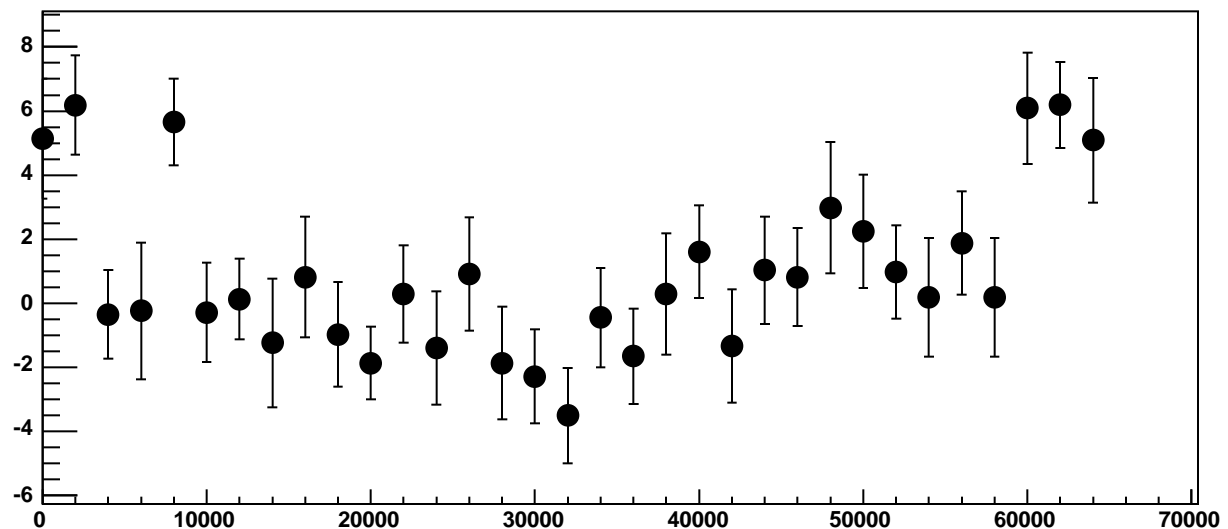


χ^2 / ndf 39.3 / 23
p0 375.9 ± 0.7302
p1 $0.003631 \pm 2.256e-05$

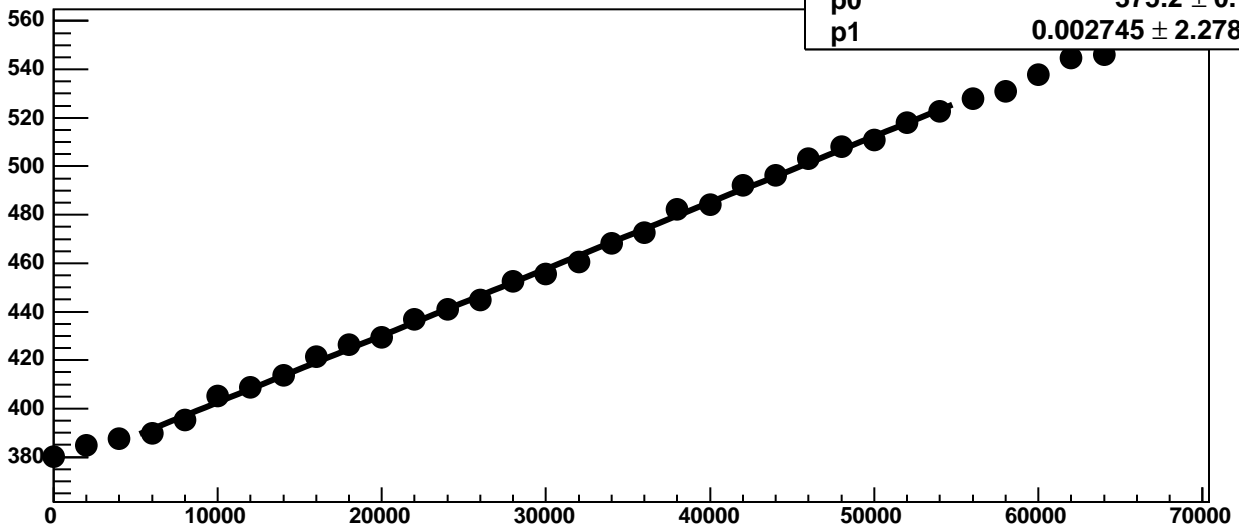
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

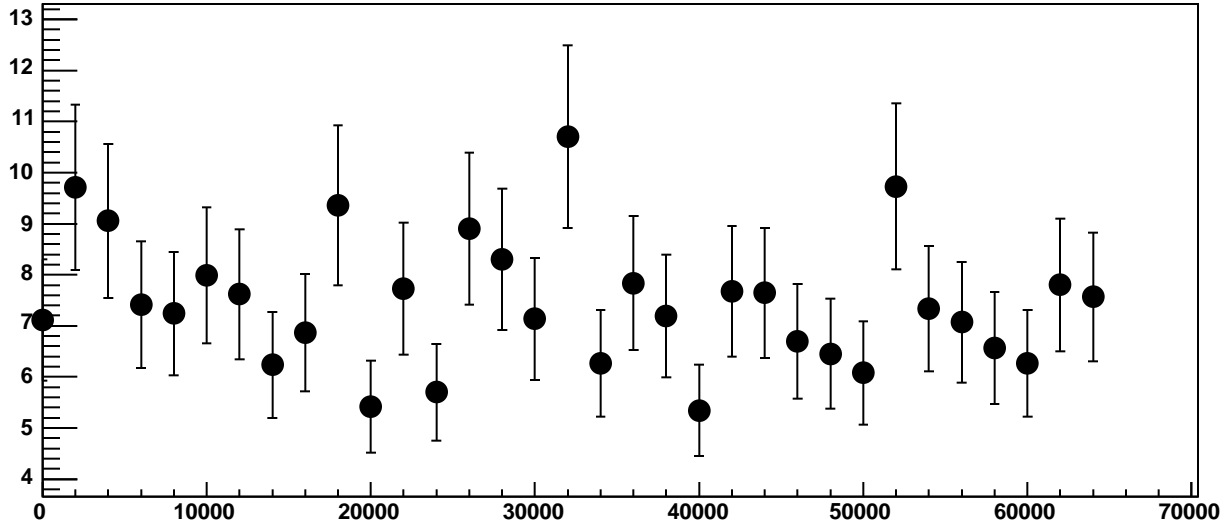


Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

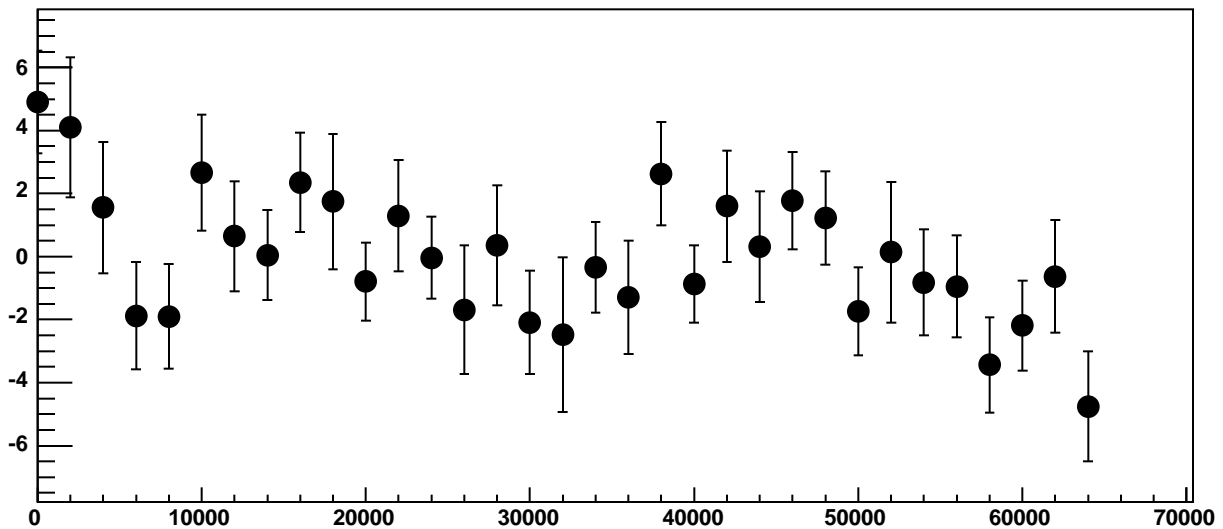


χ^2 / ndf 20.24 / 23
p0 375.2 ± 0.7612
p1 $0.002745 \pm 2.278e-05$

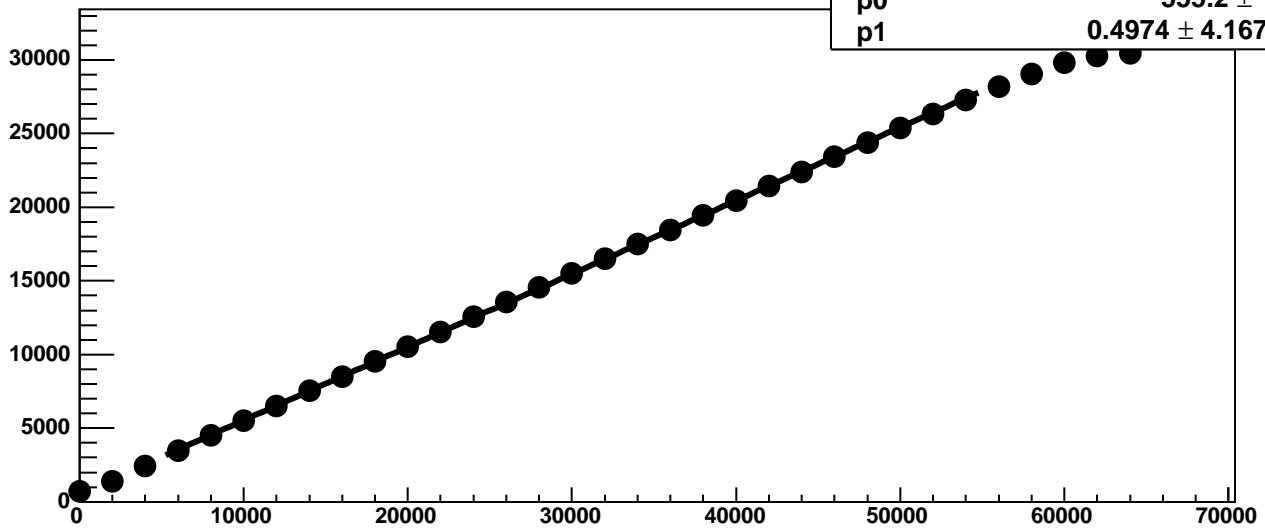
Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



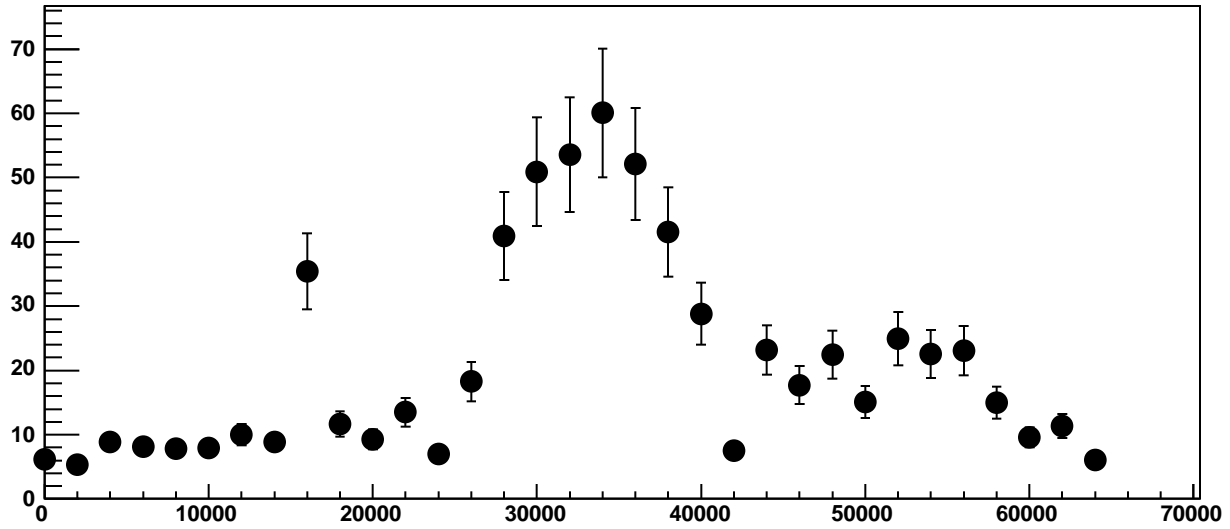
Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



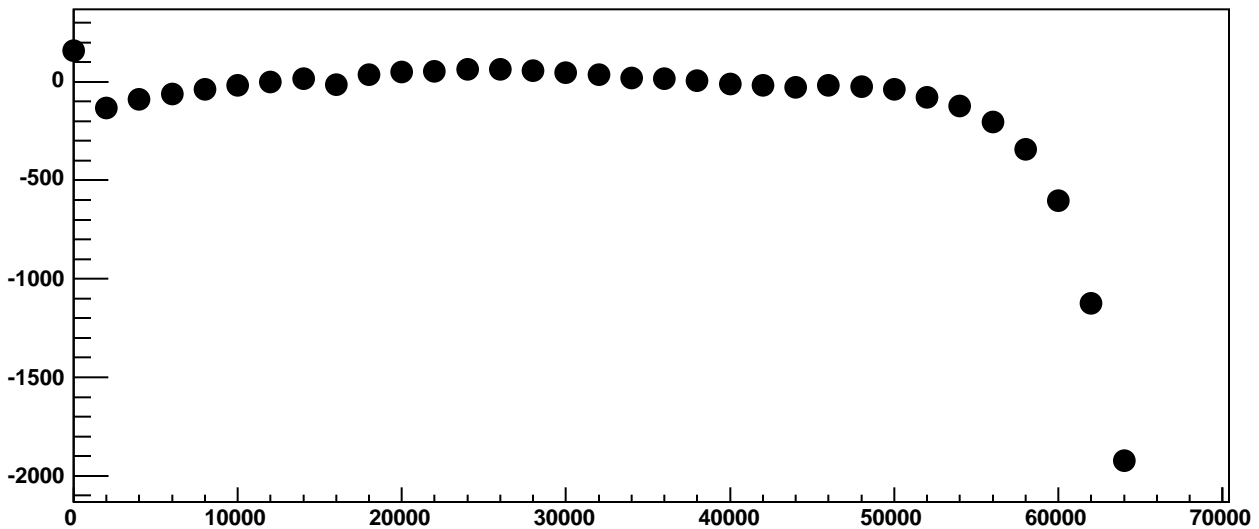
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



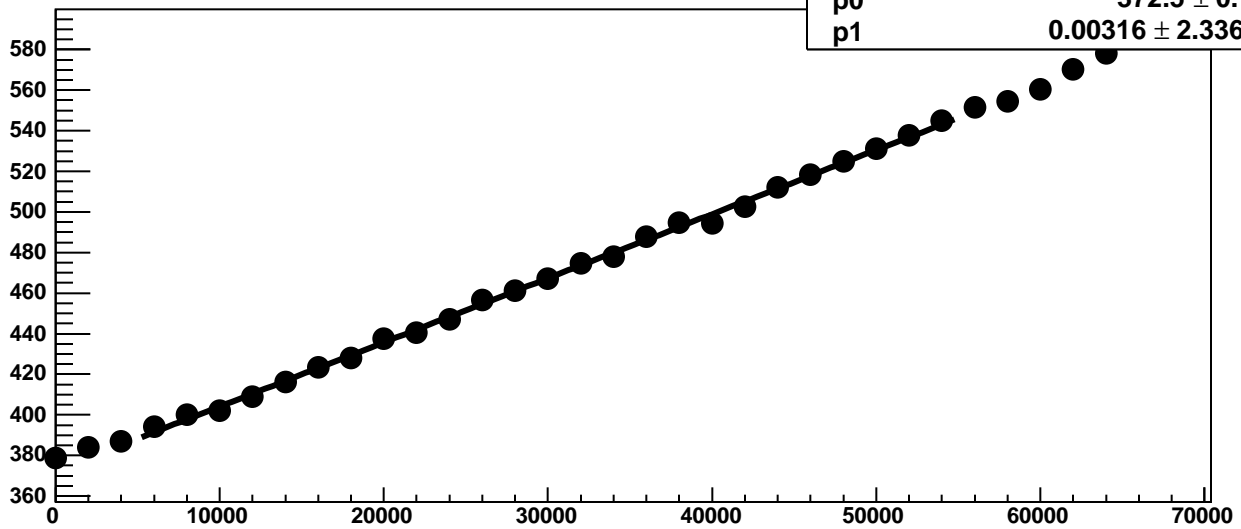
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.3 / 23

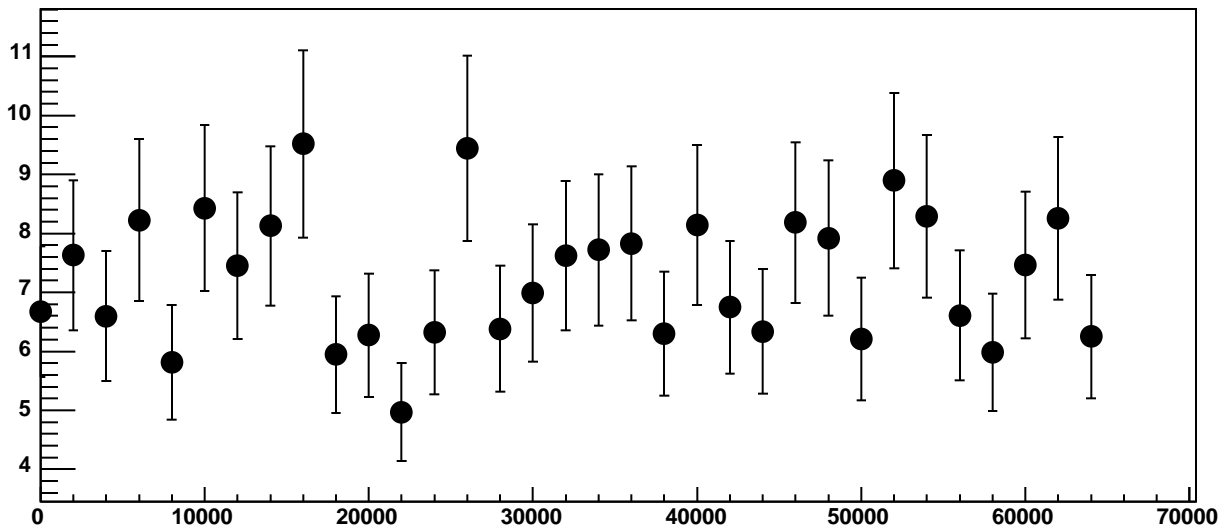
p0

372.5 ± 0.7583

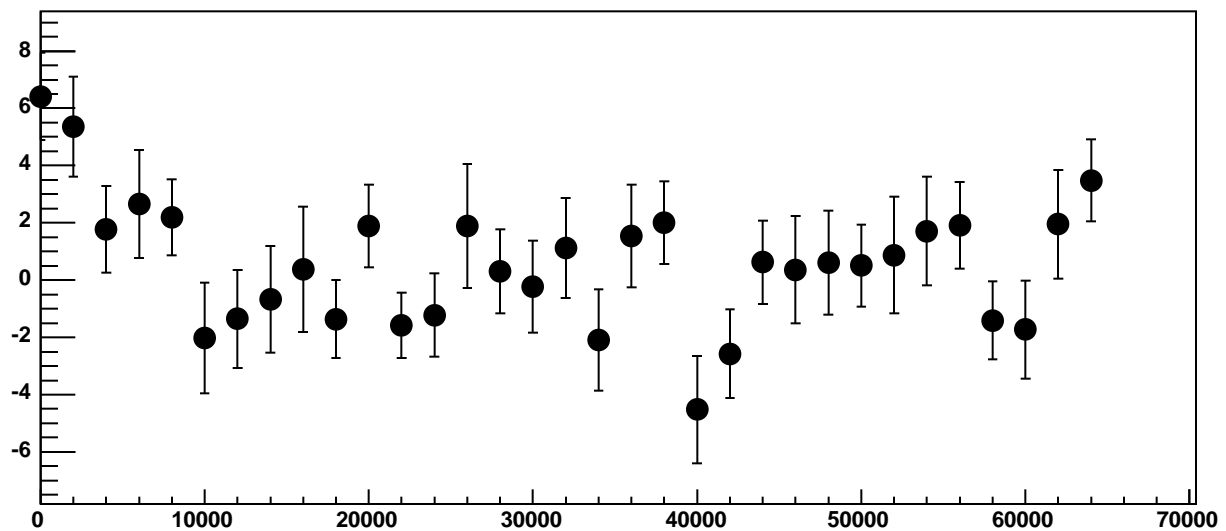
p1

$0.00316 \pm 2.336e-05$

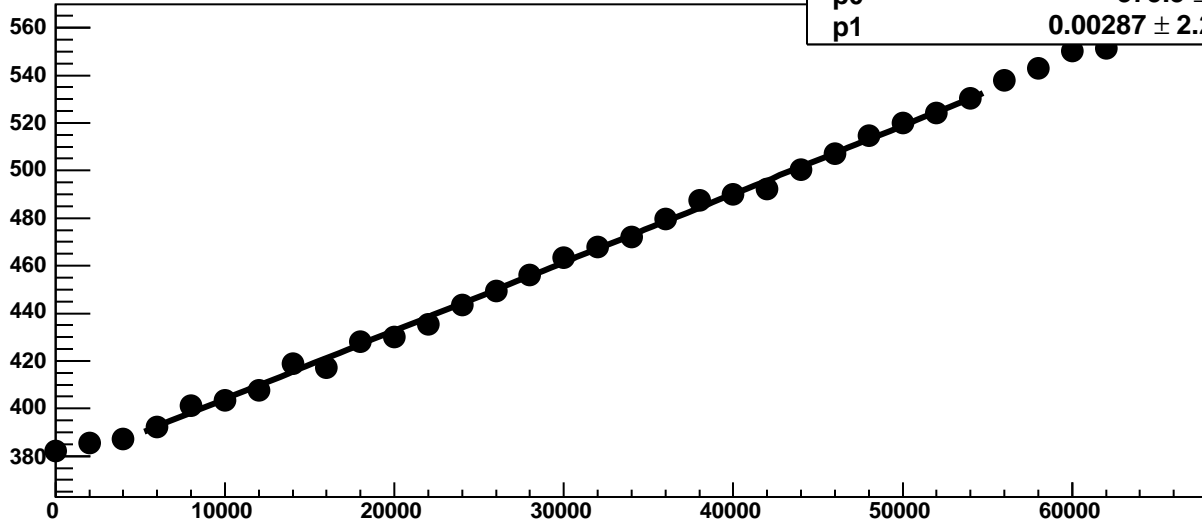
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

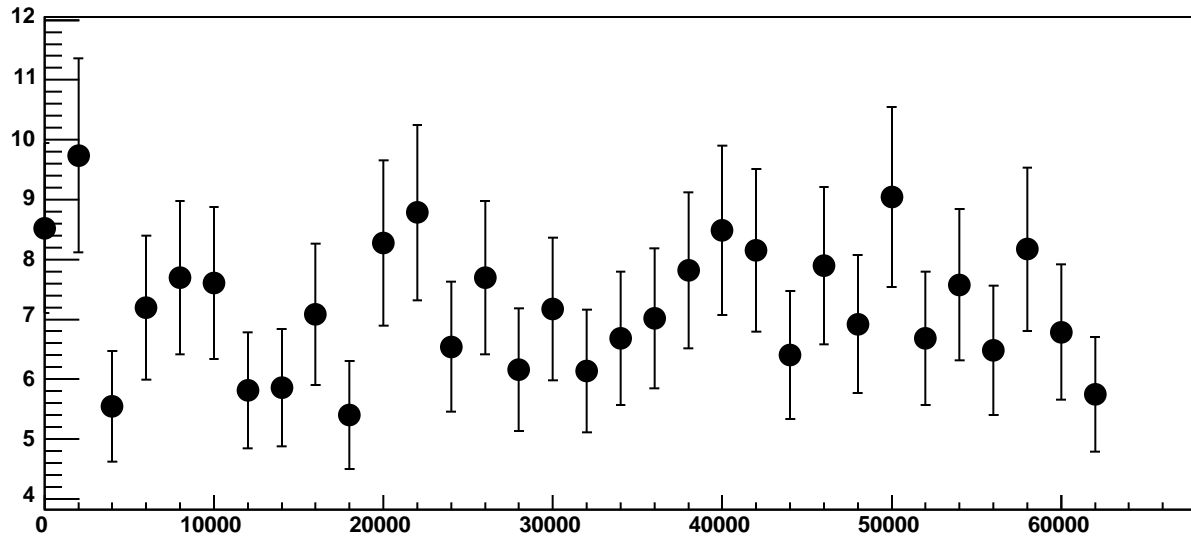


Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

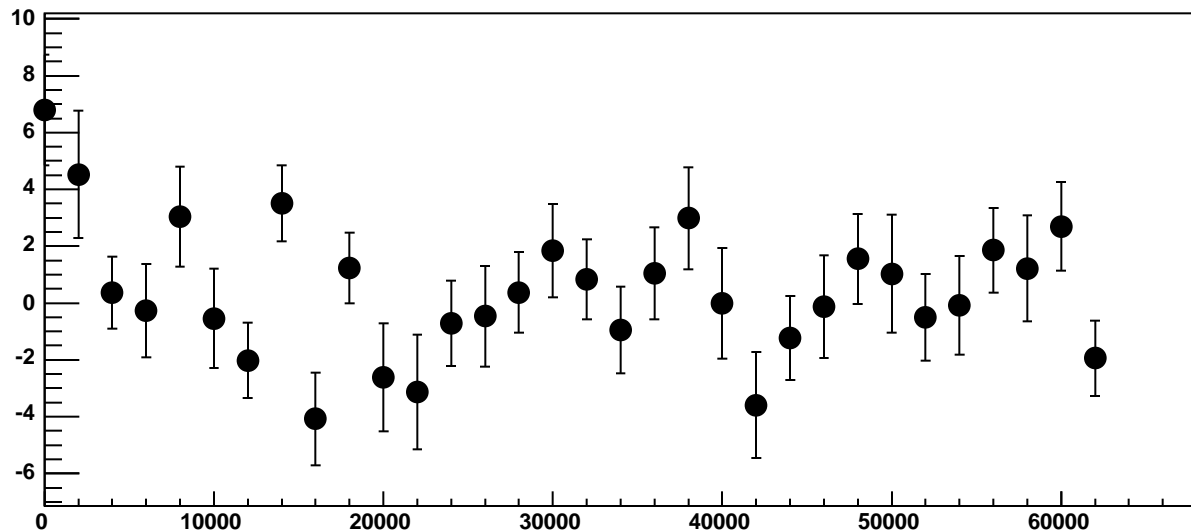


χ^2 / ndf 35.07 / 23
p0 375.3 ± 0.7314
p1 $0.00287 \pm 2.269e-05$

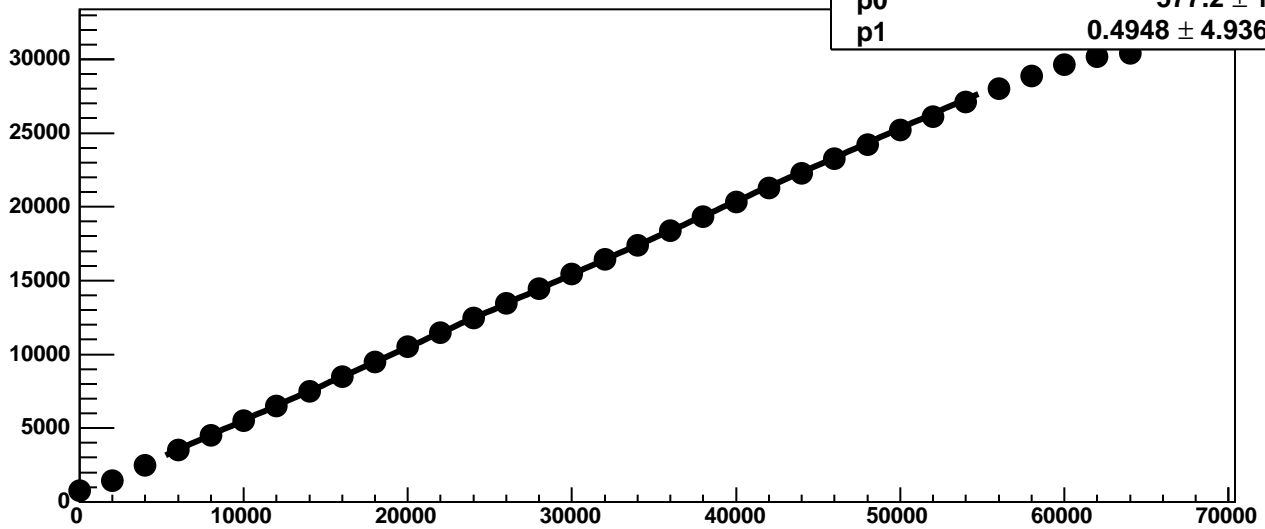
Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



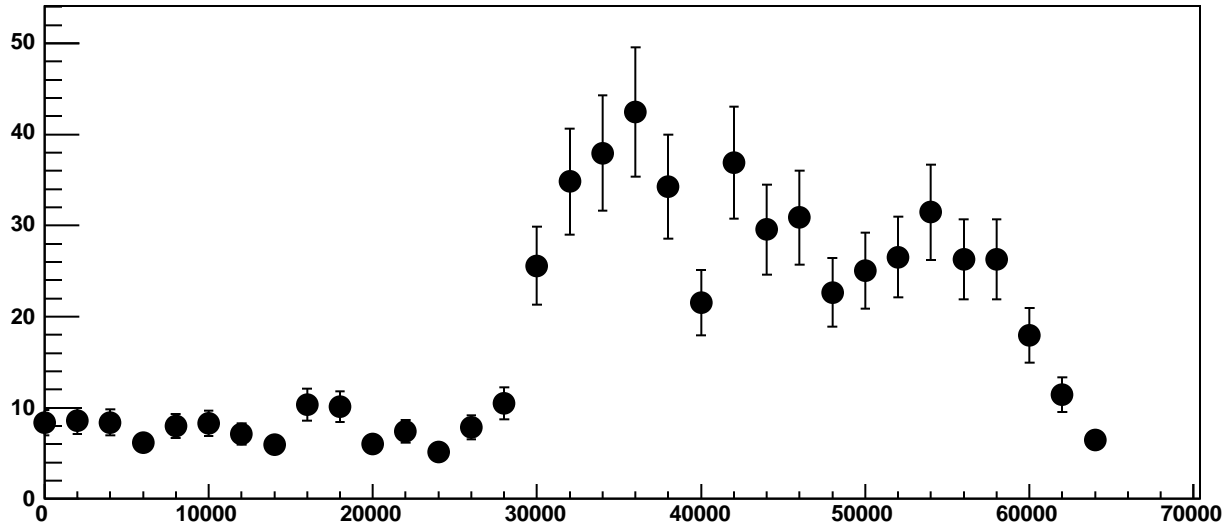
Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



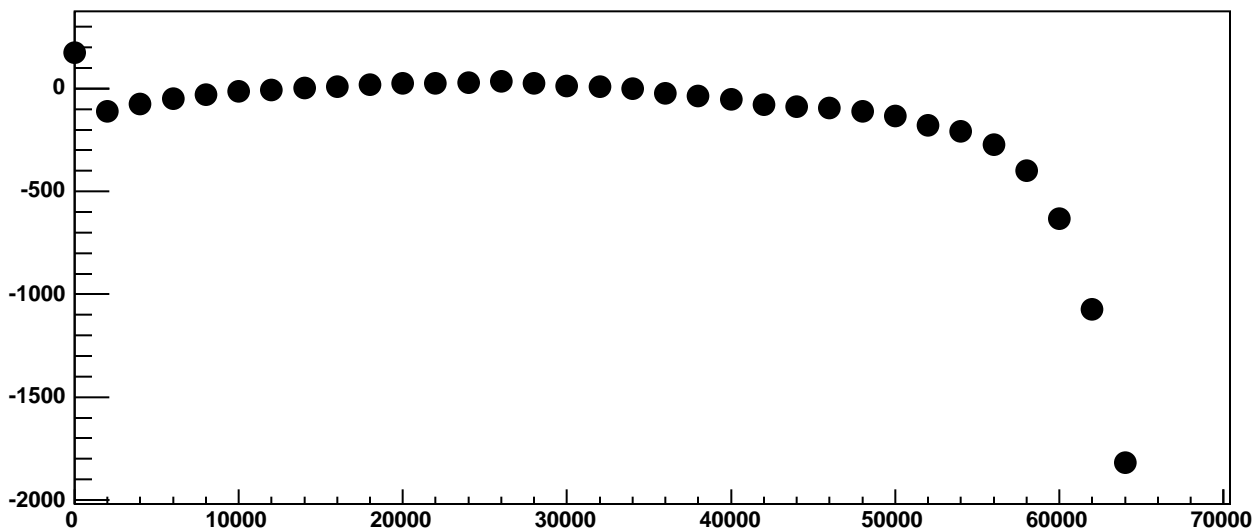
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



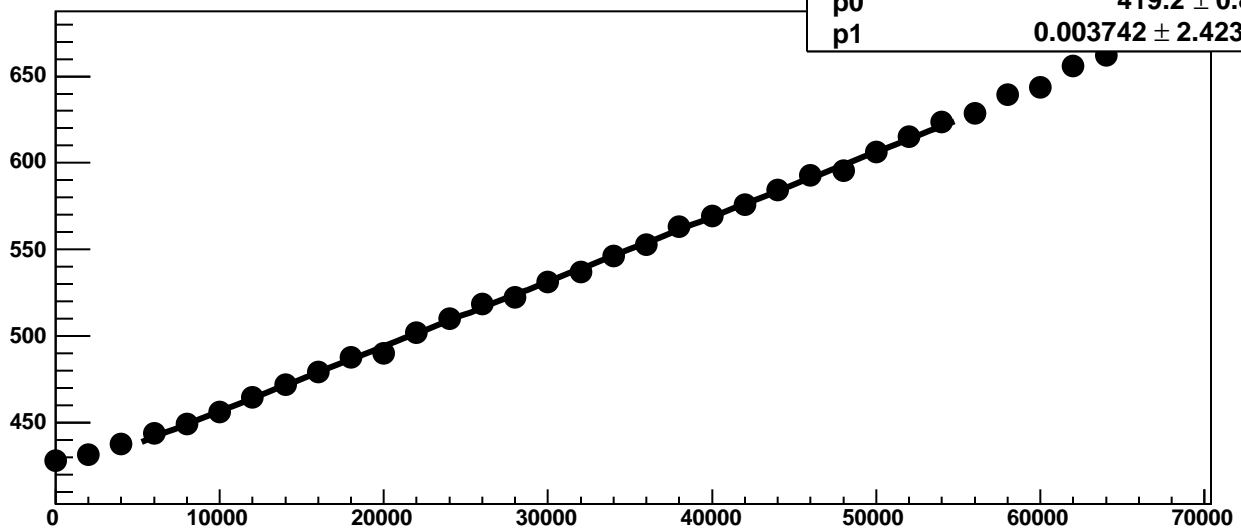
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

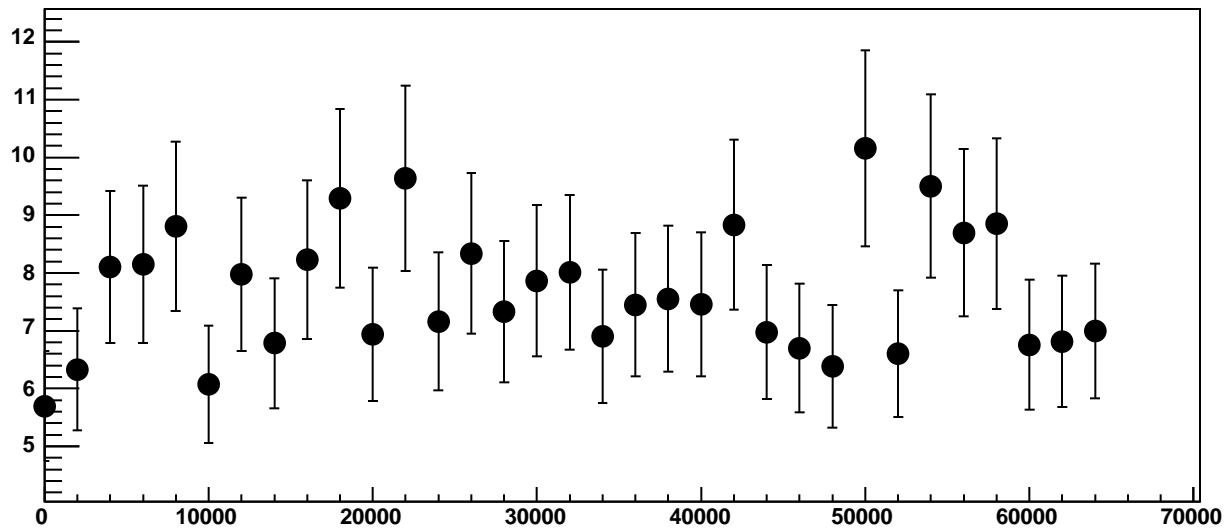


Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

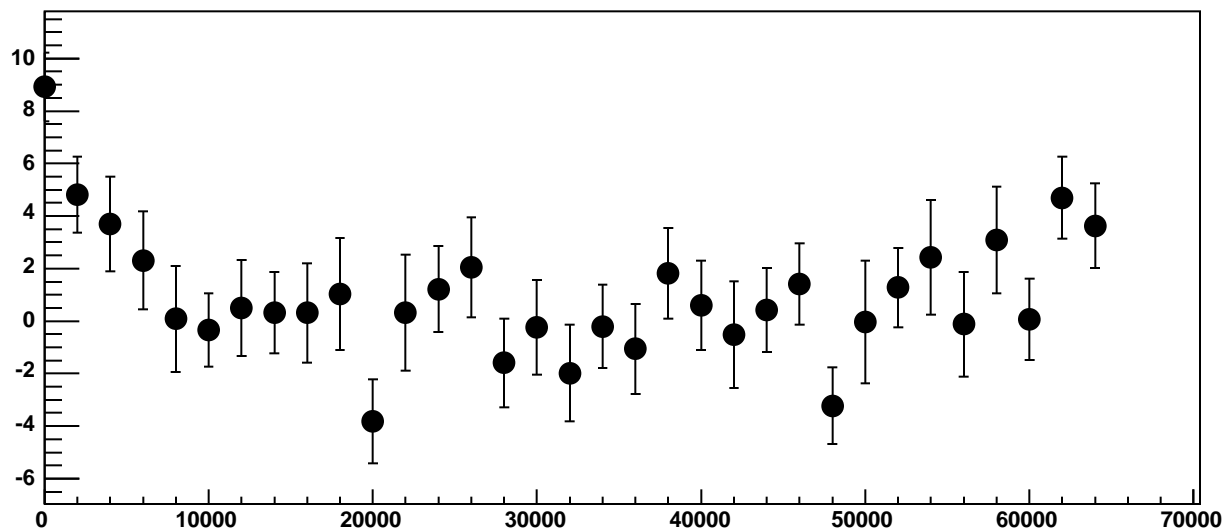


χ^2 / ndf 20.95 / 23
p0 419.2 ± 0.8102
p1 $0.003742 \pm 2.423e-05$

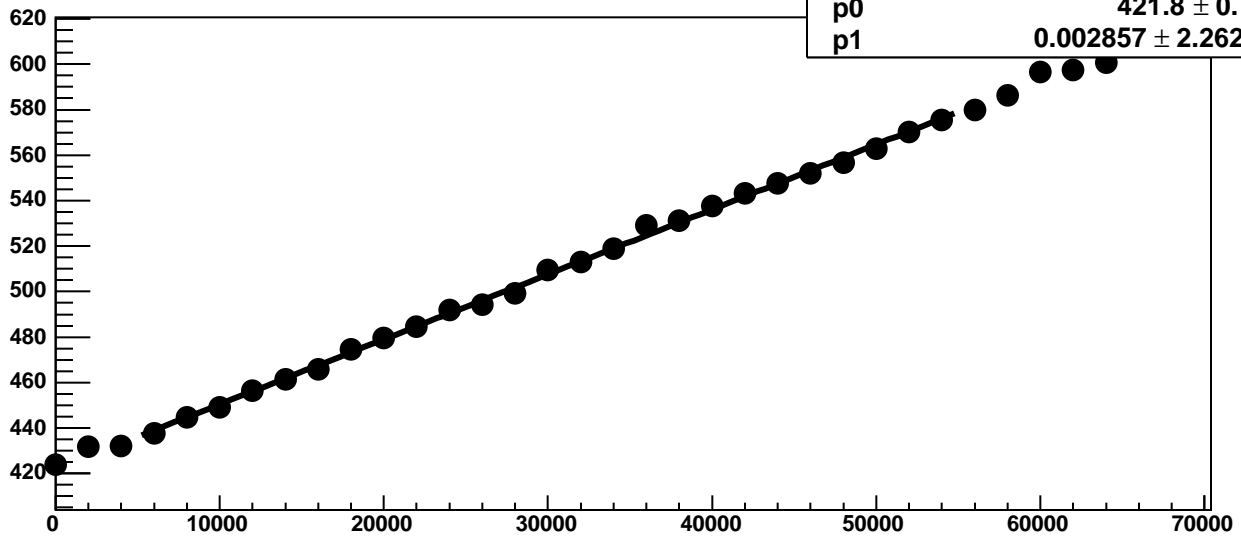
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

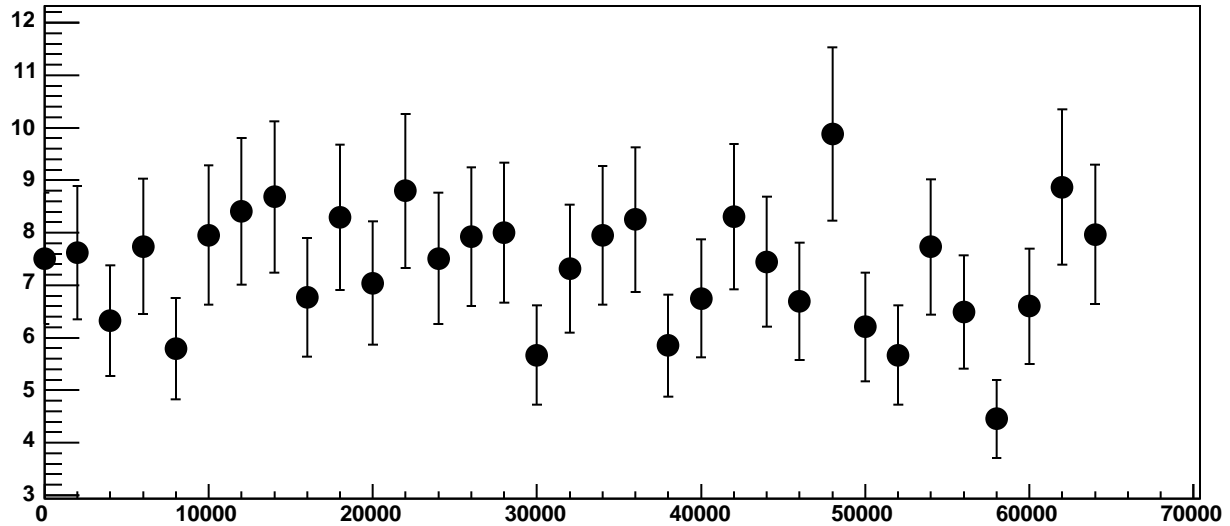


Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

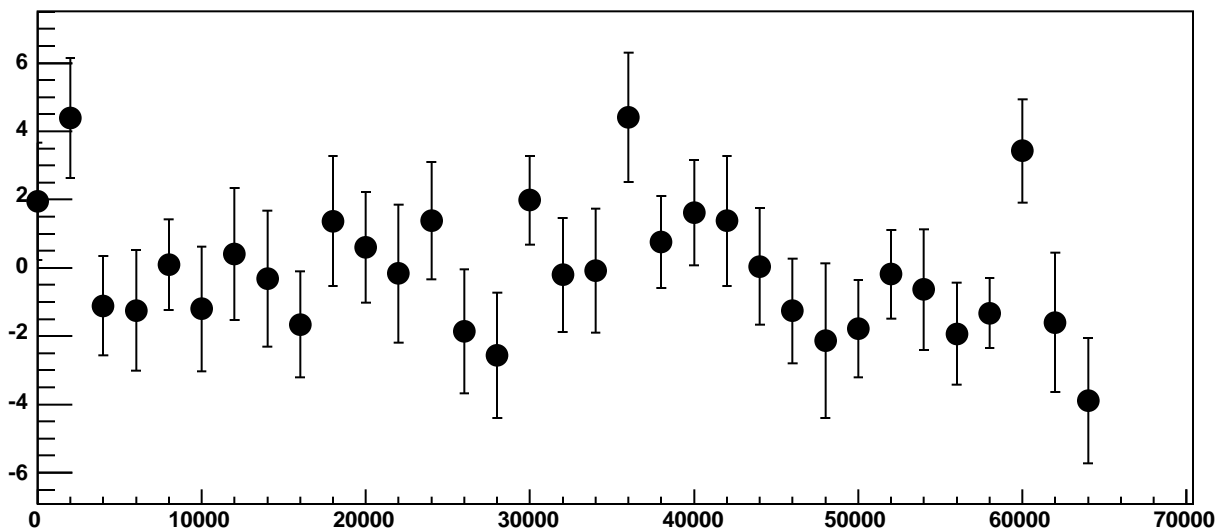


χ^2 / ndf 19.43 / 23
p0 421.8 ± 0.7718
p1 $0.002857 \pm 2.262e-05$

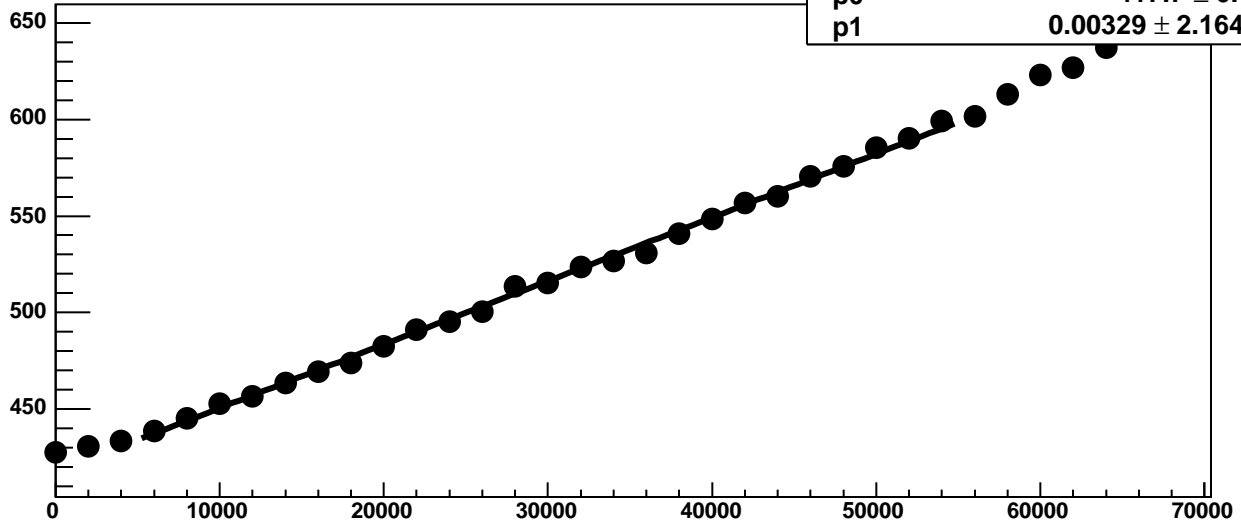
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

50.4 / 23

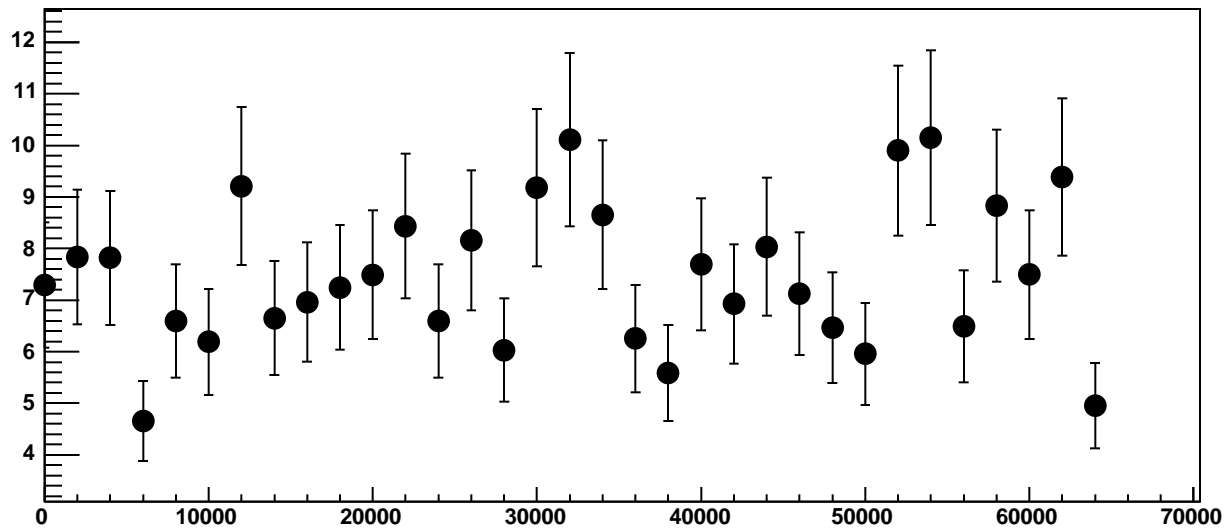
p0

417.7 ± 0.6915

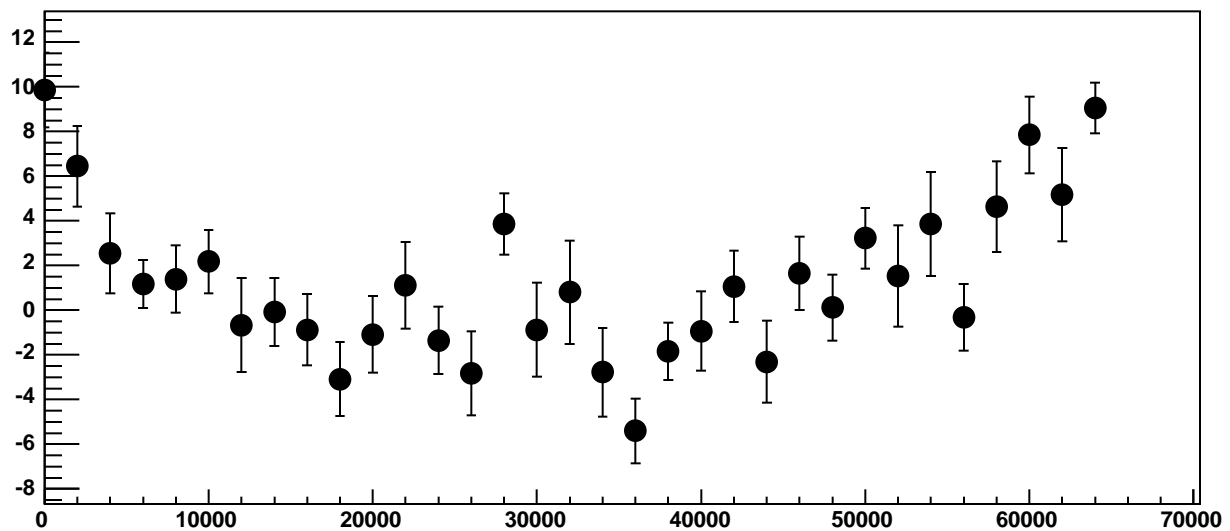
p1

$0.00329 \pm 2.164e-05$

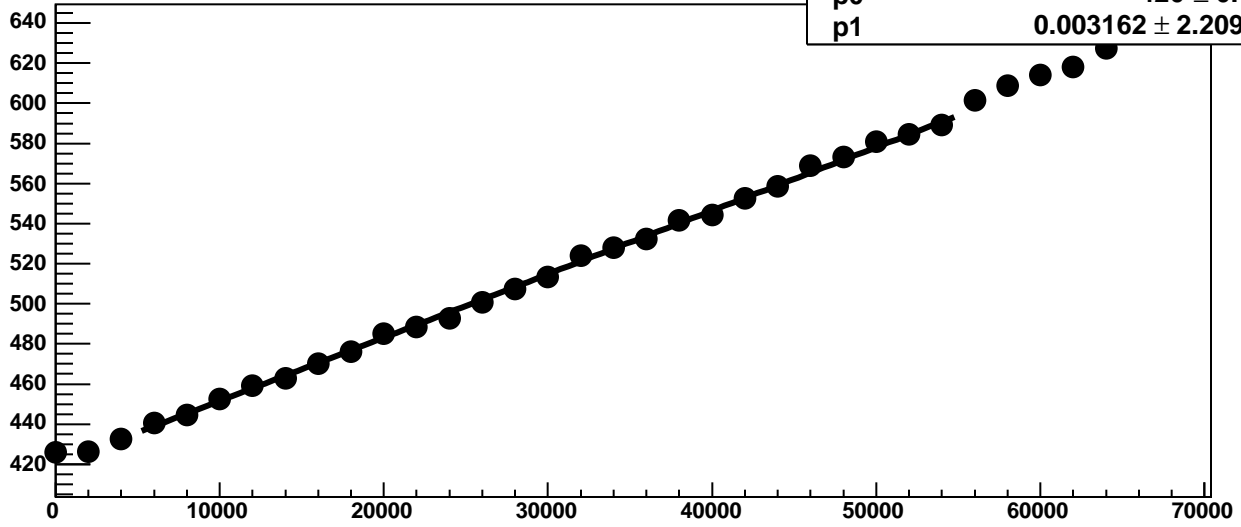
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

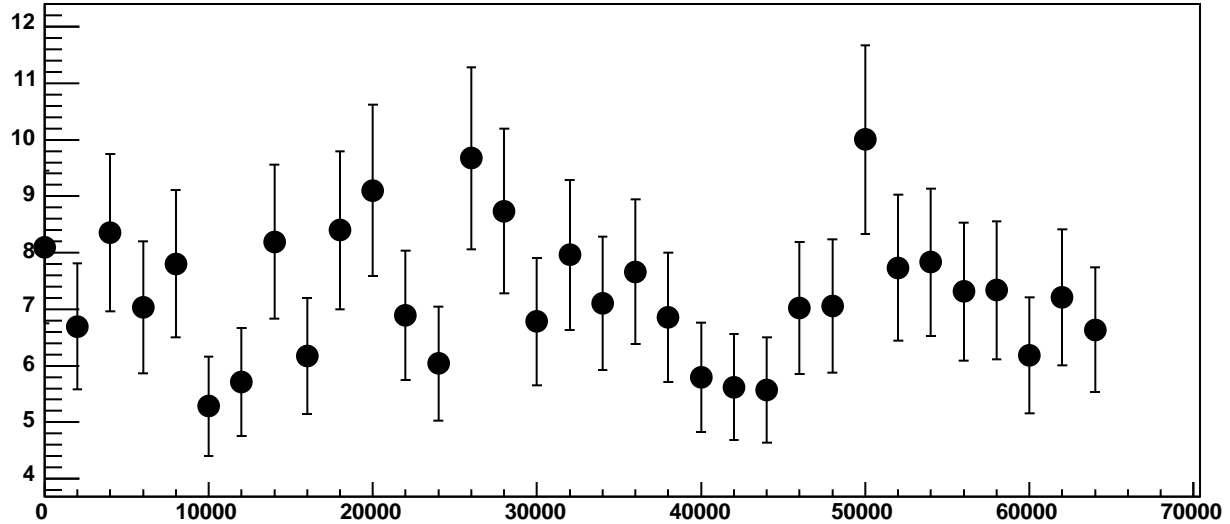


Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

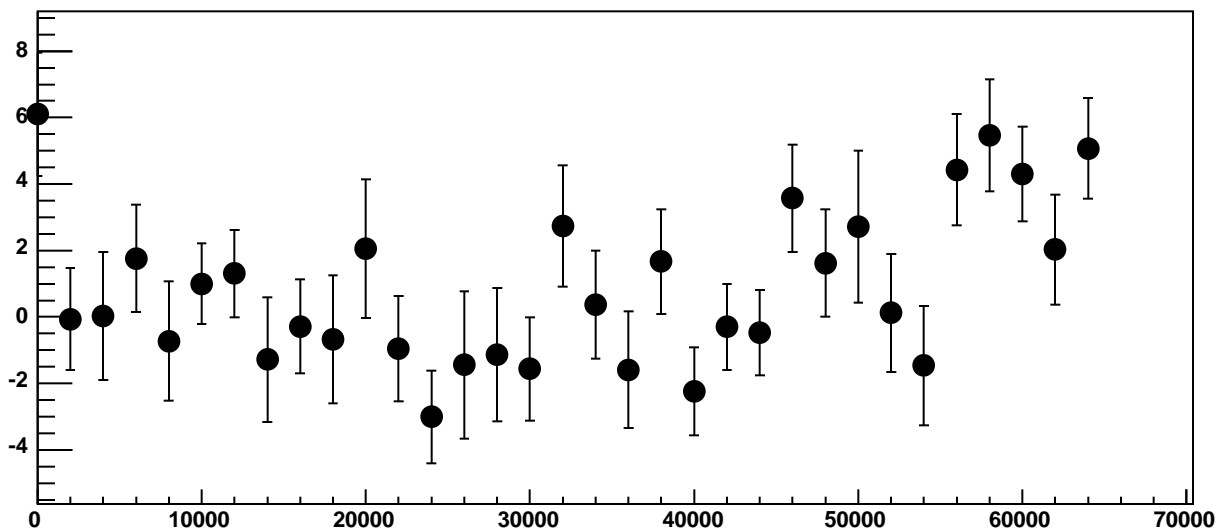


χ^2 / ndf 26.71 / 23
p0 420 ± 0.7258
p1 $0.003162 \pm 2.209e-05$

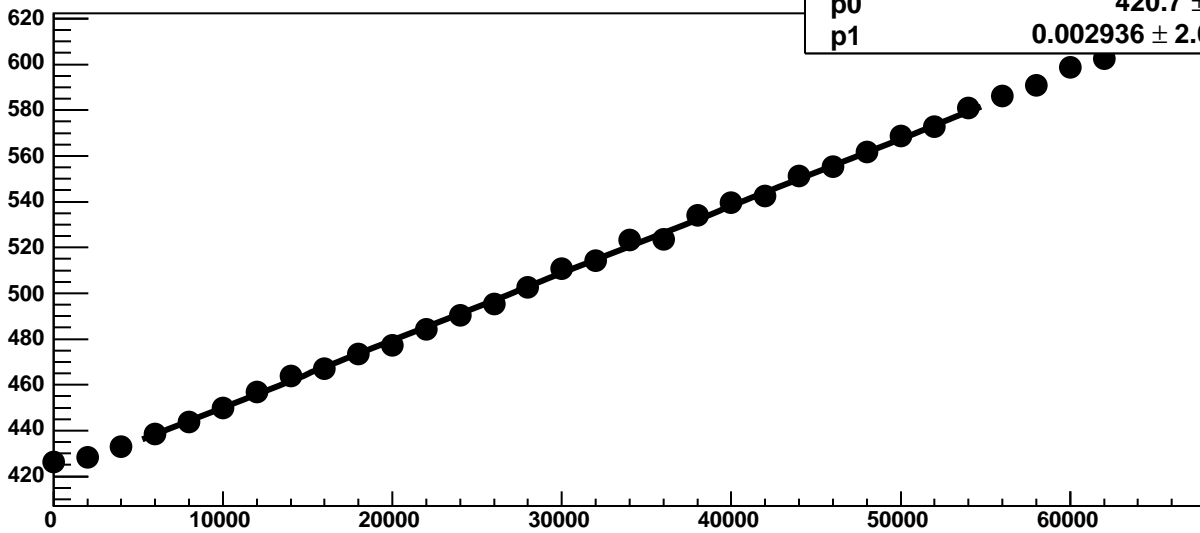
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

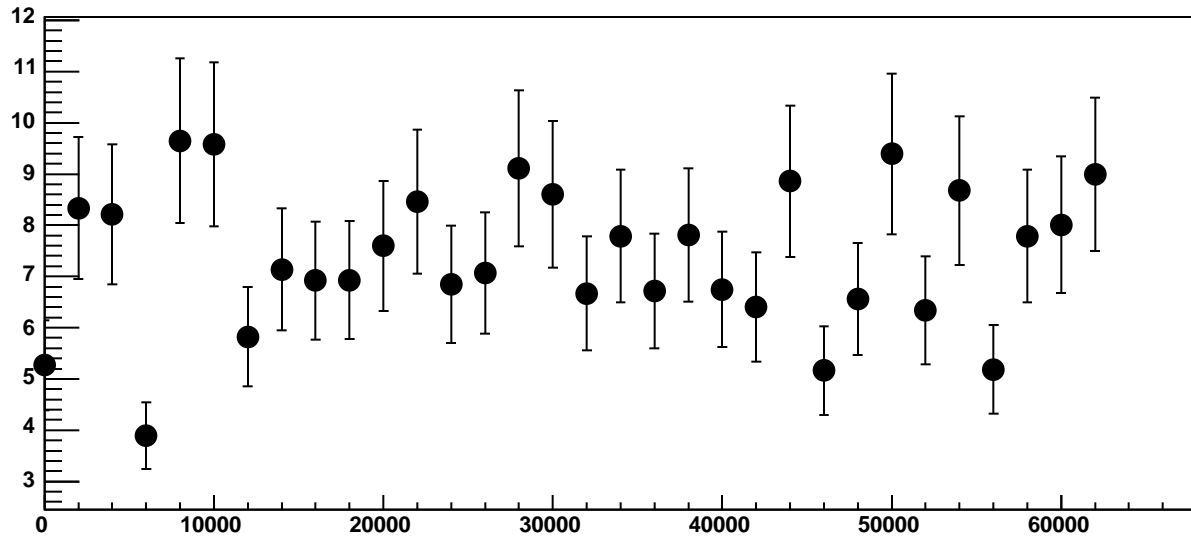


Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

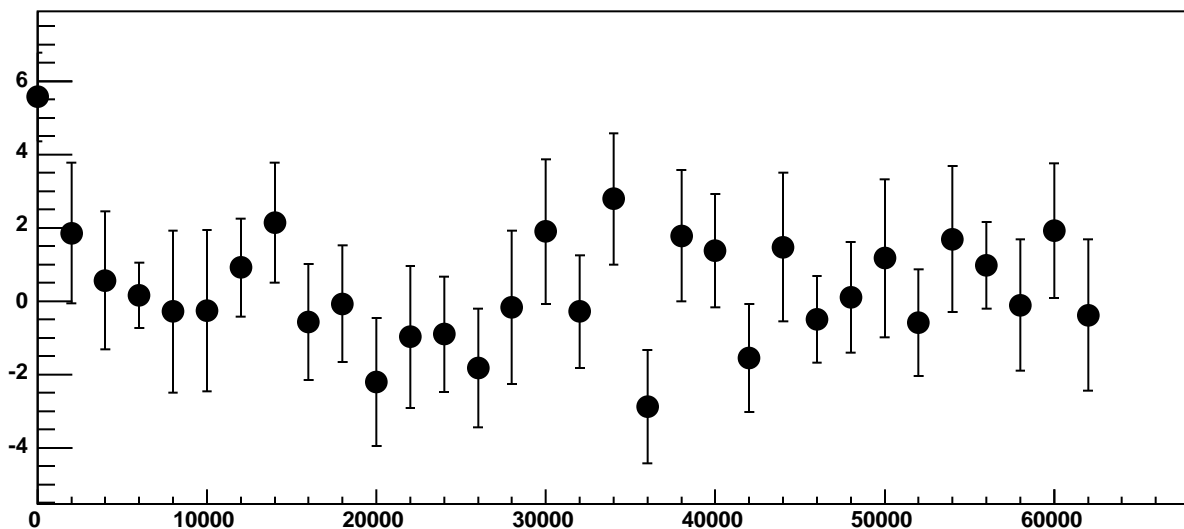


χ^2 / ndf 17.5 / 23
p0 420.7 ± 0.6687
p1 $0.002936 \pm 2.063e-05$

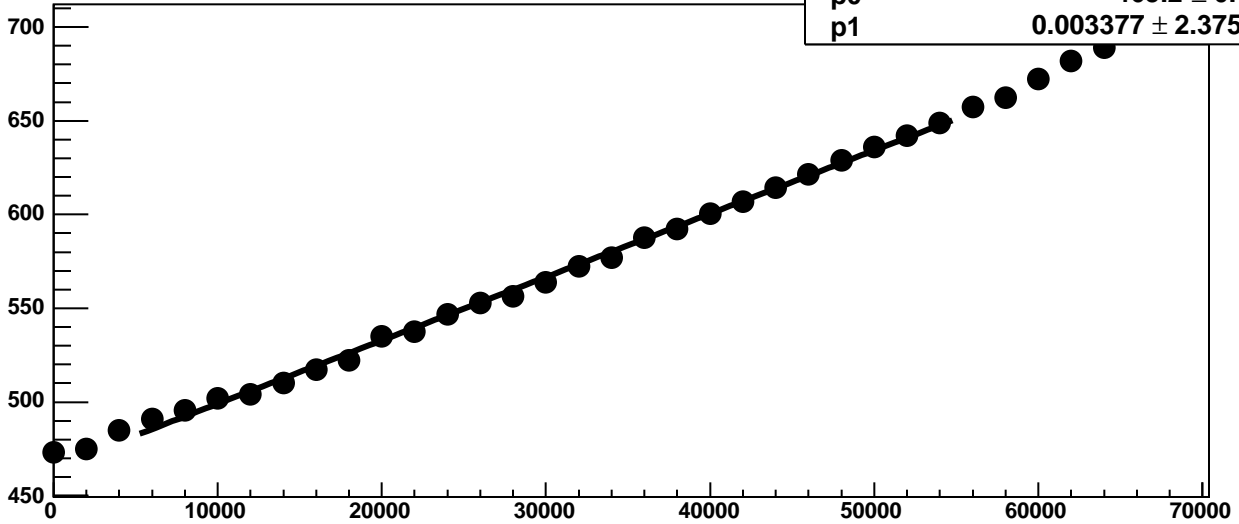
Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.82 / 23

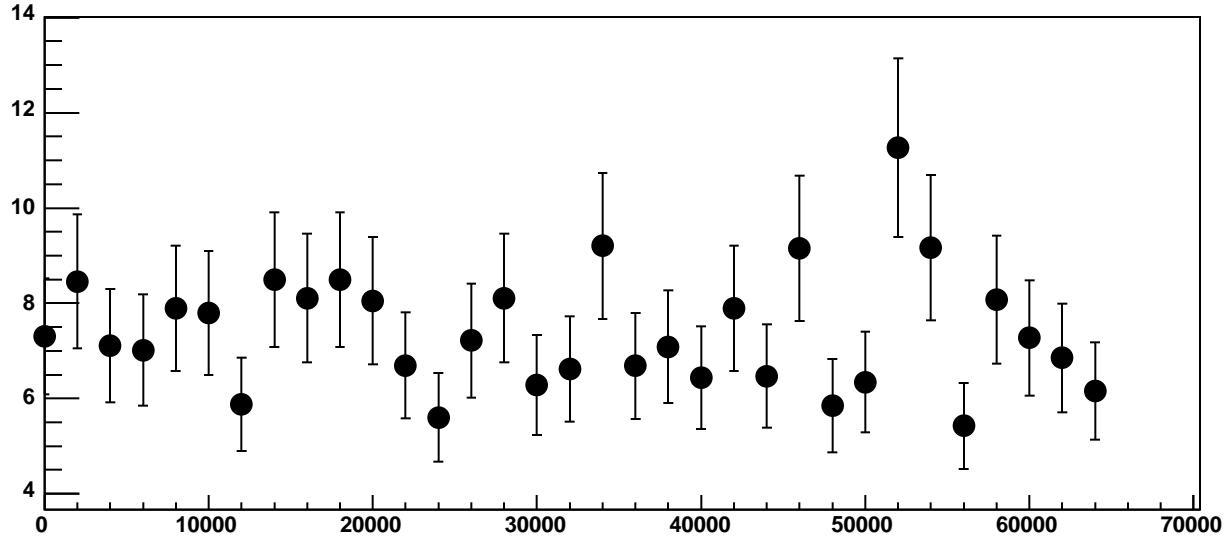
p0

465.2 ± 0.7813

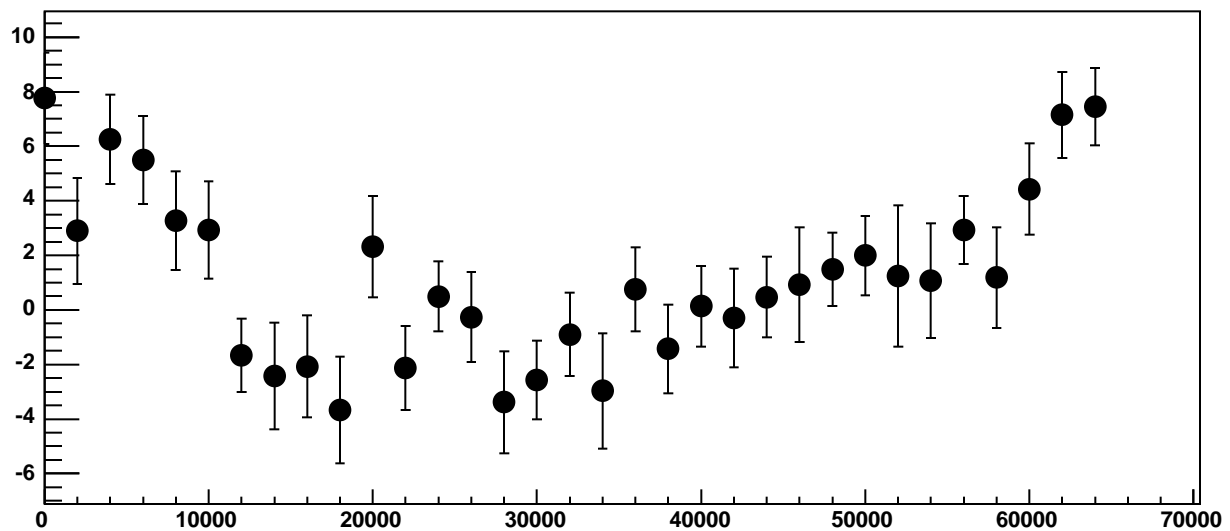
p1

$0.003377 \pm 2.375e-05$

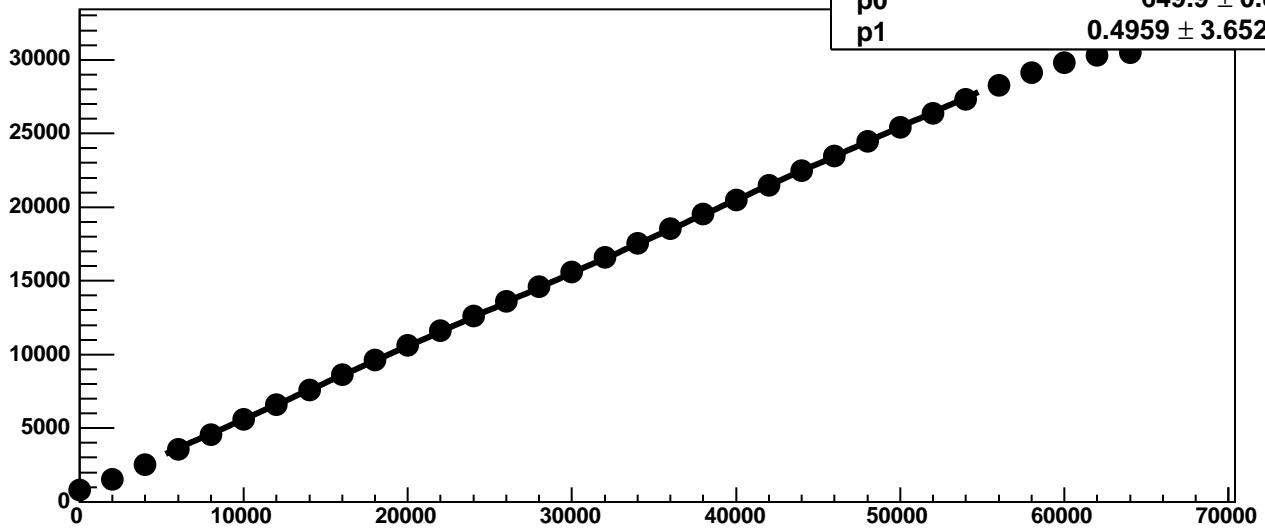
Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



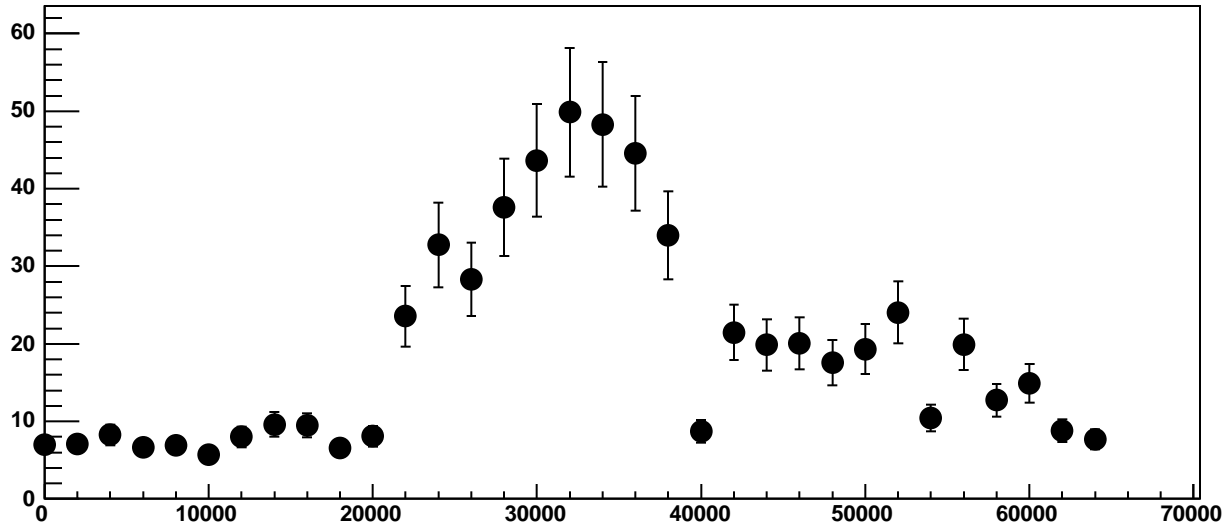
Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



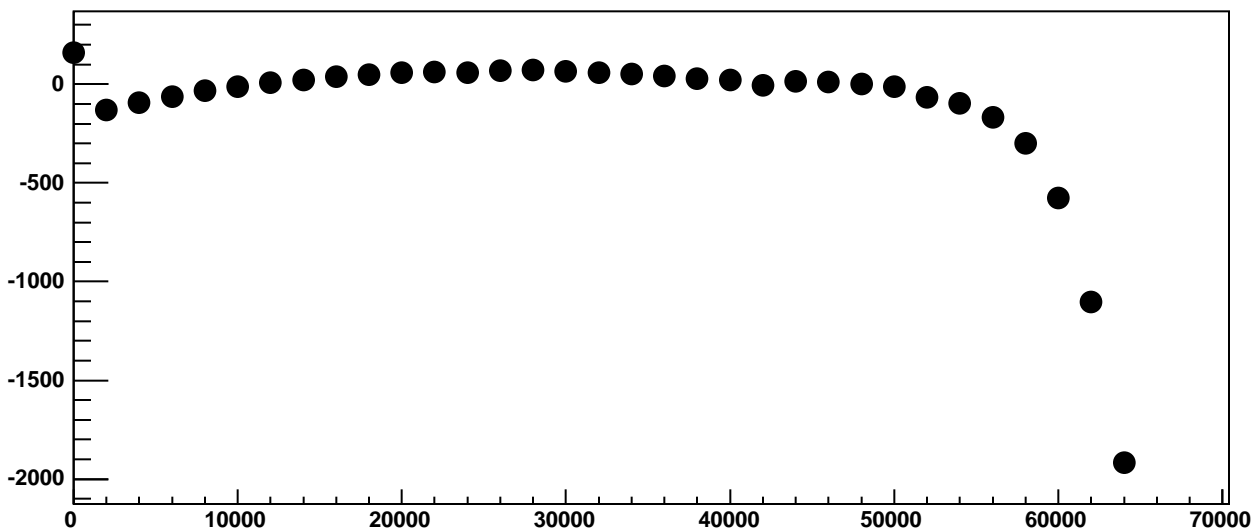
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



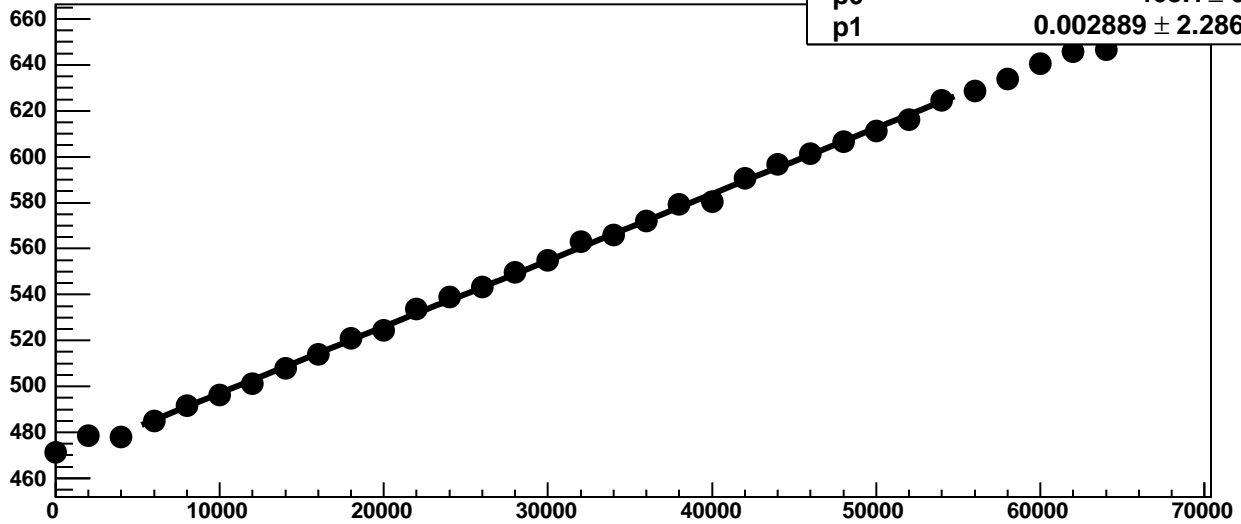
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

13.26 / 23

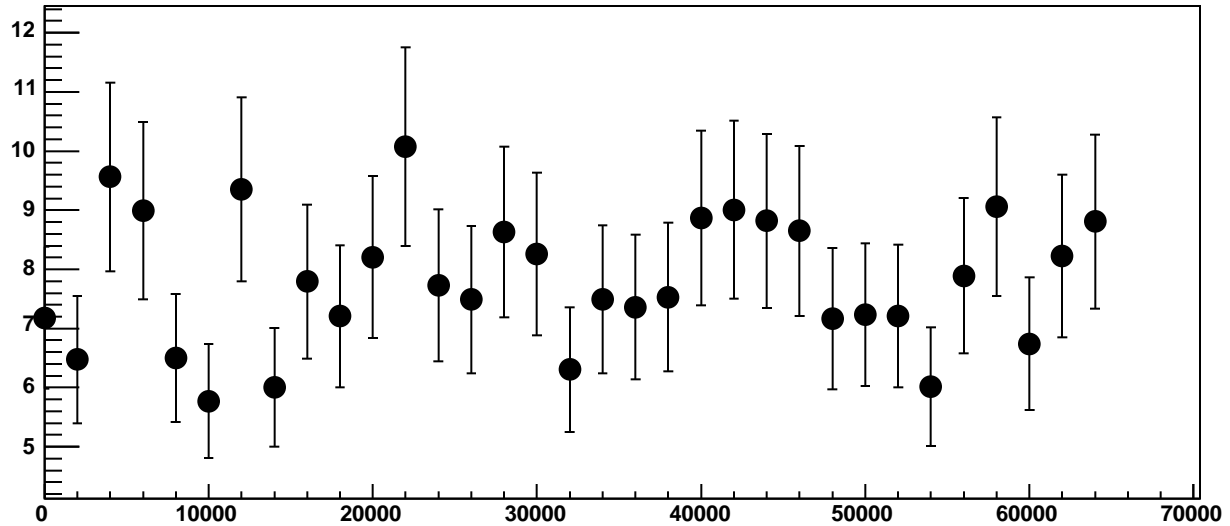
p0

468.1 ± 0.763

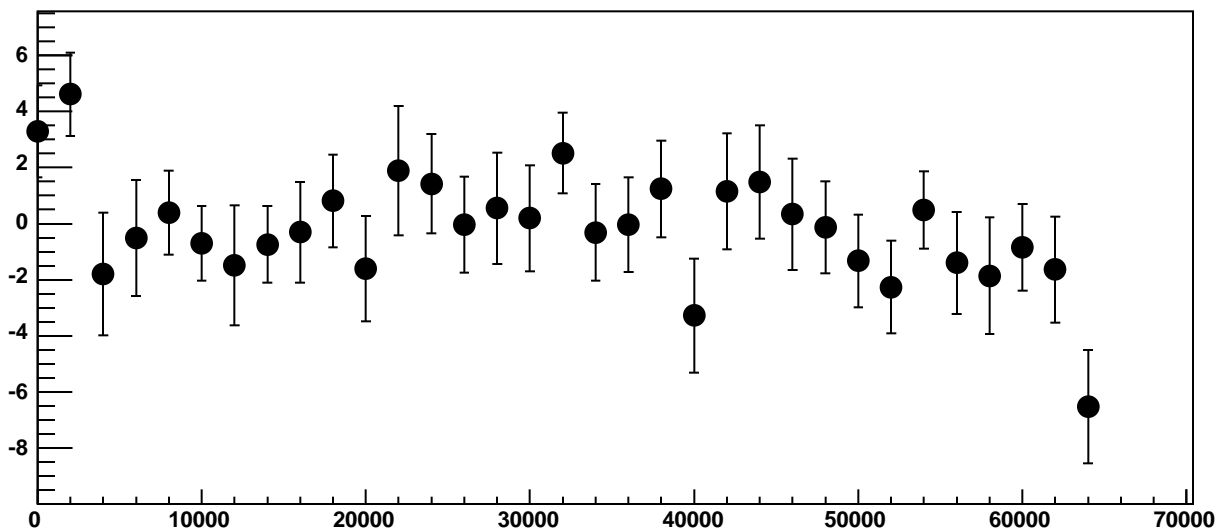
p1

$0.002889 \pm 2.286e-05$

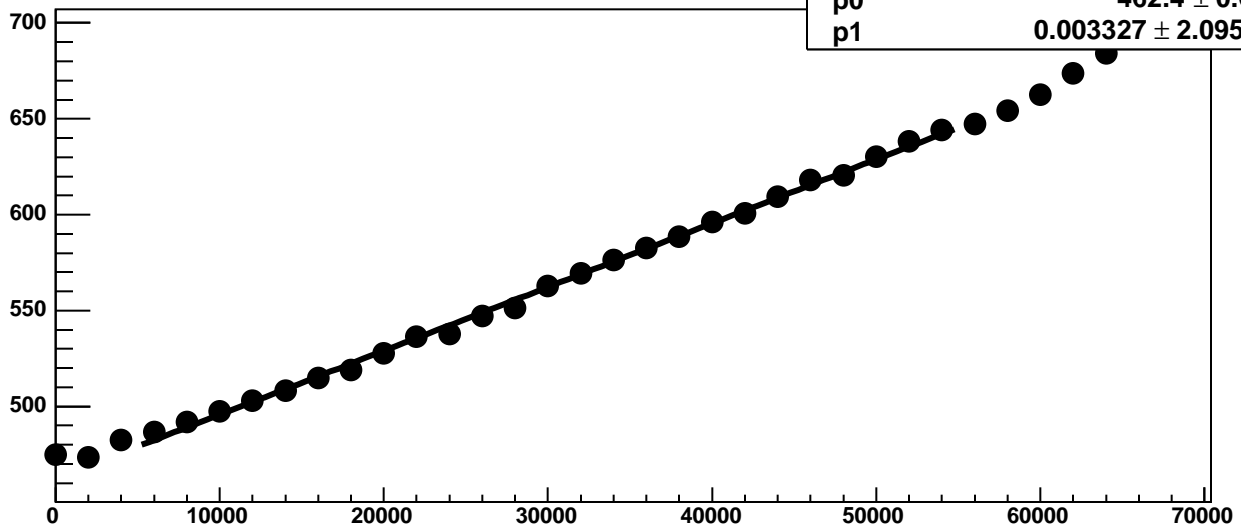
Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



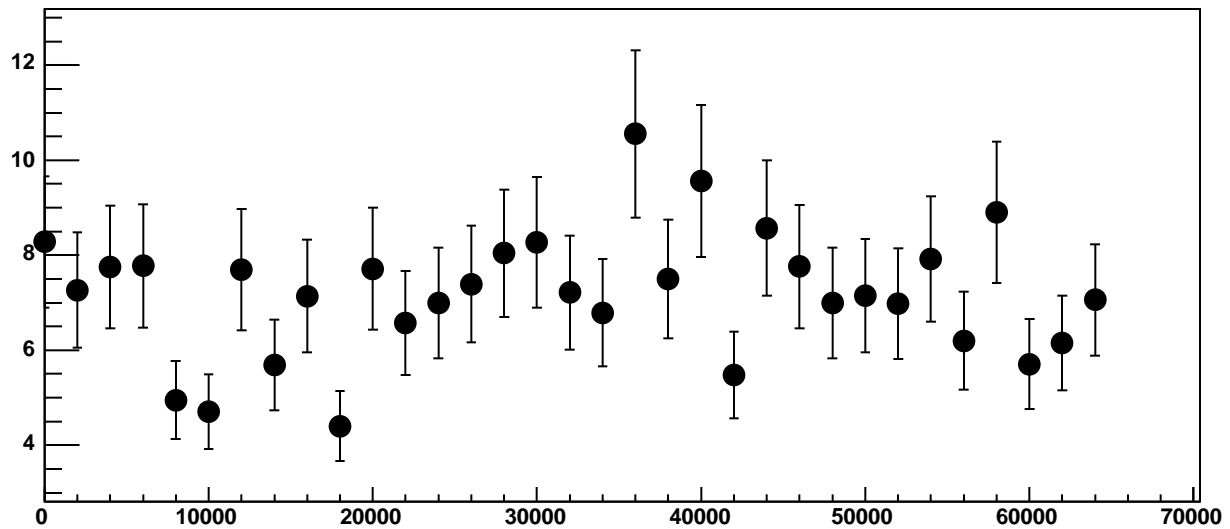
Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



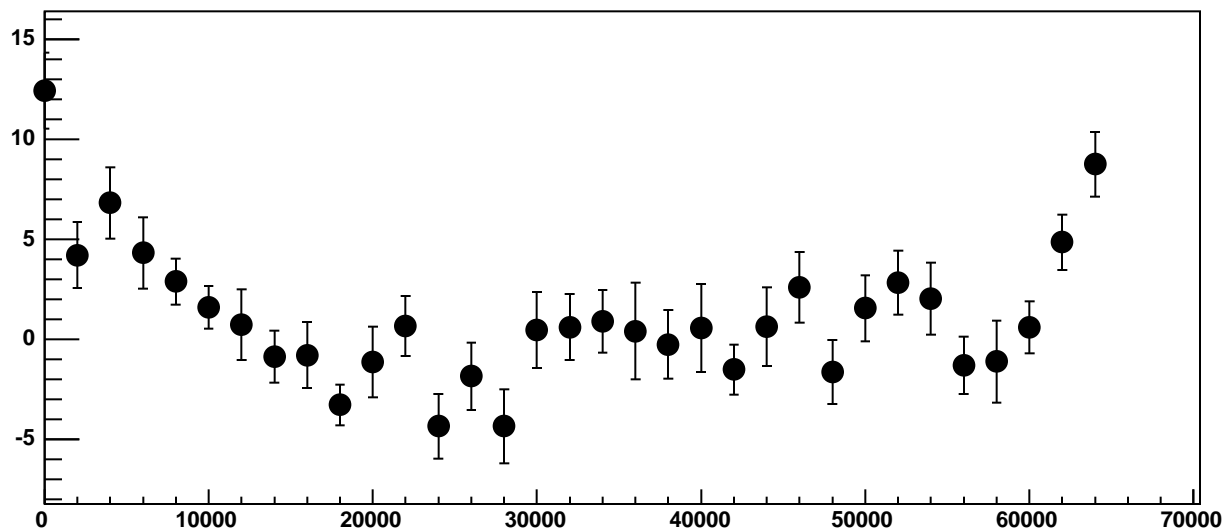
Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



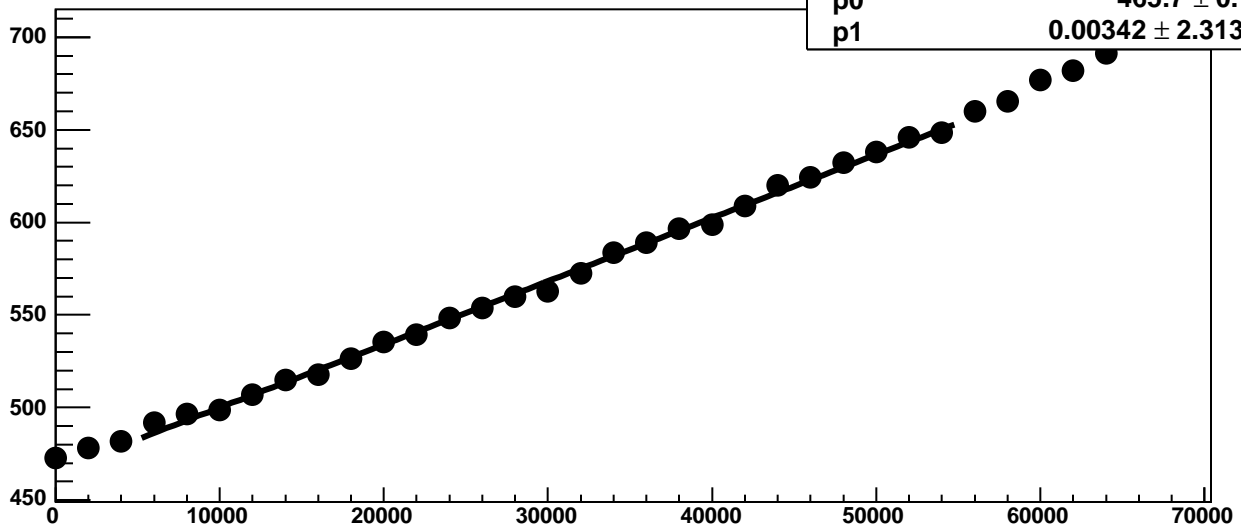
Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



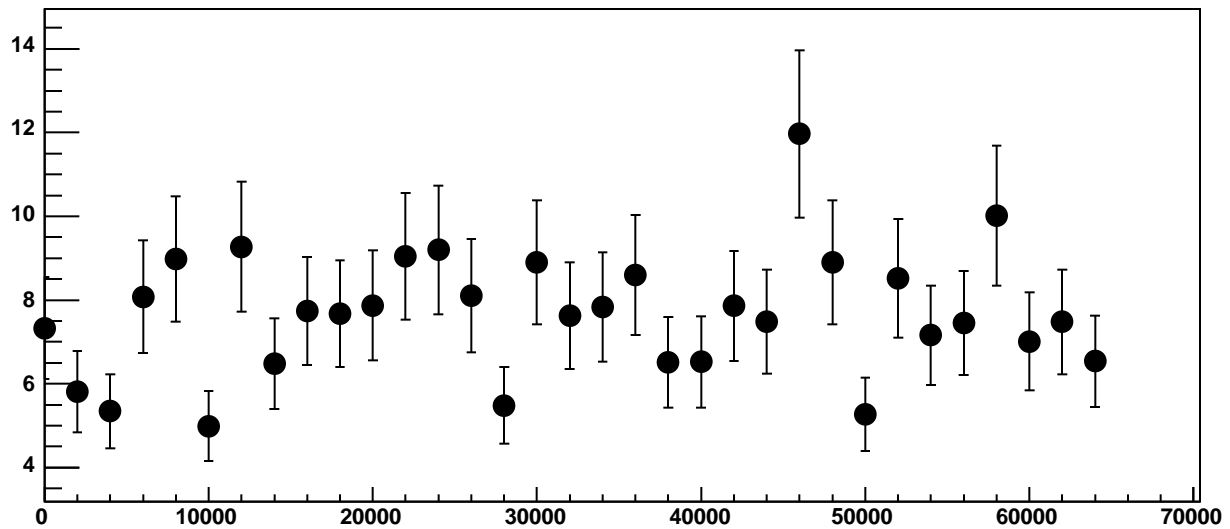
Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



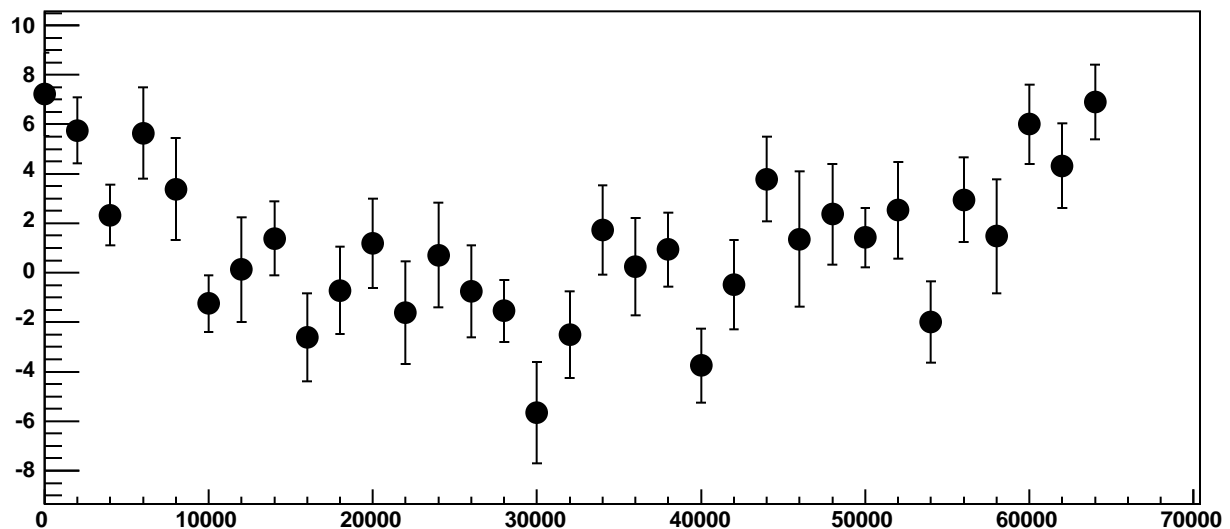
Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



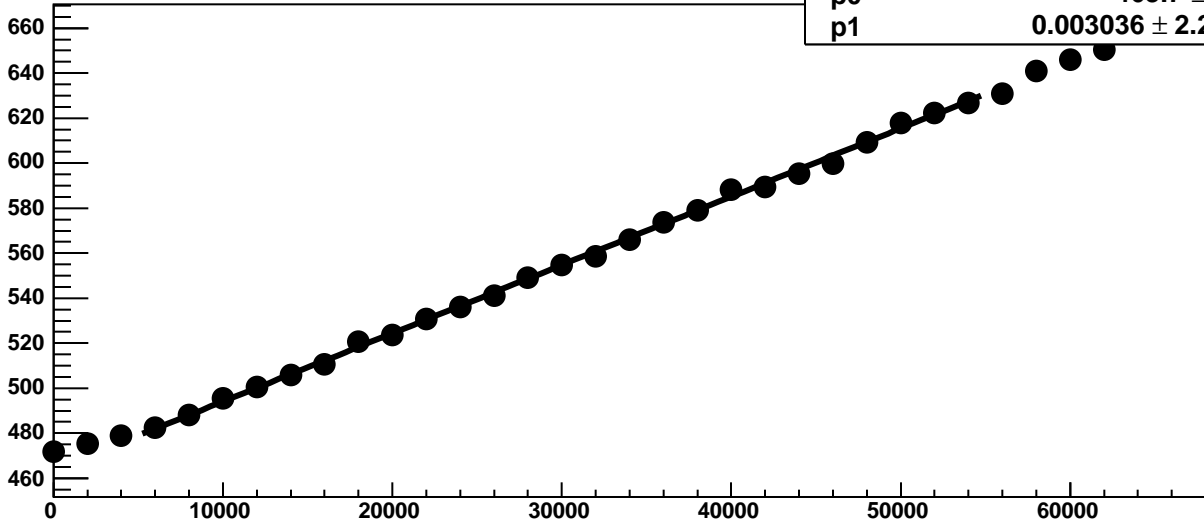
Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

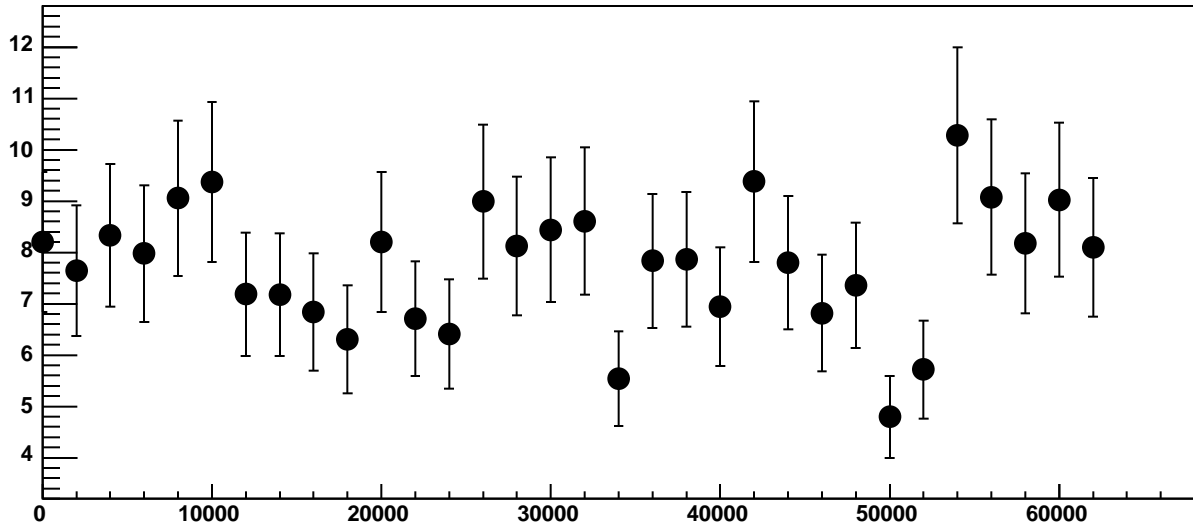


Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

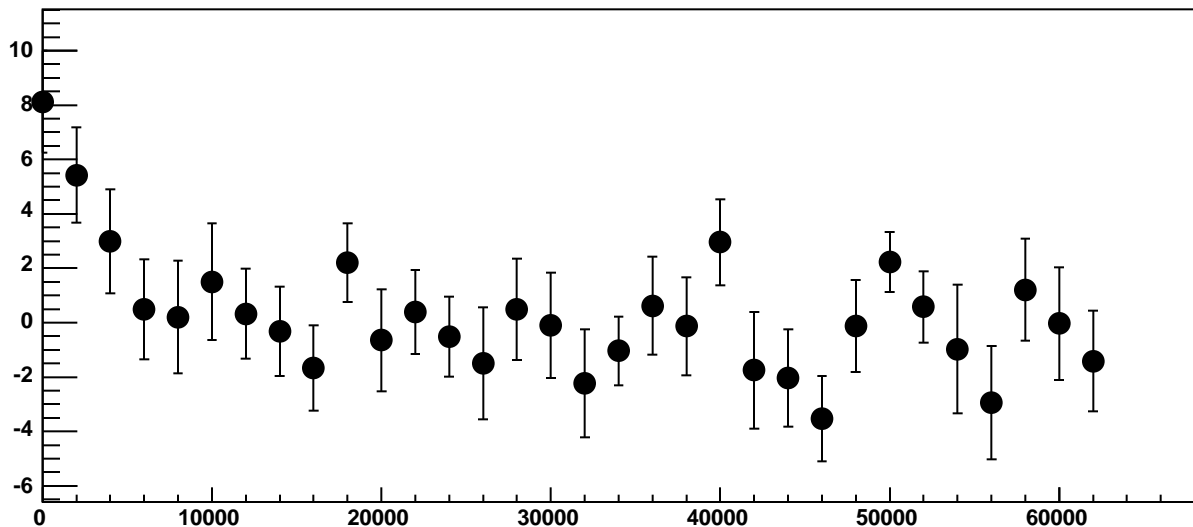


χ^2 / ndf 22.04 / 23
p0 463.7 ± 0.7958
p1 $0.003036 \pm 2.296e-05$

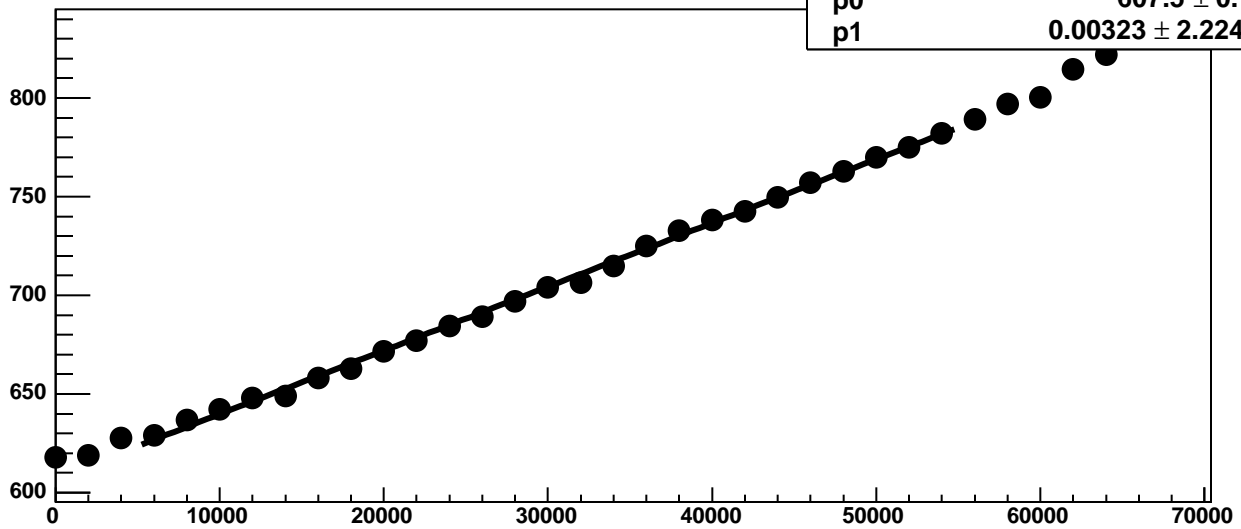
Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

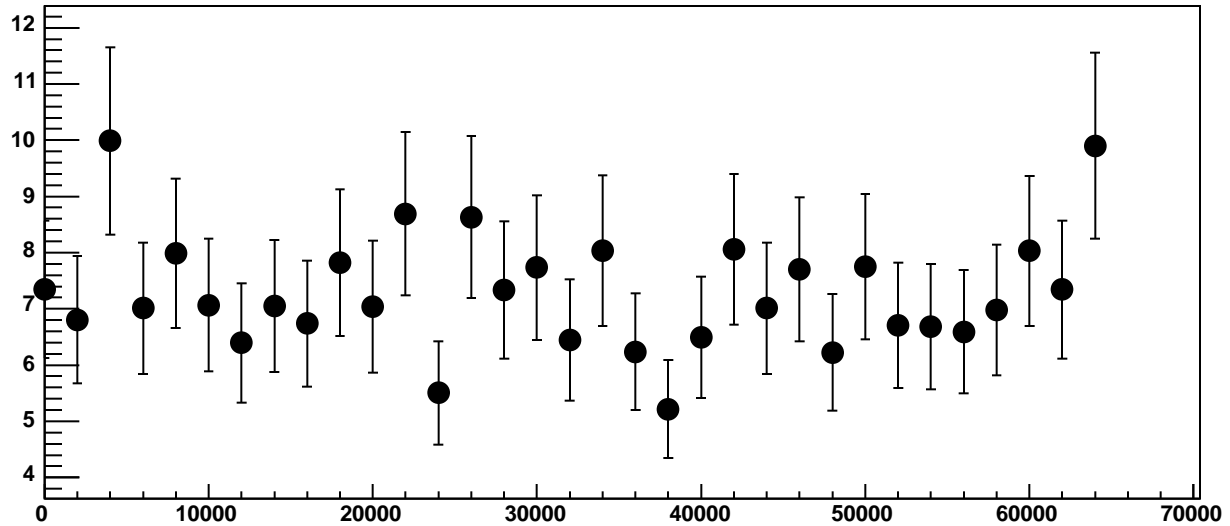


Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

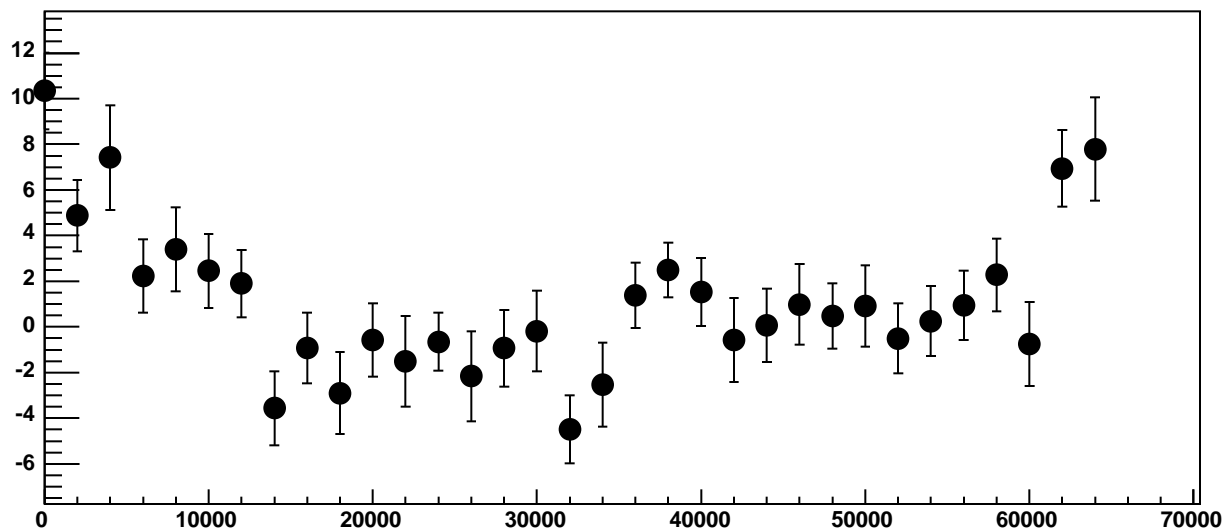


χ^2 / ndf 37.94 / 23
p0 607.5 ± 0.7493
p1 $0.00323 \pm 2.224e-05$

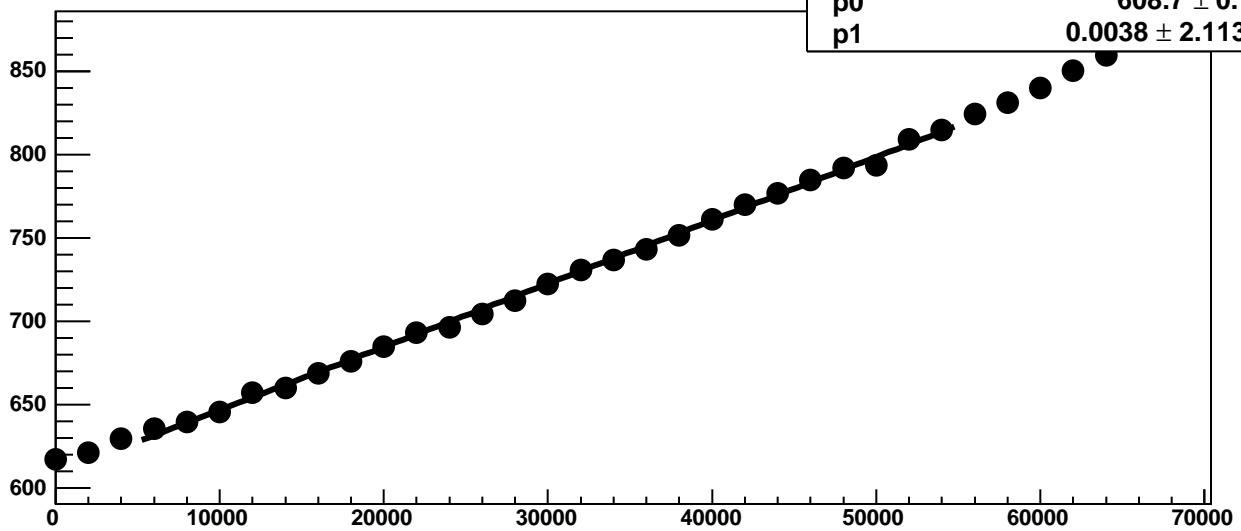
Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

44.9 / 23

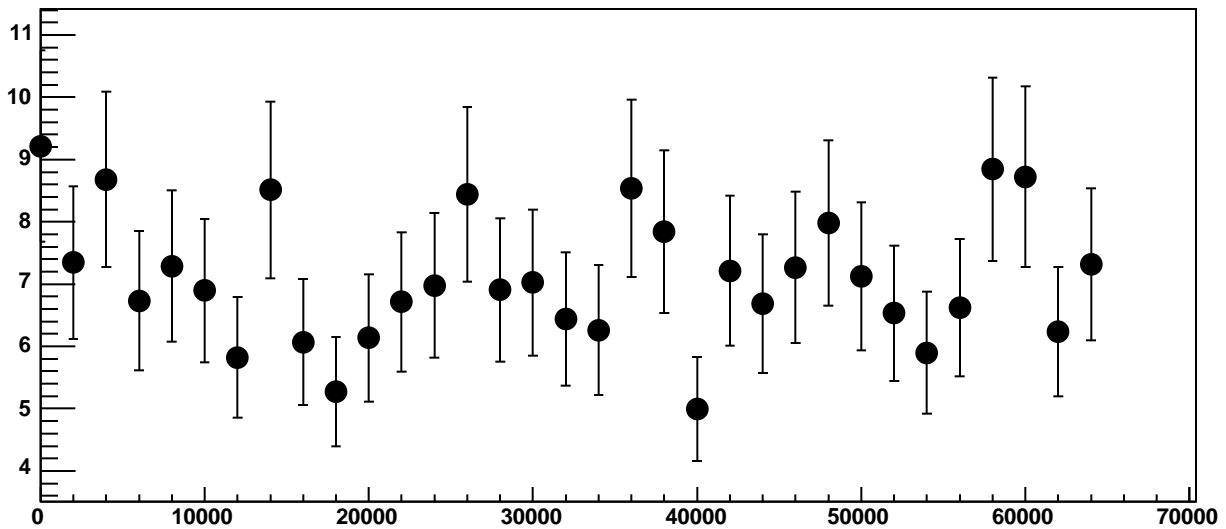
p0

608.7 ± 0.7007

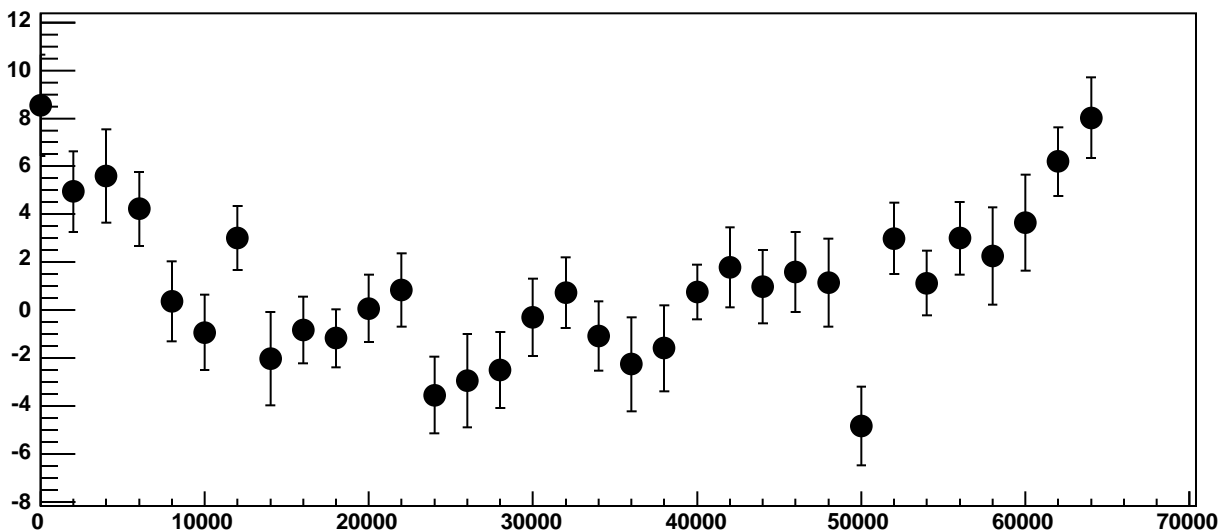
p1

$0.0038 \pm 2.113e-05$

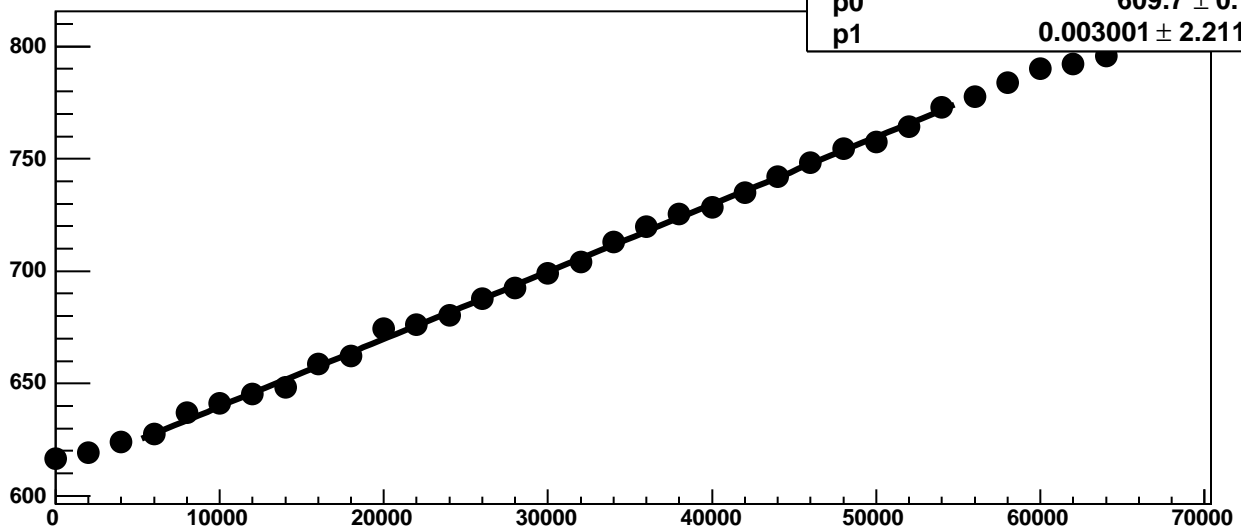
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

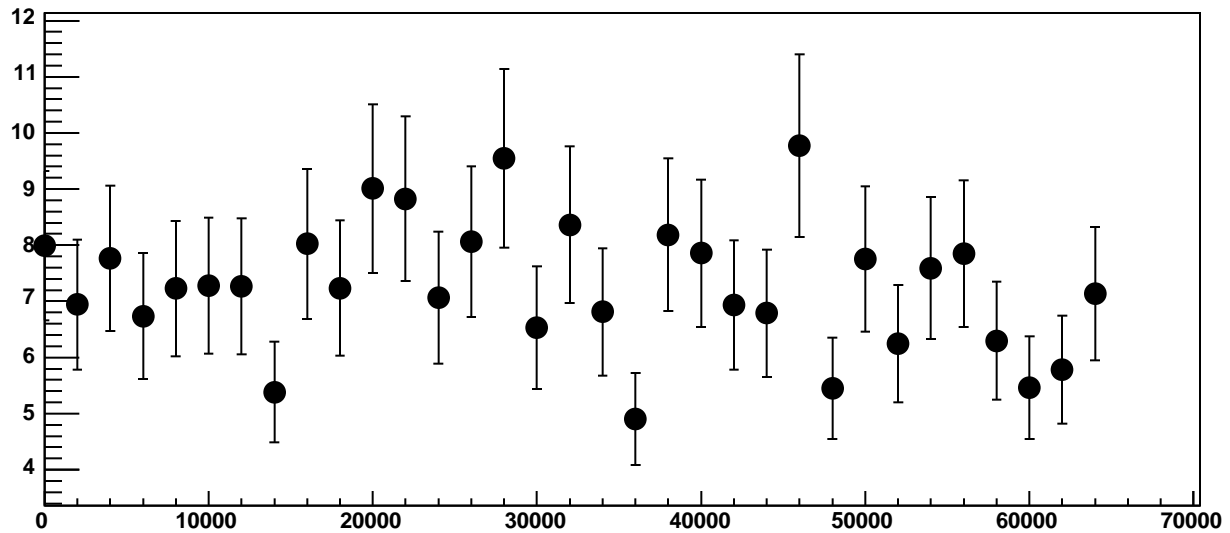


Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

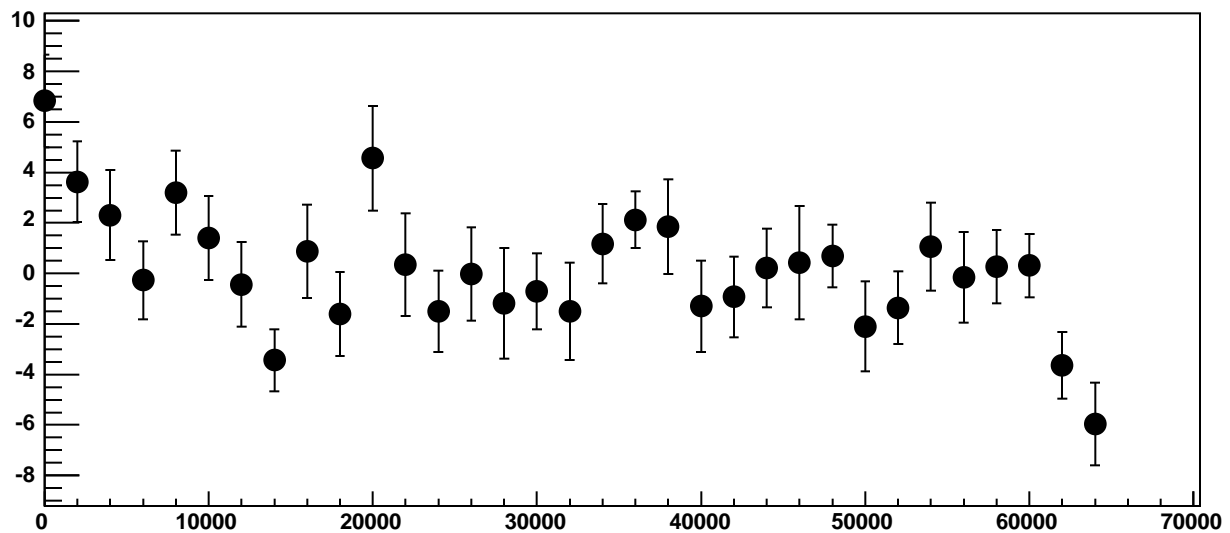


χ^2 / ndf 29.32 / 23
p0 609.7 ± 0.7463
p1 $0.003001 \pm 2.211e-05$

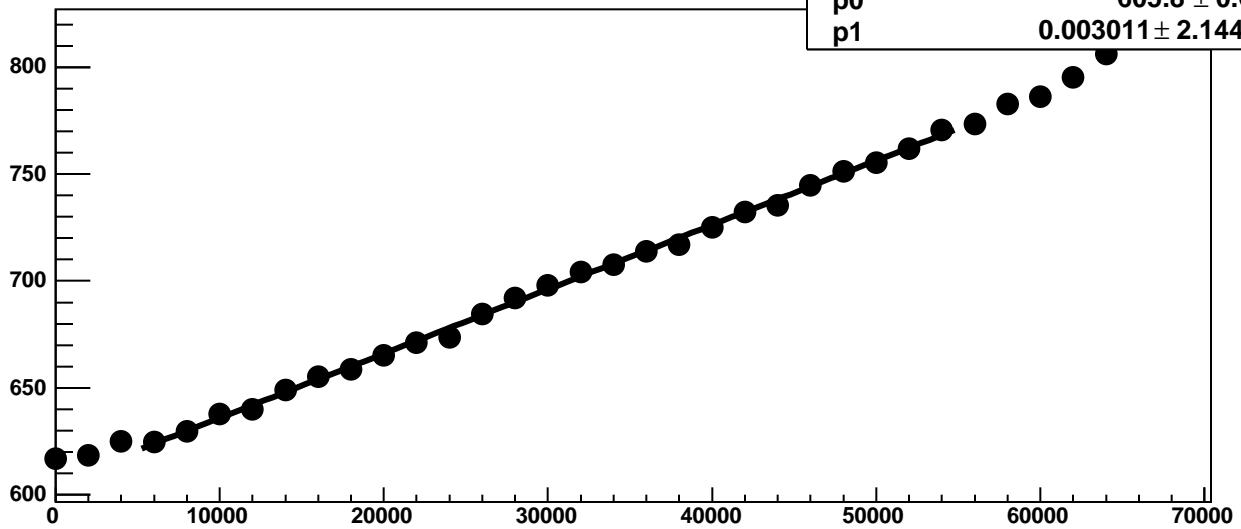
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.74 / 23

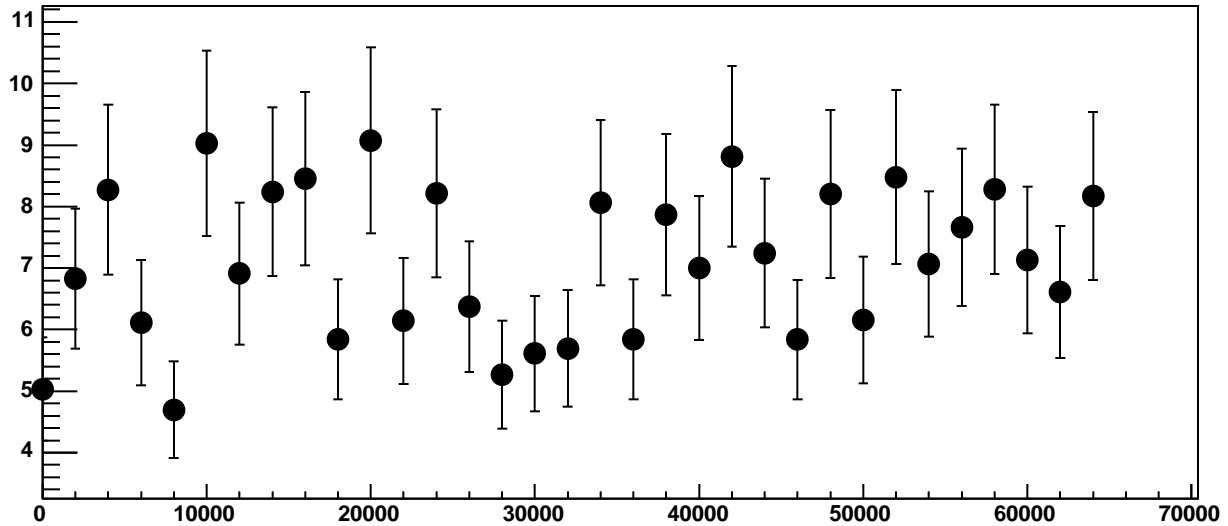
p0

605.8 ± 0.6944

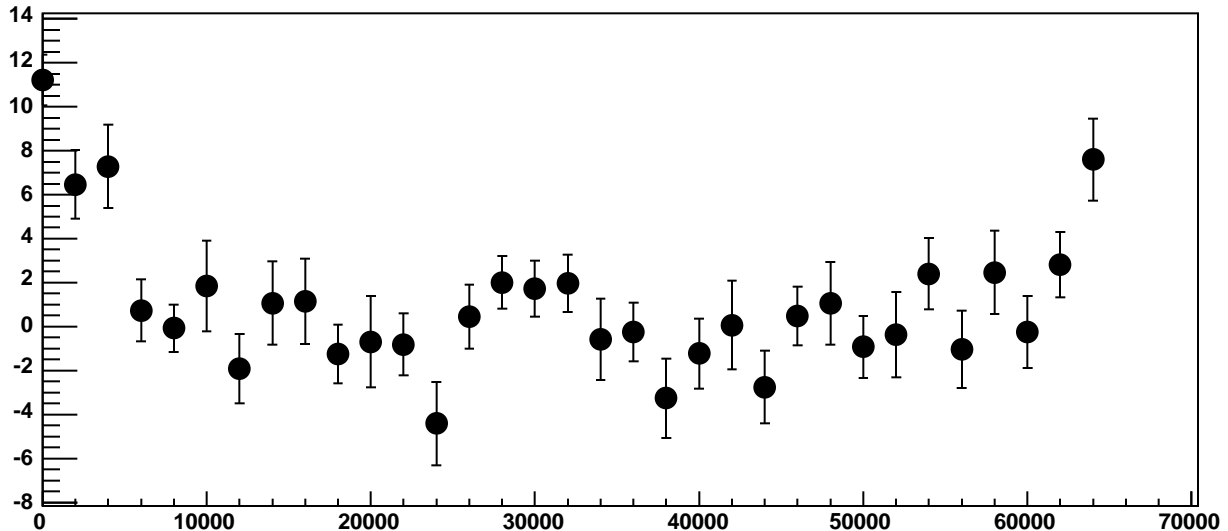
p1

$0.003011 \pm 2.144e-05$

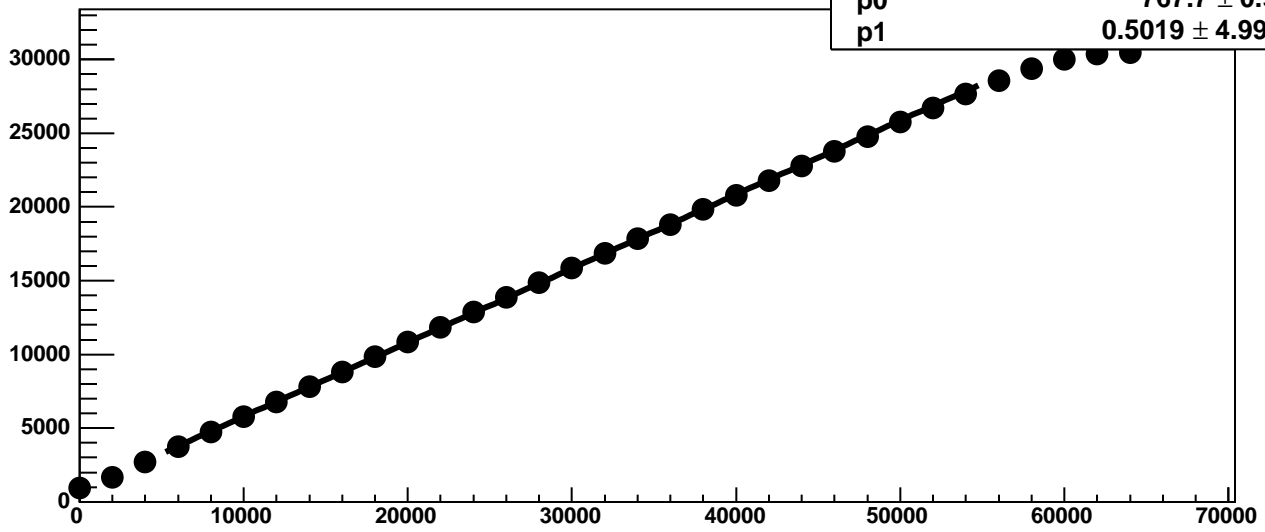
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



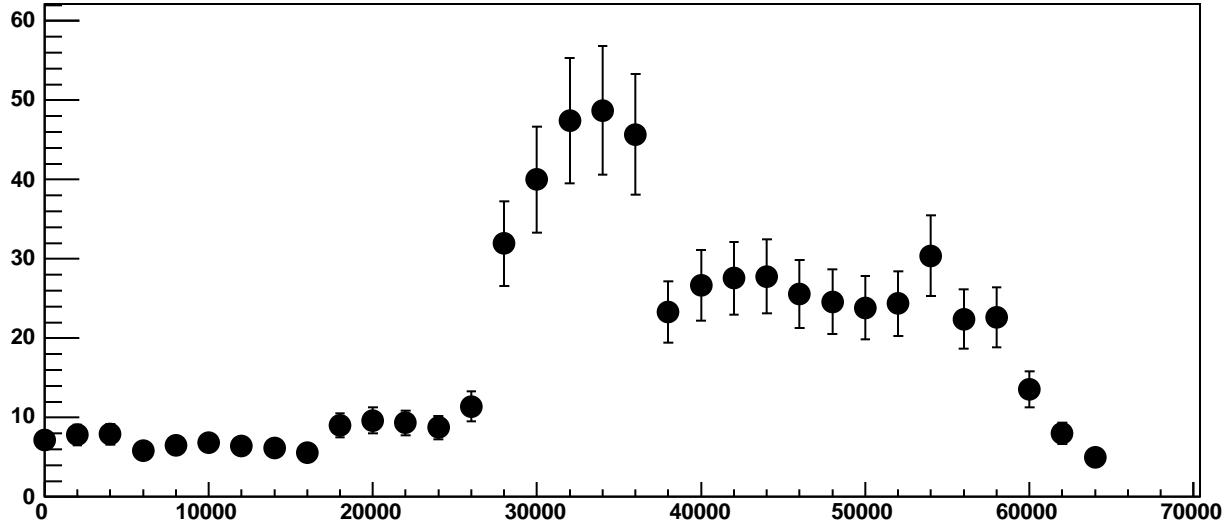
Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



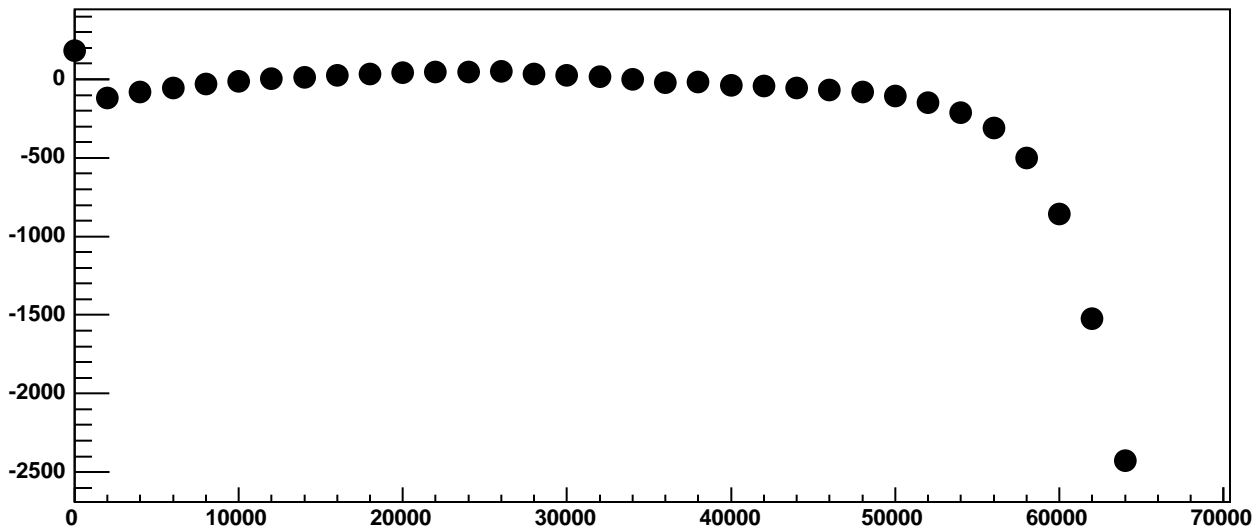
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



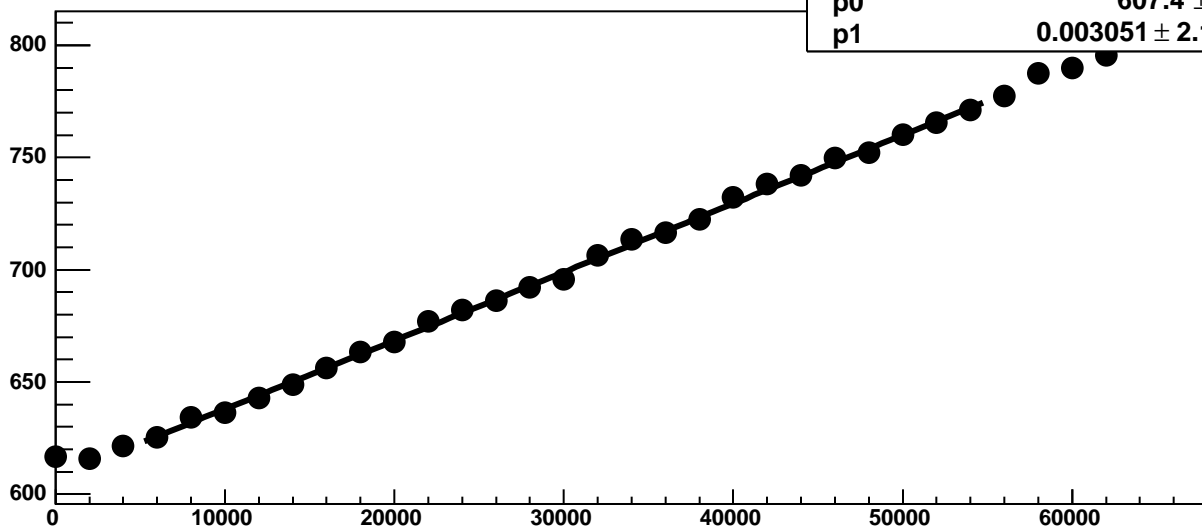
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

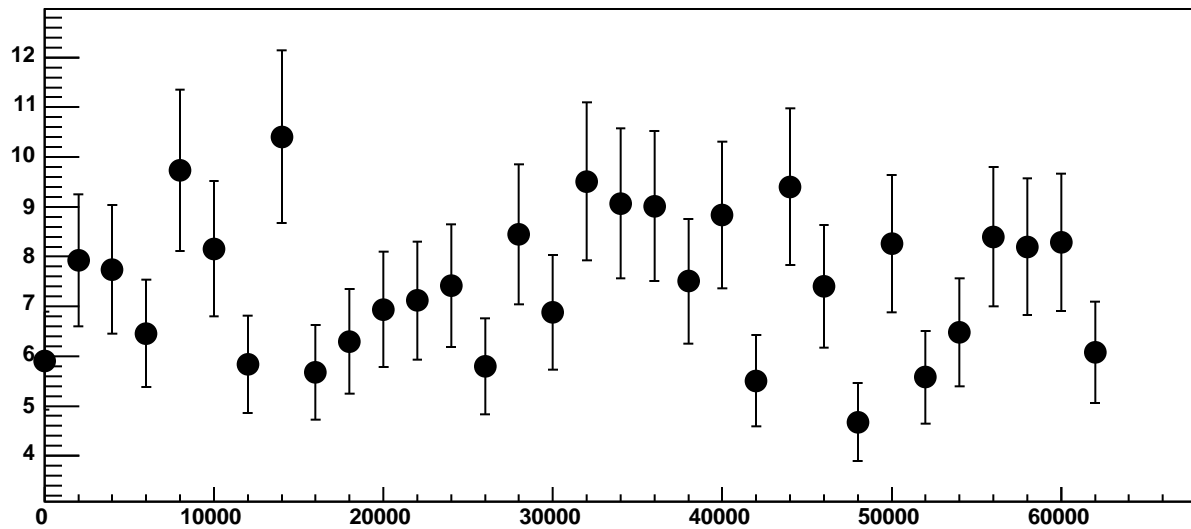


Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

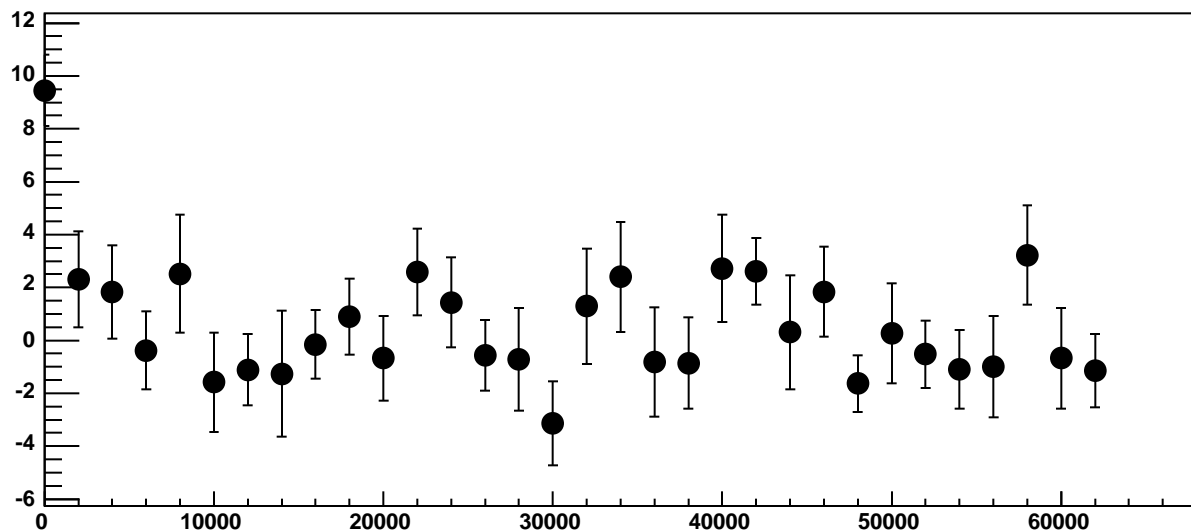


χ^2 / ndf 23.52 / 23
p0 607.4 ± 0.7359
p1 $0.003051 \pm 2.137e-05$

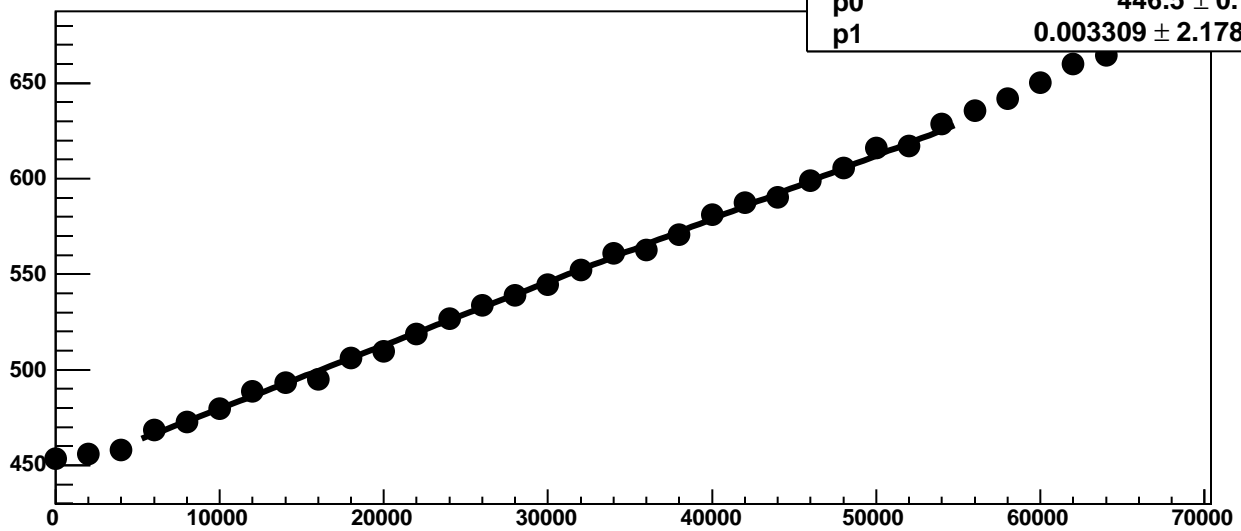
Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

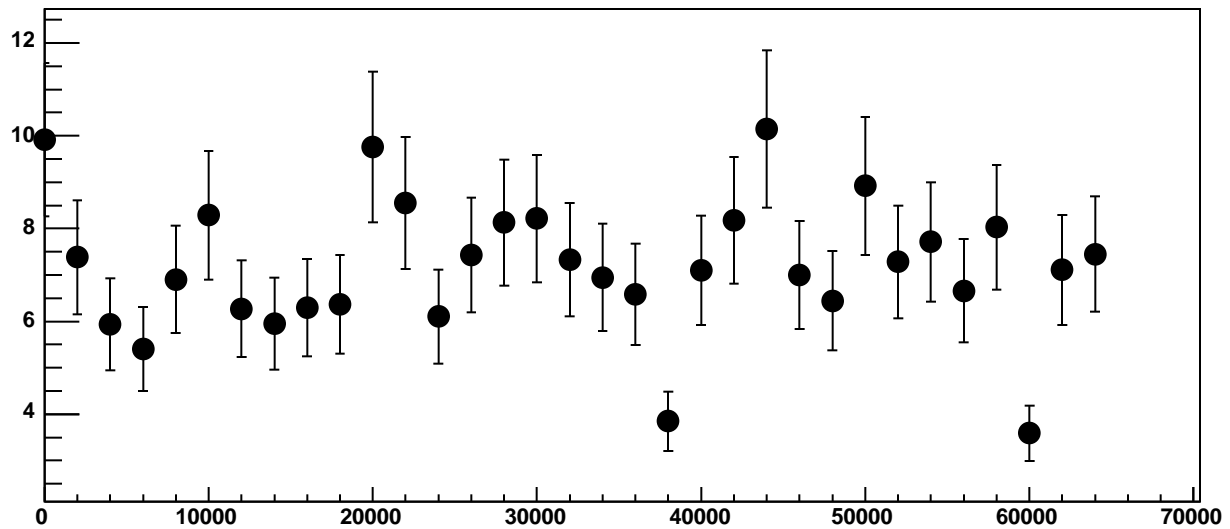


Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

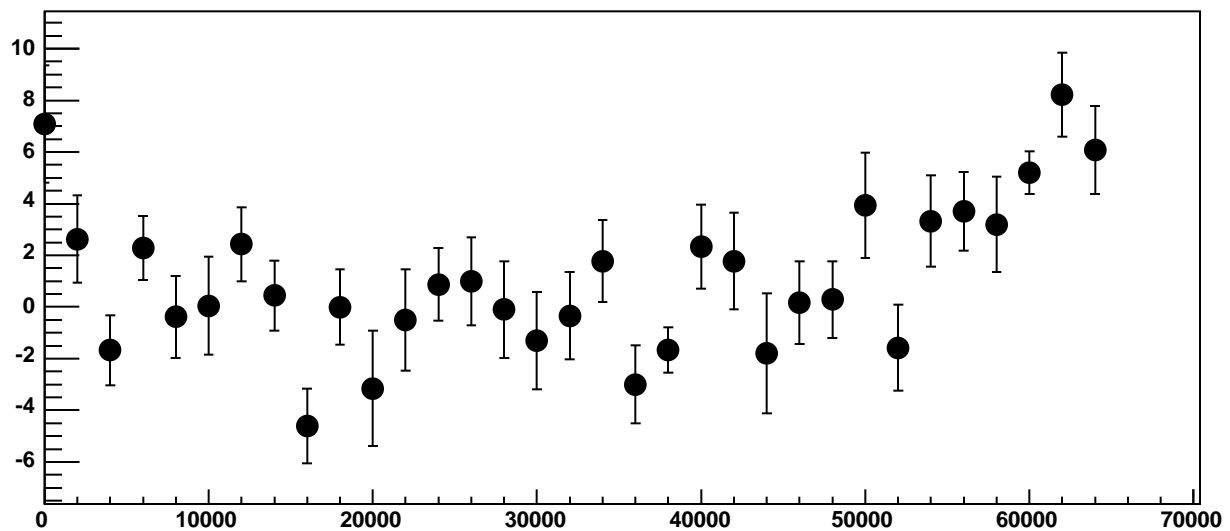


χ^2 / ndf 40.33 / 23
p0 446.5 ± 0.7072
p1 0.003309 ± 2.178e-05

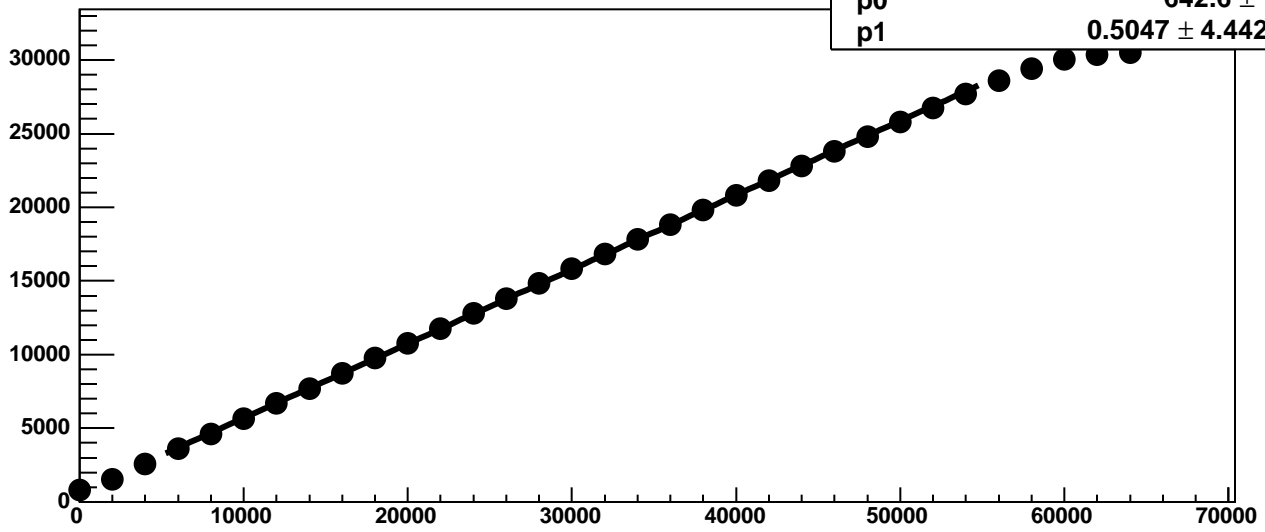
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



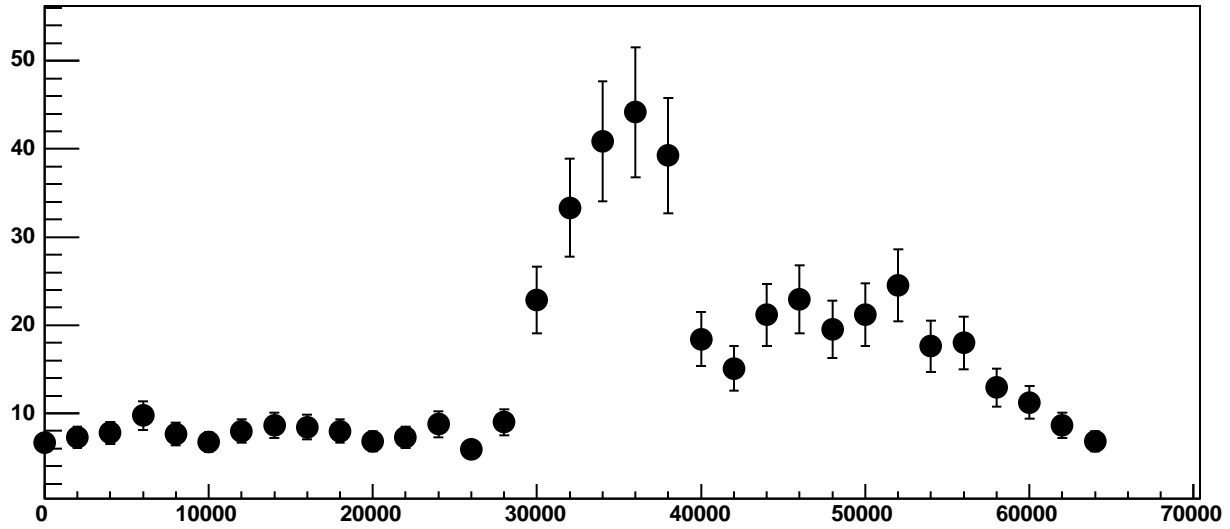
Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



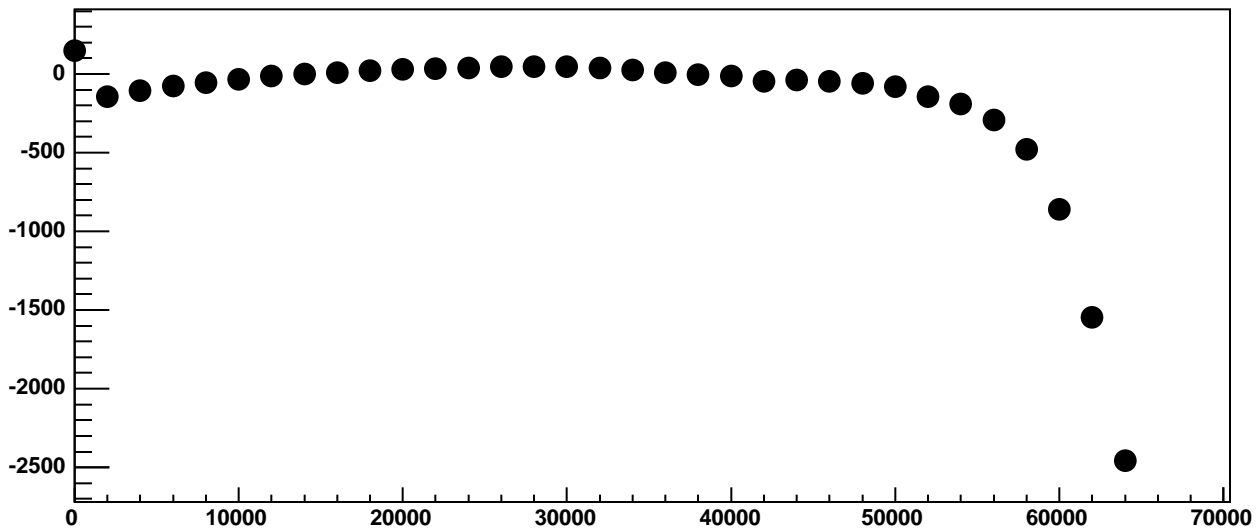
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



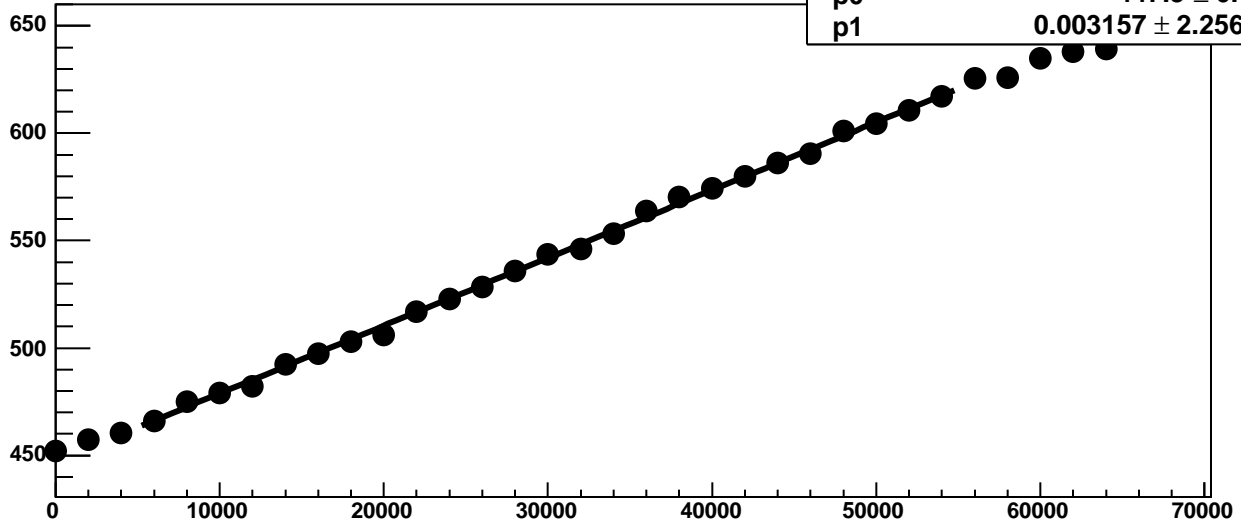
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

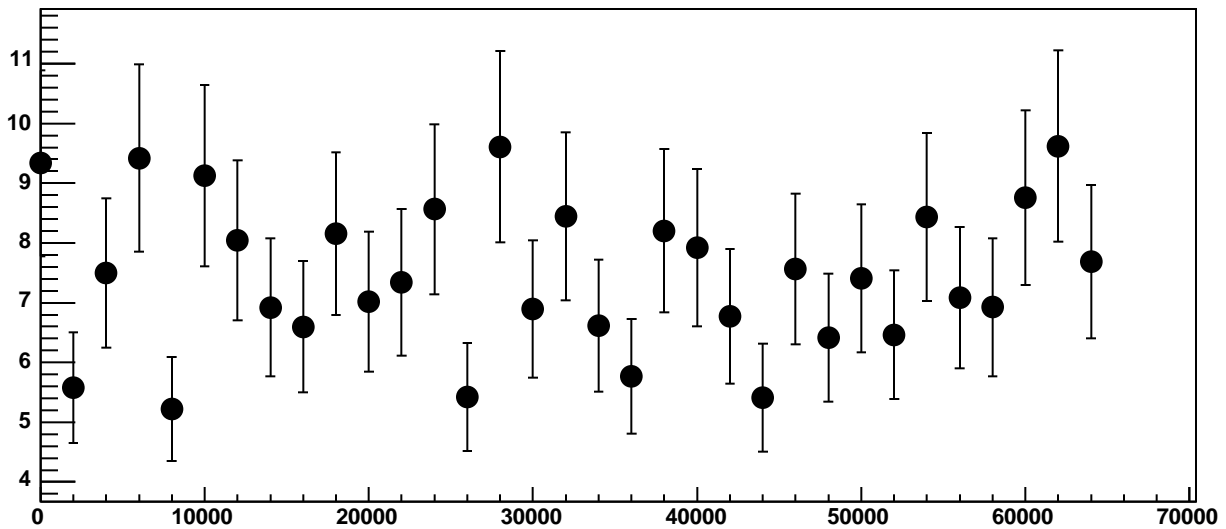


Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

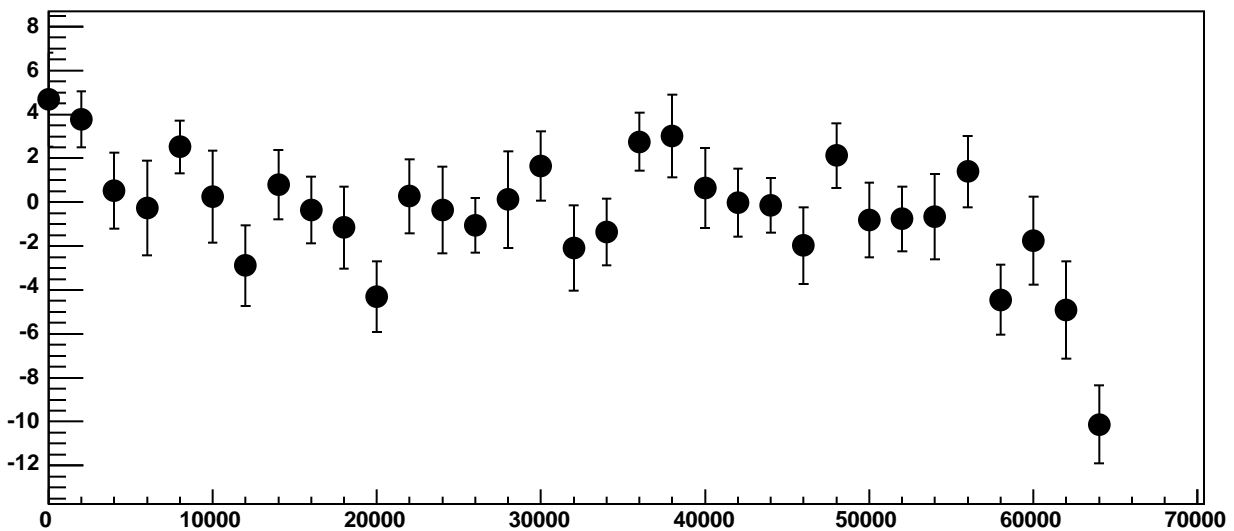


χ^2 / ndf 29.64 / 23
p0 447.3 ± 0.7595
p1 $0.003157 \pm 2.256e-05$

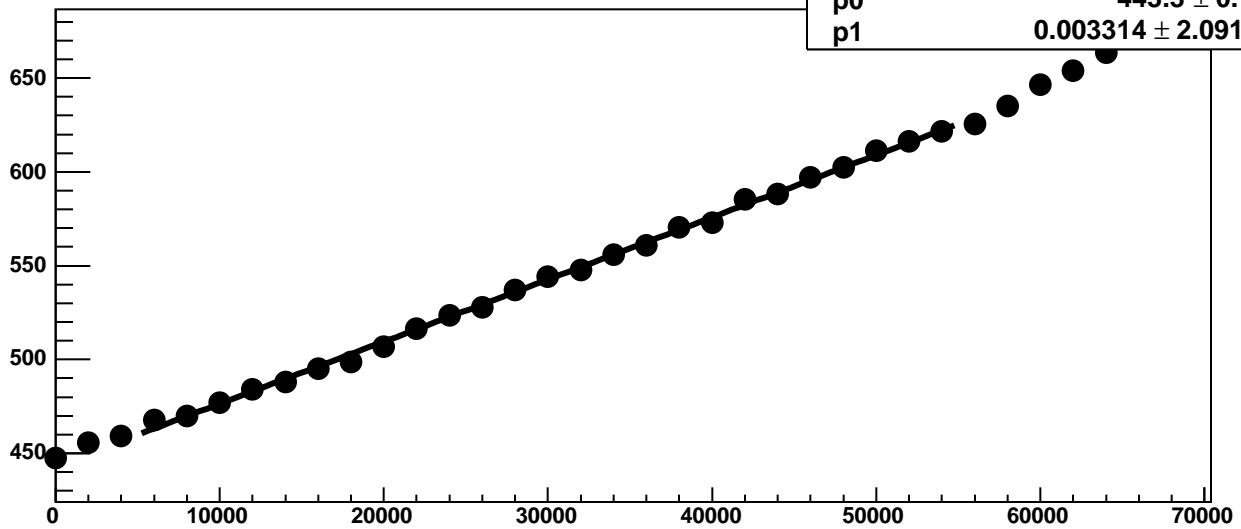
Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

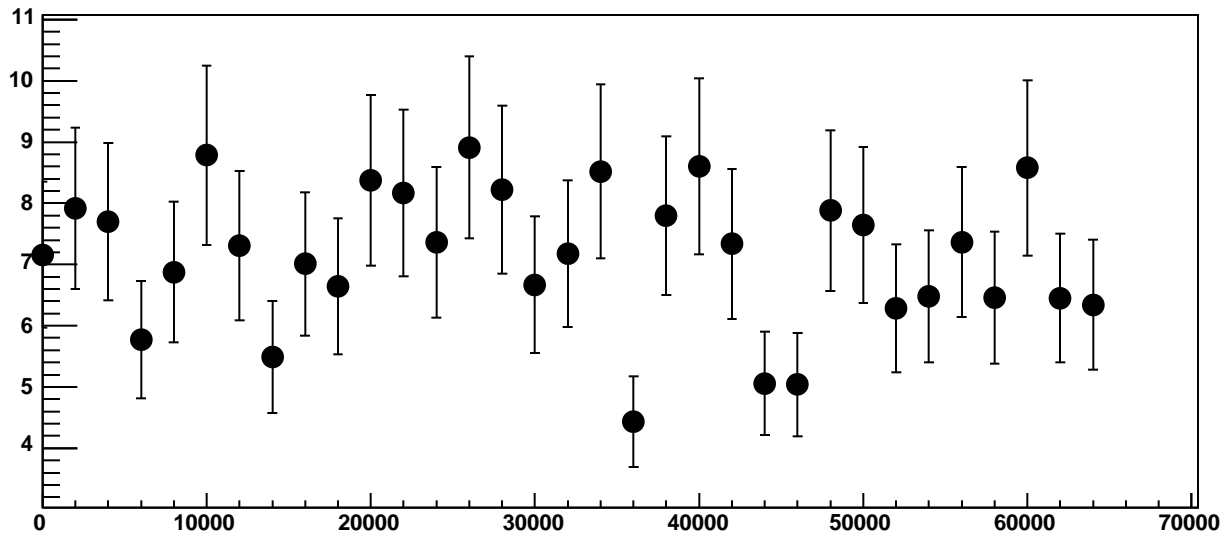


Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

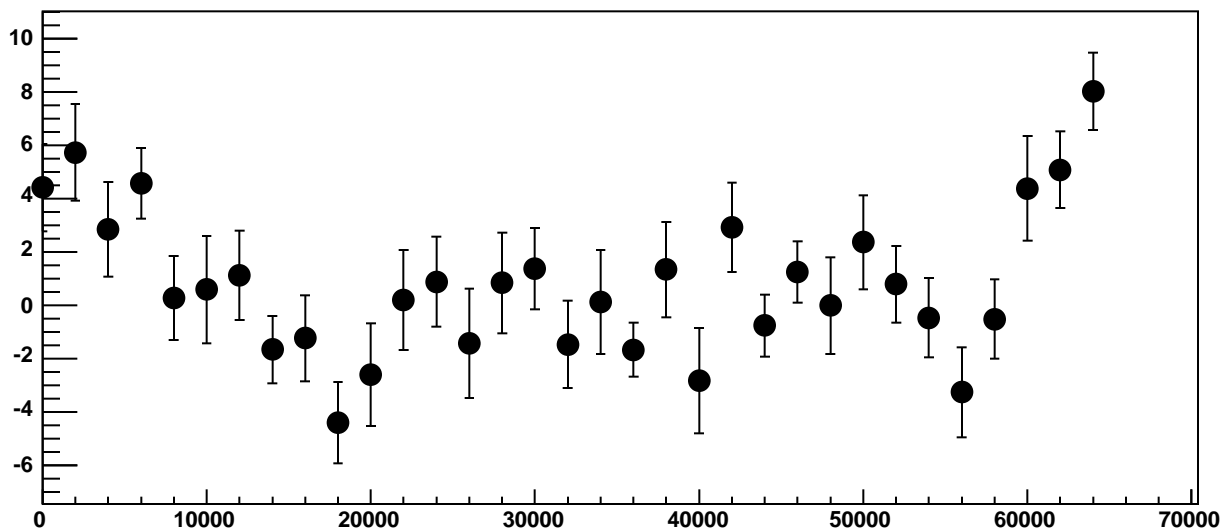


χ^2 / ndf 39.68 / 23
p0 443.3 ± 0.7167
p1 $0.003314 \pm 2.091\text{e-}05$

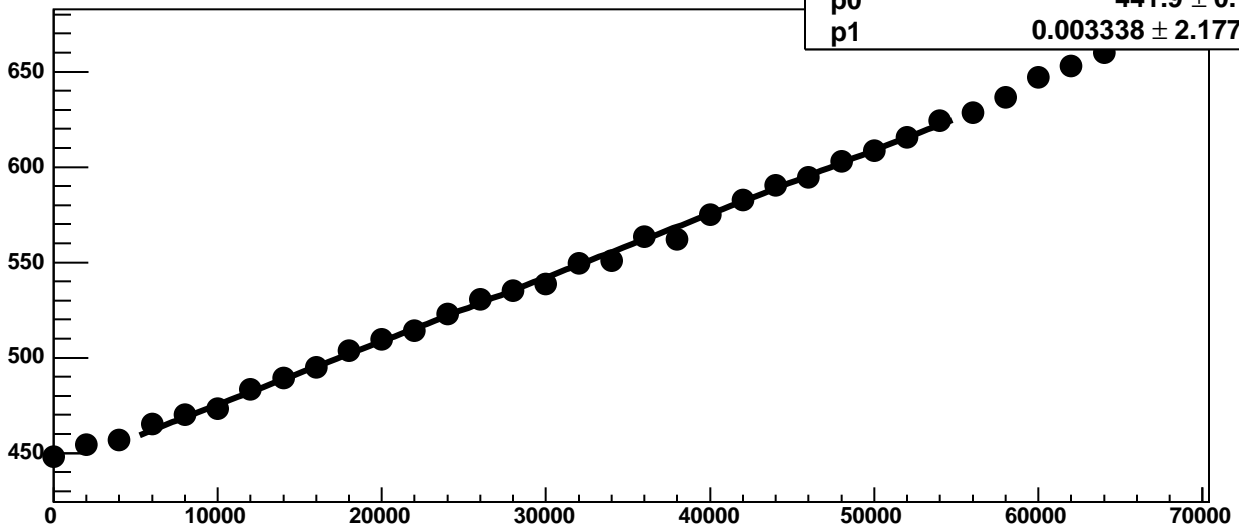
Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

48.68 / 23

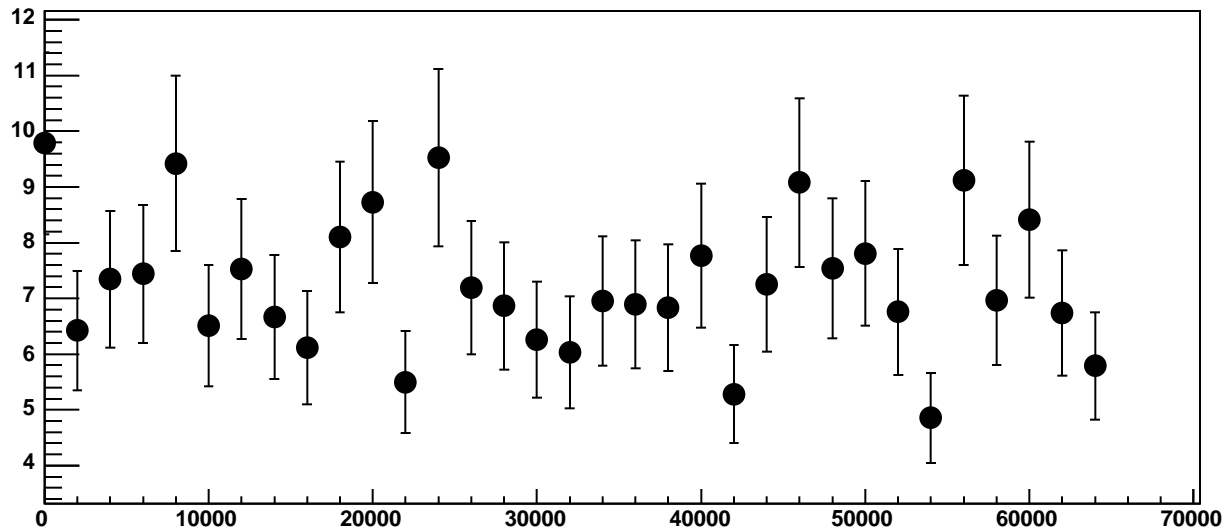
p0

441.9 ± 0.7497

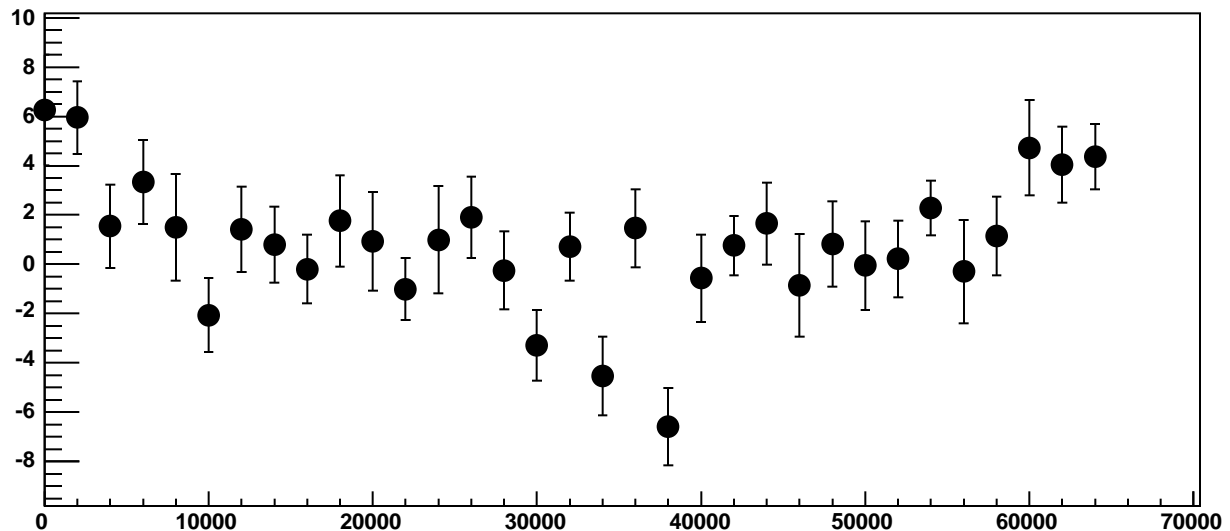
p1

$0.003338 \pm 2.177\text{e-}05$

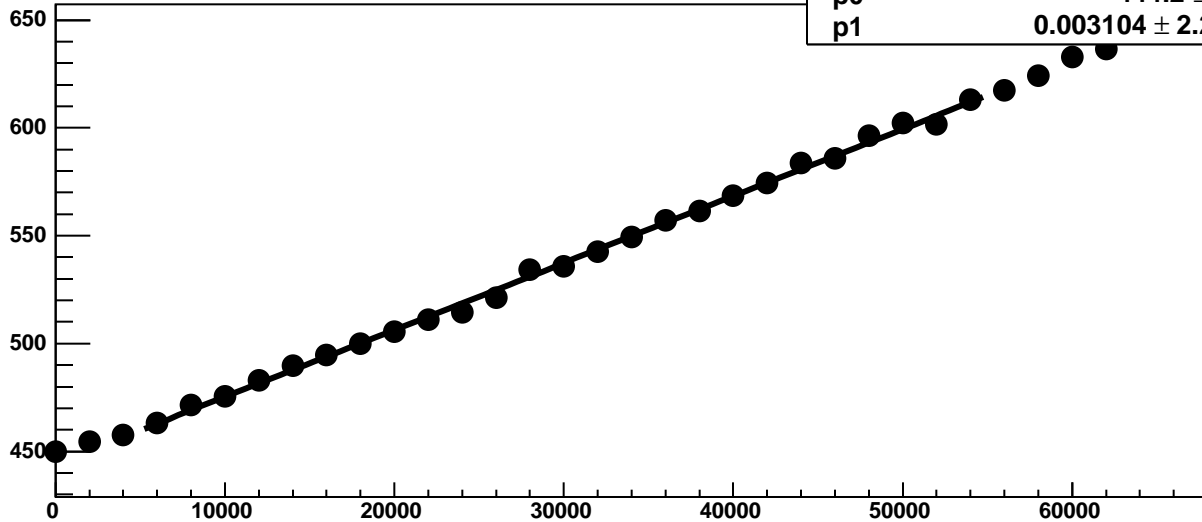
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

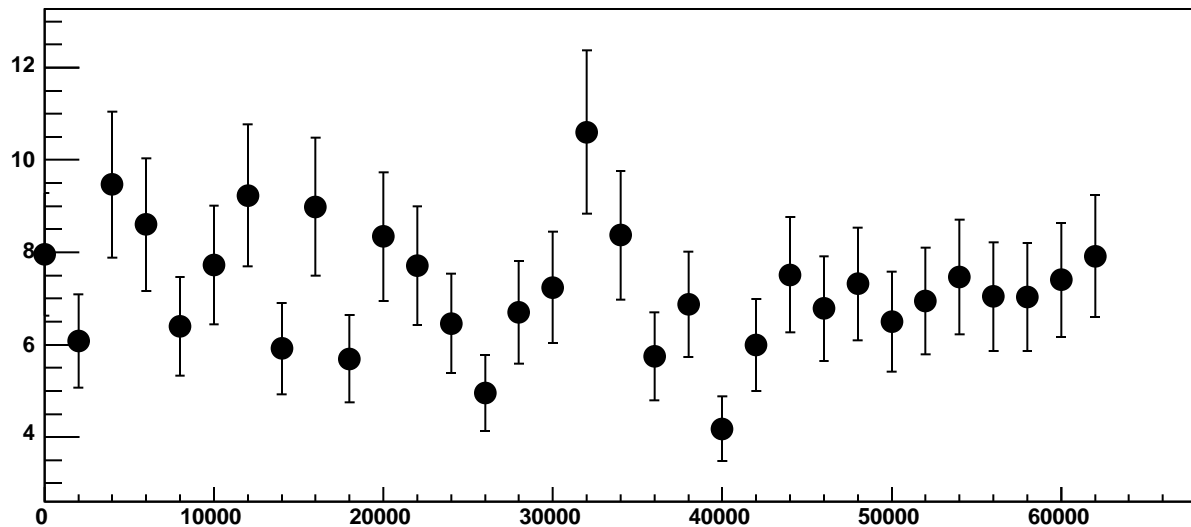


Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

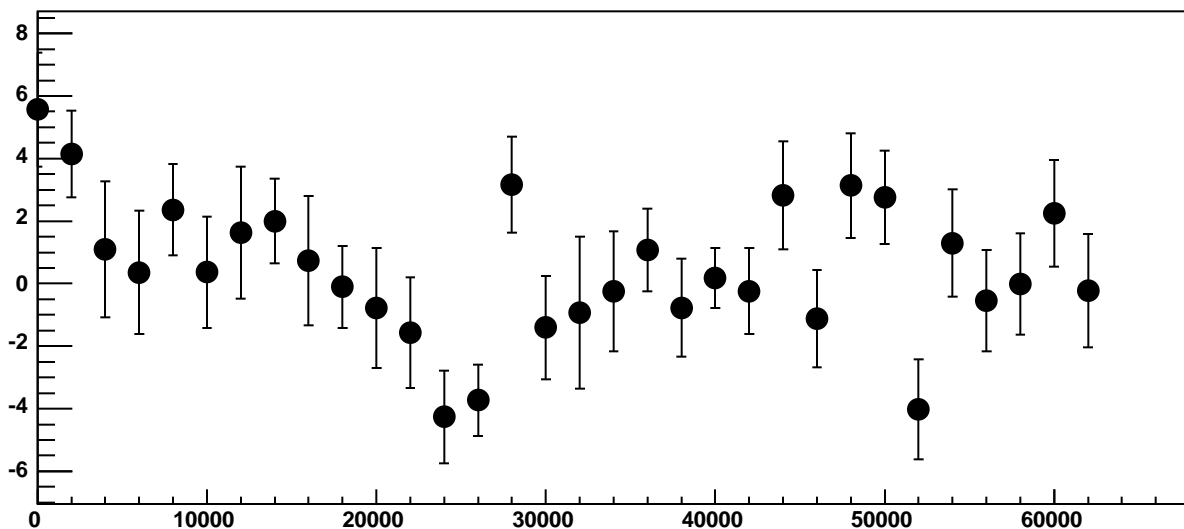


χ^2 / ndf 48.69 / 23
p0 444.2 ± 0.7598
p1 0.003104 ± 2.237e-05

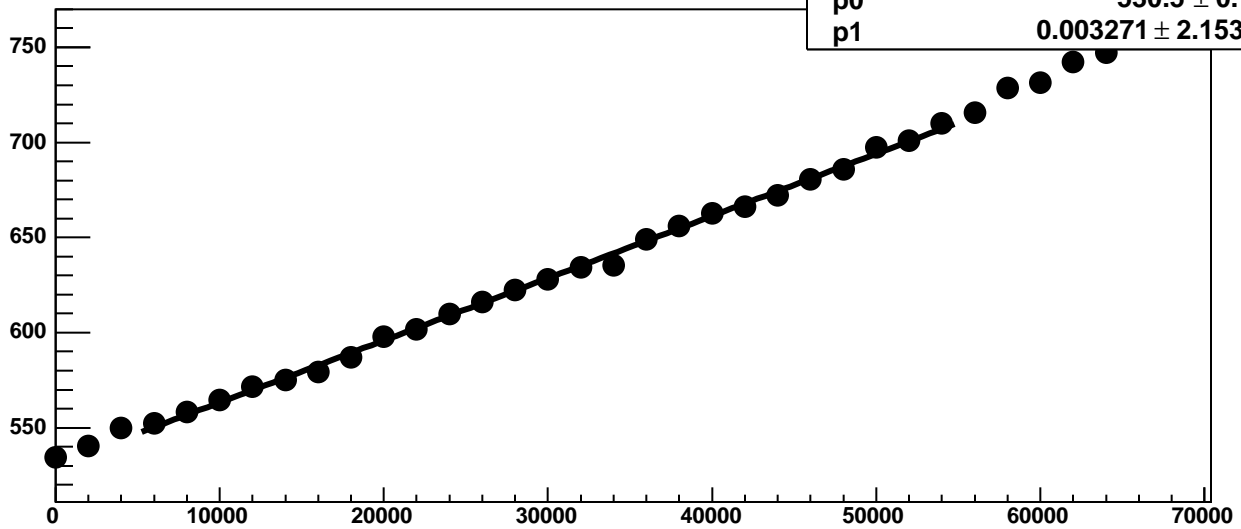
Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

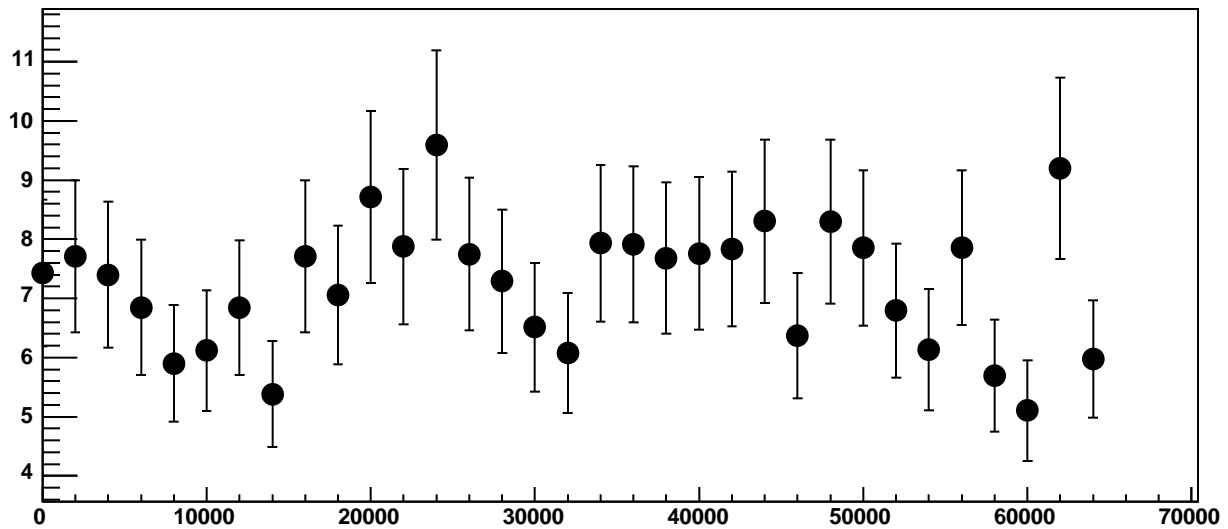


Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

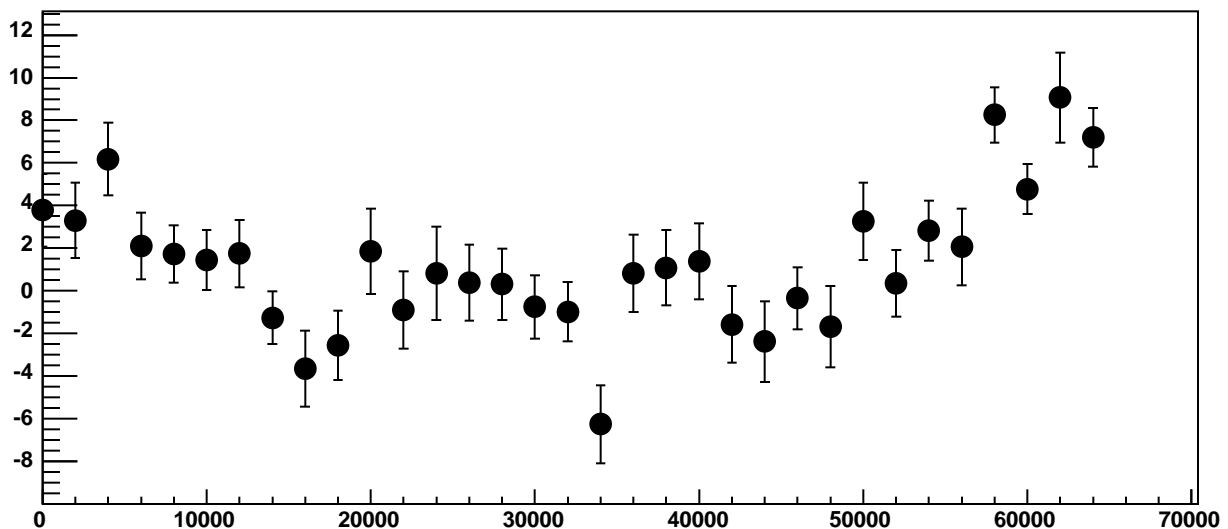


χ^2 / ndf 39.09 / 23
p0 530.5 ± 0.7036
p1 $0.003271 \pm 2.153e-05$

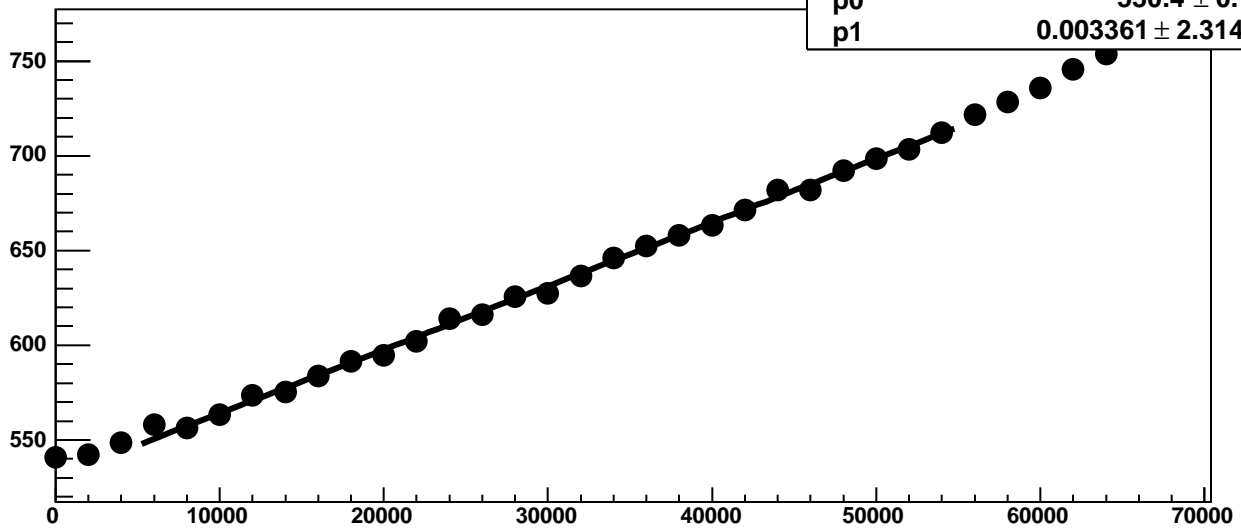
Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

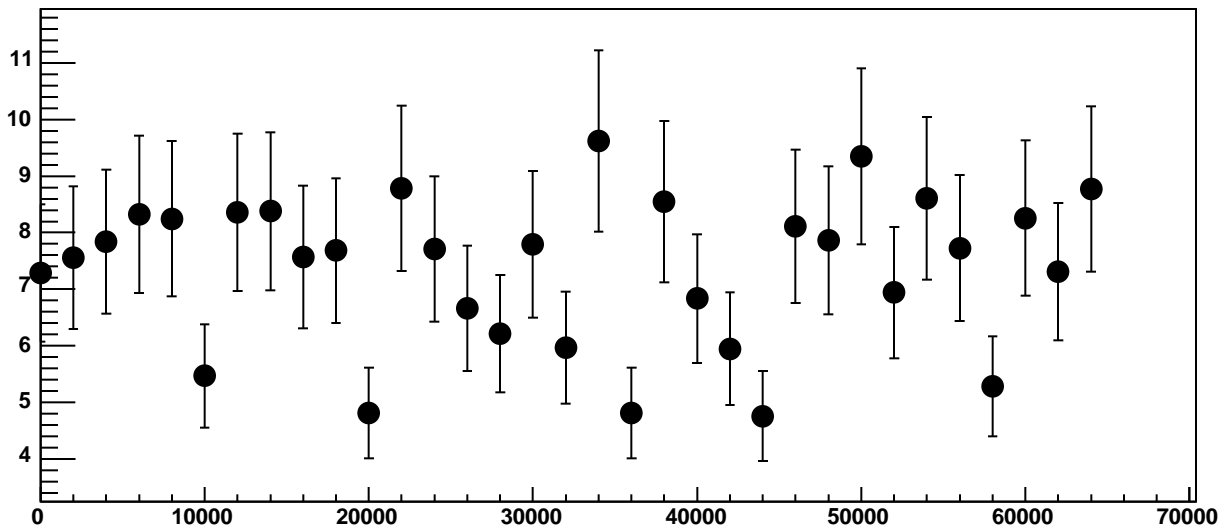


Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

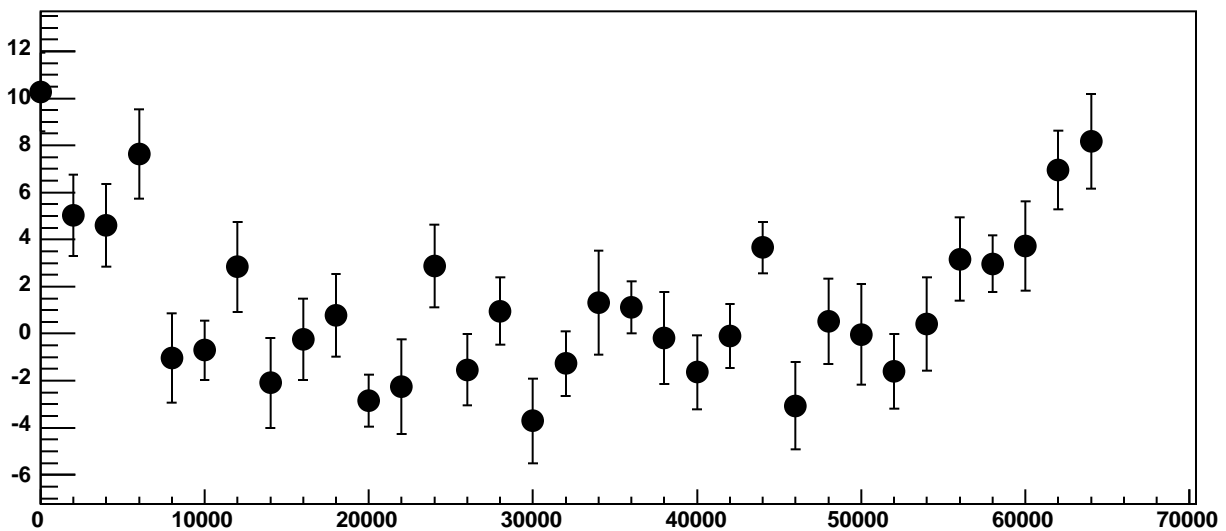


χ^2 / ndf 54.9 / 23
p0 530.4 ± 0.7678
p1 $0.003361 \pm 2.314e-05$

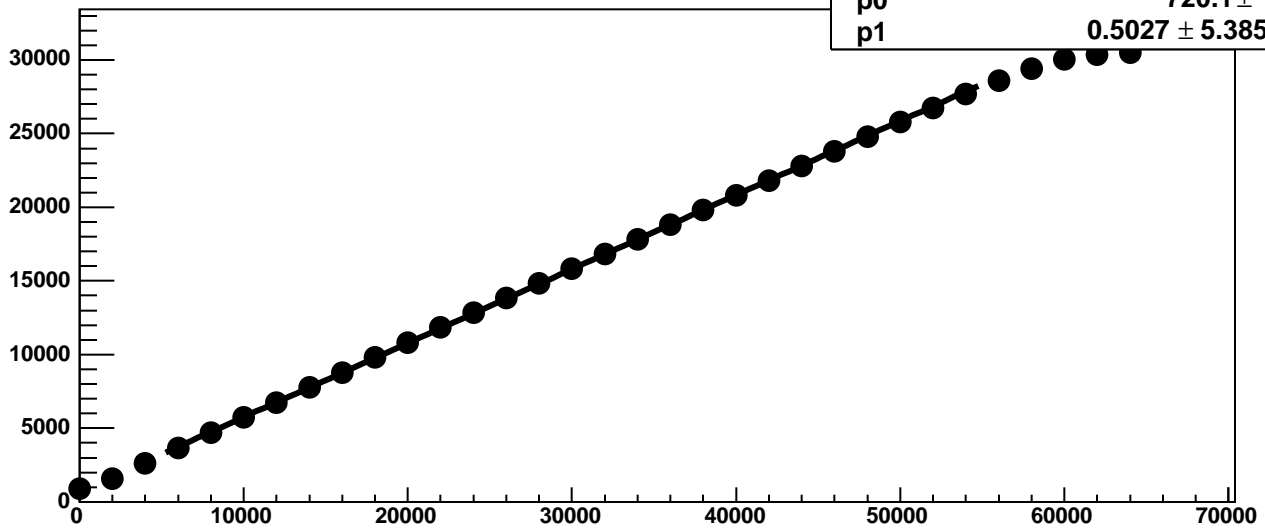
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



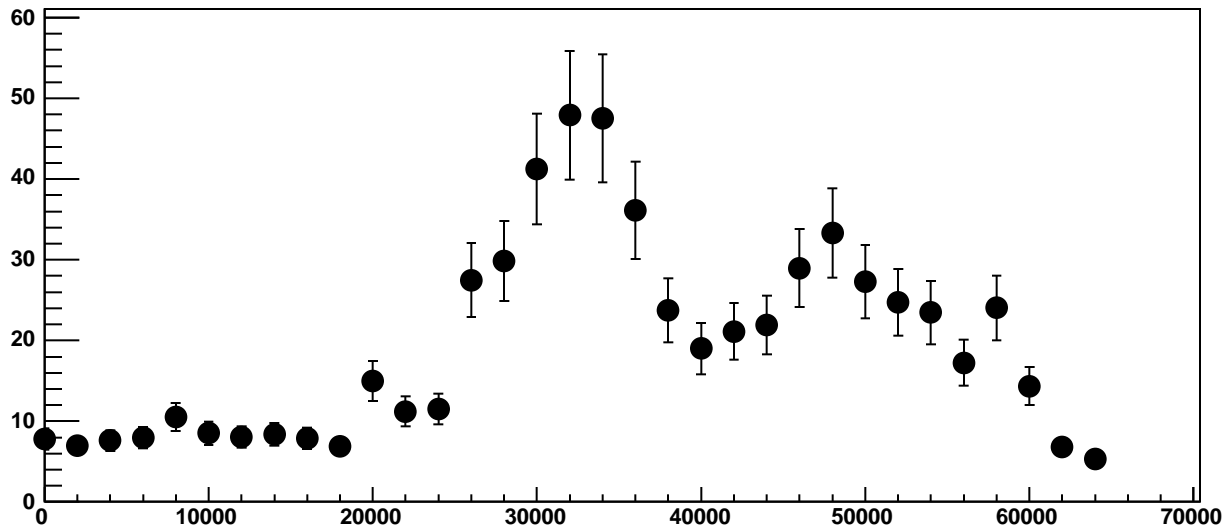
Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



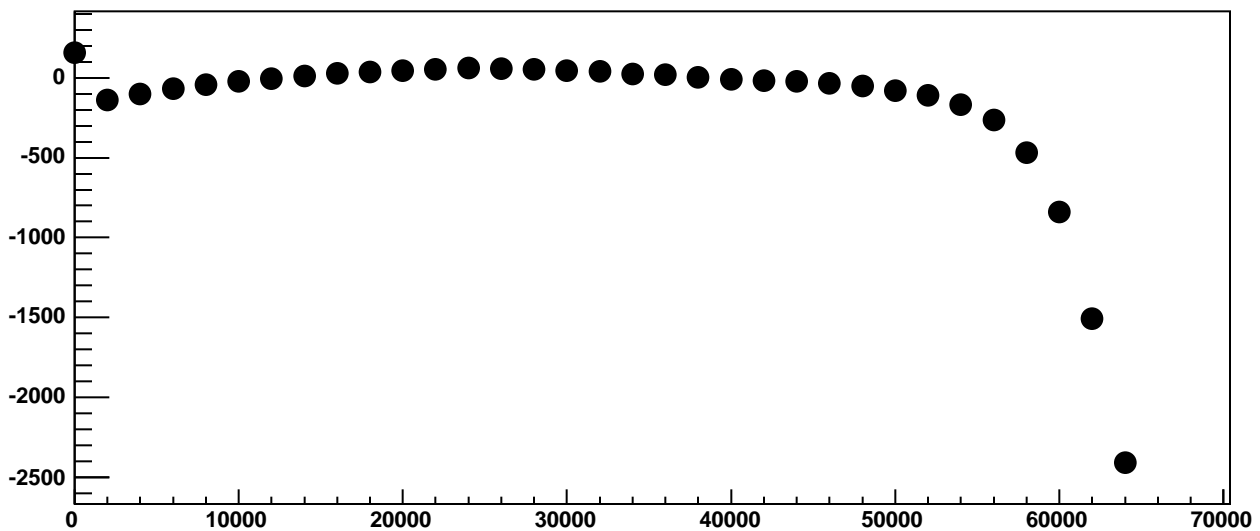
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



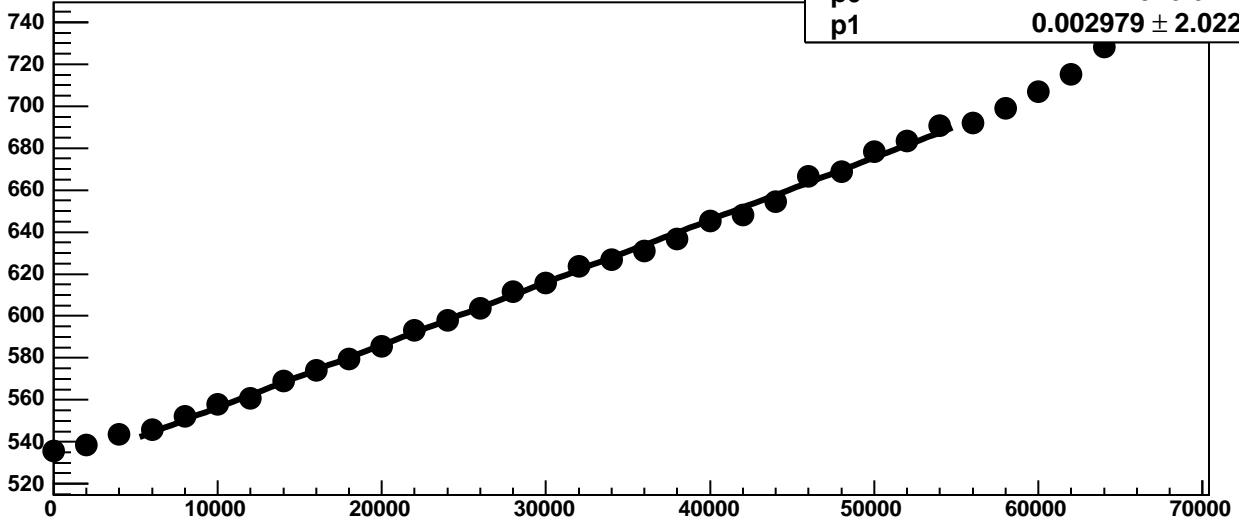
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

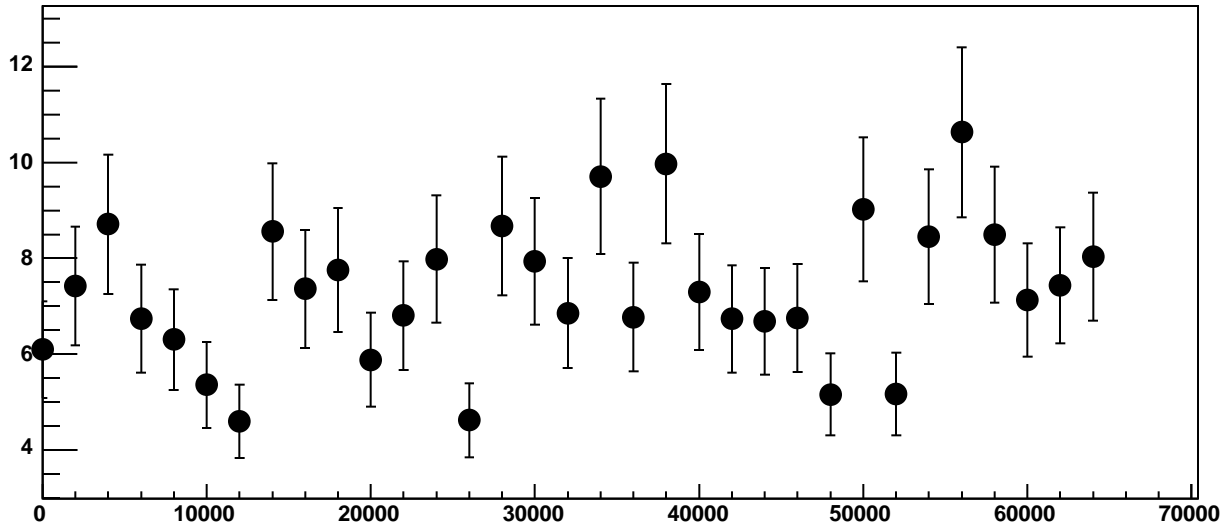


Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

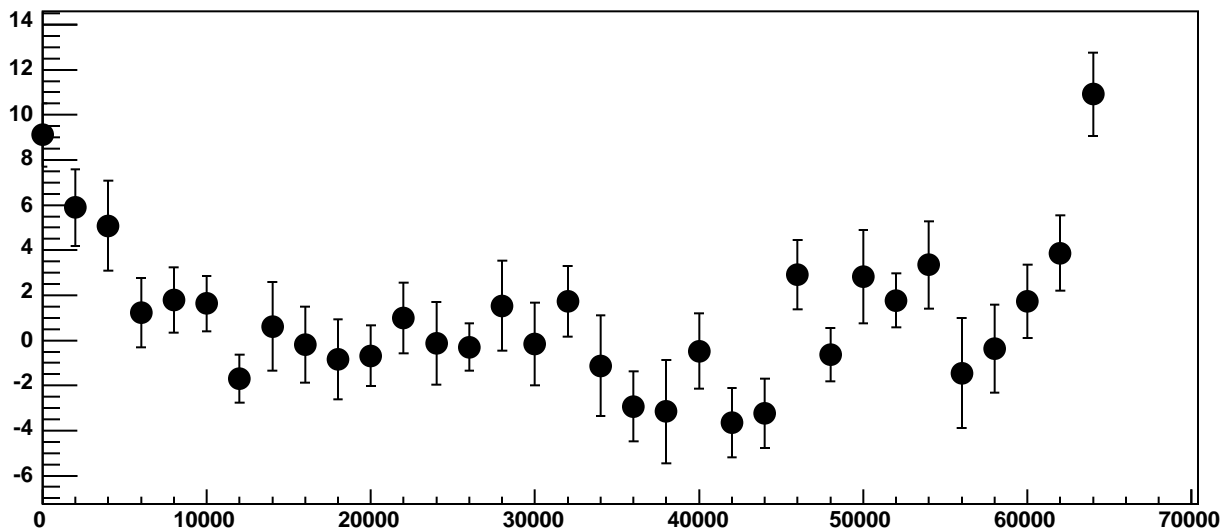


χ^2 / ndf 36.12 / 23
p0 526.6 ± 0.66
p1 $0.002979 \pm 2.022e-05$

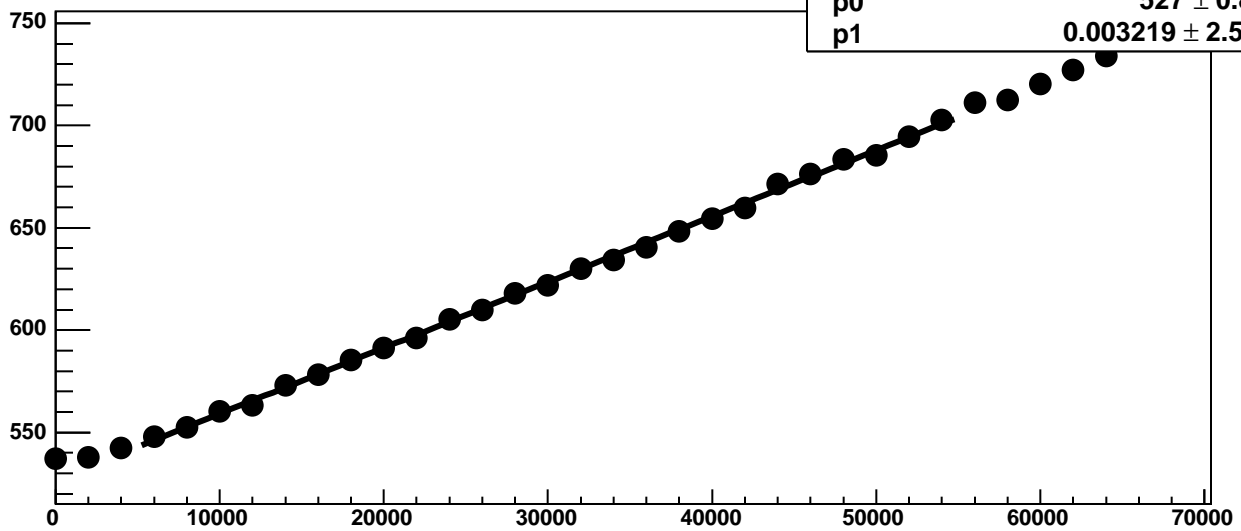
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.03 / 23

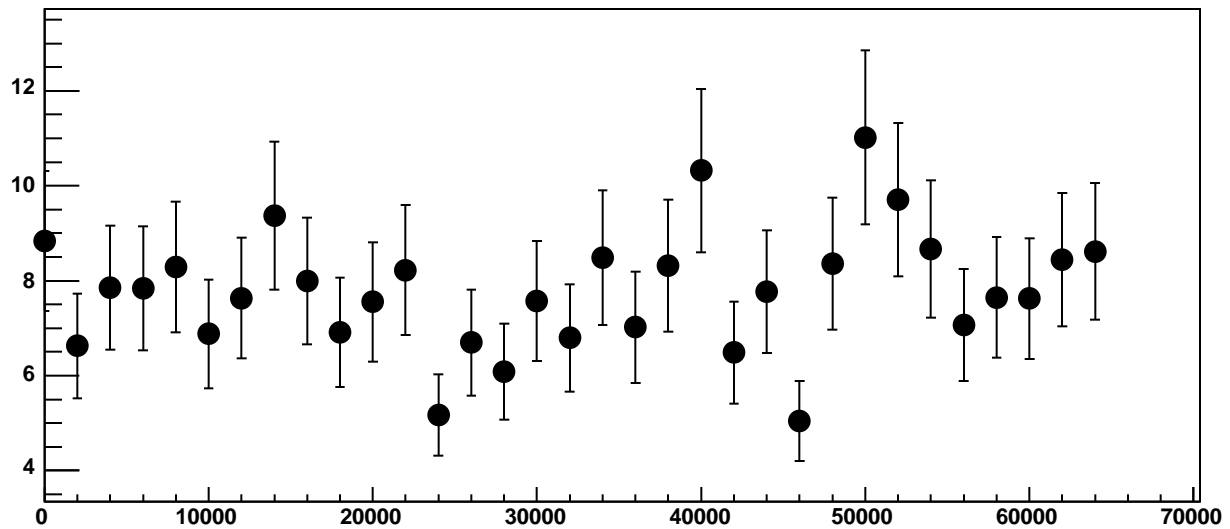
p0

527 ± 0.8127

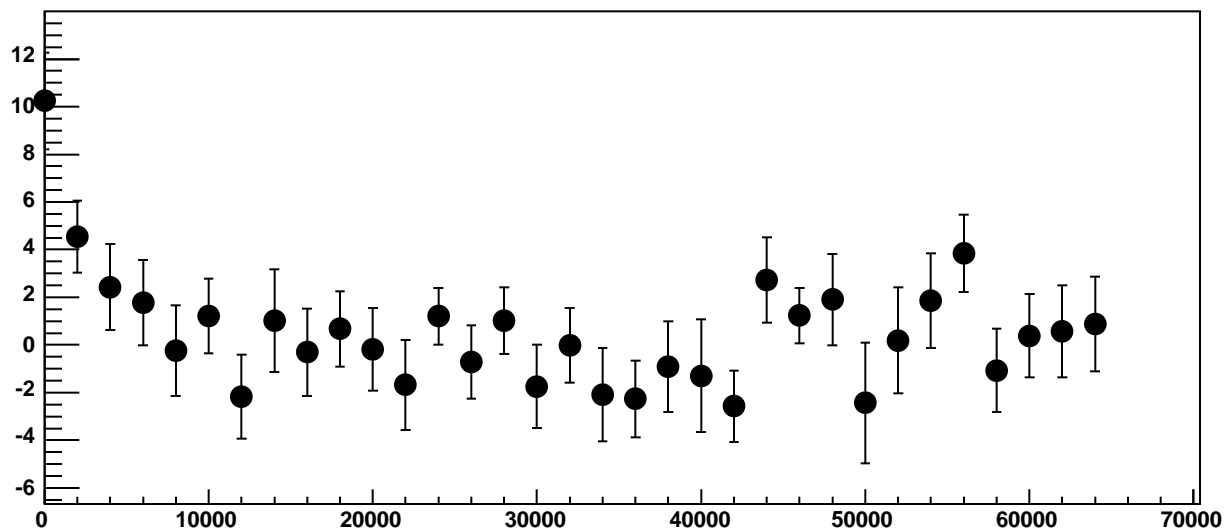
p1

$0.003219 \pm 2.5e-05$

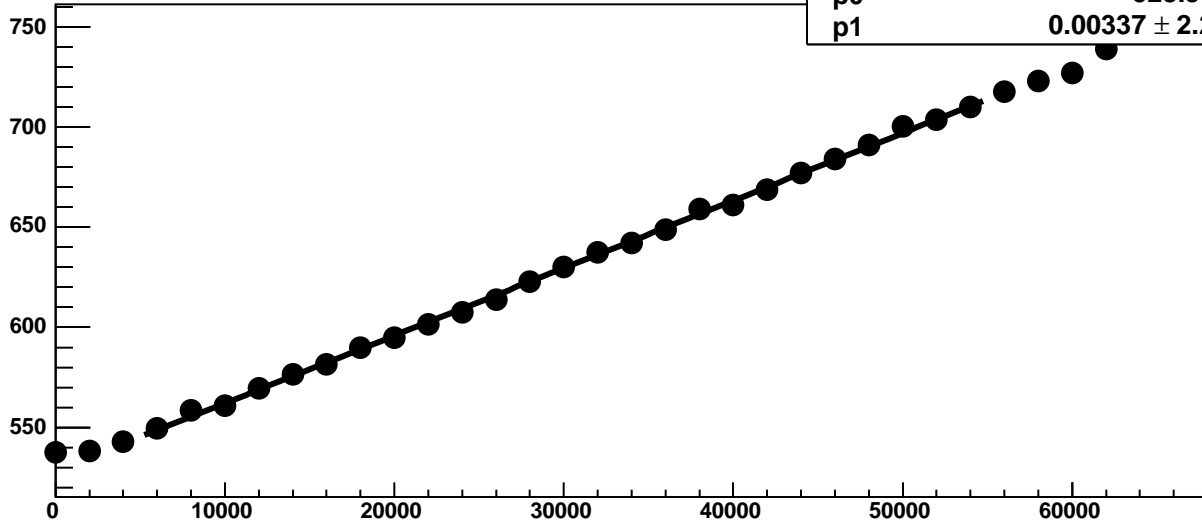
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

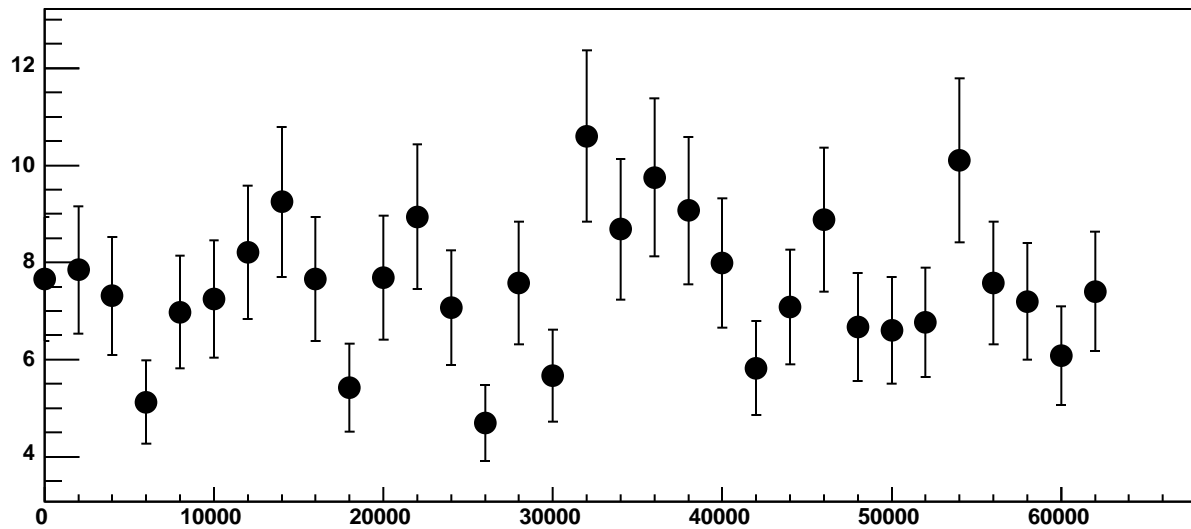


Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

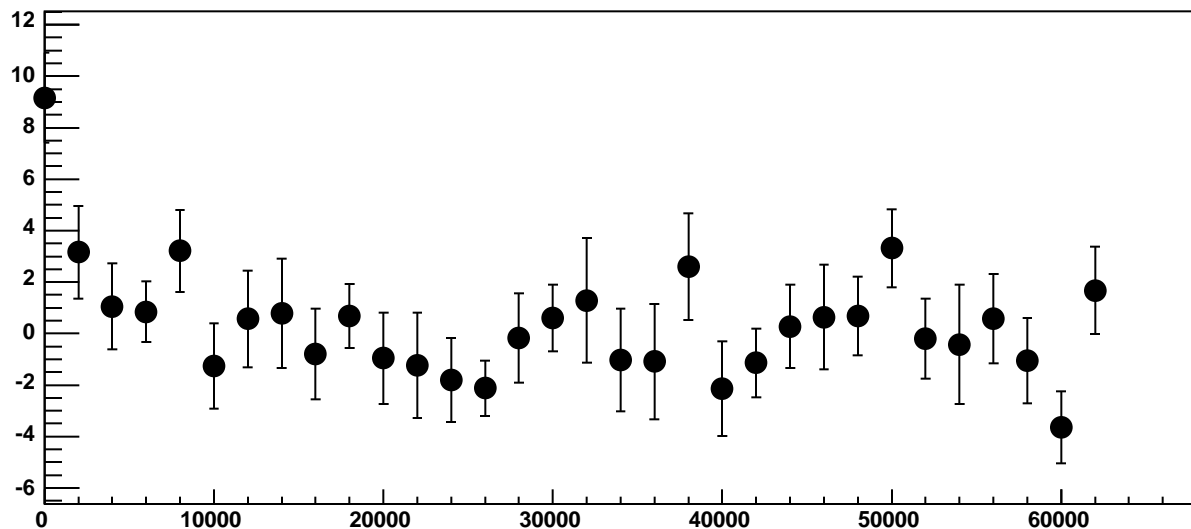


χ^2 / ndf 21.5 / 23
p0 528.5 ± 0.718
p1 $0.00337 \pm 2.231\text{e-}05$

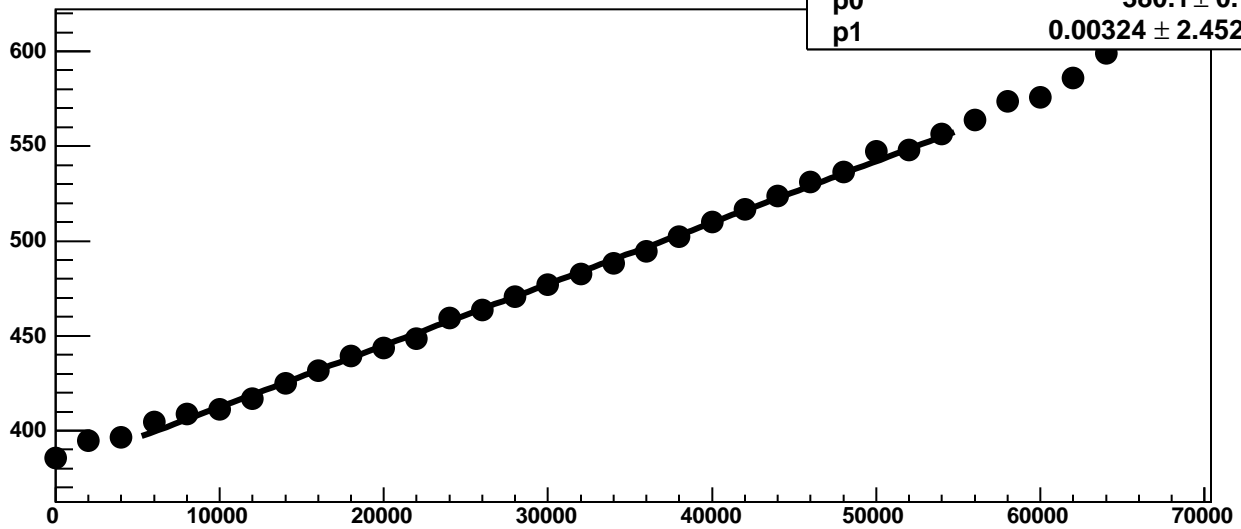
Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

33.4 / 23

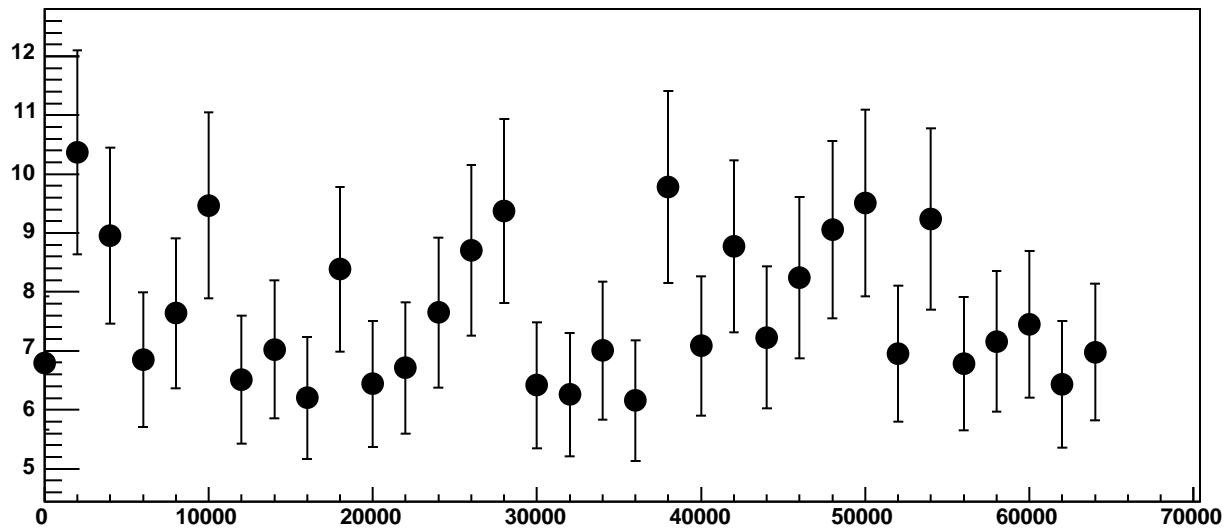
p0

380.1 ± 0.7826

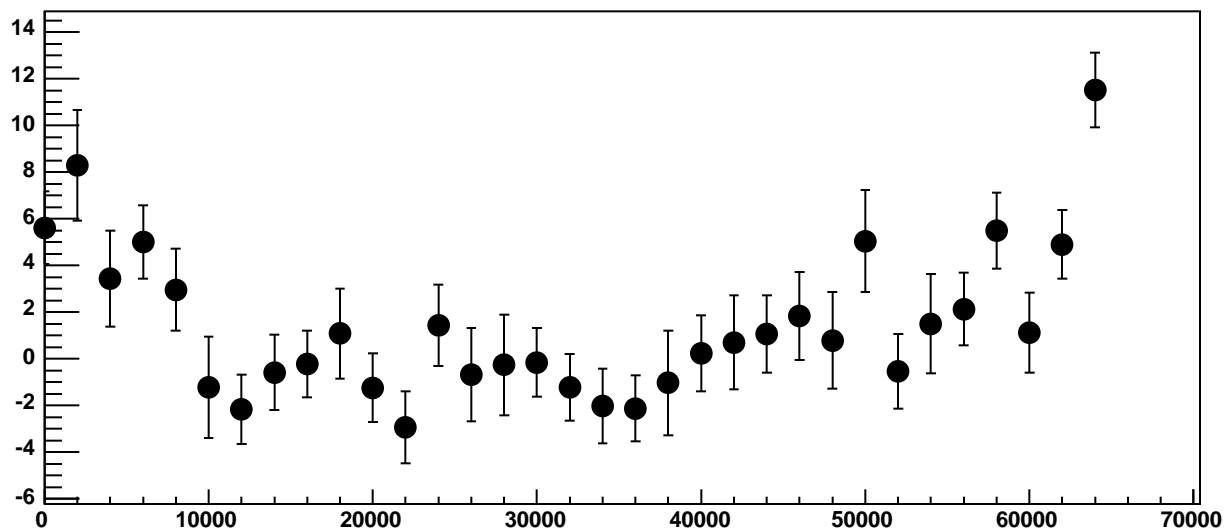
p1

$0.00324 \pm 2.452e-05$

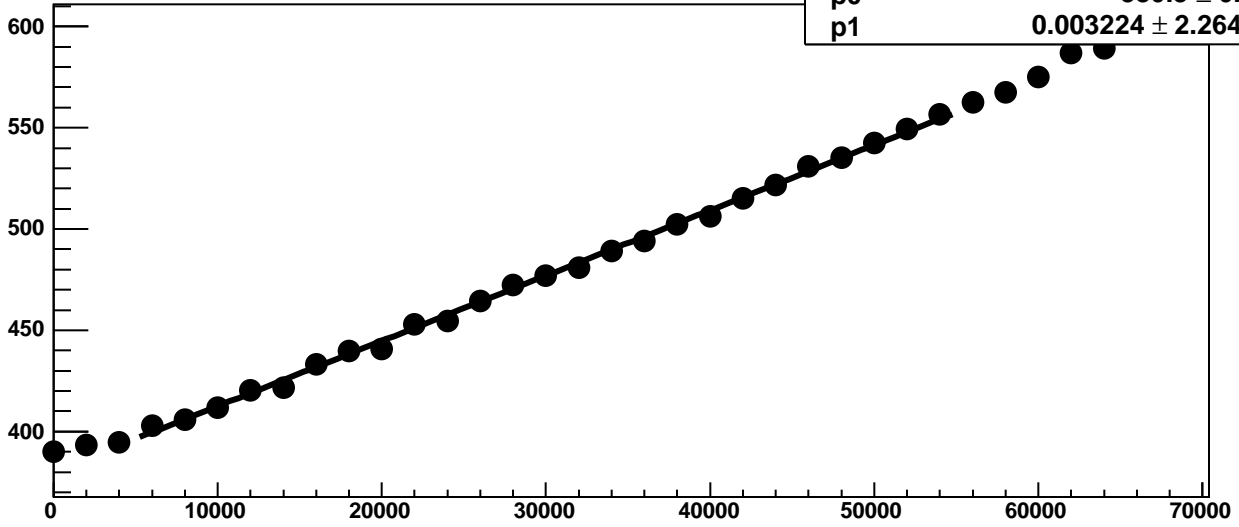
Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

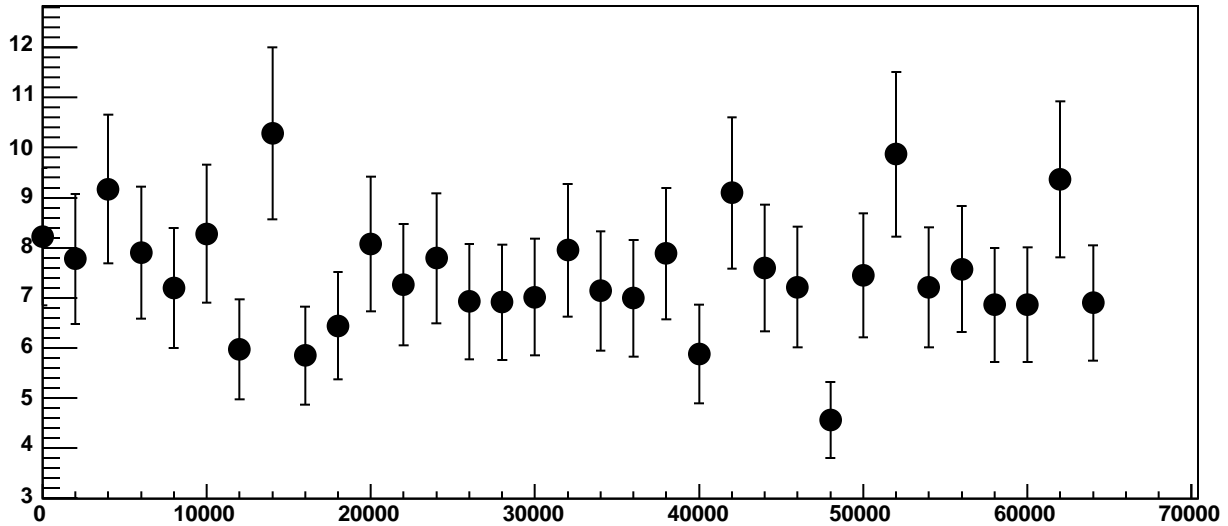


Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

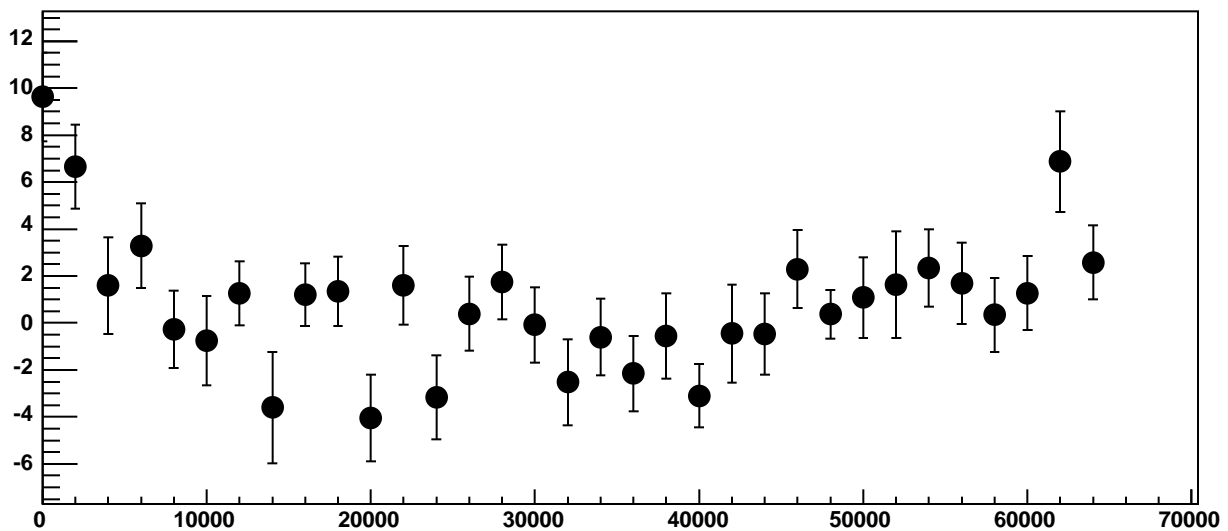


χ^2 / ndf 32.74 / 23
p0 380.3 ± 0.7641
p1 $0.003224 \pm 2.264e-05$

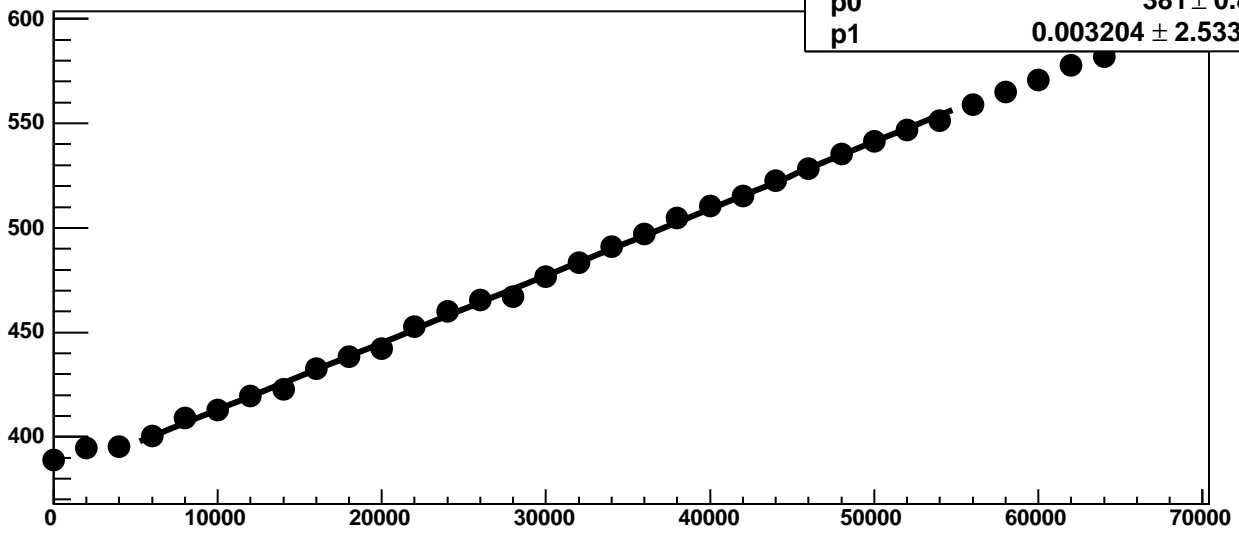
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

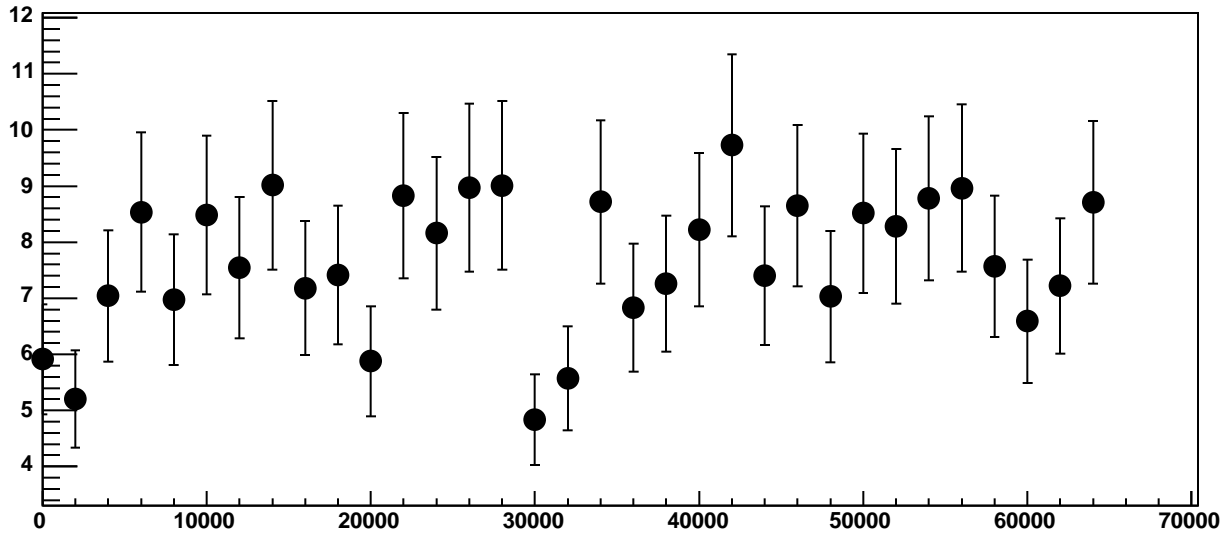


Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

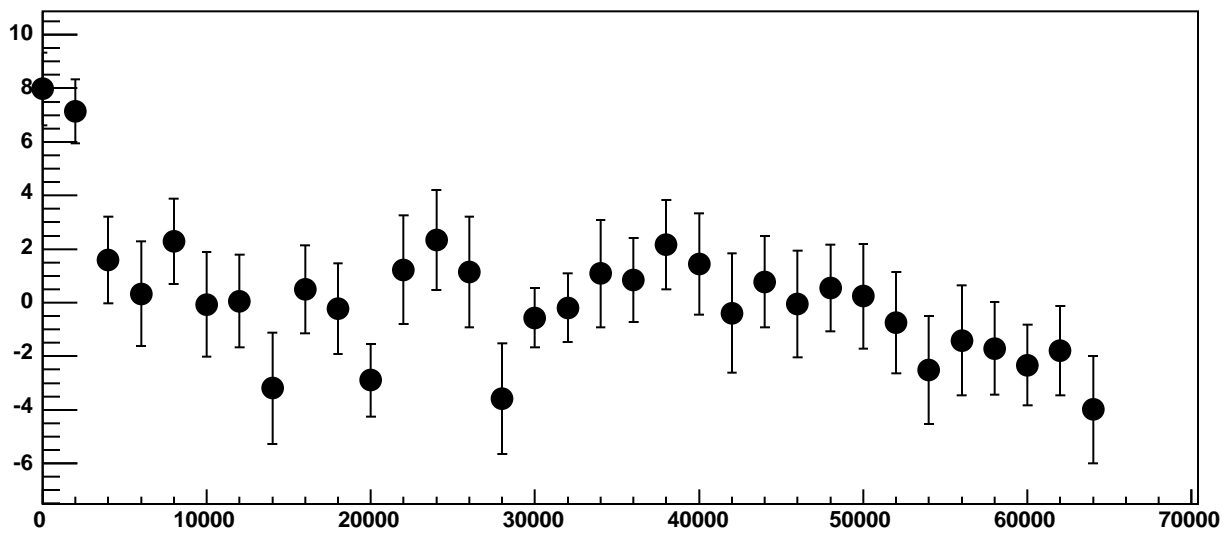


χ^2 / ndf 19.66 / 23
p0 381 ± 0.8237
p1 $0.003204 \pm 2.533e-05$

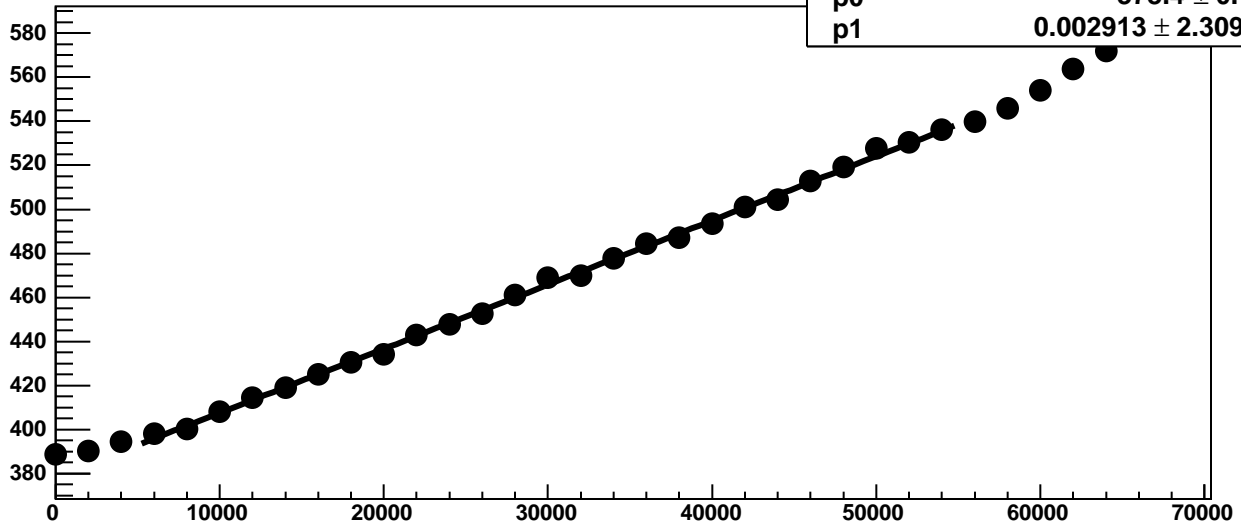
Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.03 / 23

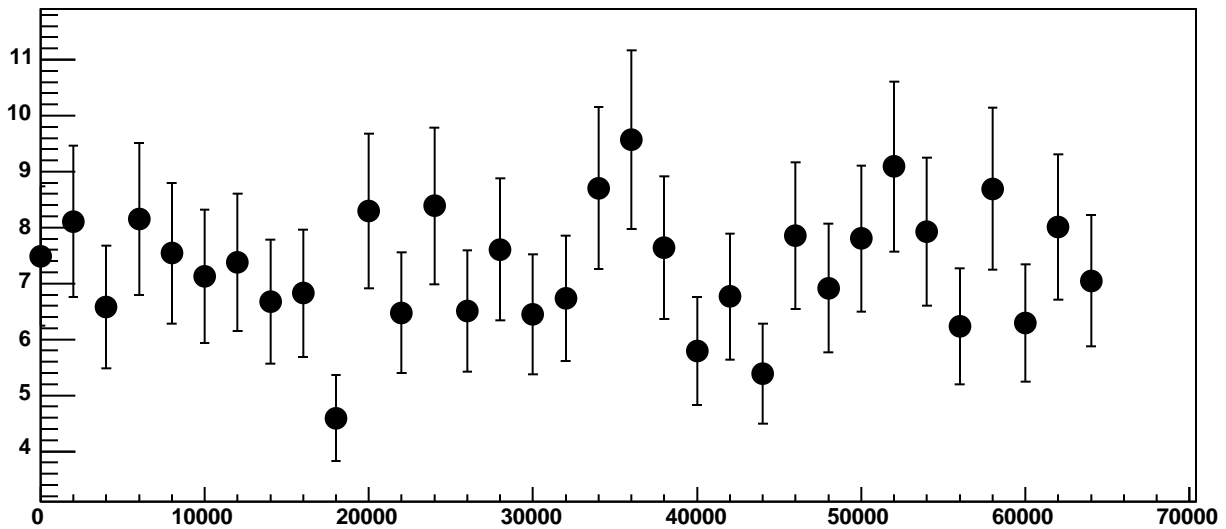
p0

378.4 ± 0.7514

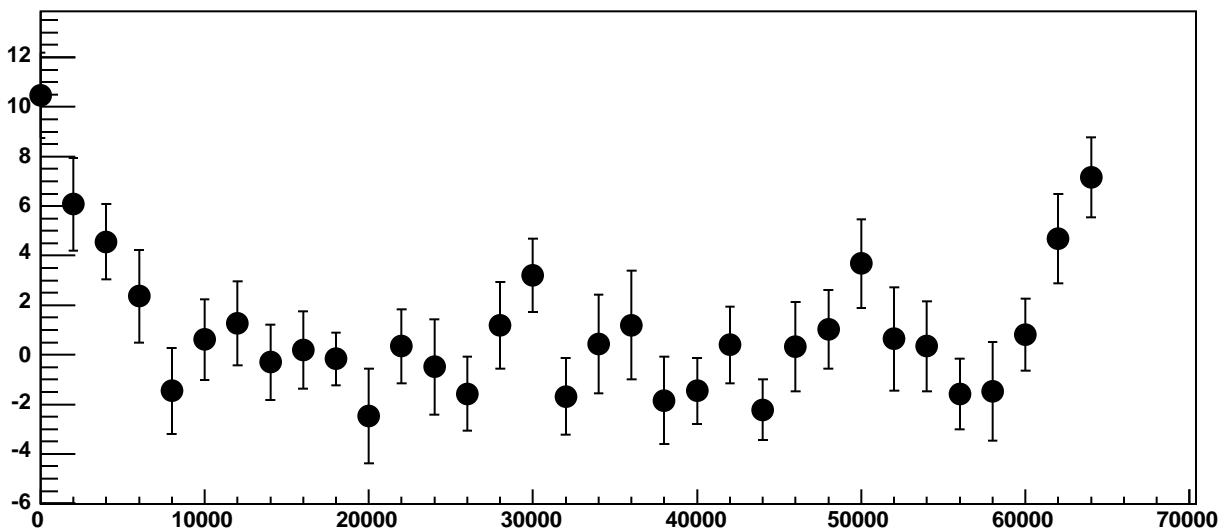
p1

$0.002913 \pm 2.309e-05$

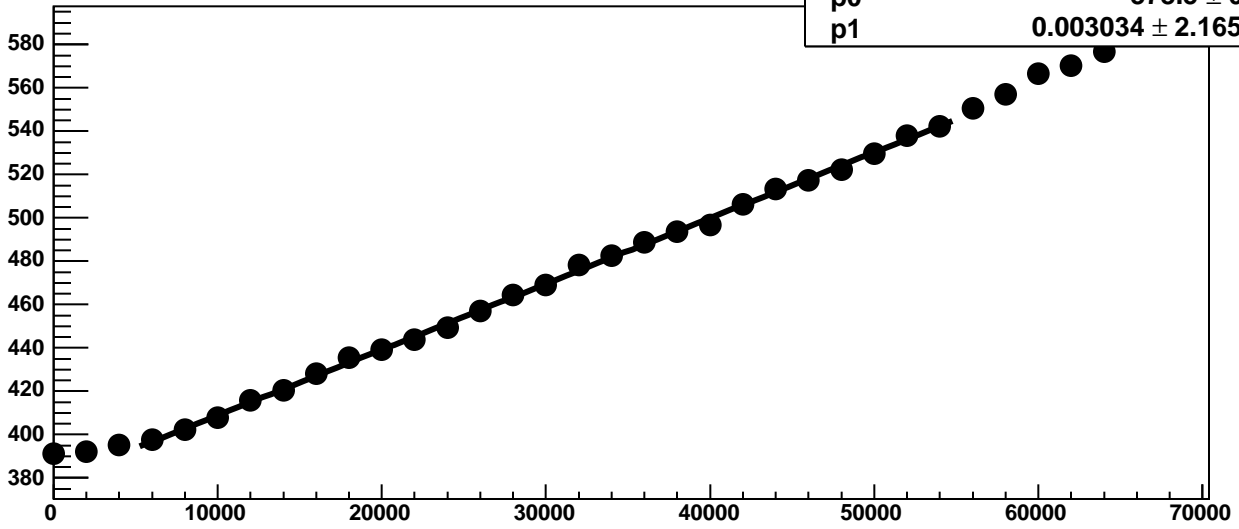
Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

16.42 / 23

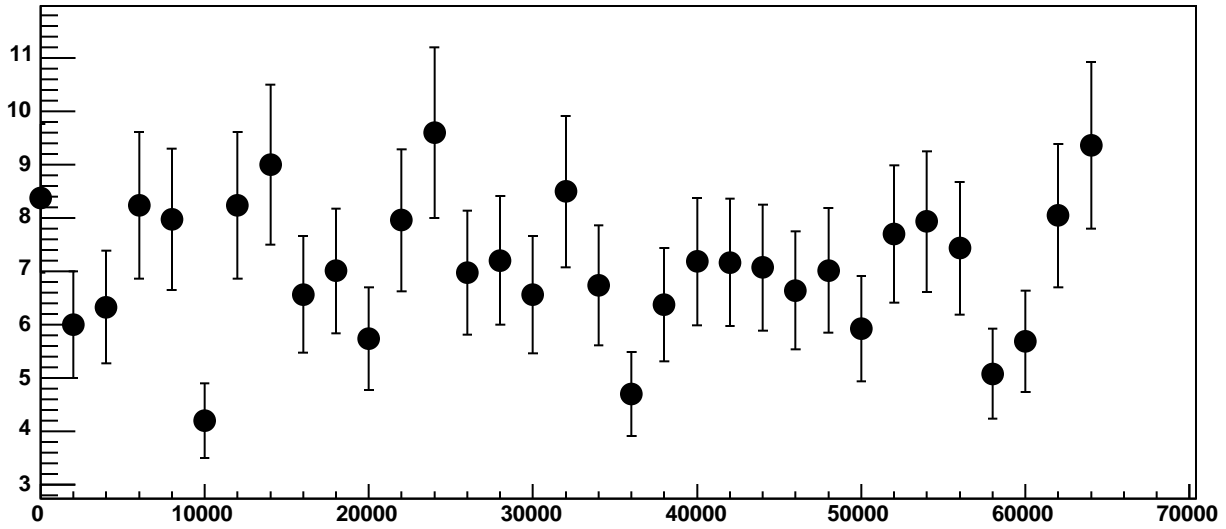
p0

378.5 ± 0.715

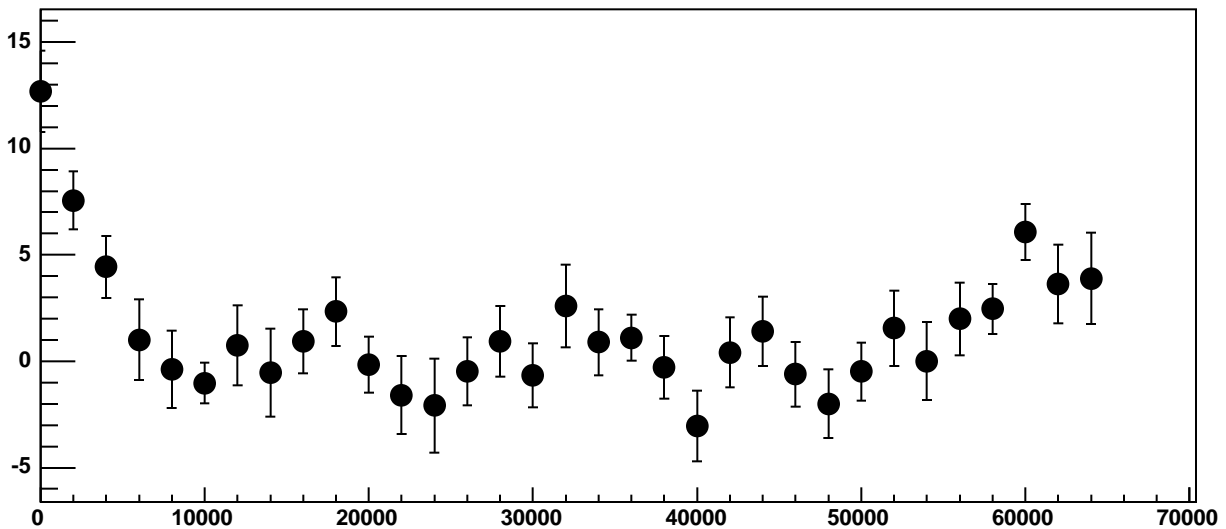
p1

$0.003034 \pm 2.165e-05$

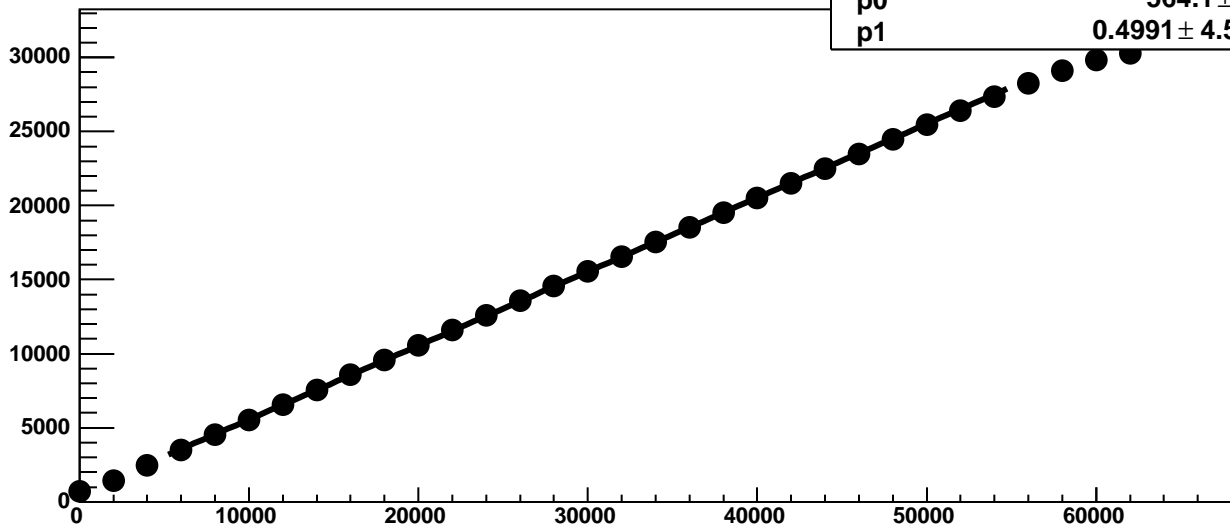
Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



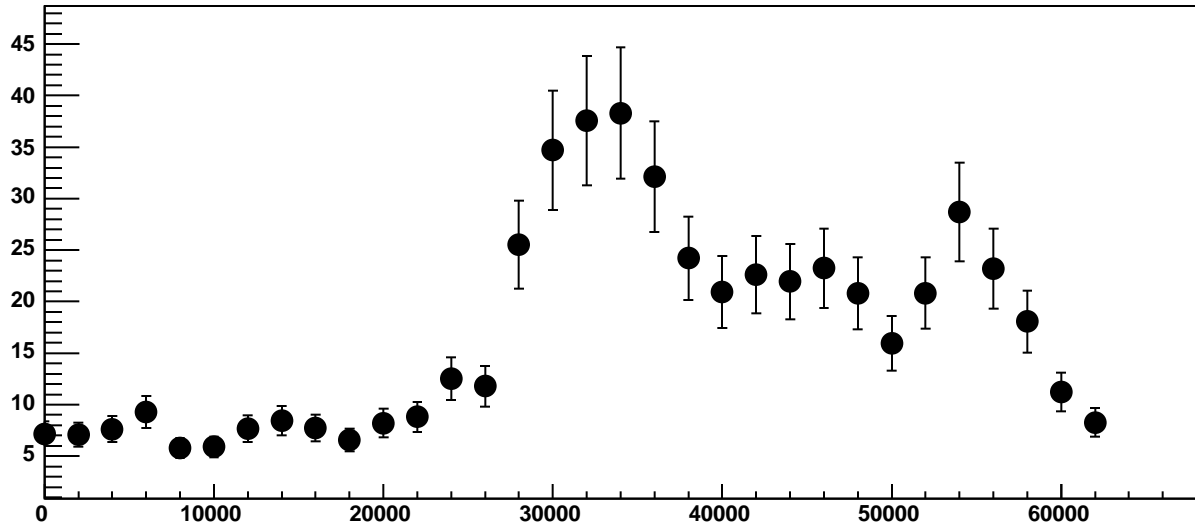
Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



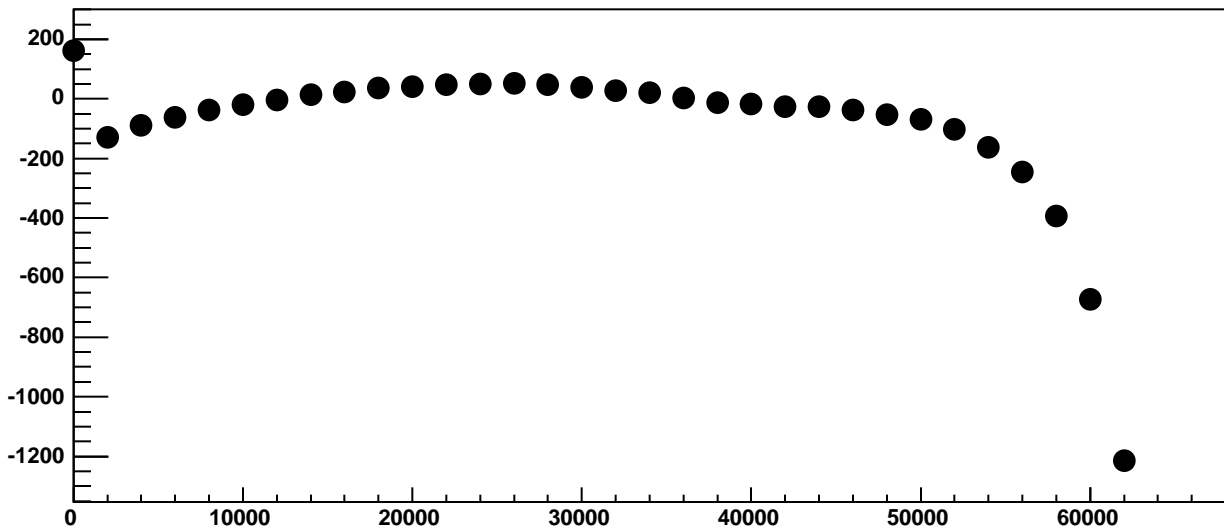
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



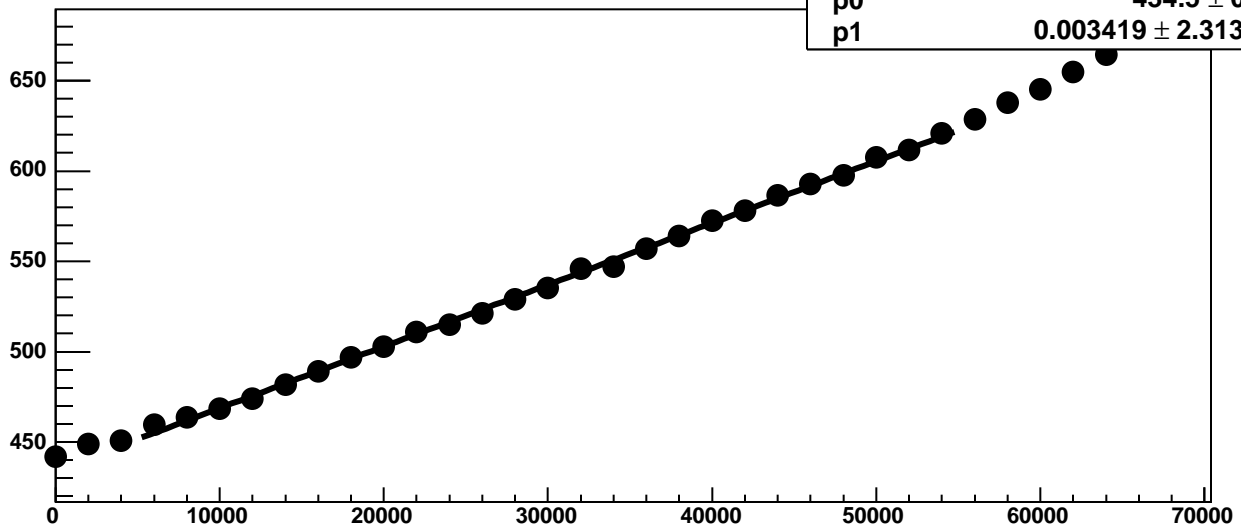
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

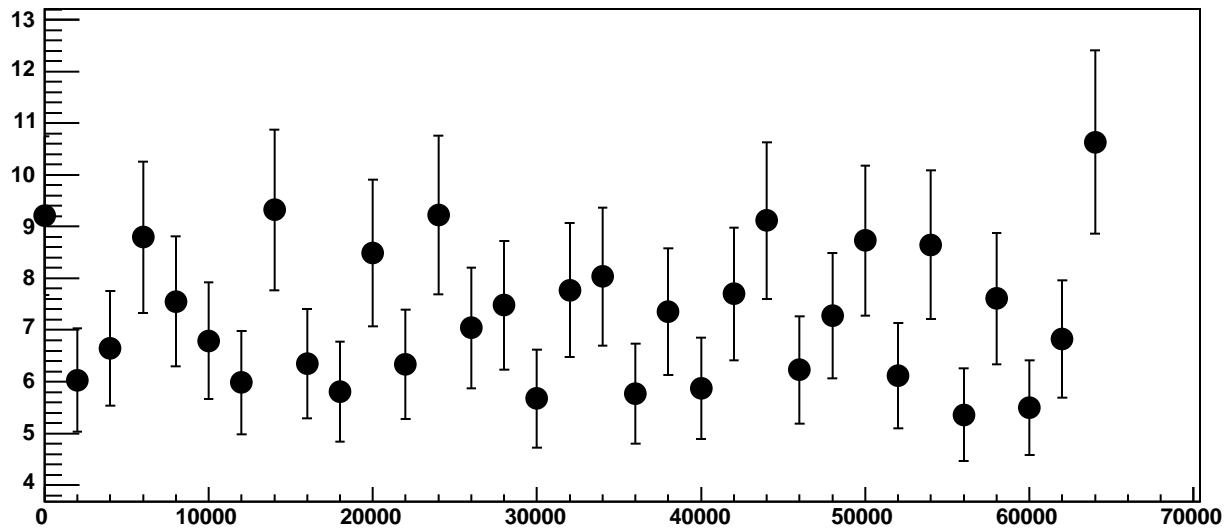


Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

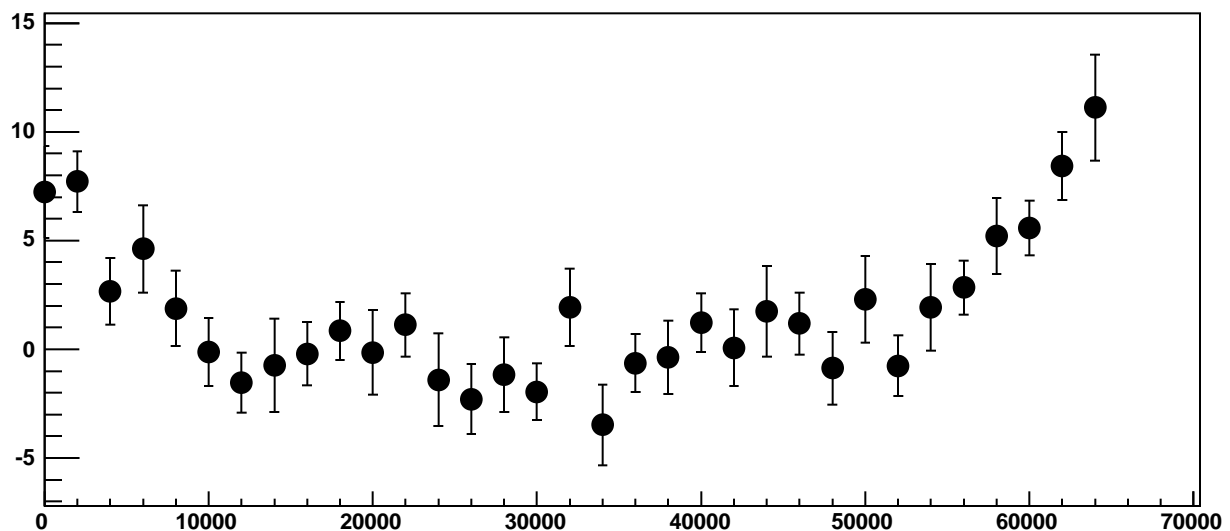


χ^2 / ndf 24.04 / 23
p0 434.5 ± 0.764
p1 $0.003419 \pm 2.313e-05$

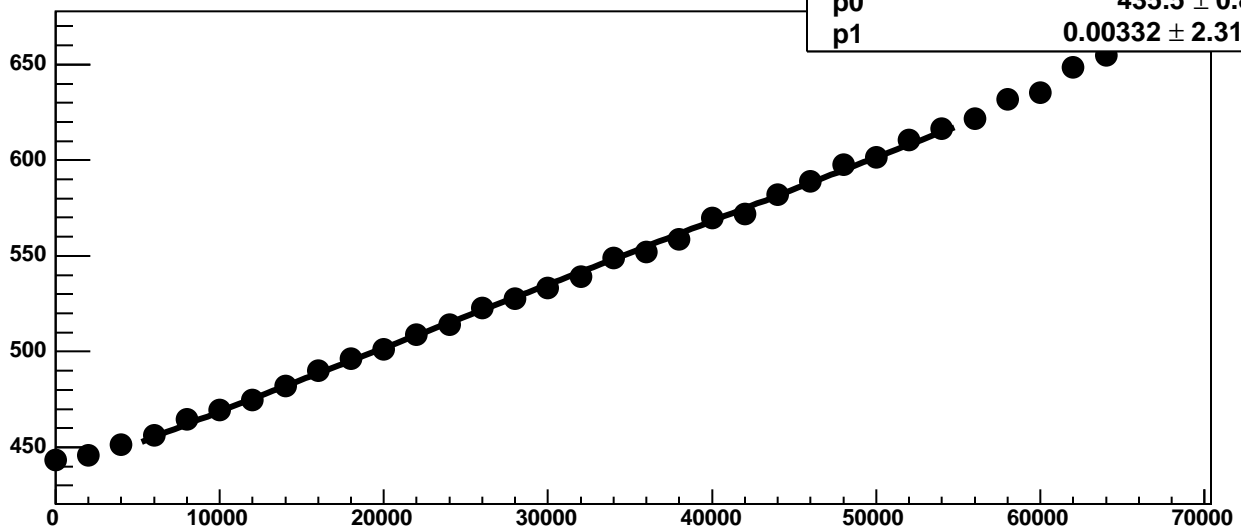
Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



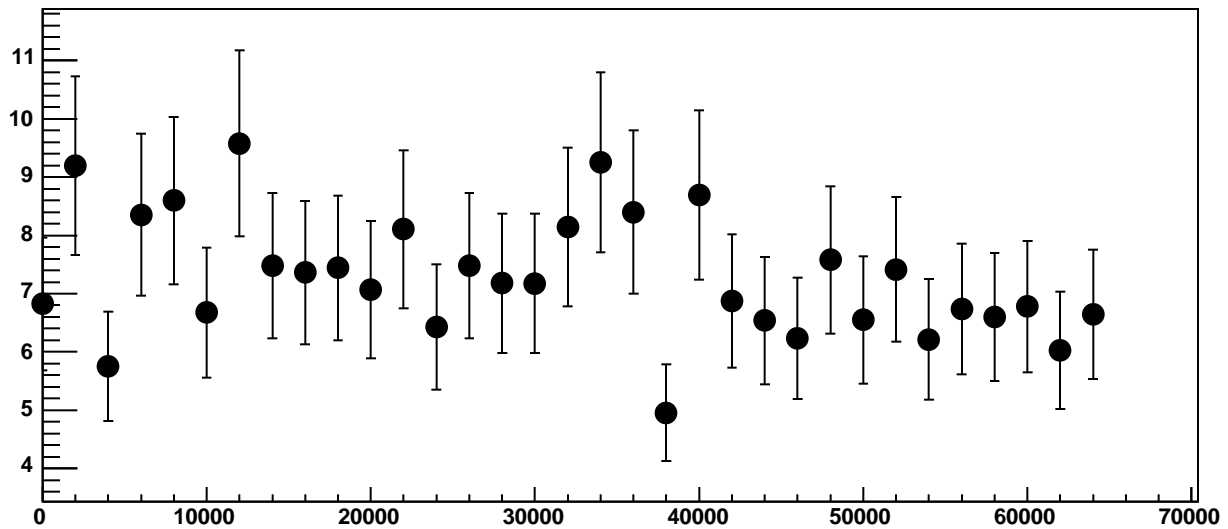
Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



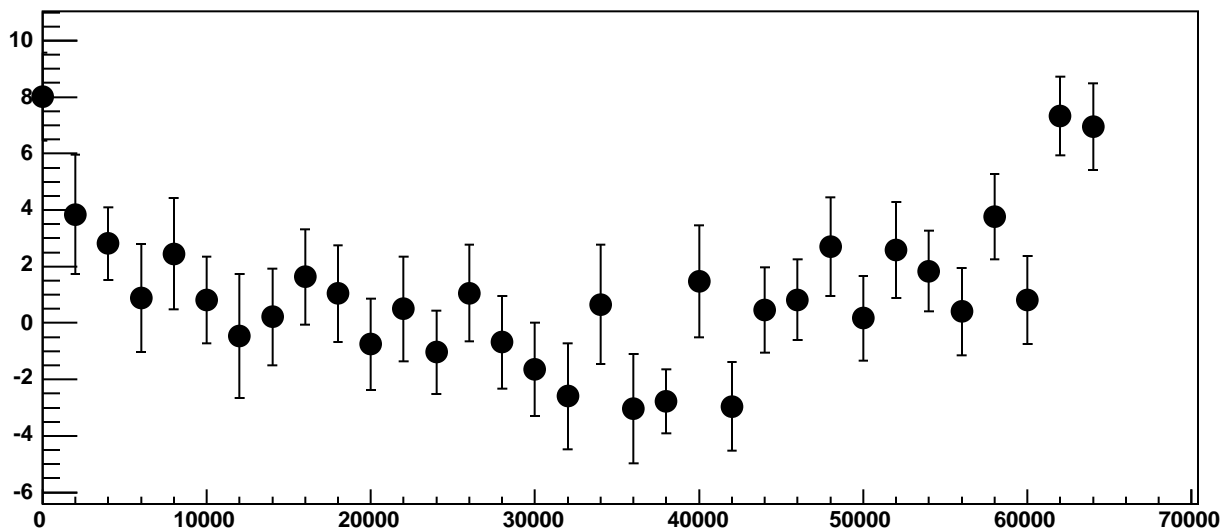
Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



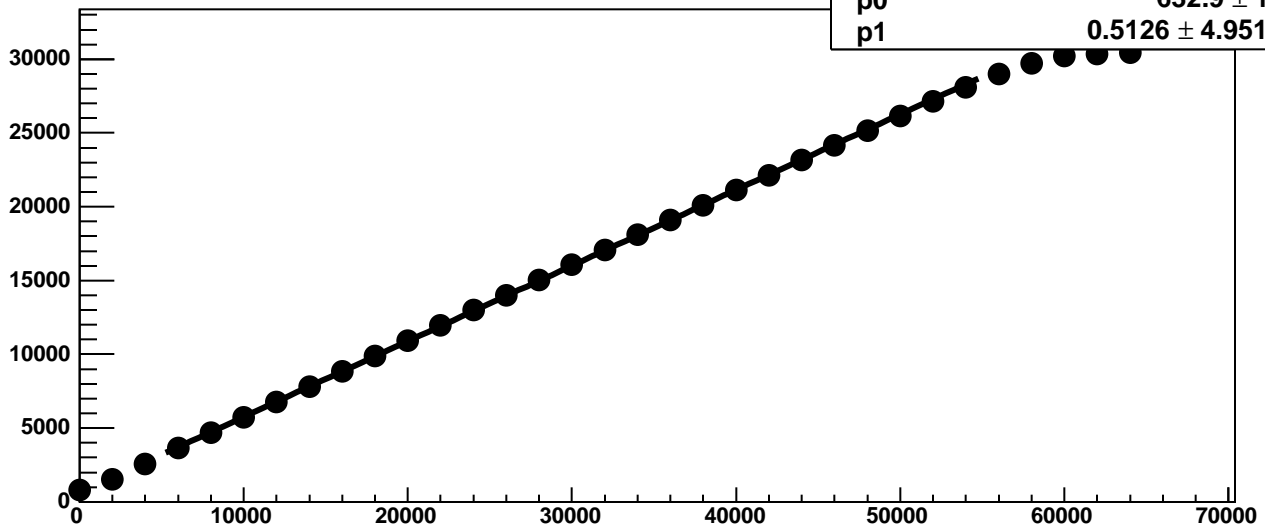
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



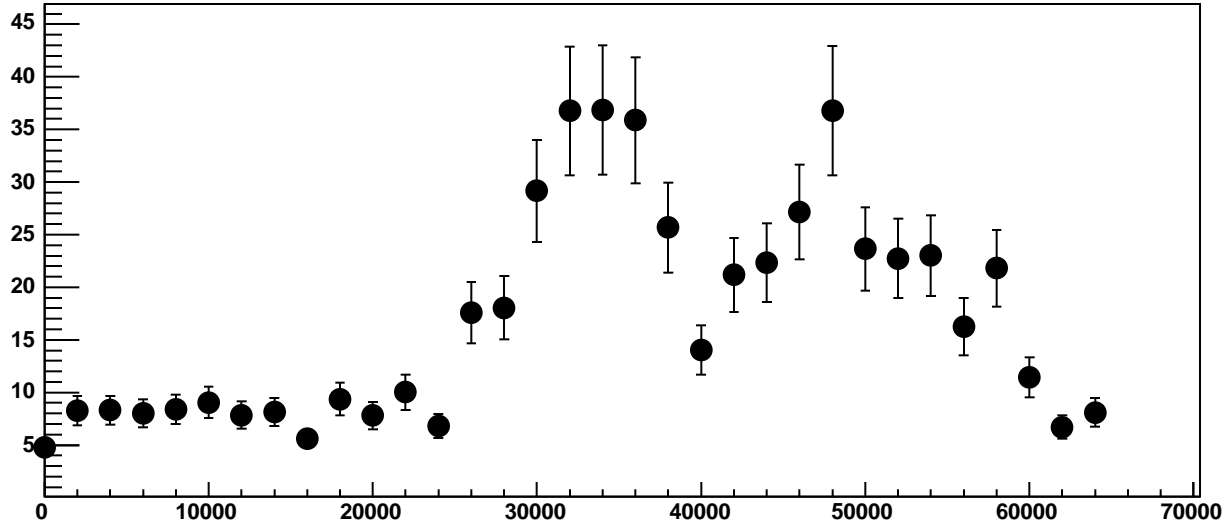
Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



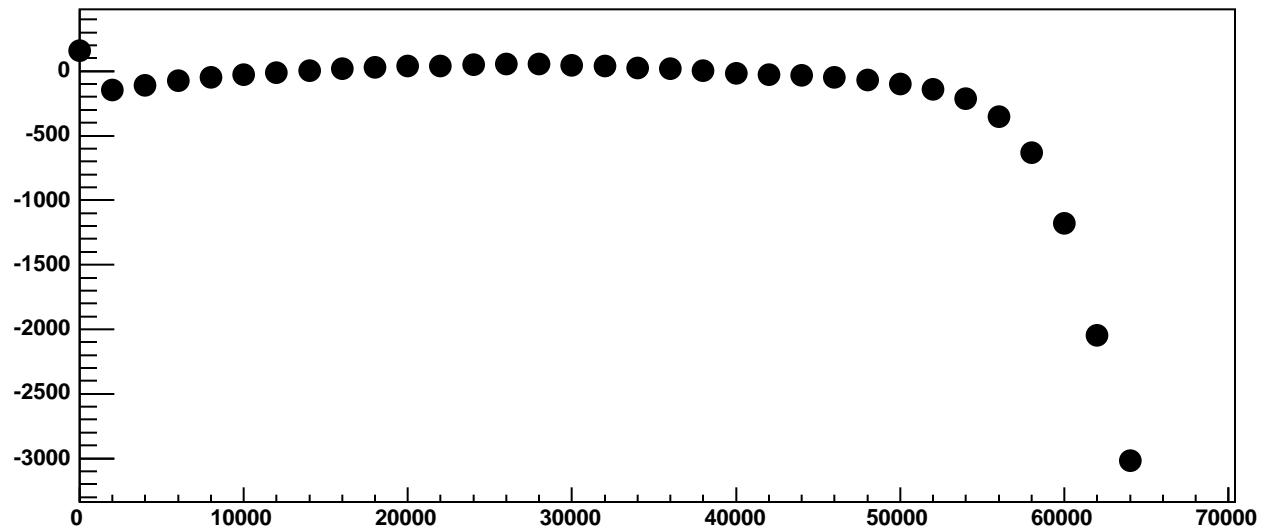
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



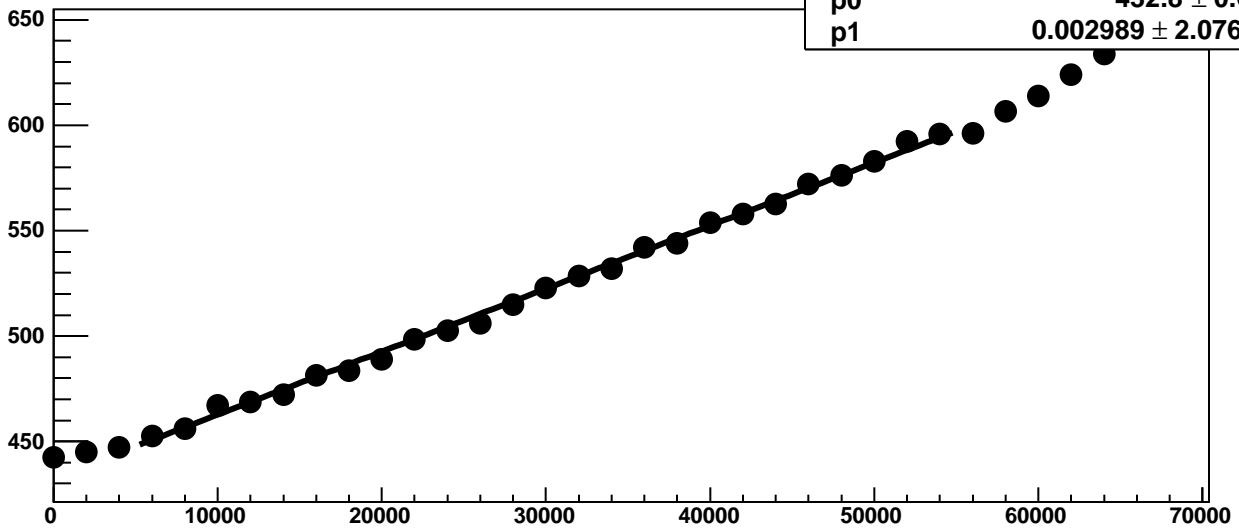
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

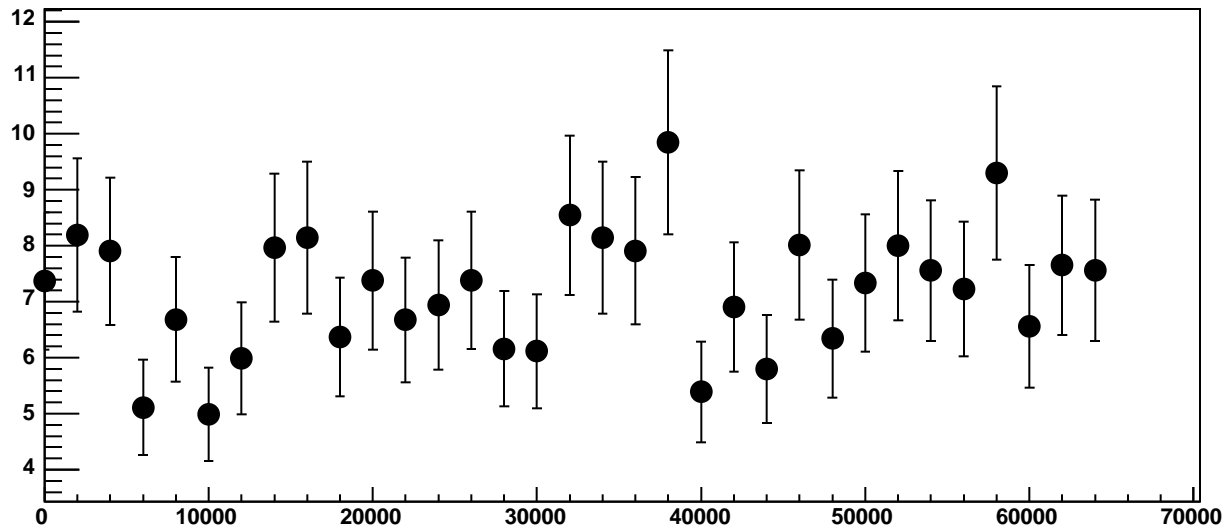


Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

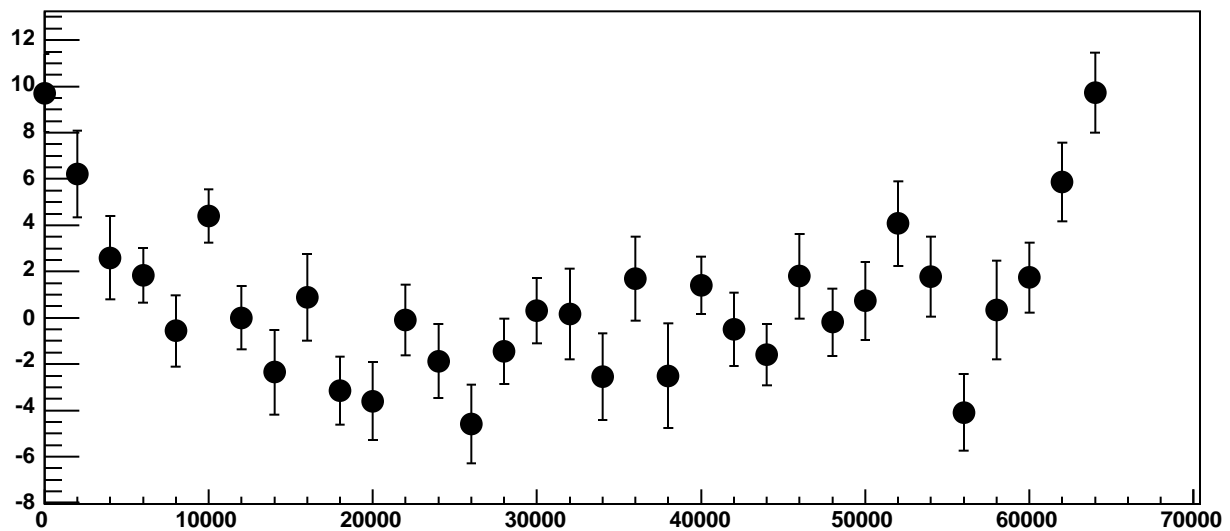


χ^2 / ndf 52.11 / 23
p0 432.8 ± 0.6626
p1 $0.002989 \pm 2.076e-05$

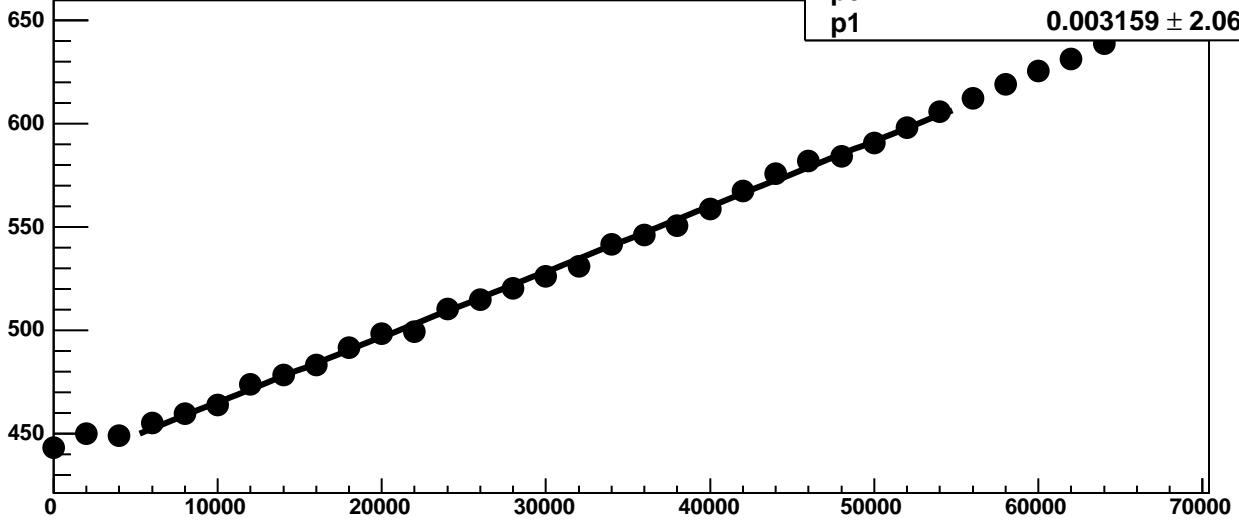
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

44.86 / 23

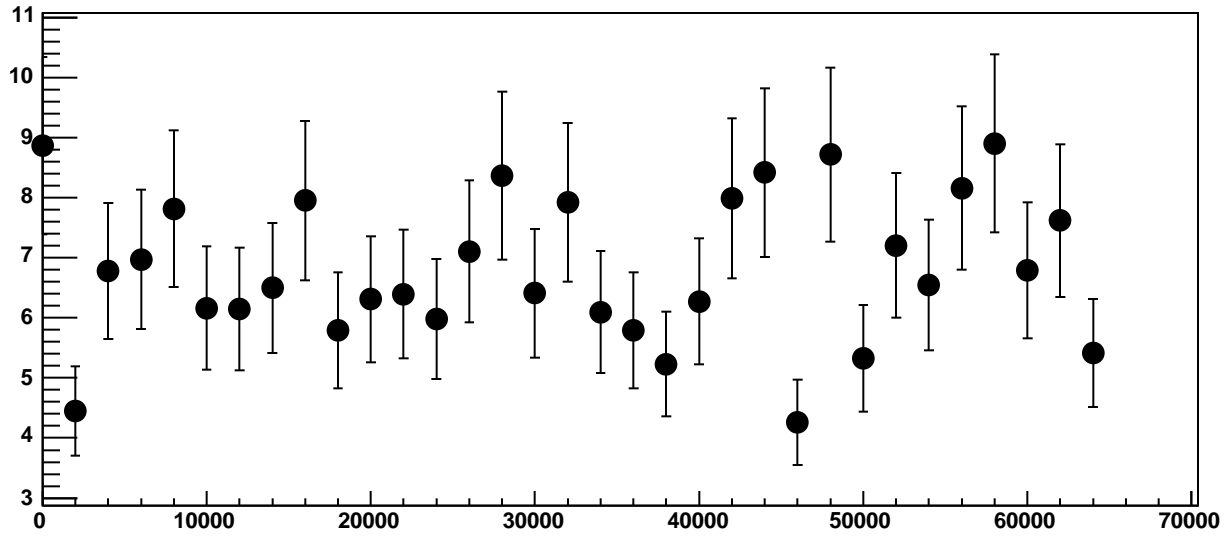
p0

433.7 ± 0.7008

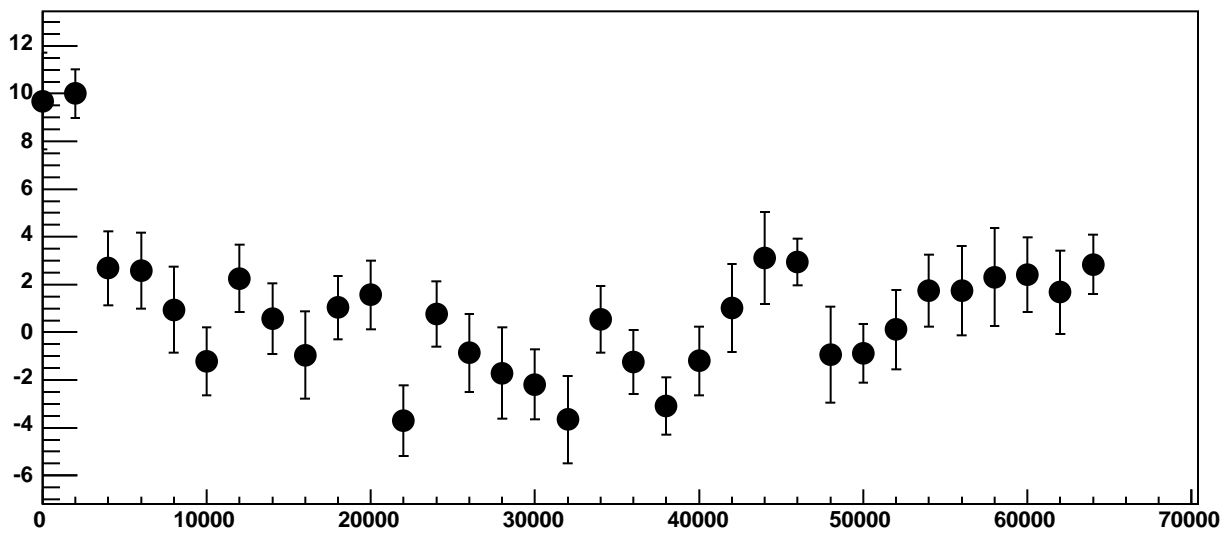
p1

$0.003159 \pm 2.06e-05$

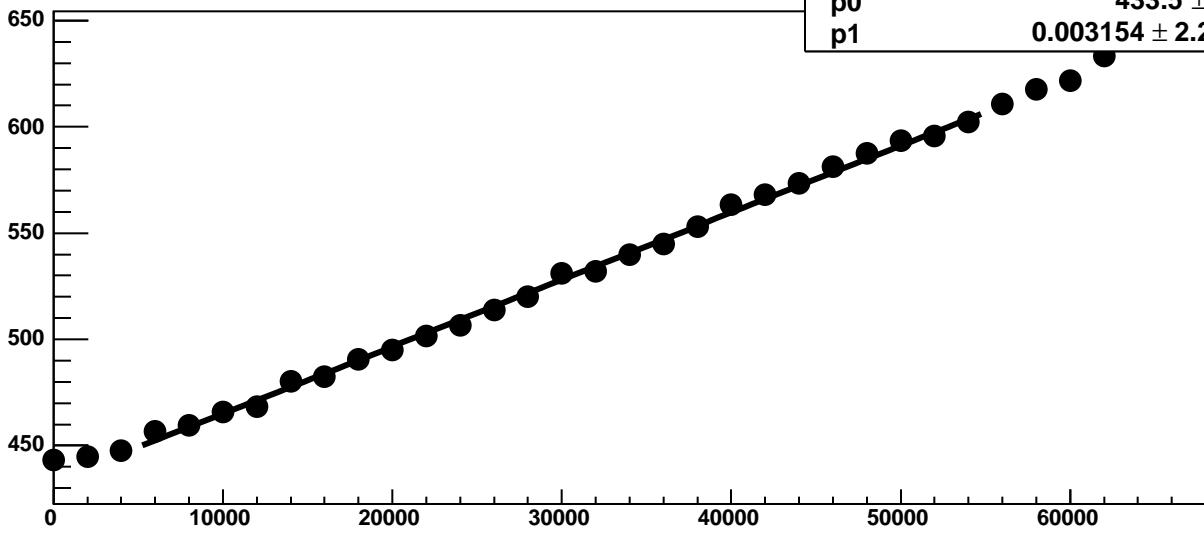
Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

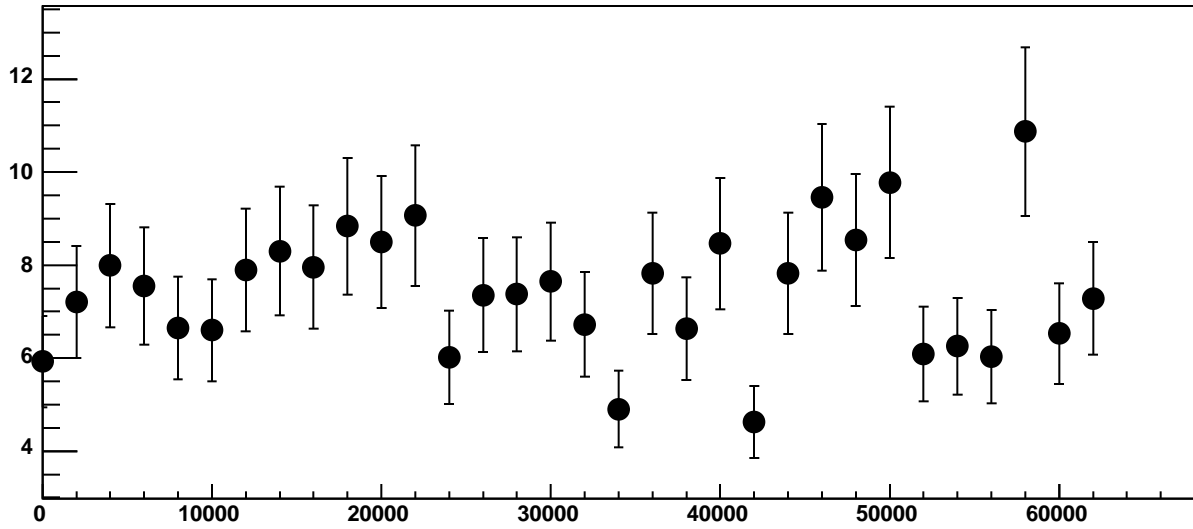


Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

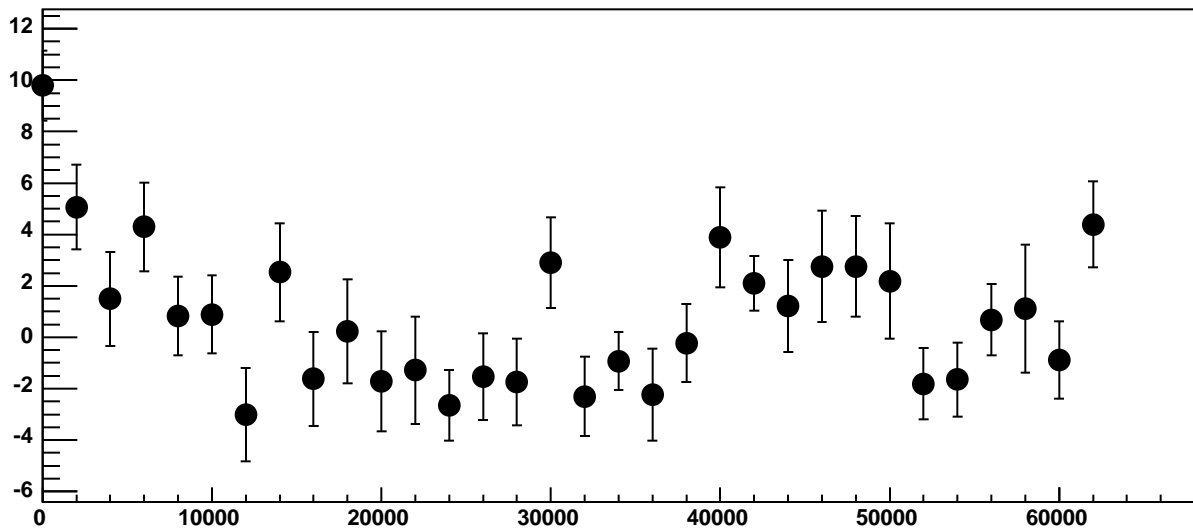


χ^2 / ndf 41.92 / 23
p0 433.5 ± 0.7792
p1 $0.003154 \pm 2.288e-05$

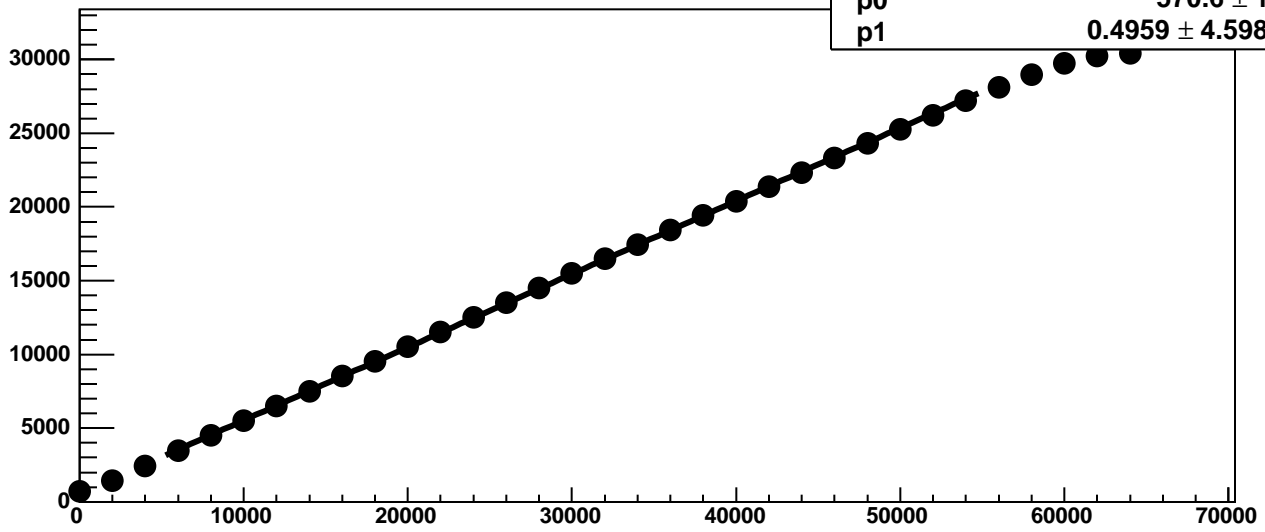
Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



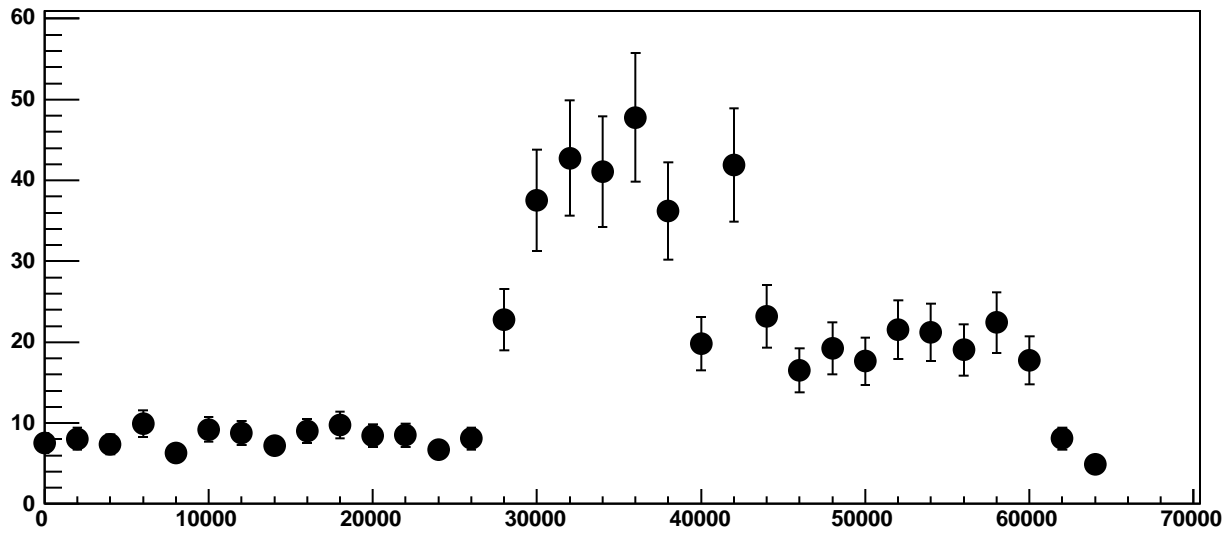
Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



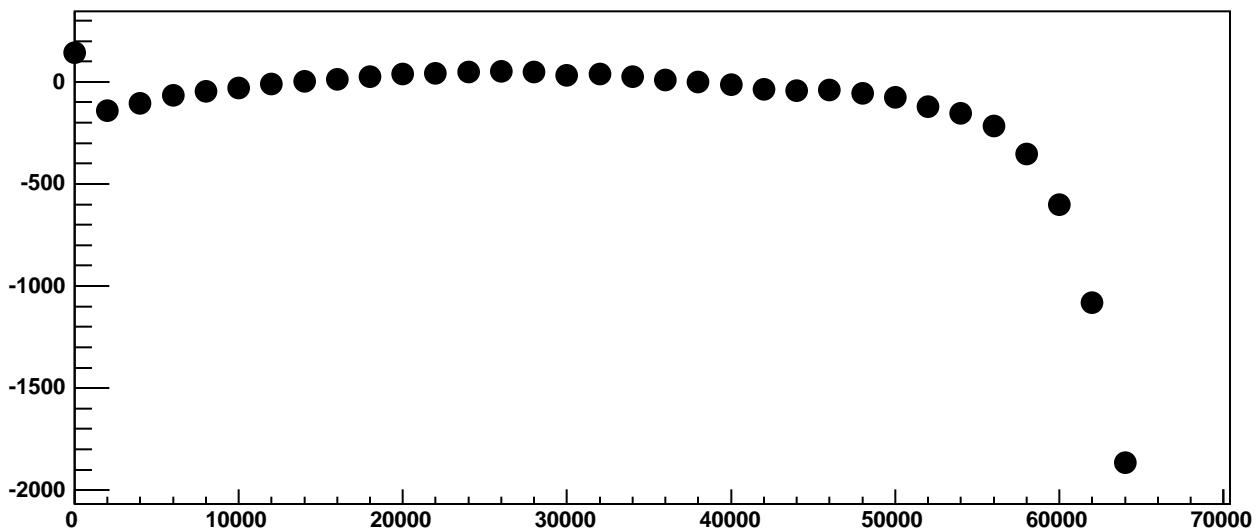
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



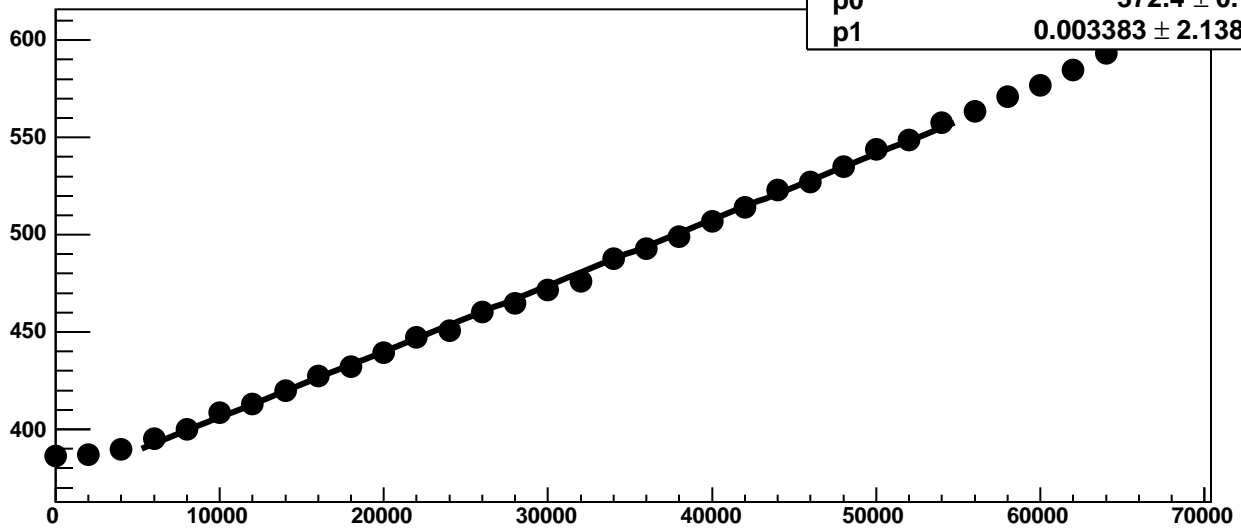
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

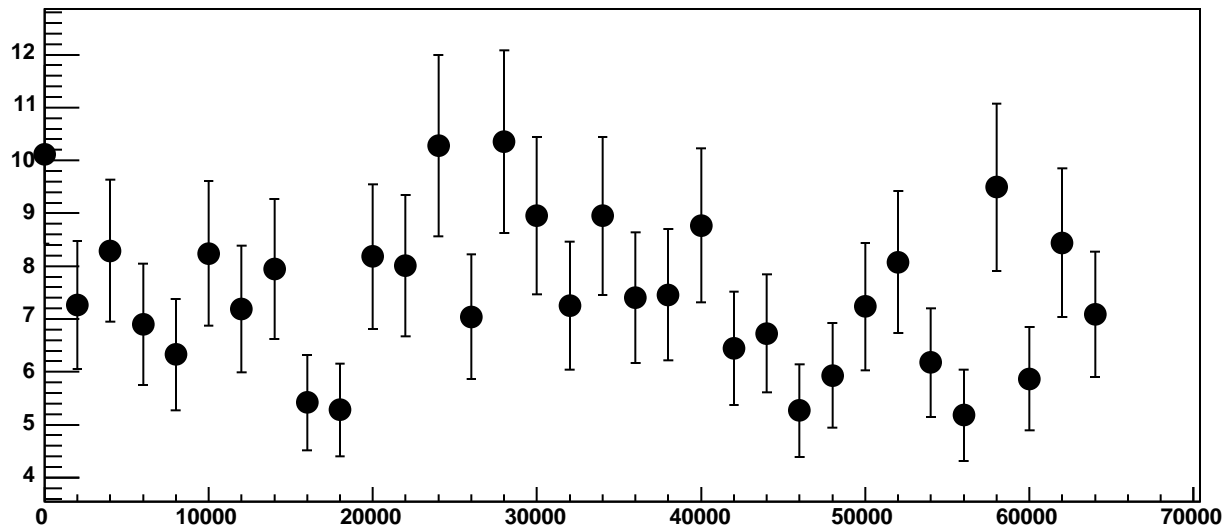


Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

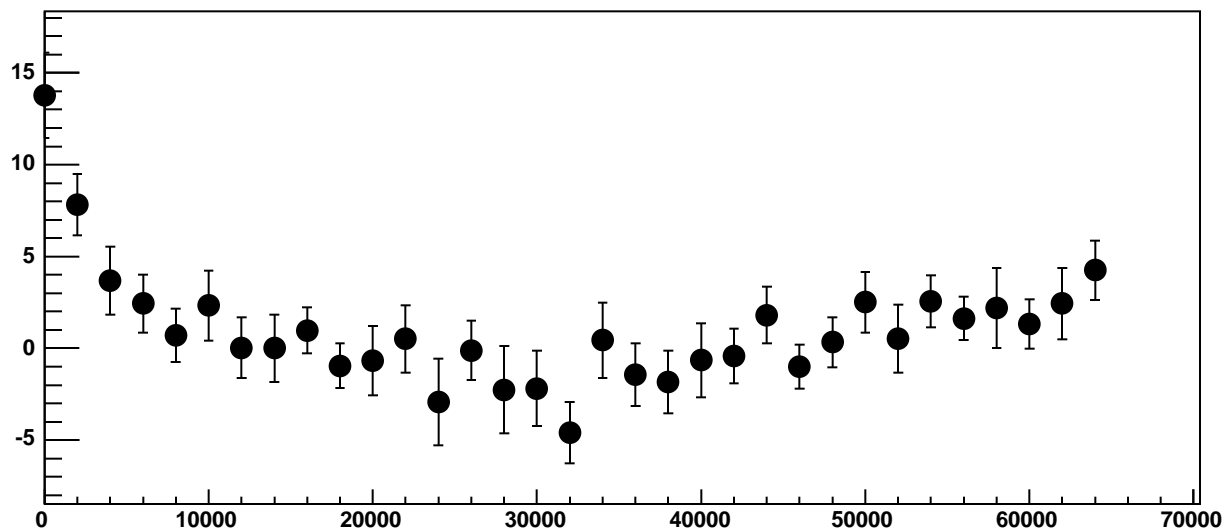


χ^2 / ndf 26.61 / 23
p0 372.4 ± 0.7272
p1 $0.003383 \pm 2.138e-05$

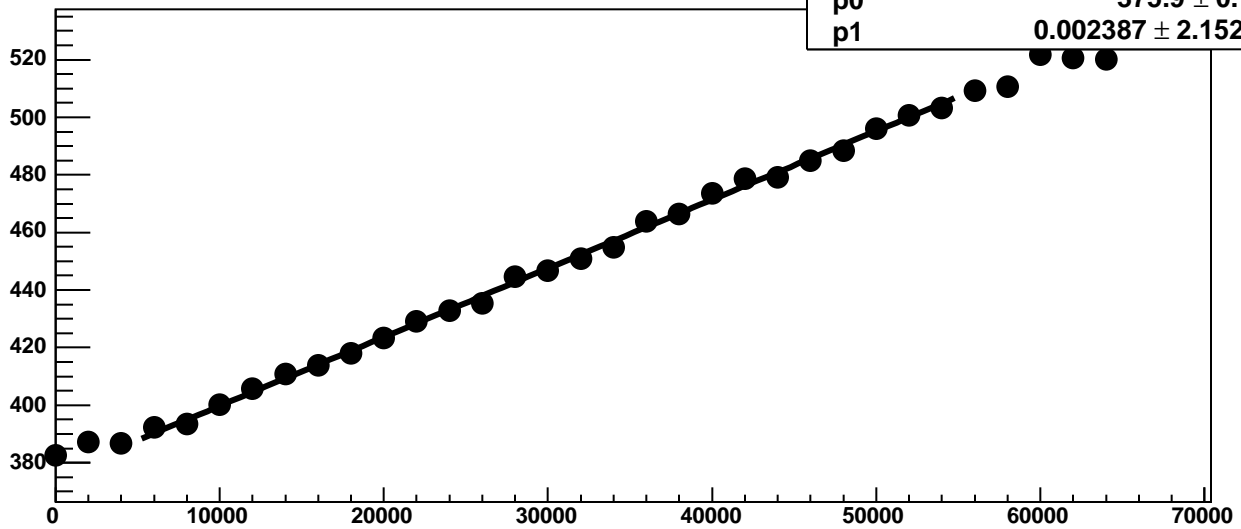
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

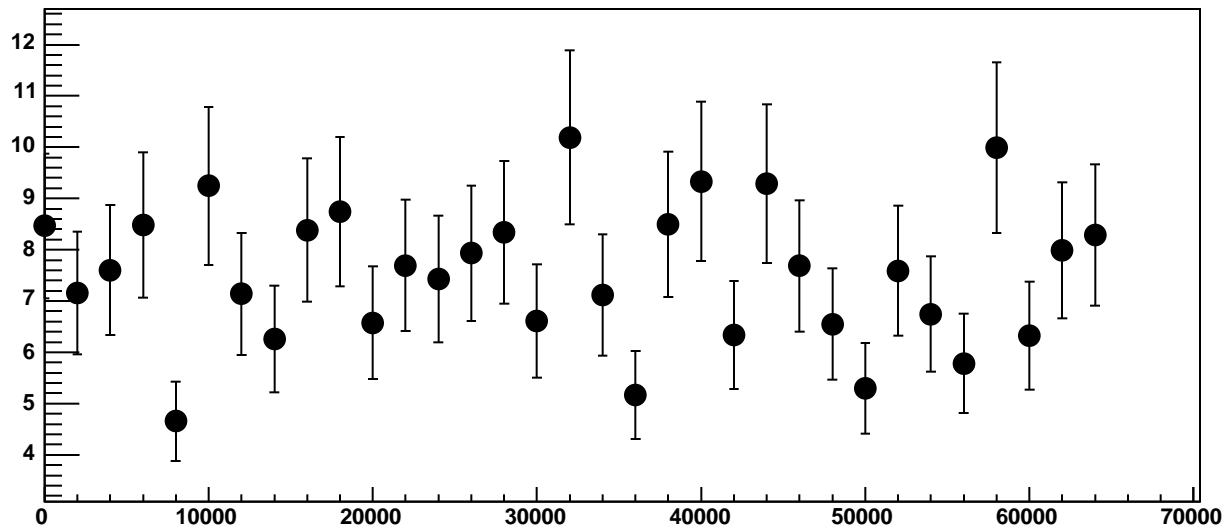


Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

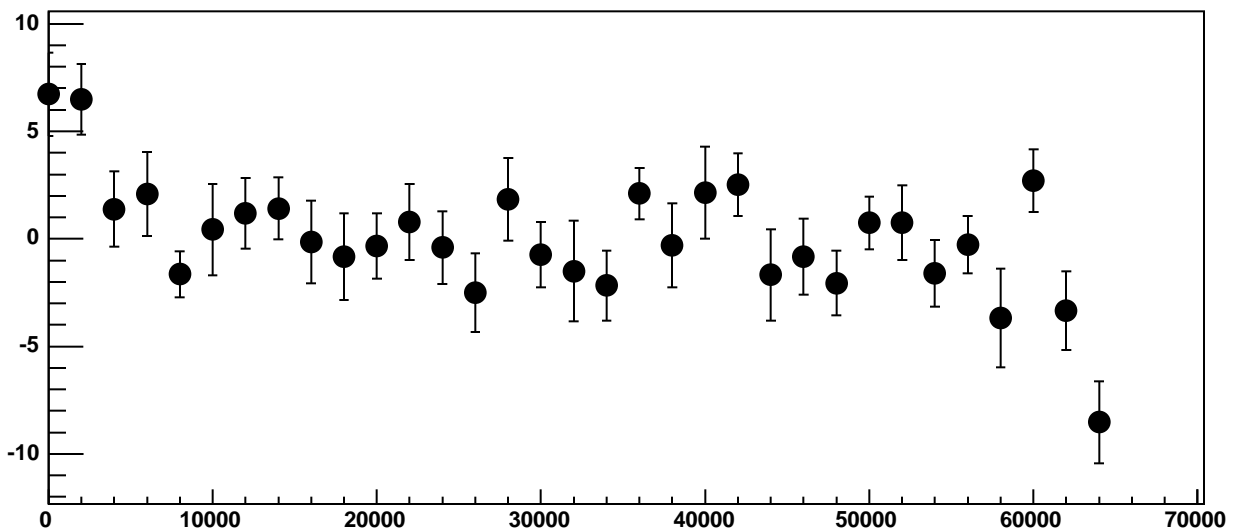


χ^2 / ndf 22.24 / 23
p0 375.9 ± 0.7247
p1 $0.002387 \pm 2.152e-05$

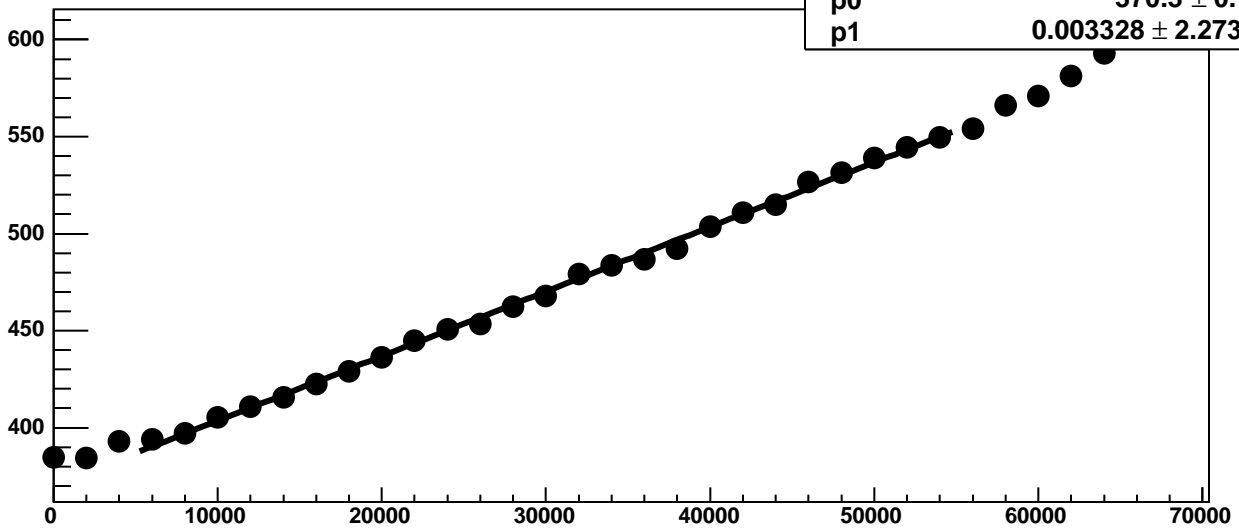
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



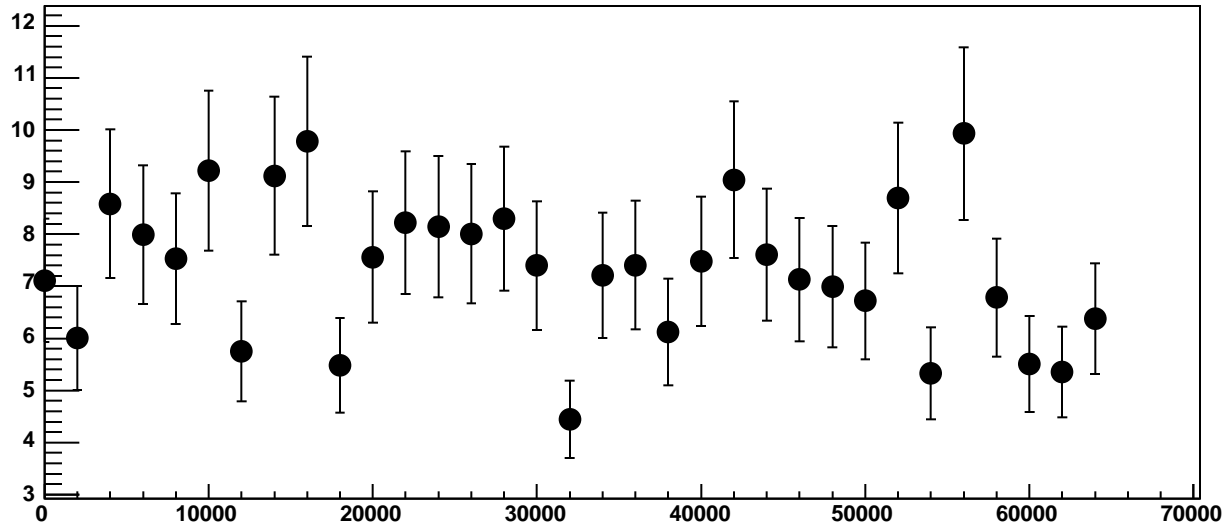
Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



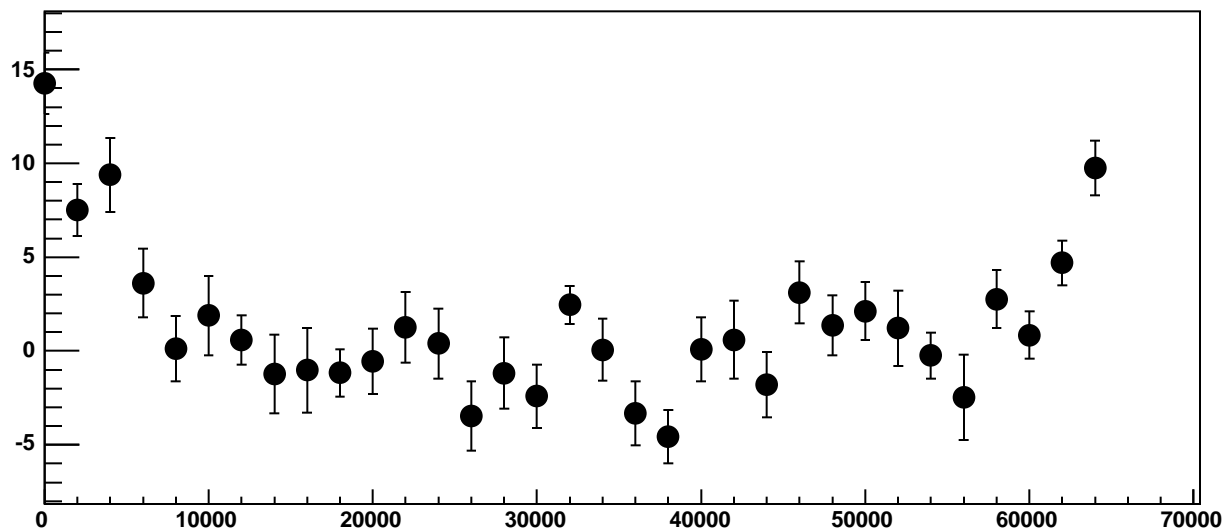
Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC



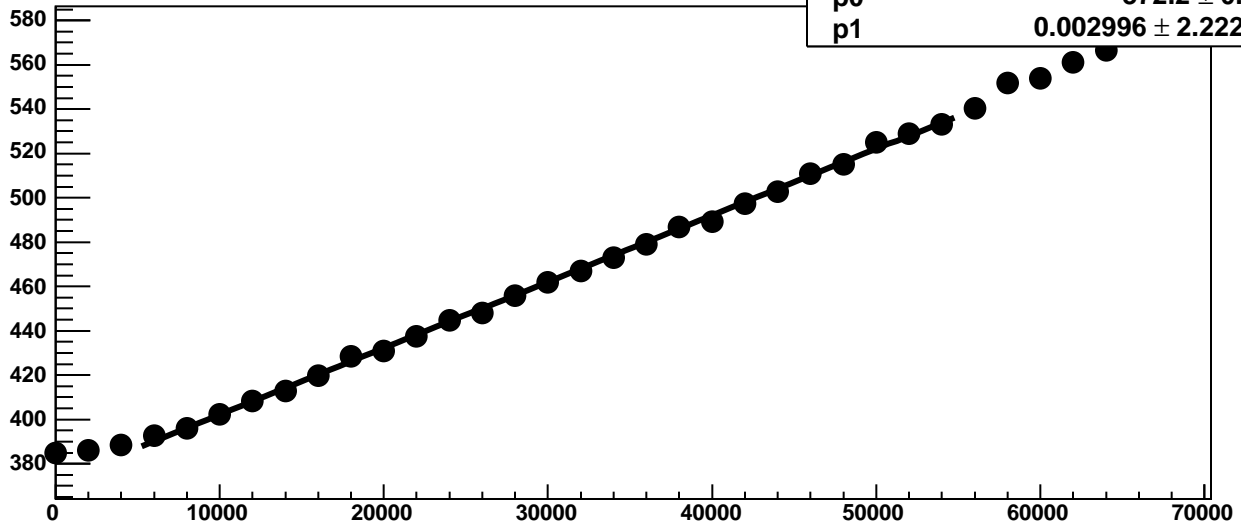
Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

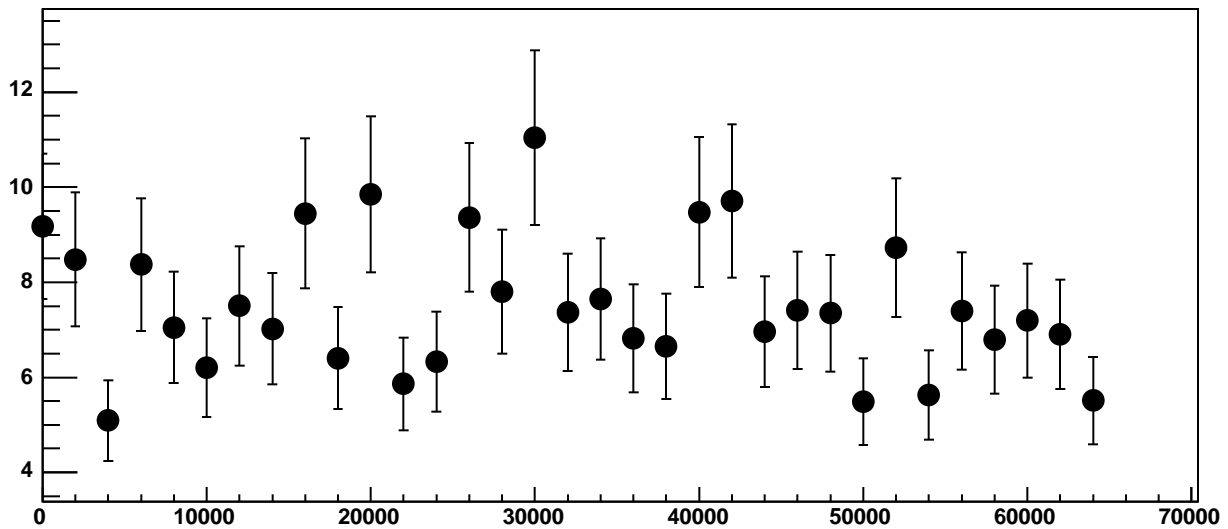


Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

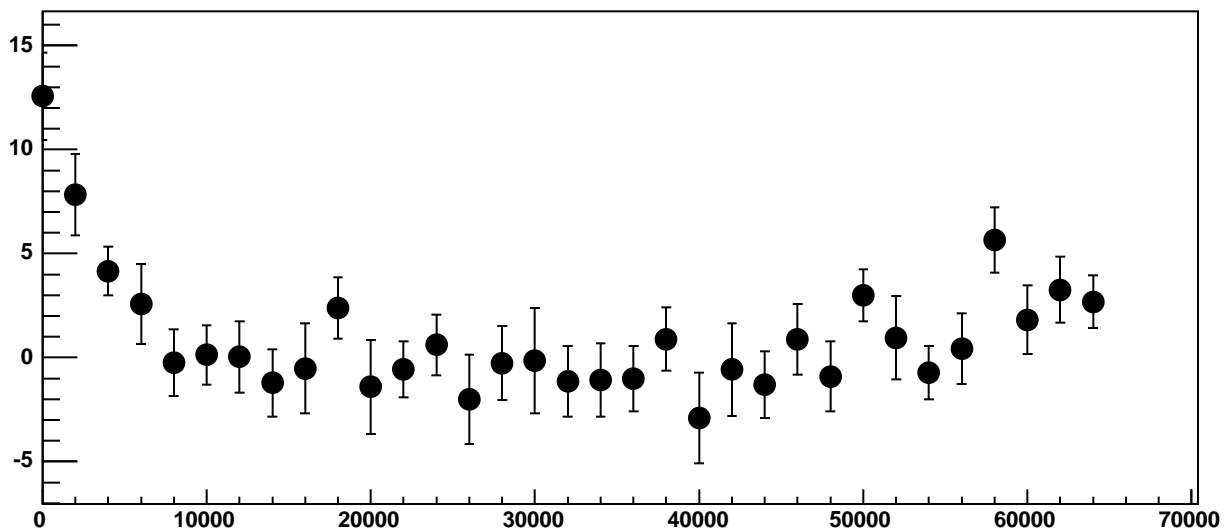


χ^2 / ndf 17.56 / 23
p0 372.2 ± 0.7591
p1 $0.002996 \pm 2.222e-05$

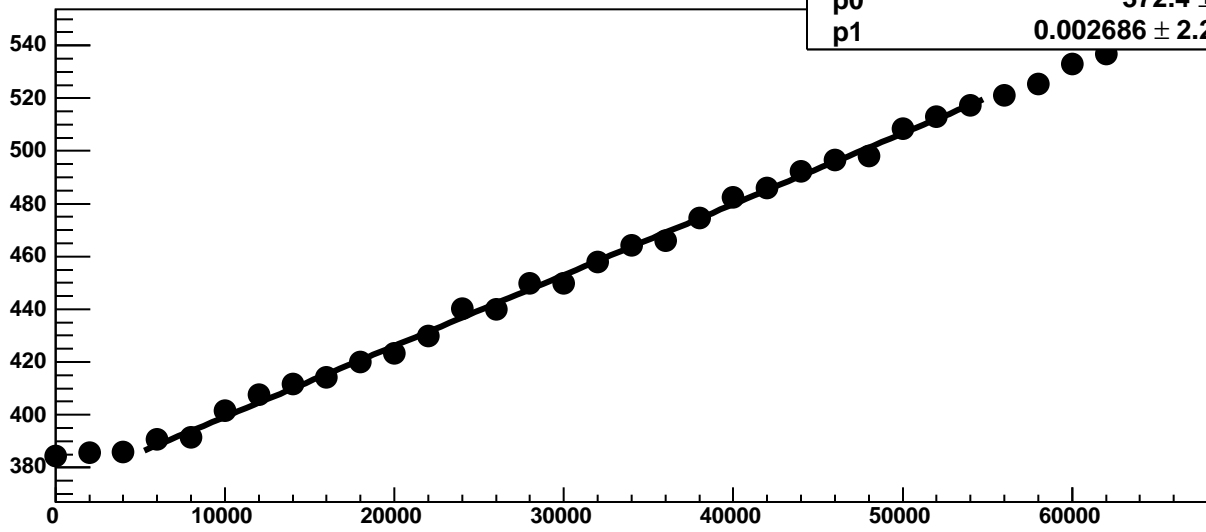
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

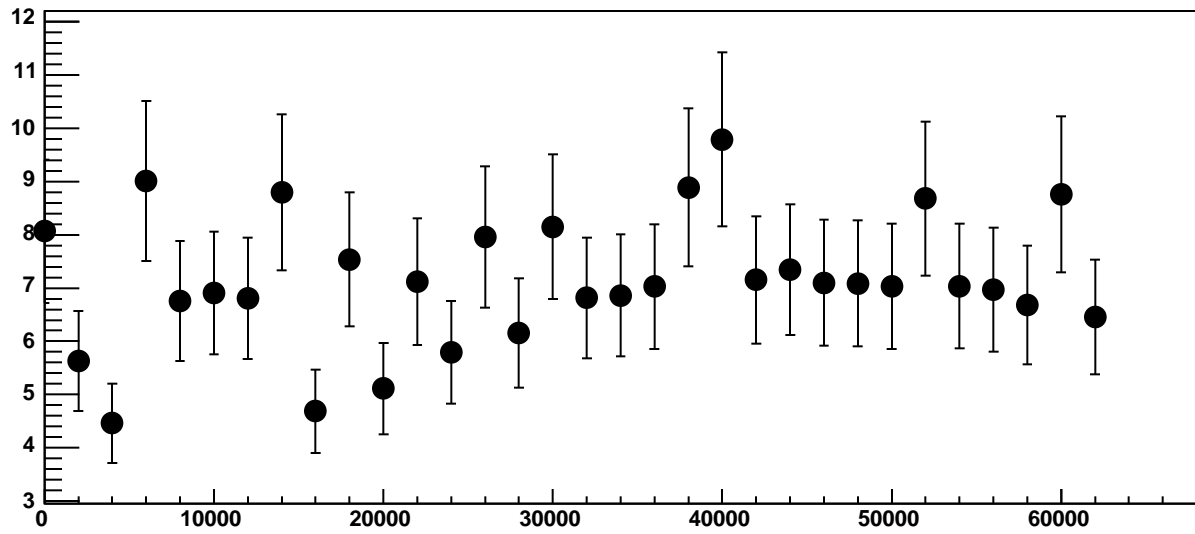


Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

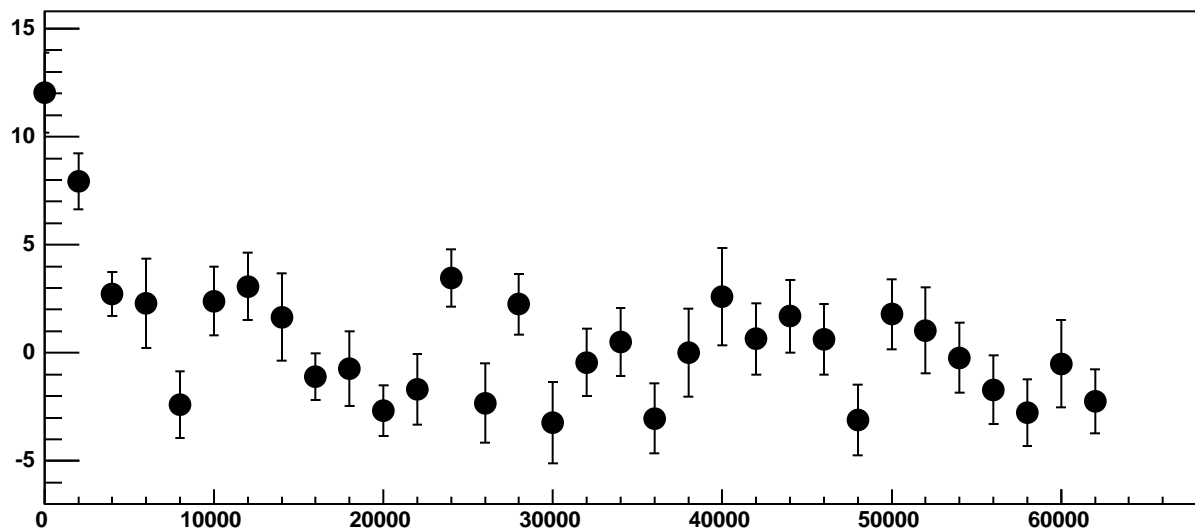


χ^2 / ndf 43.41 / 23
p0 372.4 ± 0.7301
p1 $0.002686 \pm 2.287e-05$

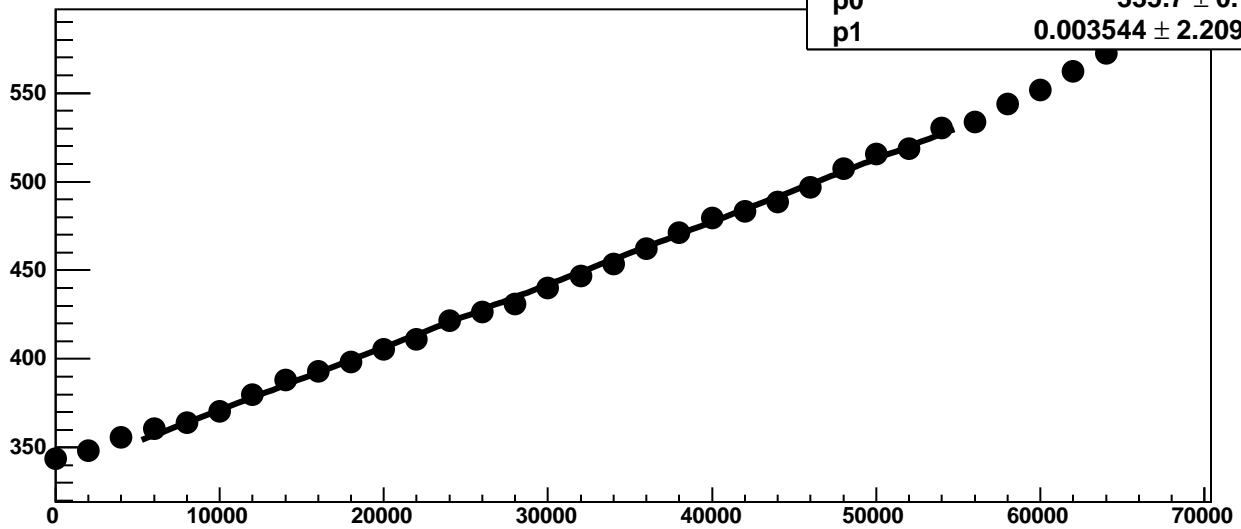
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.1 / 23

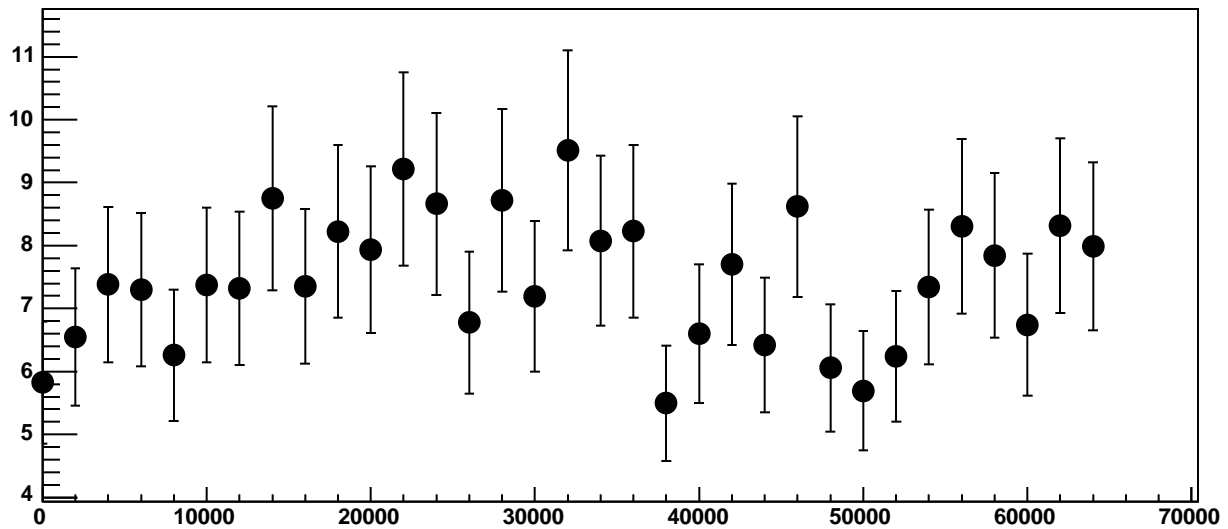
p0

335.7 ± 0.7717

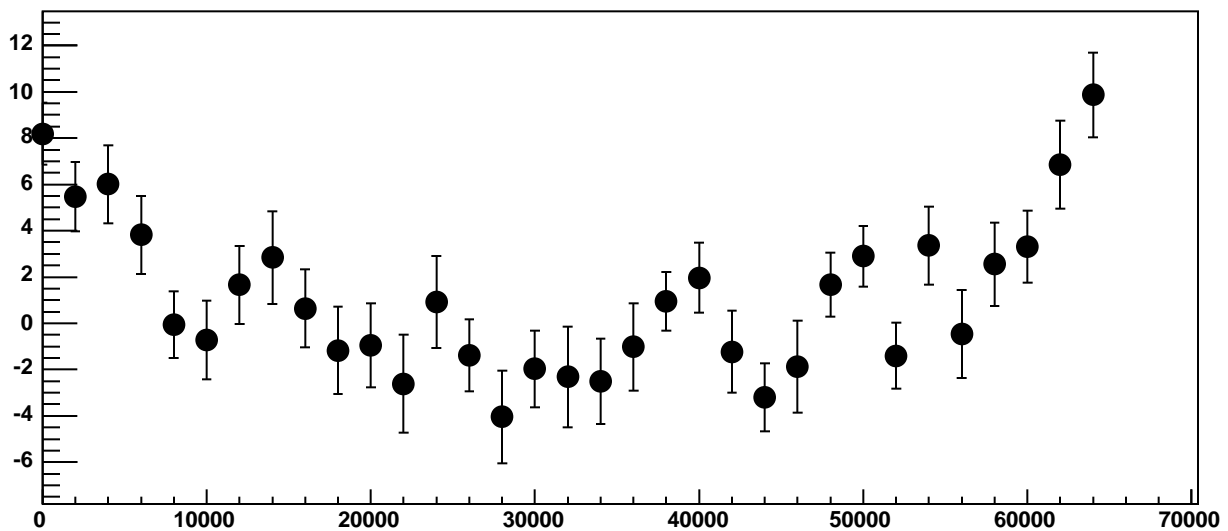
p1

$0.003544 \pm 2.209e-05$

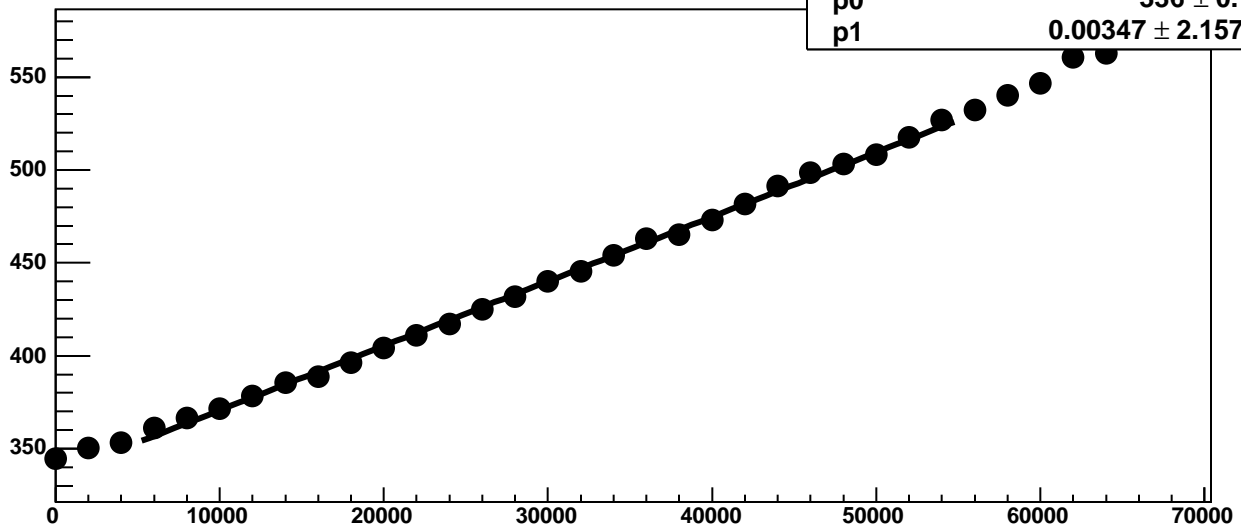
Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

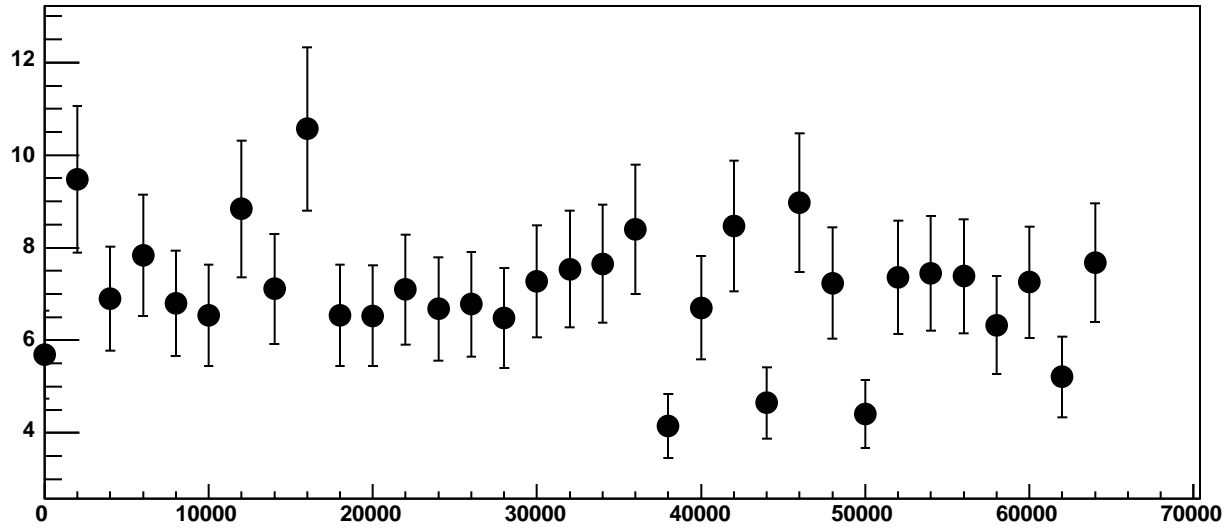


Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

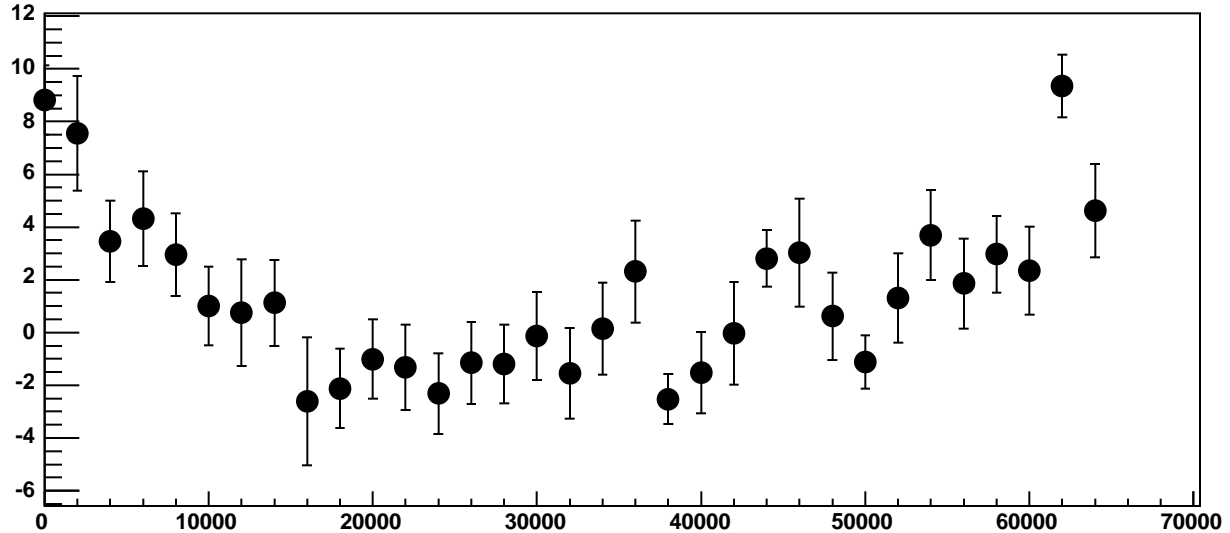


χ^2 / ndf 44.1 / 23
p0 336 ± 0.7552
p1 $0.00347 \pm 2.157e-05$

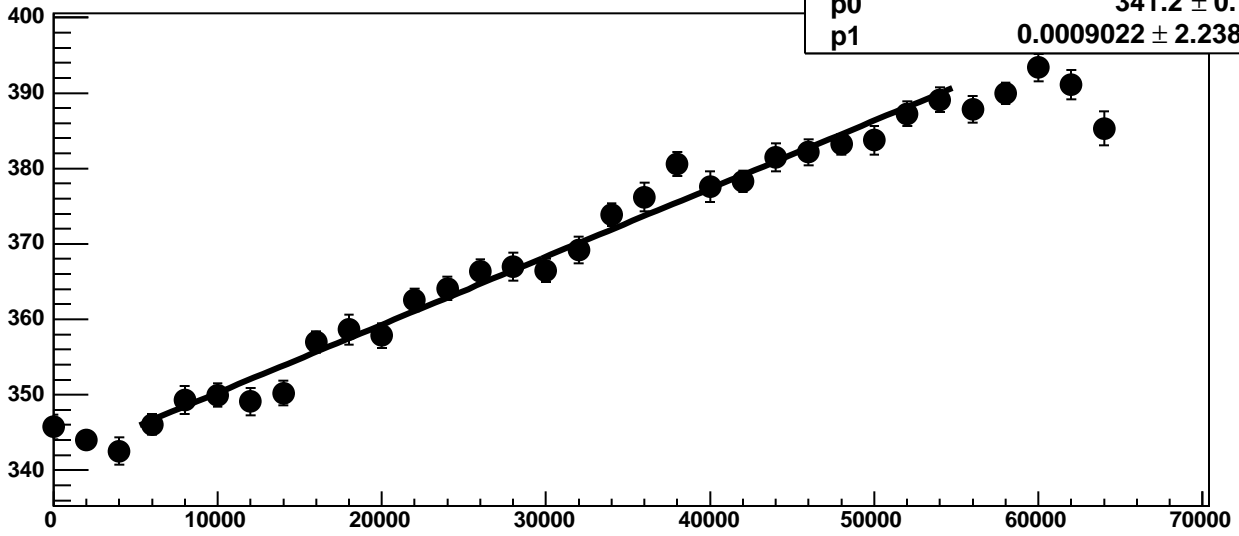
Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.3 / 23

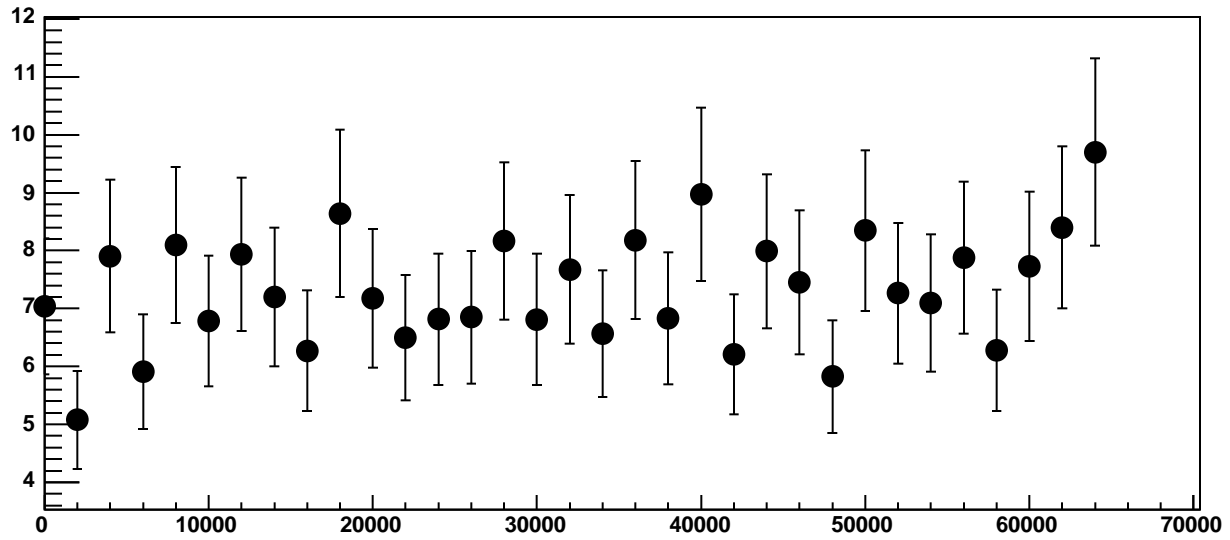
p0

341.2 ± 0.7405

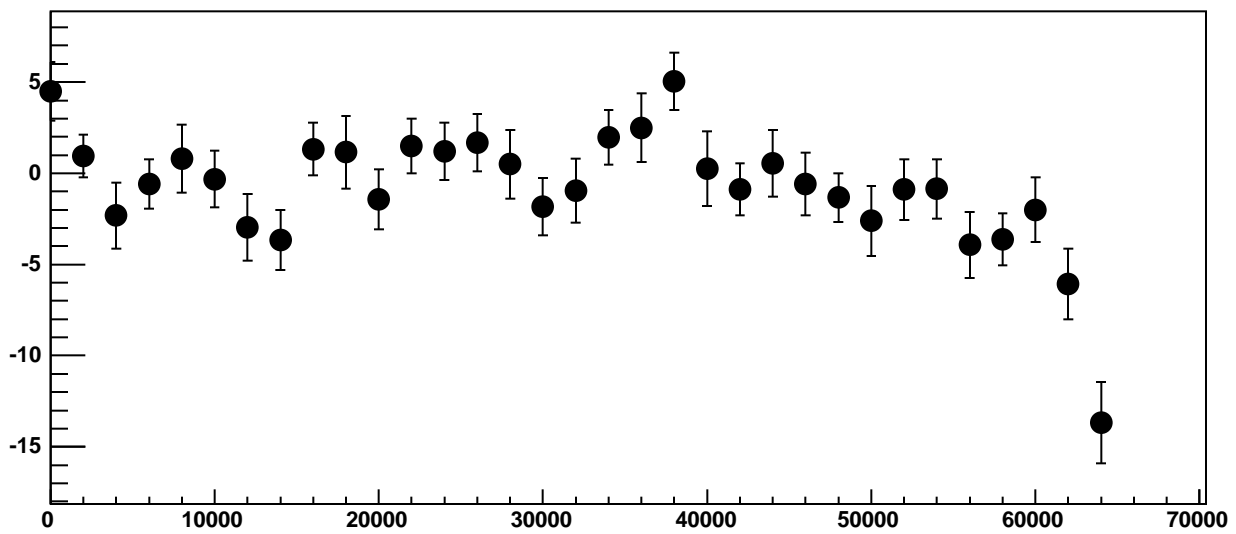
p1

$0.0009022 \pm 2.238e-05$

Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

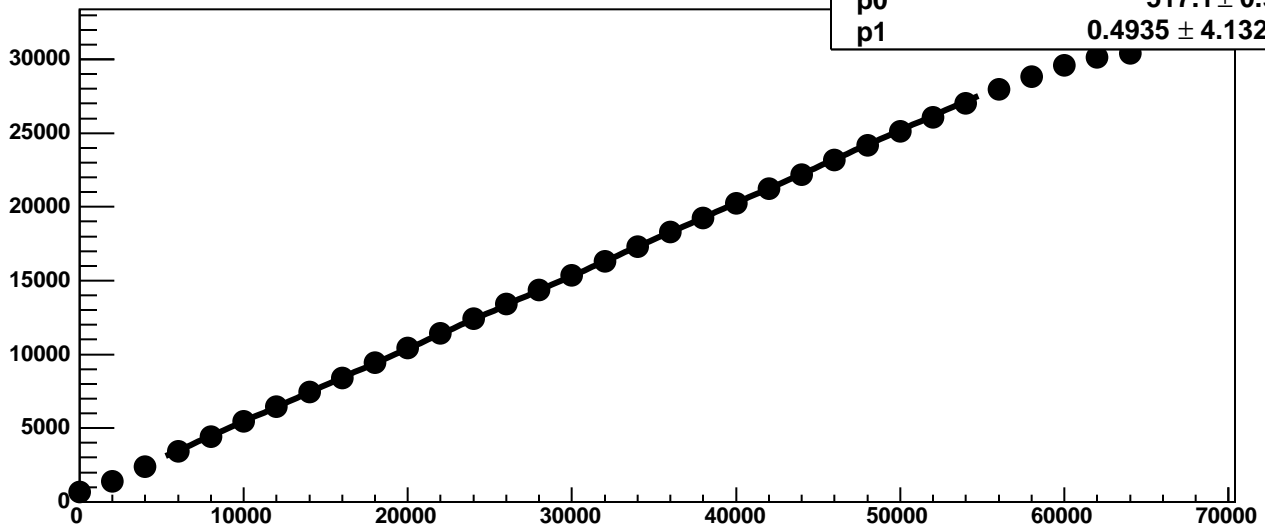


Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

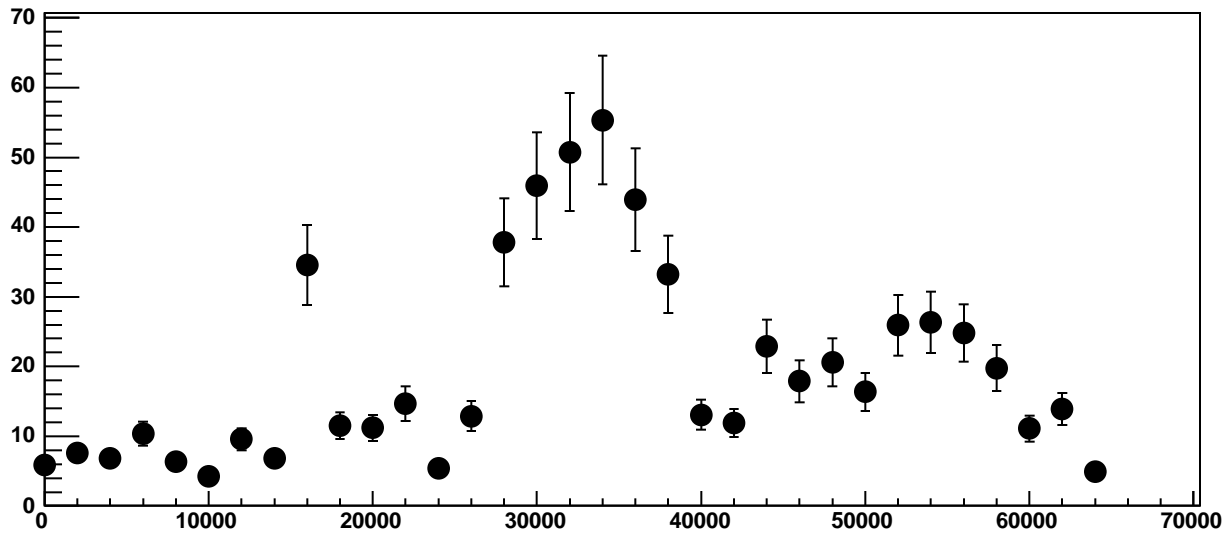


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

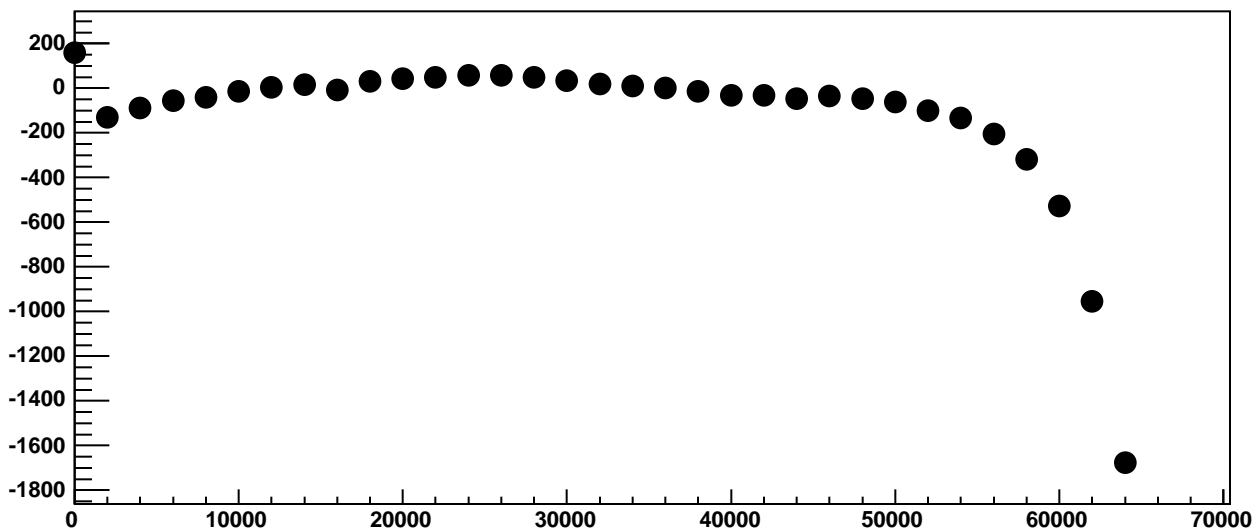
χ^2 / ndf 6262 / 23
p0 517.1 ± 0.9099
p1 $0.4935 \pm 4.132e-05$



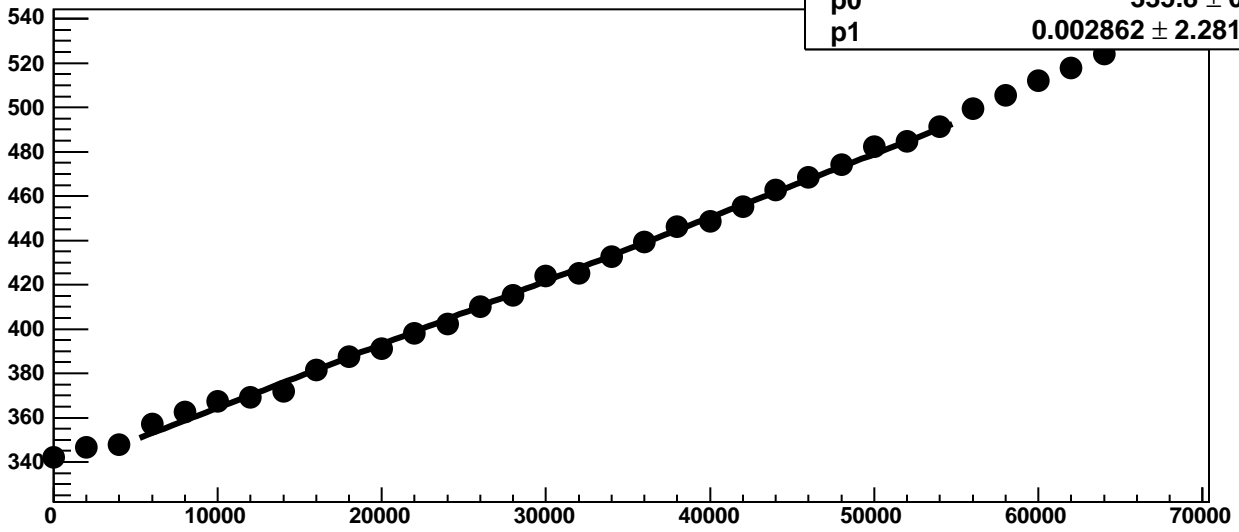
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

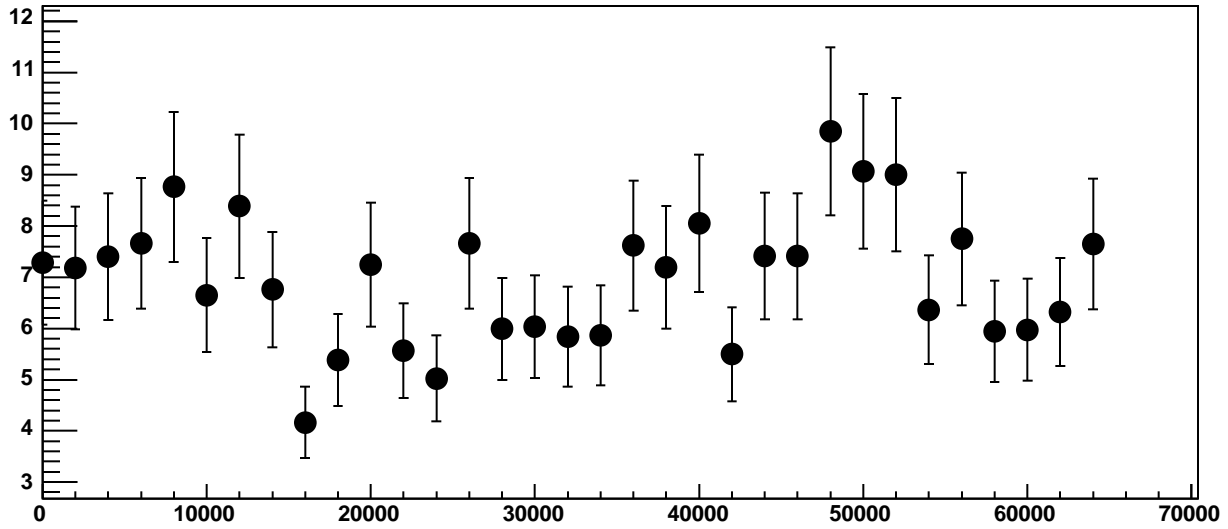


Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

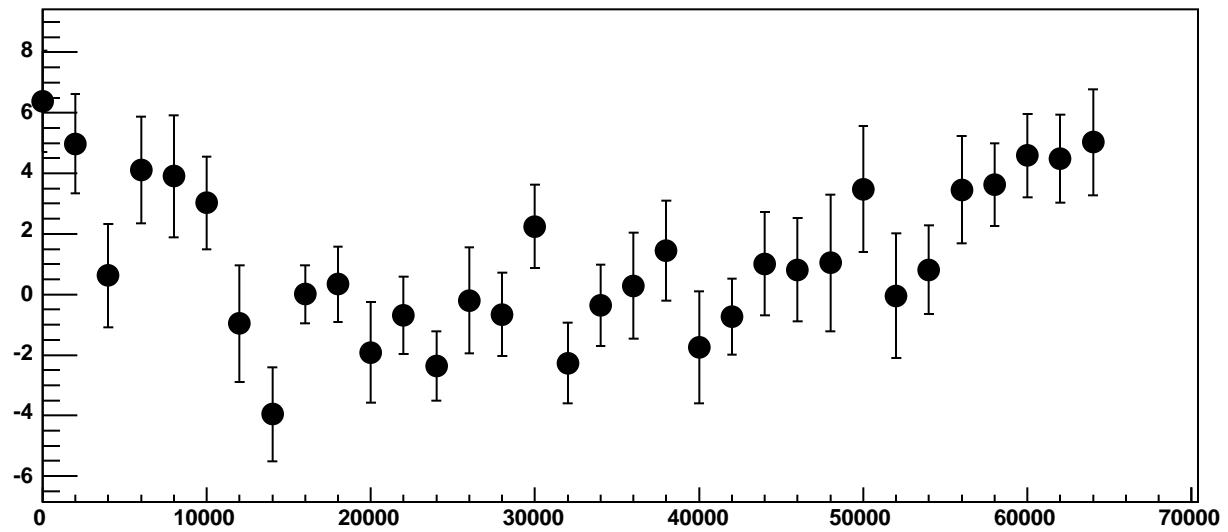


χ^2 / ndf 37.55 / 23
p0 335.8 ± 0.713
p1 $0.002862 \pm 2.281\text{e-}05$

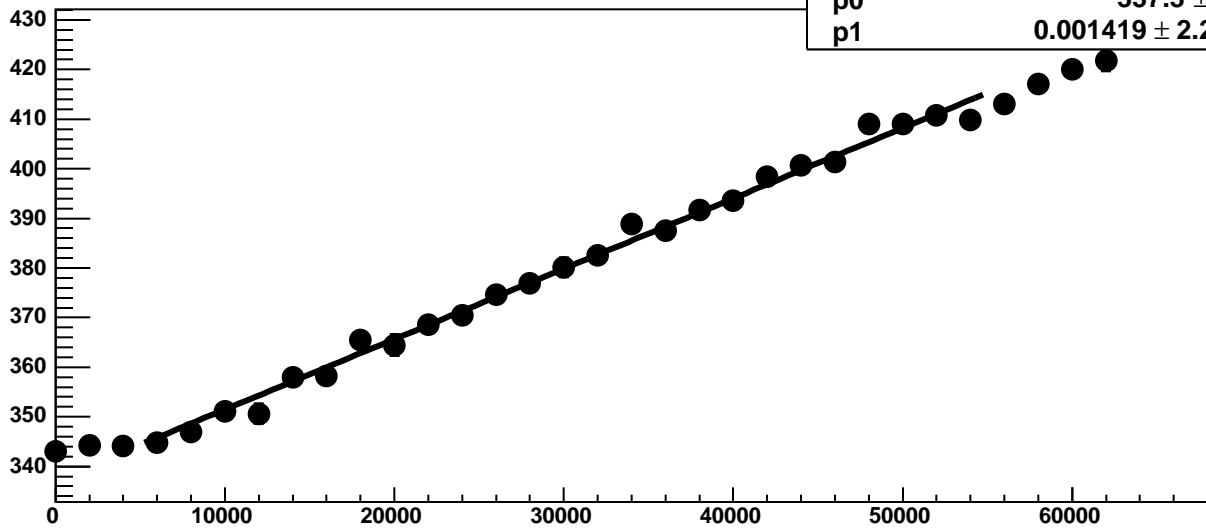
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



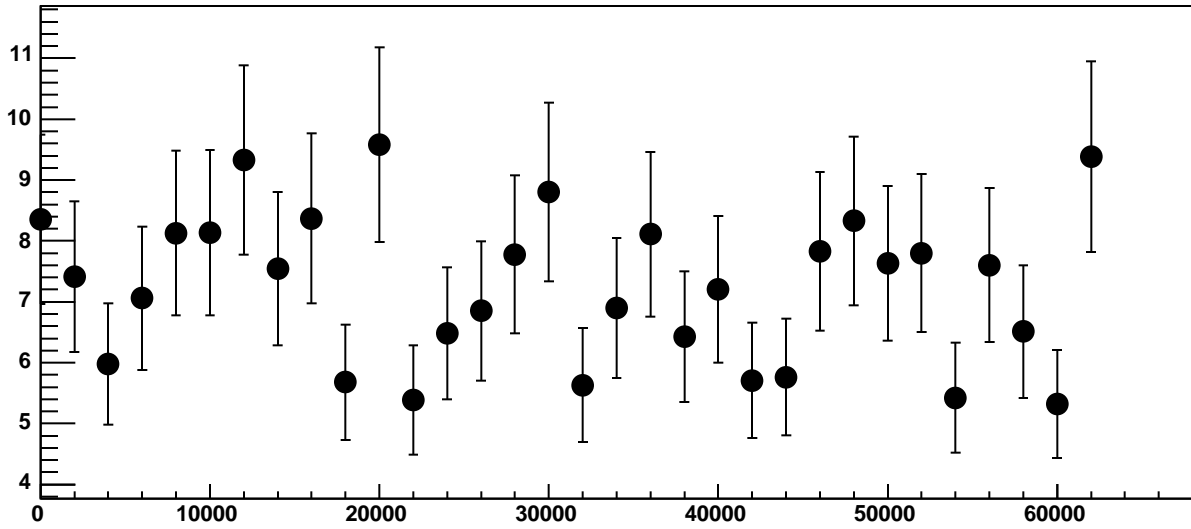
Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



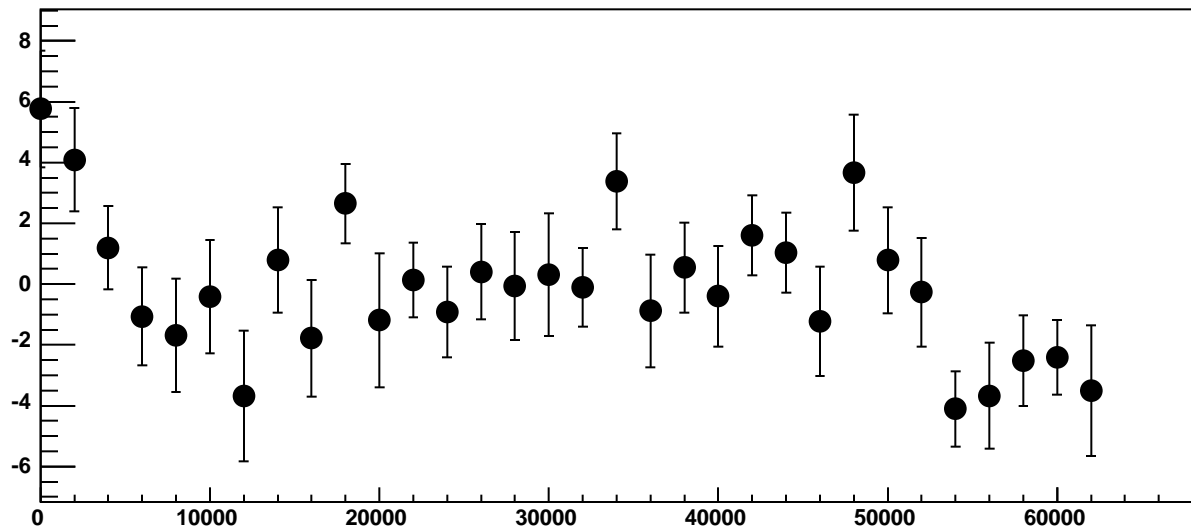
Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



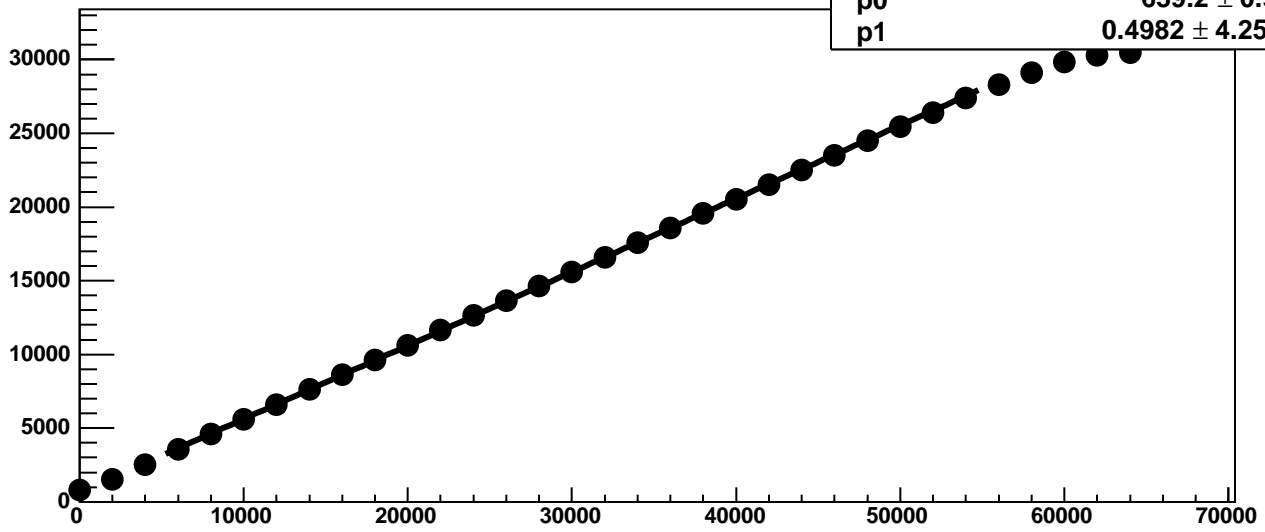
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



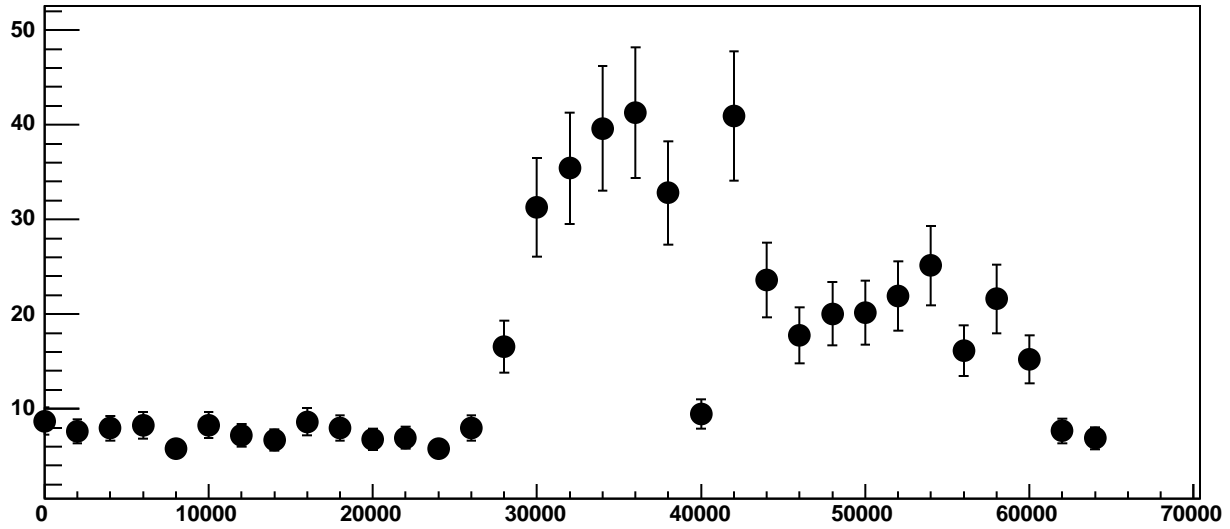
Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



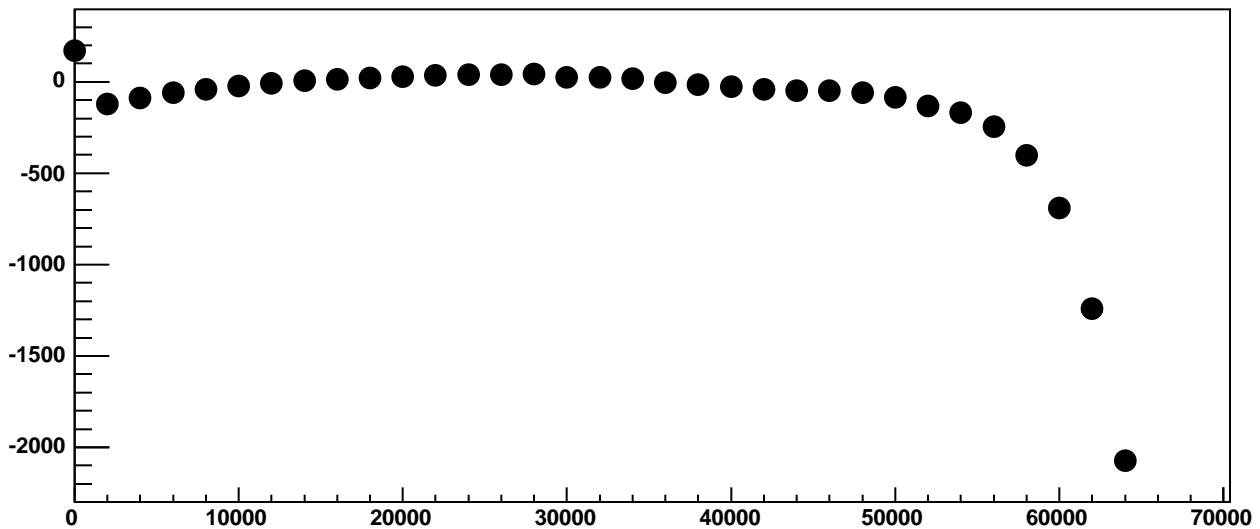
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



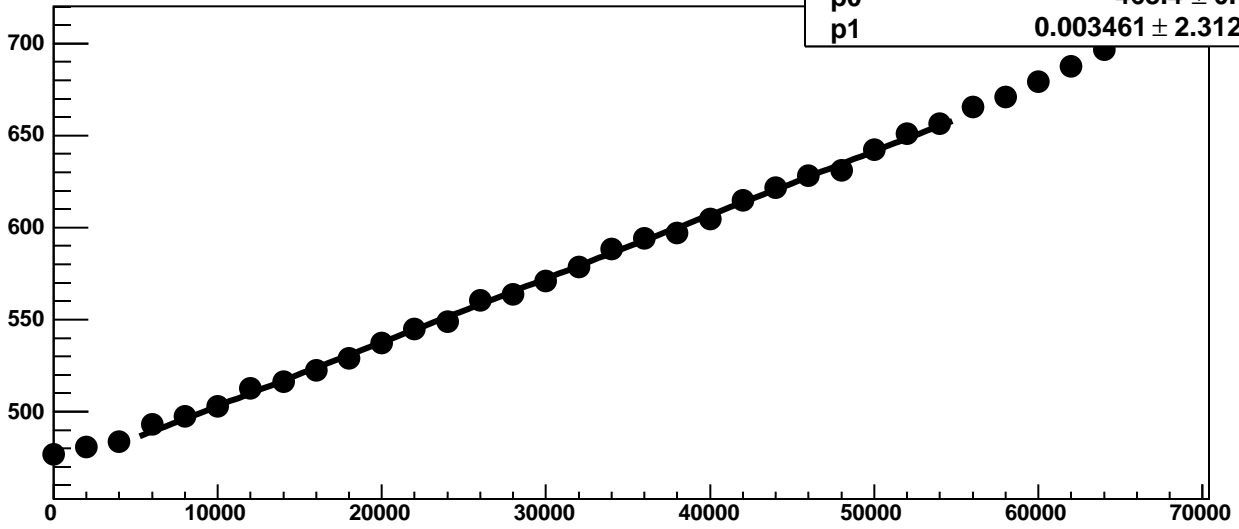
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

32.73 / 23

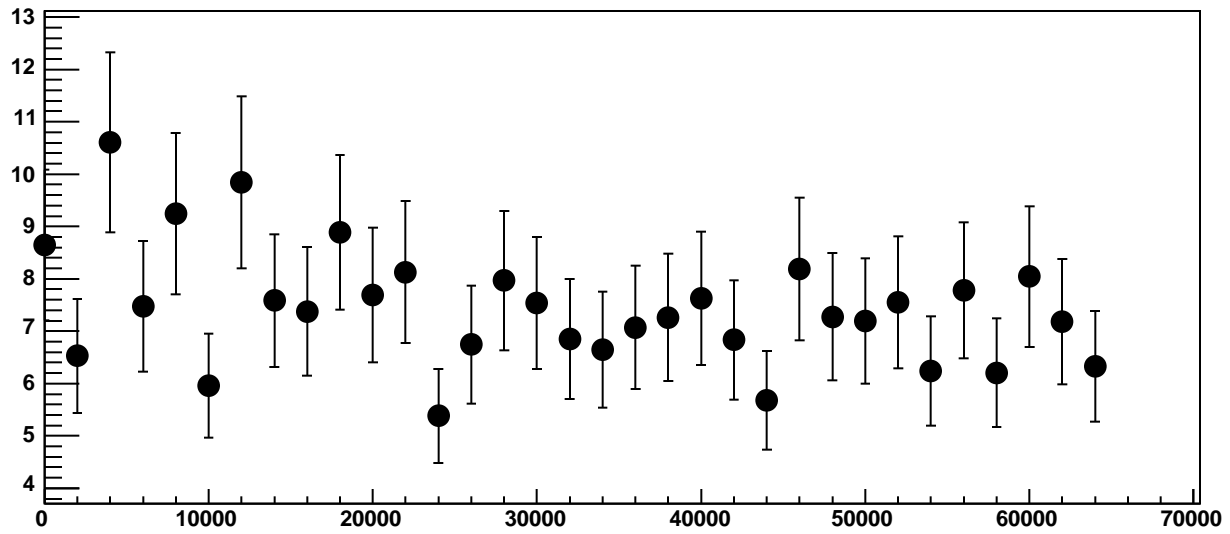
p0

468.4 ± 0.7888

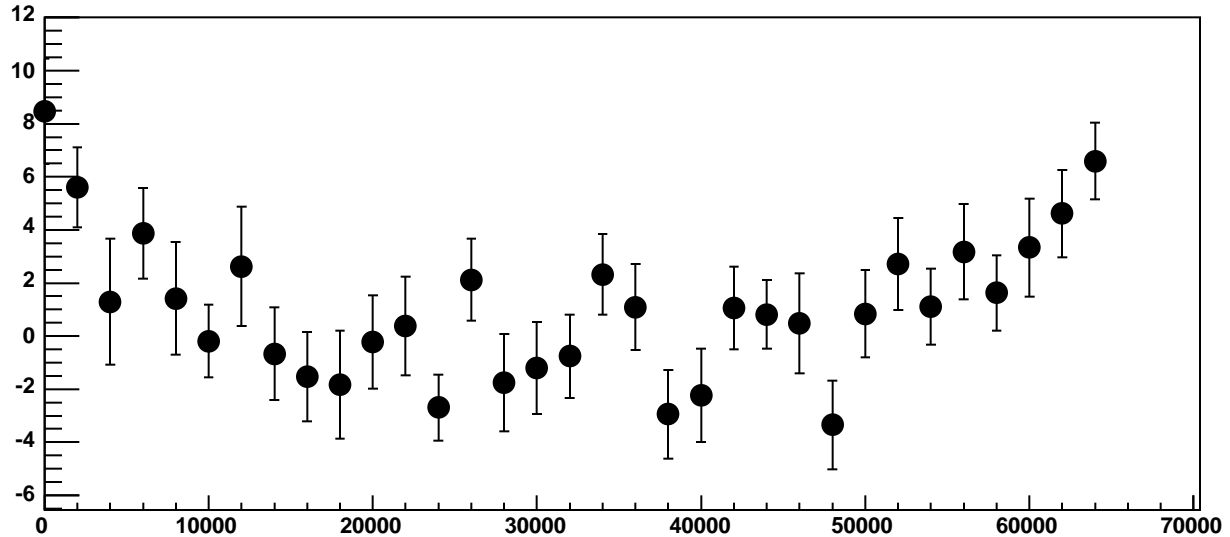
p1

$0.003461 \pm 2.312e-05$

Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

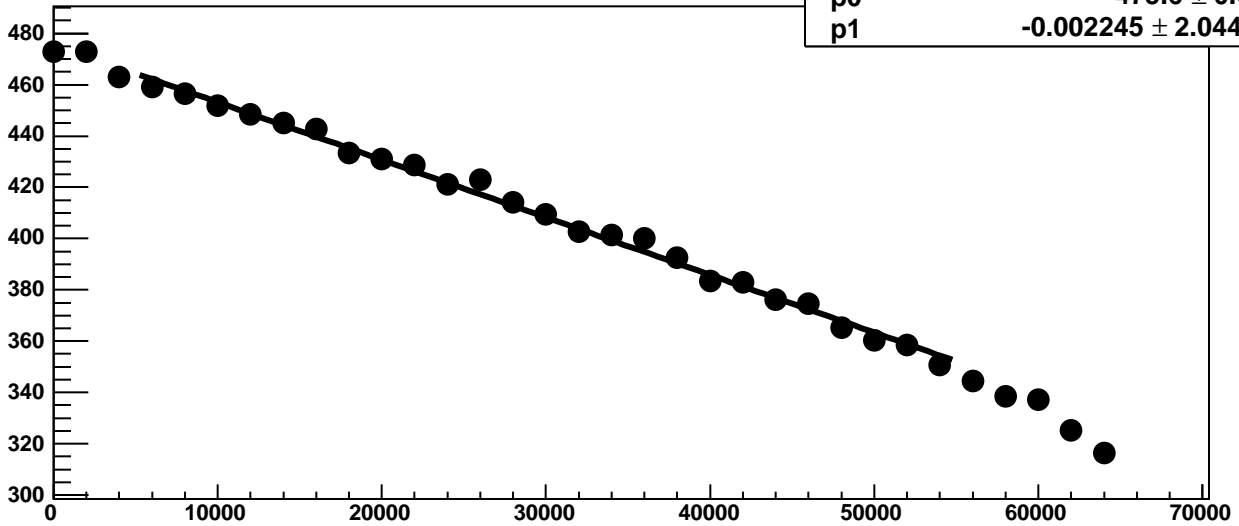


Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

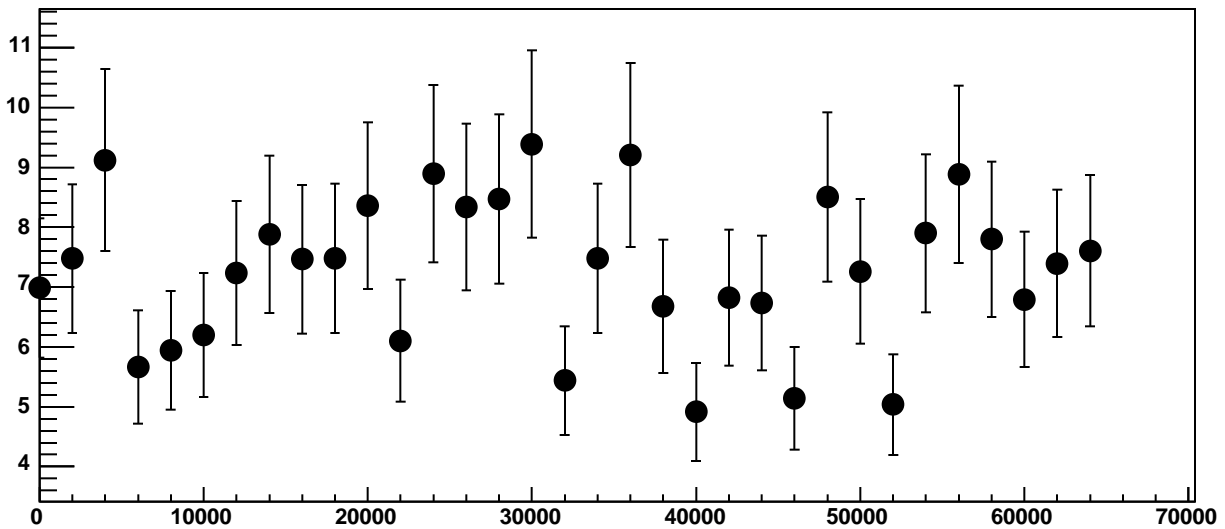


Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

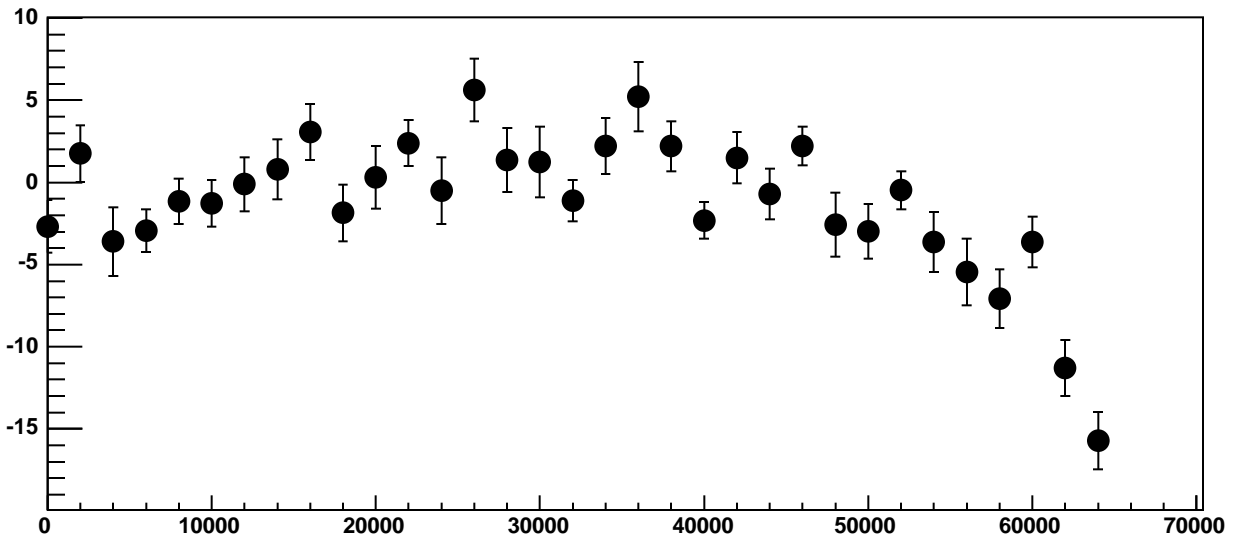
χ^2 / ndf 52.33 / 23
p0 475.6 ± 0.6998
p1 $-0.002245 \pm 2.044e-05$



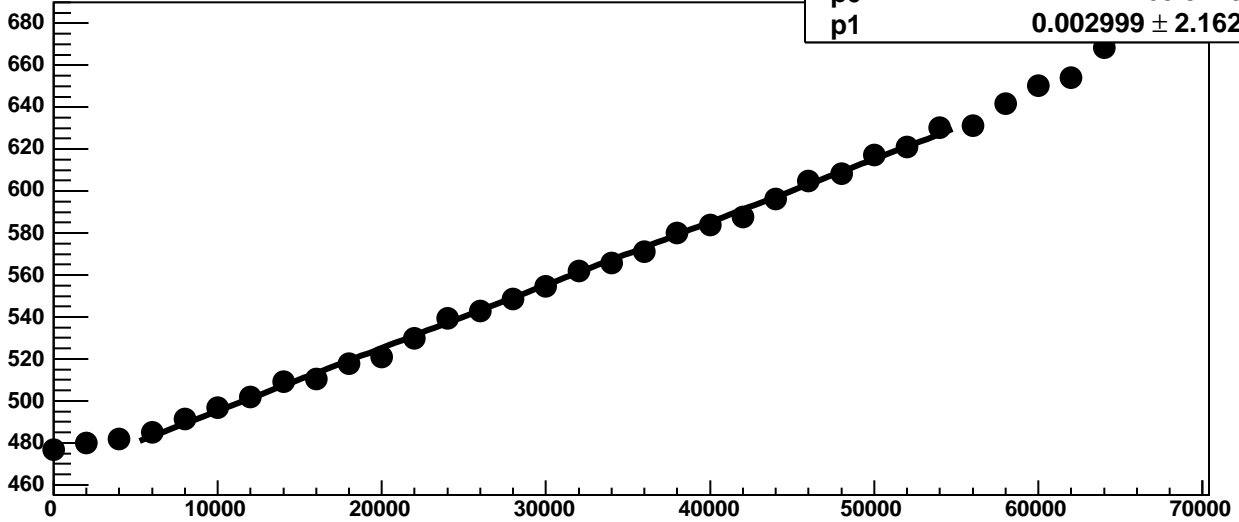
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

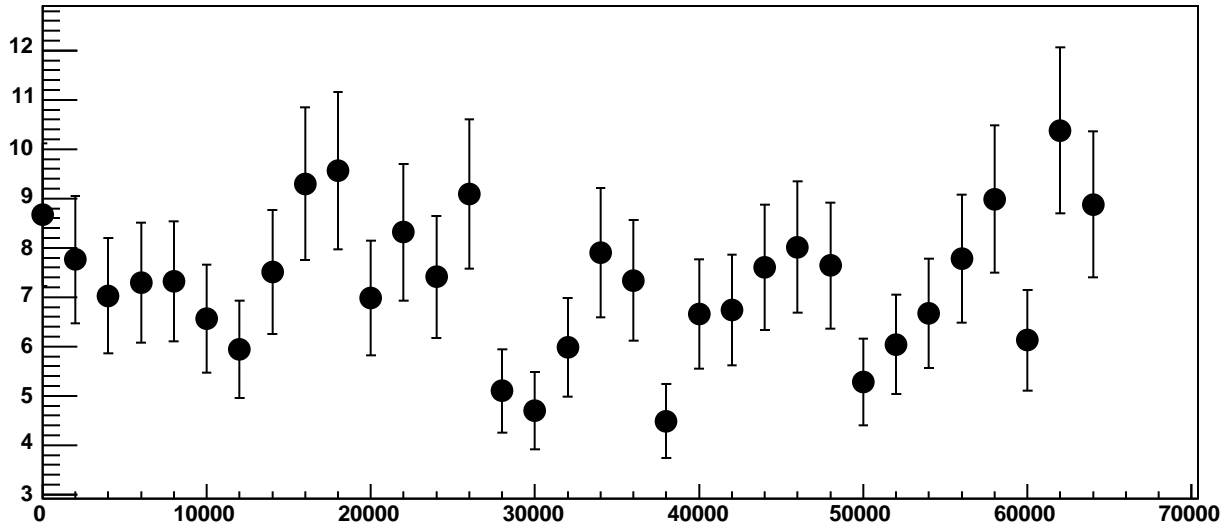


Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

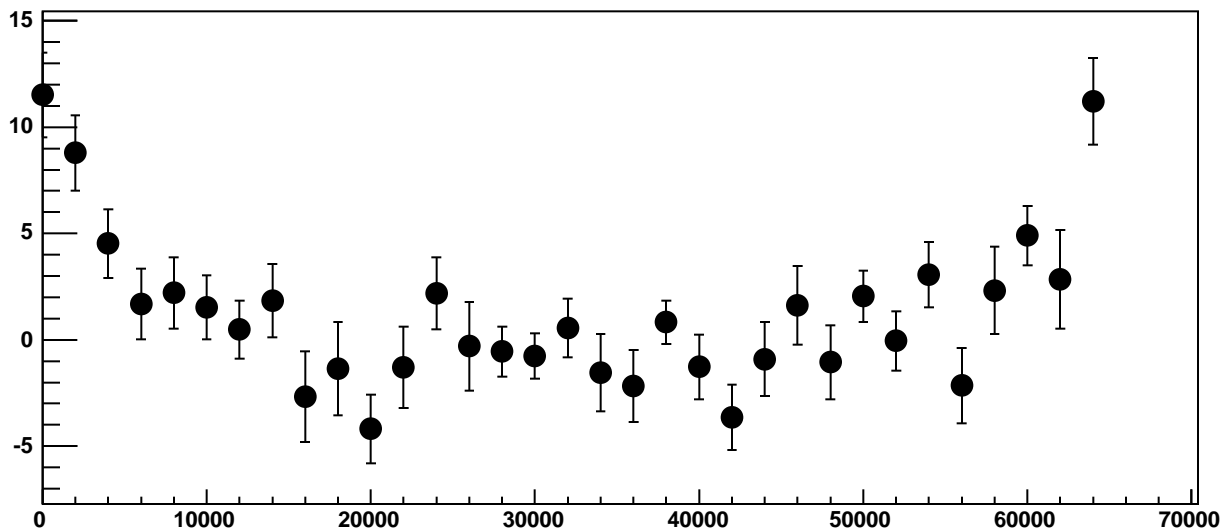


χ^2 / ndf 34.43 / 23
p0 465.3 ± 0.744
p1 $0.002999 \pm 2.162e-05$

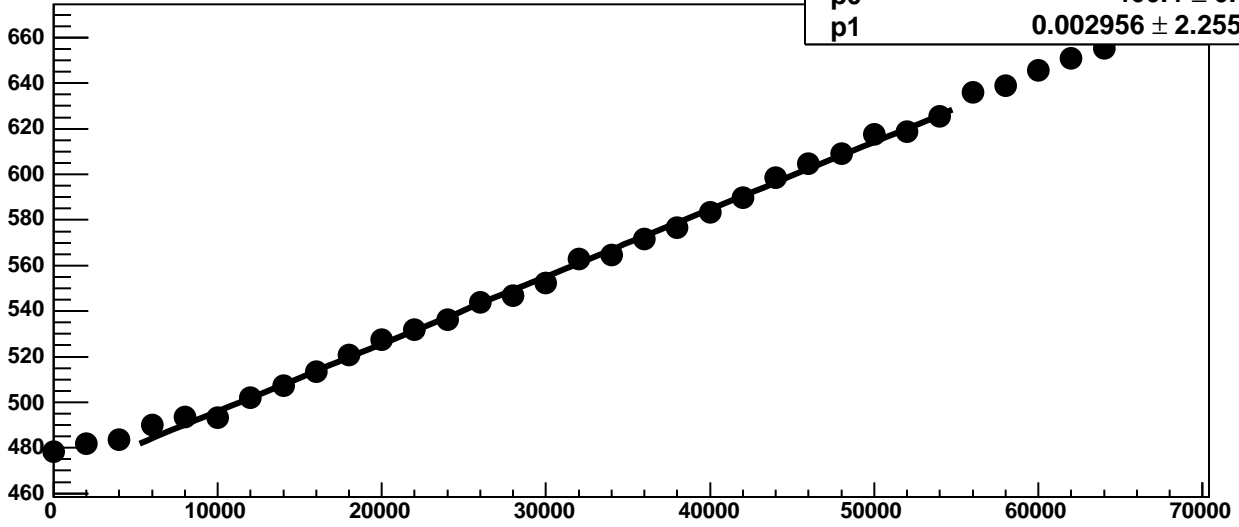
Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

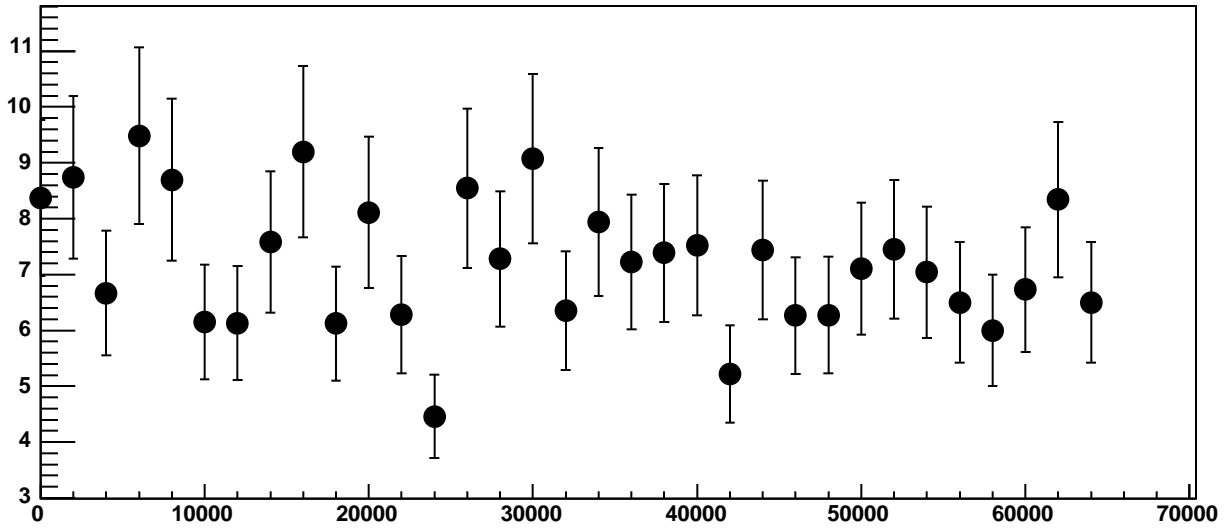


Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

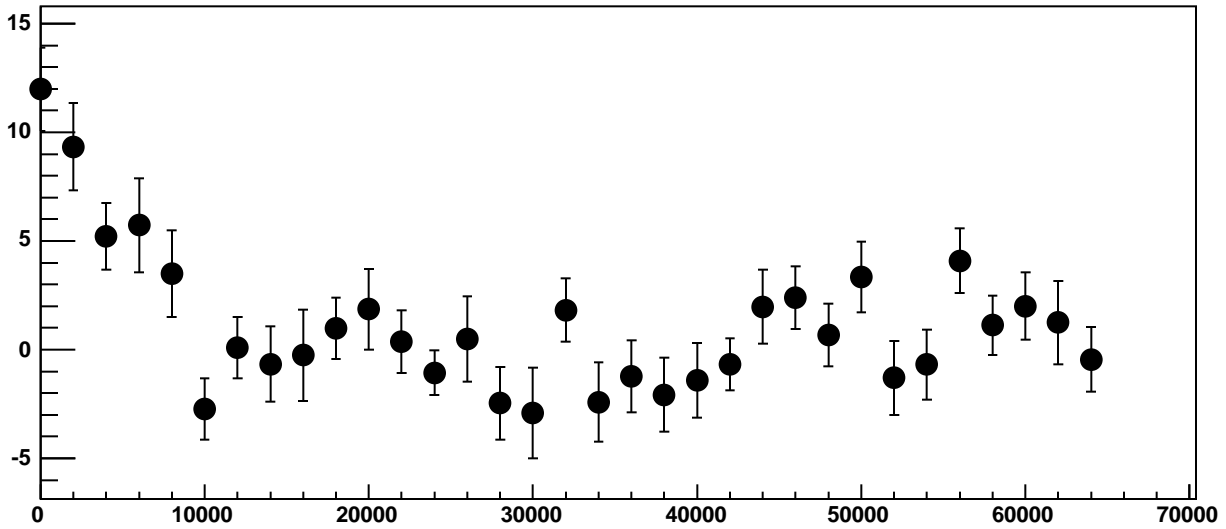


χ^2 / ndf 36.36 / 23
p0 466.4 \pm 0.7585
p1 0.002956 \pm 2.255e-05

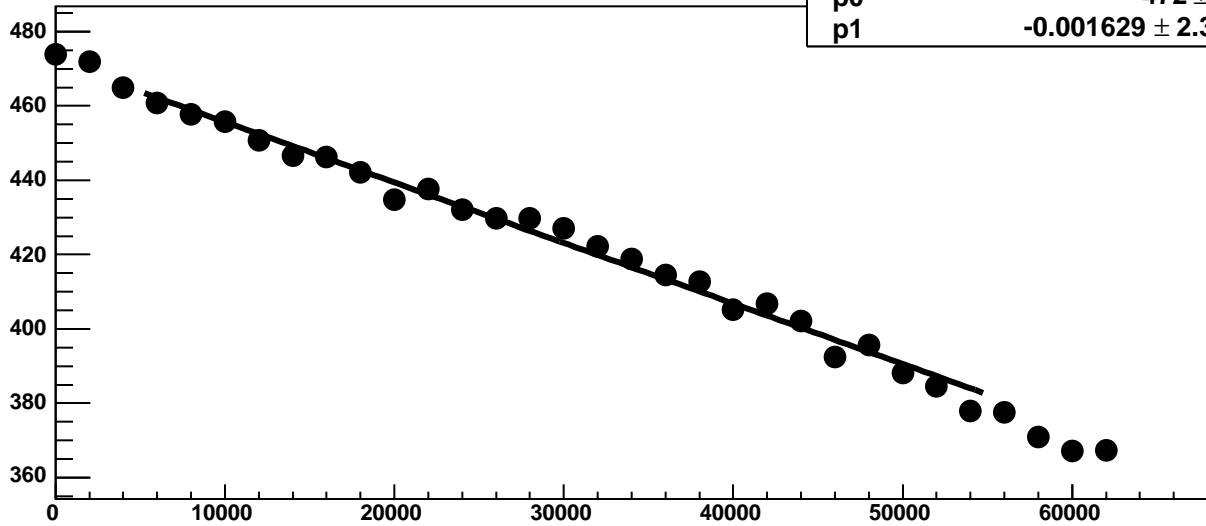
Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

72.57 / 23

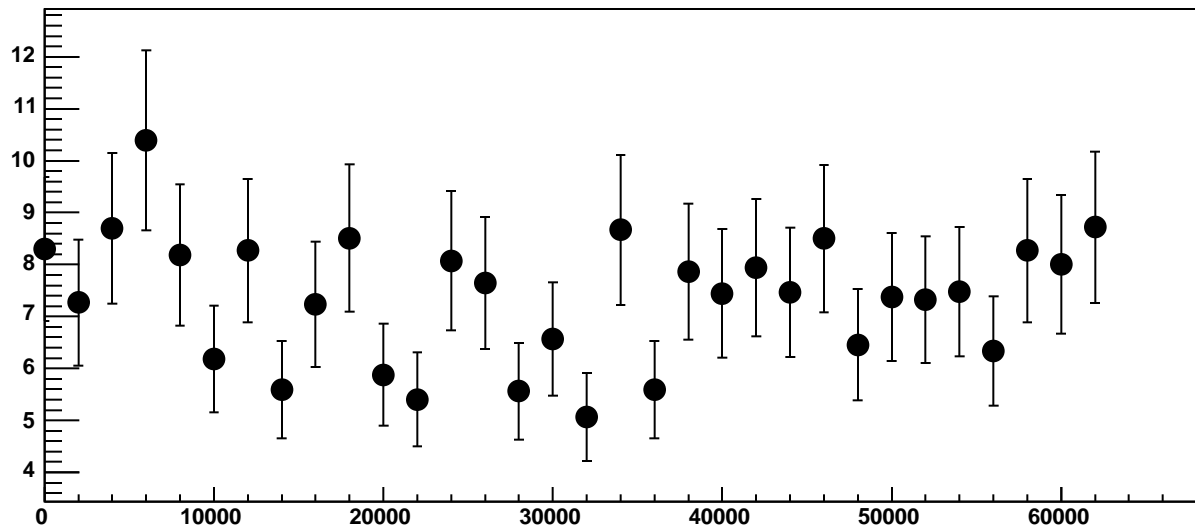
p0

472 ± 0.7655

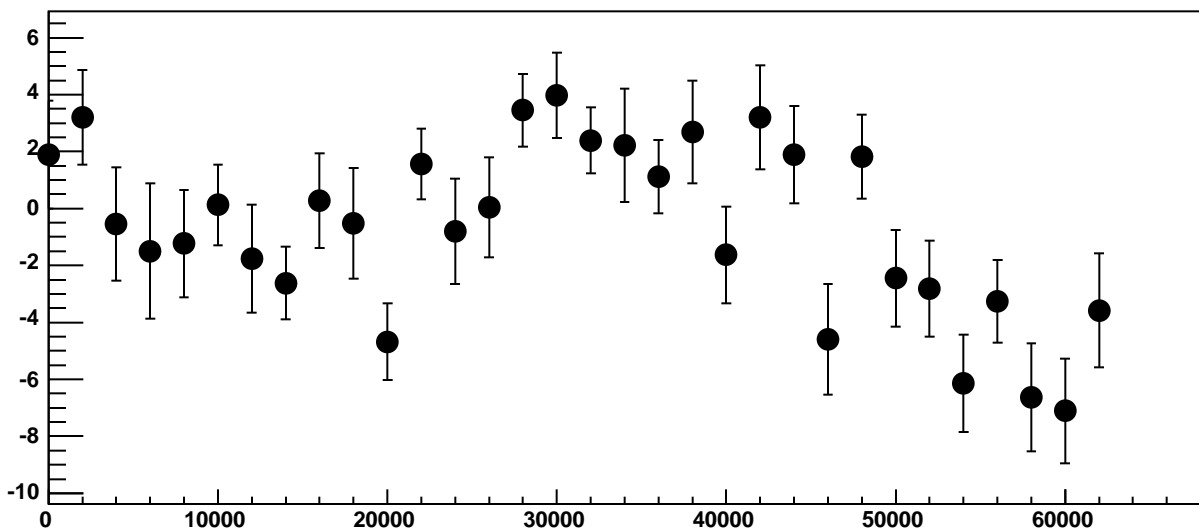
p1

$-0.001629 \pm 2.344e-05$

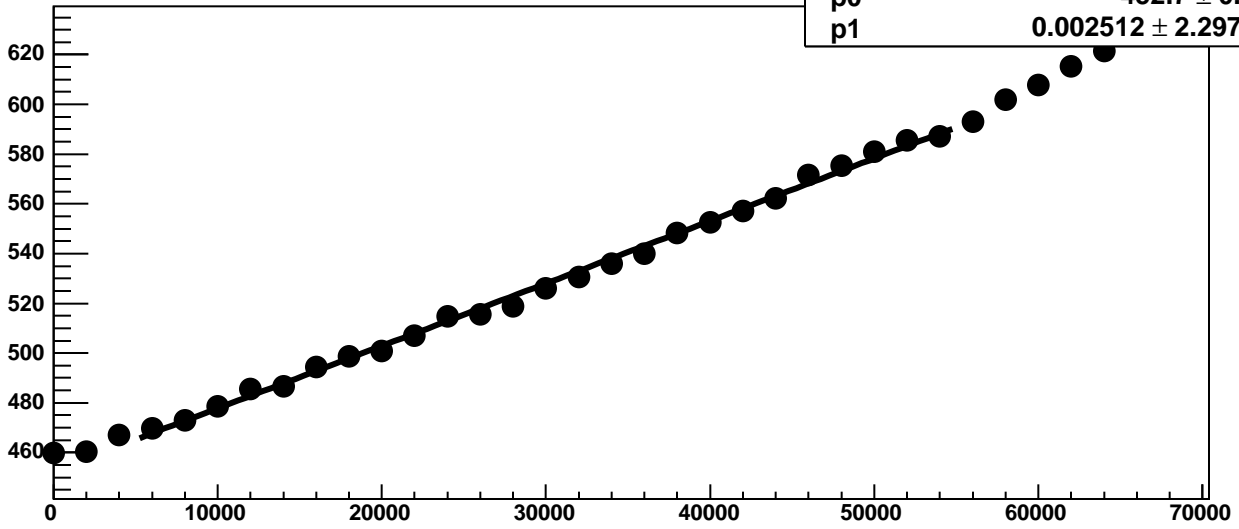
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.95 / 23

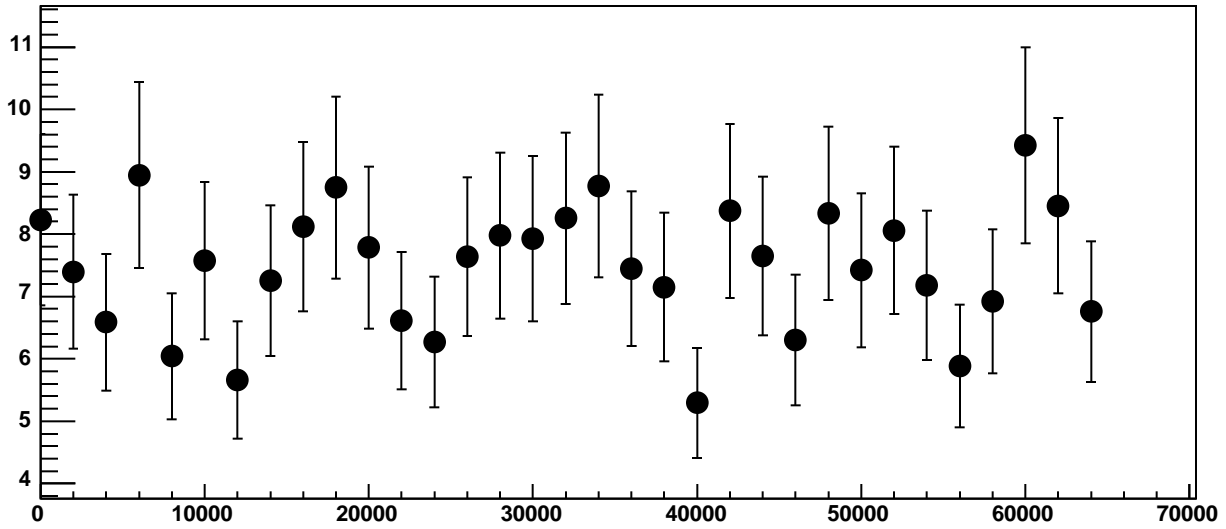
p0

452.7 ± 0.7591

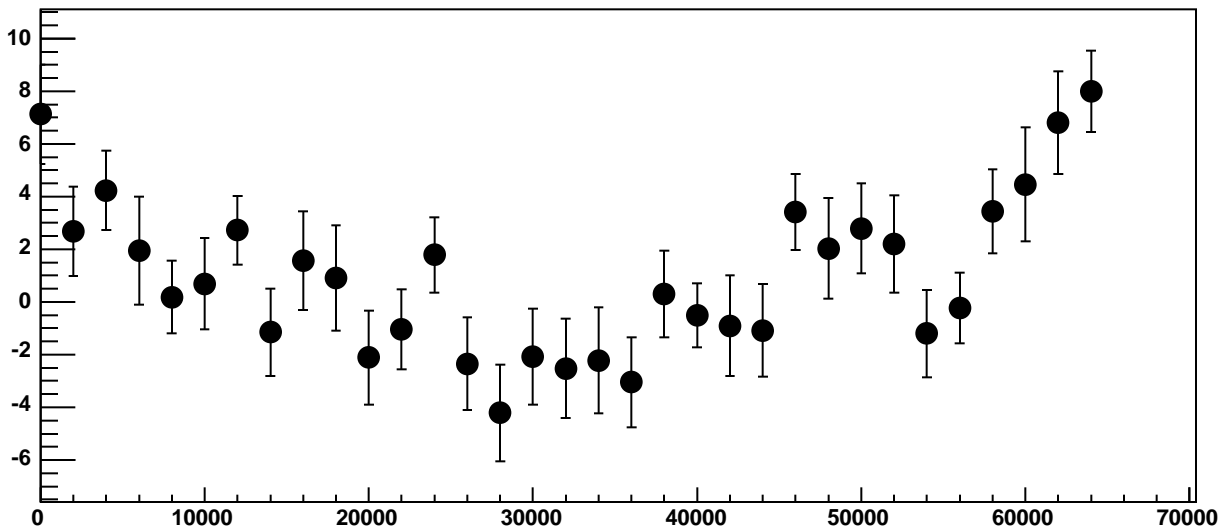
p1

$0.002512 \pm 2.297\text{e-}05$

Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

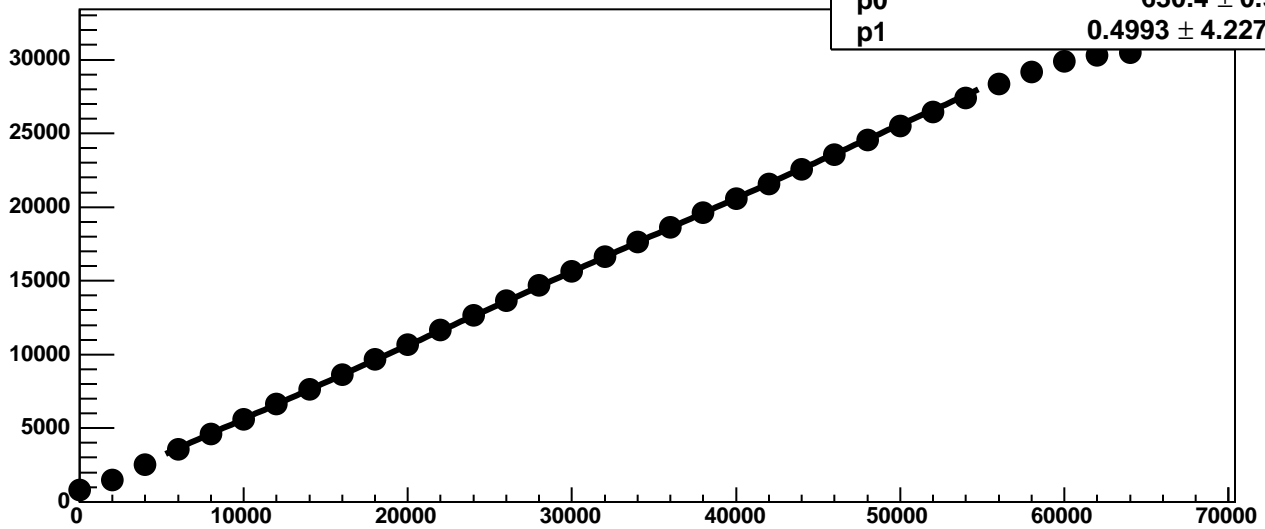


Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

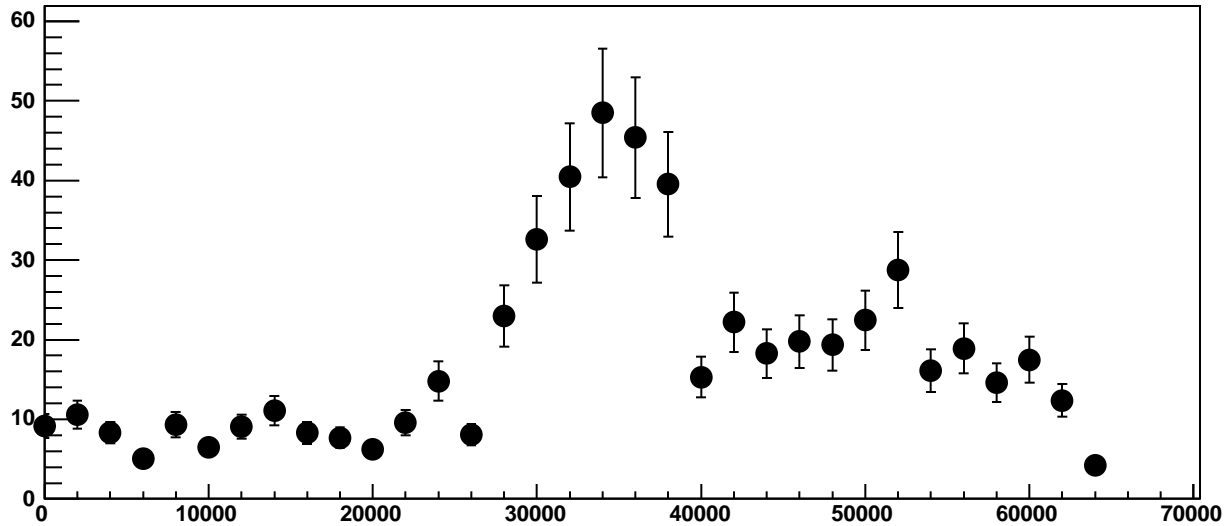


Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

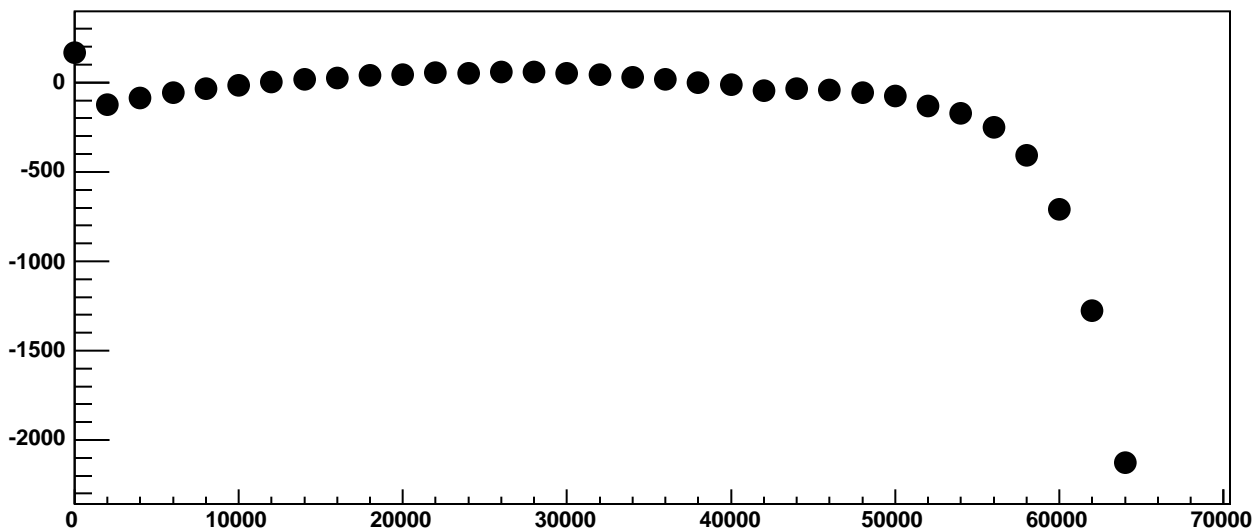
χ^2 / ndf 9689 / 23
p0 630.4 ± 0.9132
p1 $0.4993 \pm 4.227\text{e-}05$



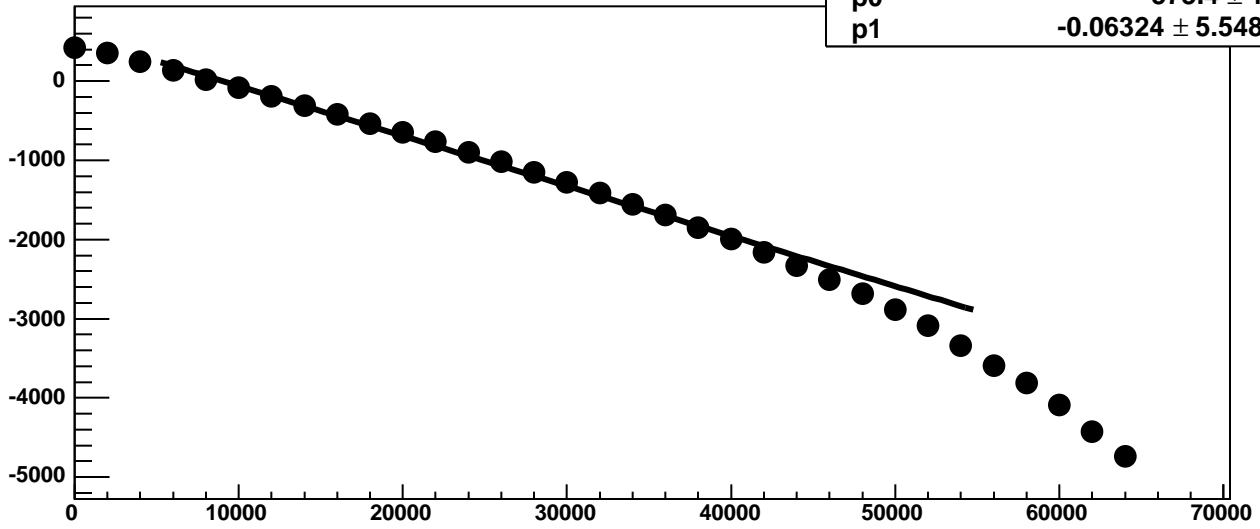
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

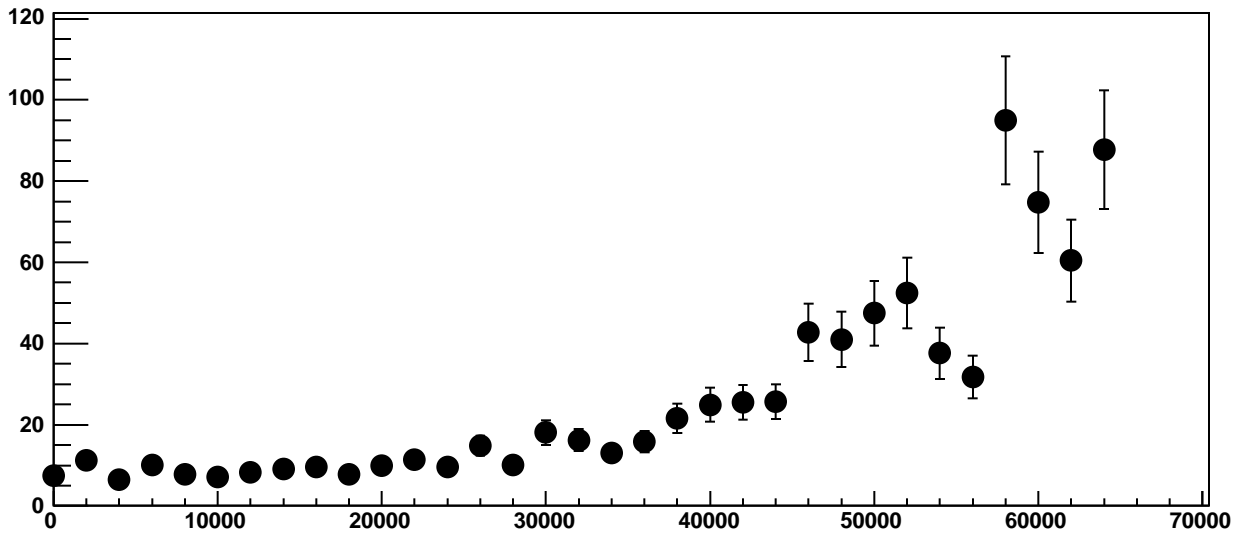


Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

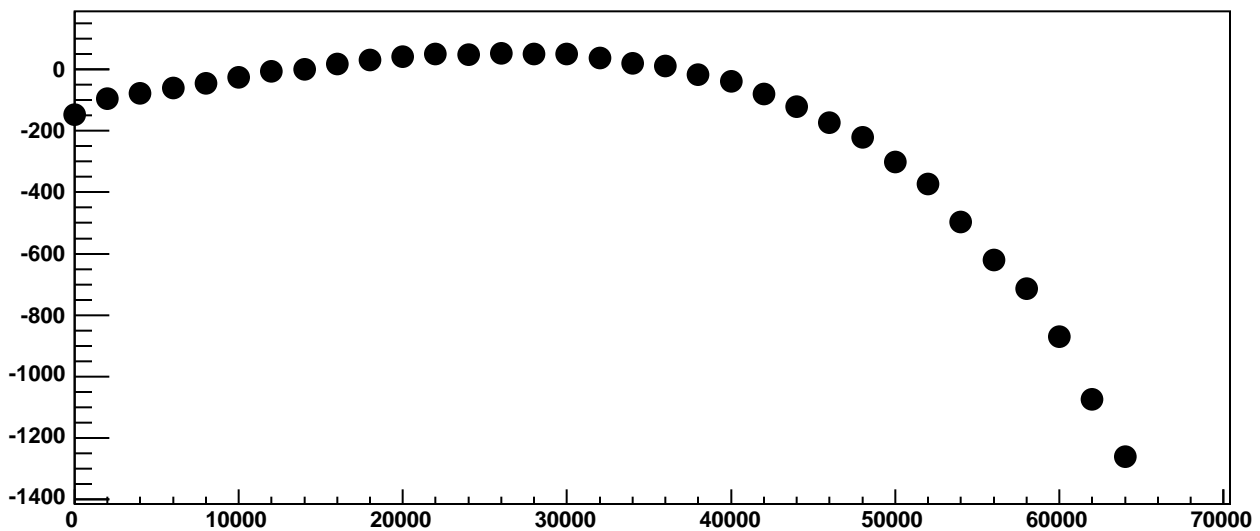


χ^2 / ndf 1.075e+04 / 23
p0 573.4 ± 1.185
p1 -0.06324 ± 5.548e-05

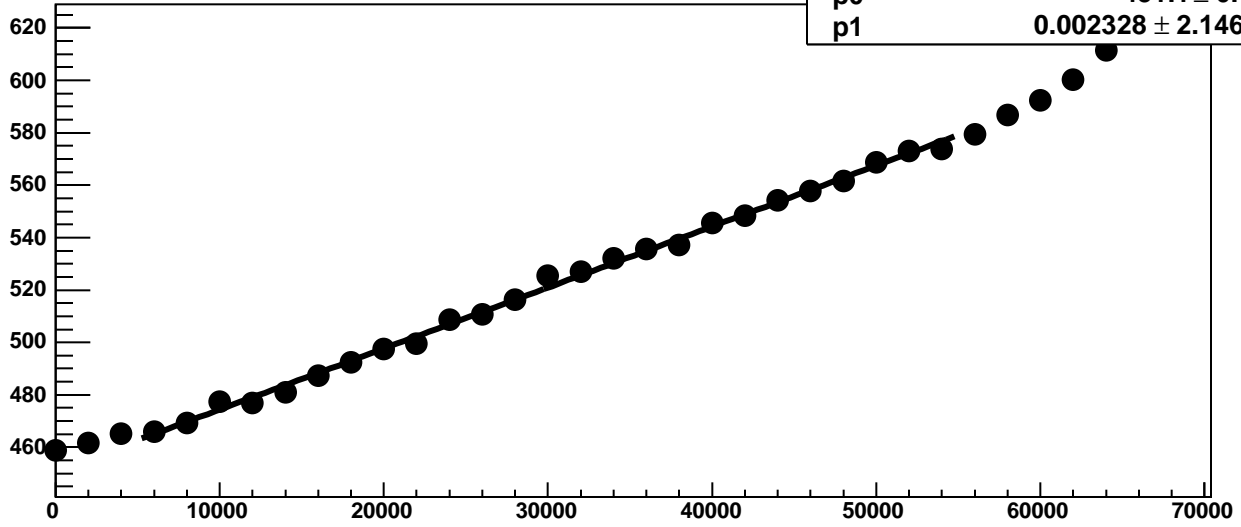
Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

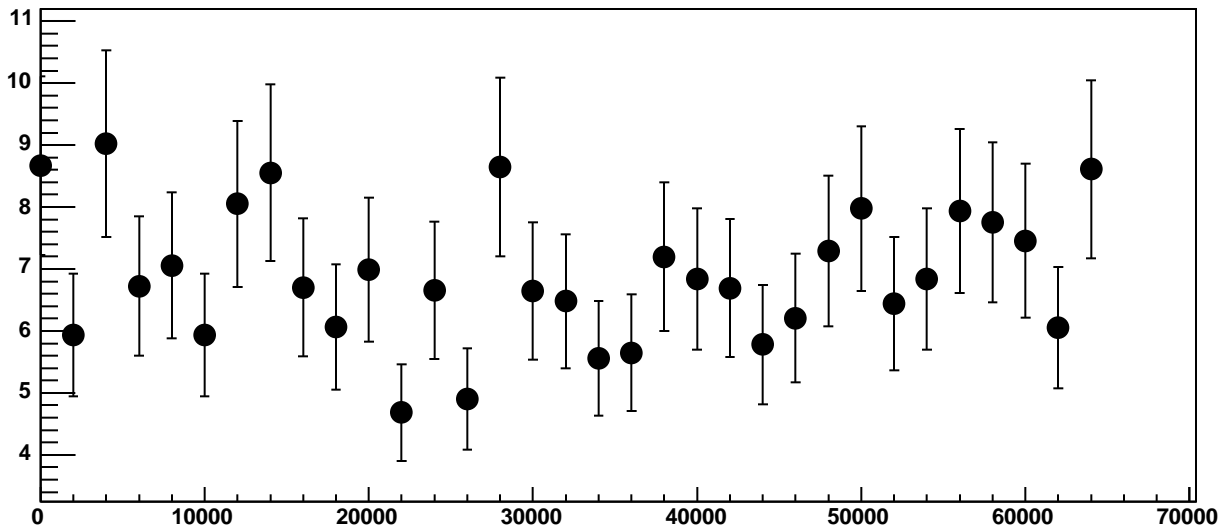


Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

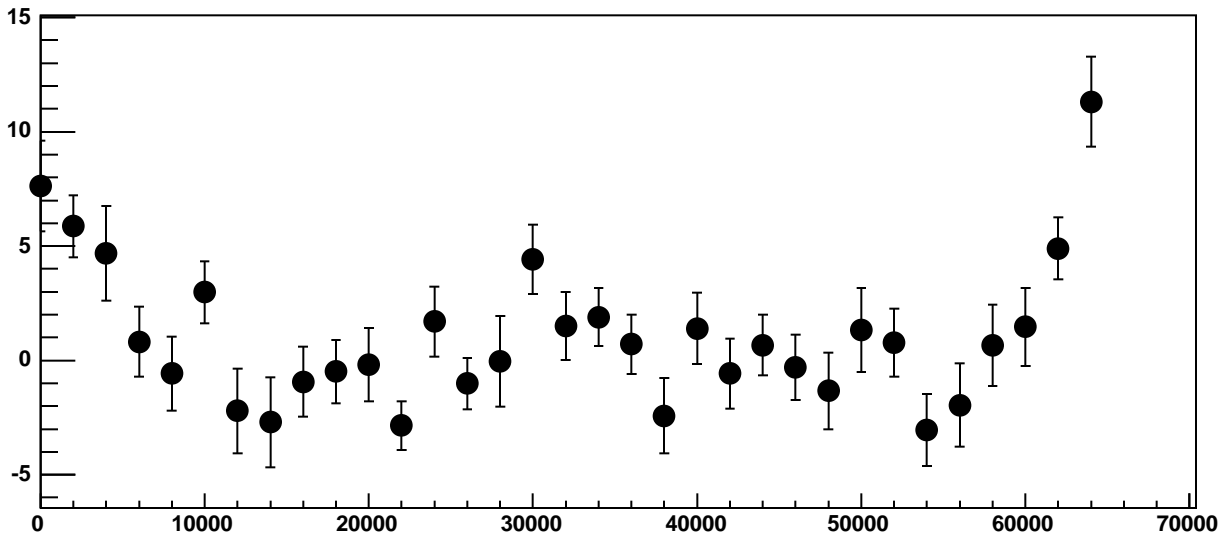


χ^2 / ndf 38.6 / 23
p0 451.1 ± 0.7072
p1 0.002328 ± 2.146e-05

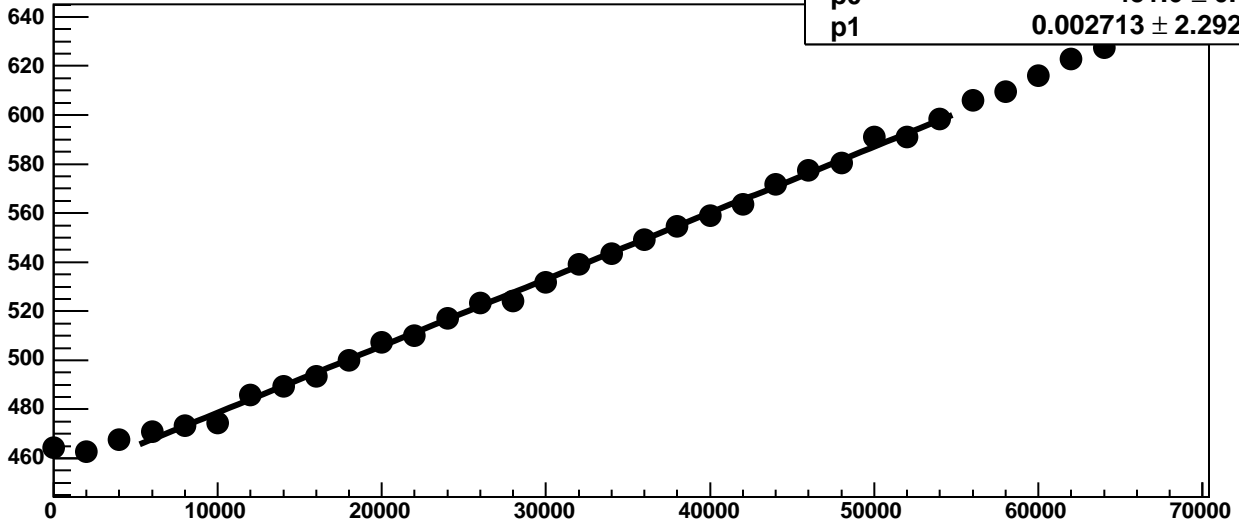
Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

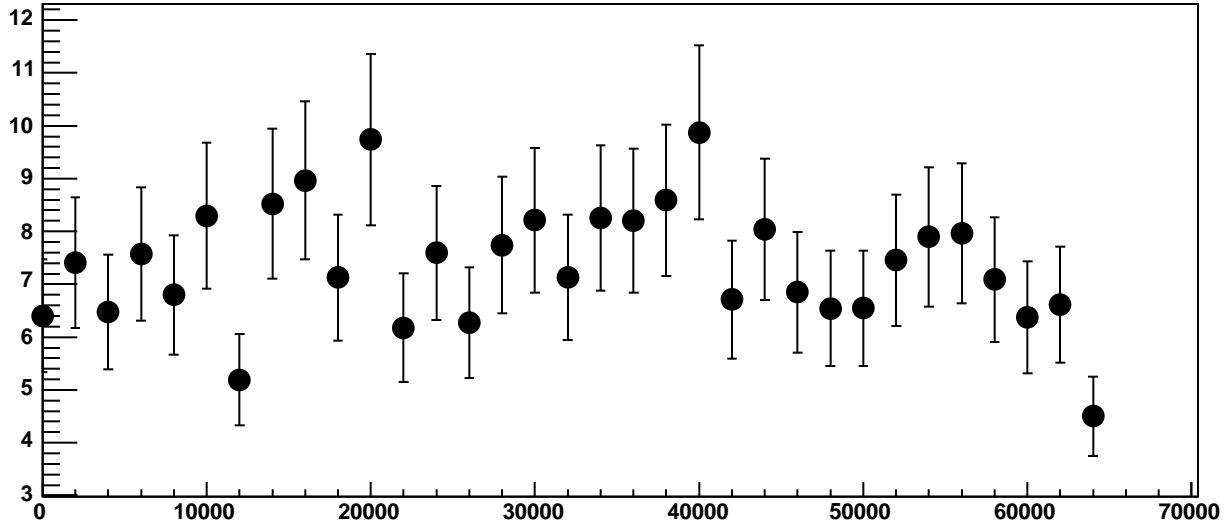


Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

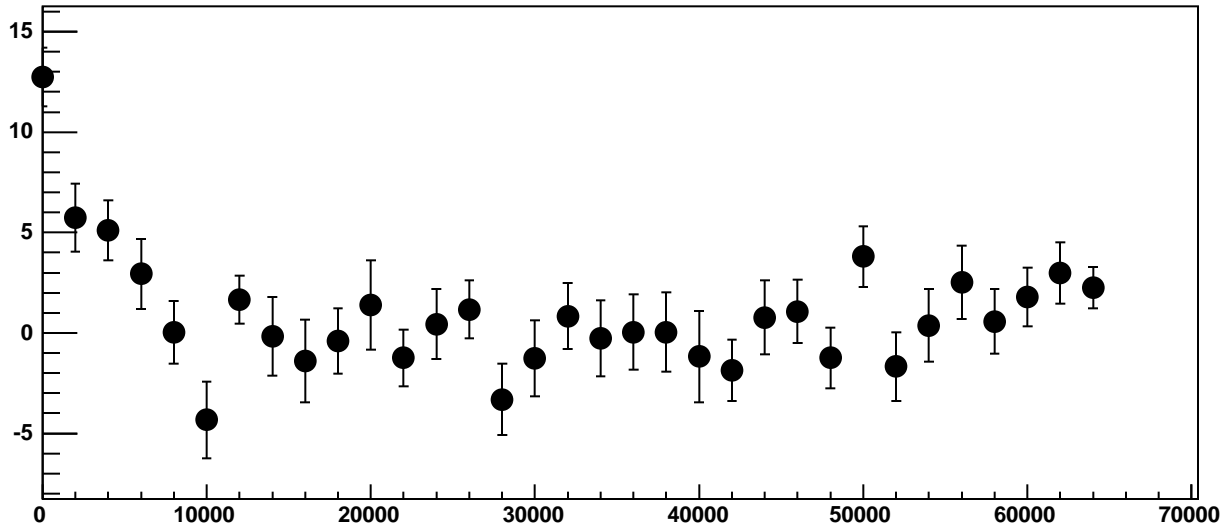


χ^2 / ndf 27.09 / 23
p0 451.6 ± 0.7596
p1 $0.002713 \pm 2.292e-05$

Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

χ^2 / ndf

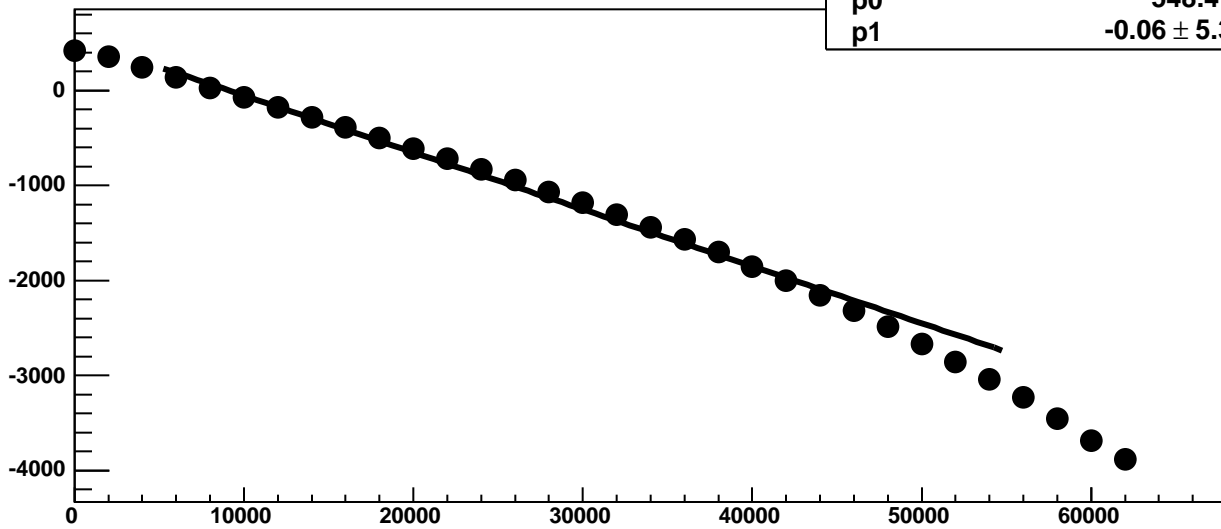
9356 / 23

p0

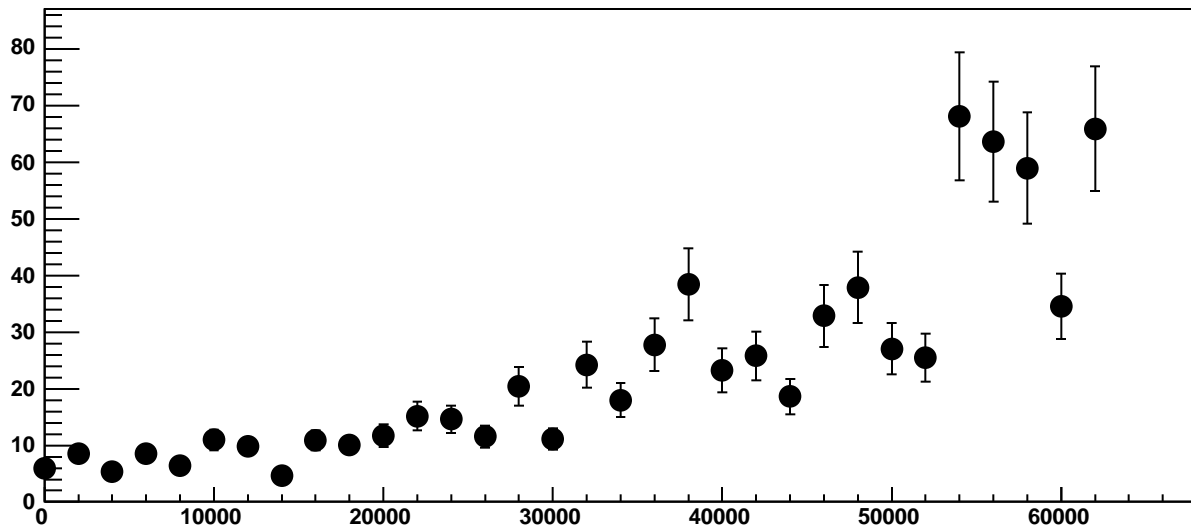
548.4 ± 1.087

p1

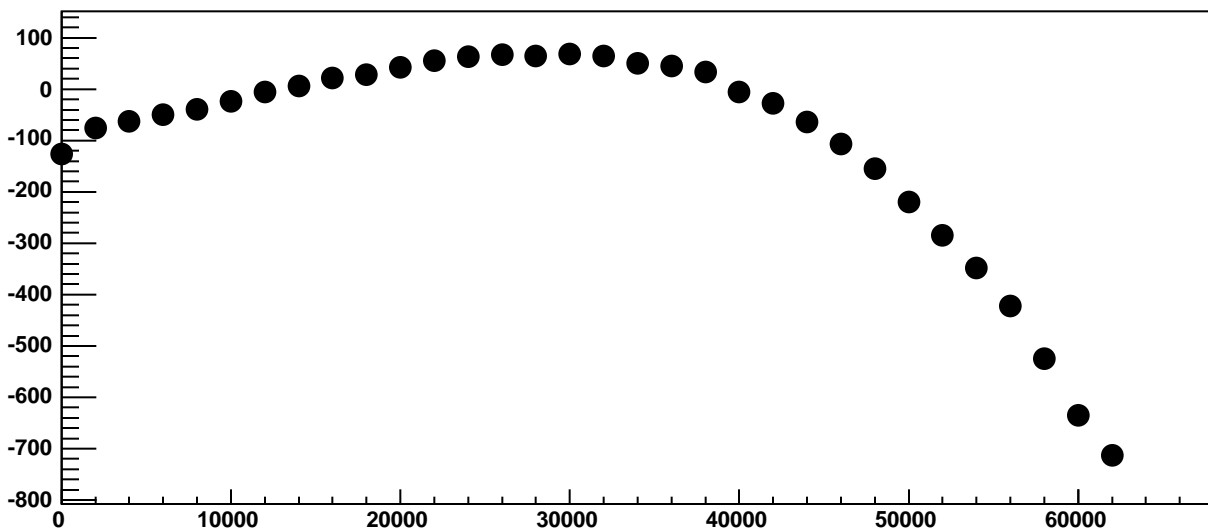
$-0.06 \pm 5.337\text{e-}05$



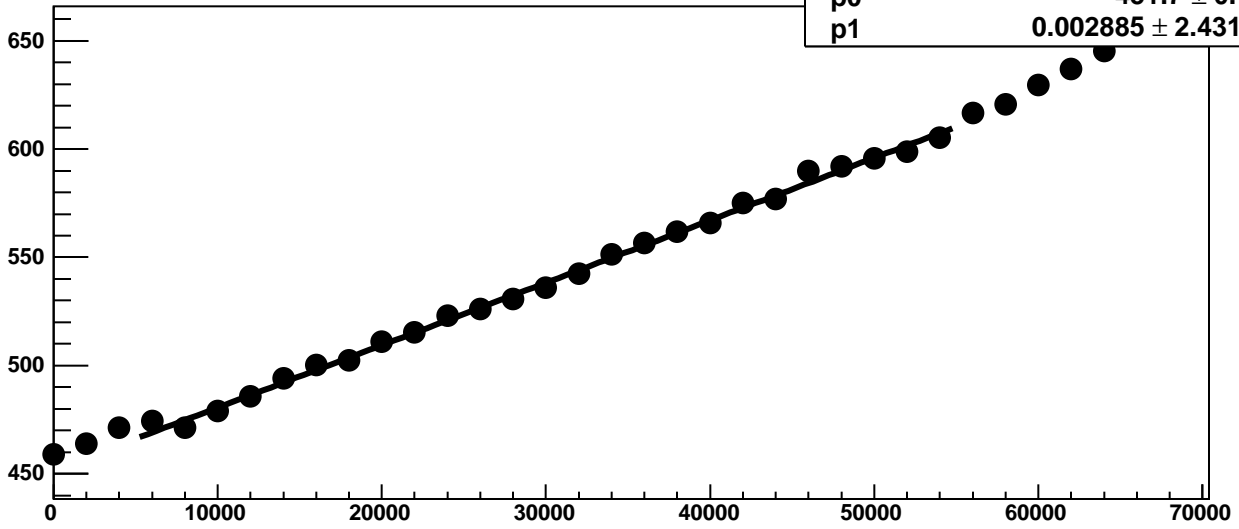
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

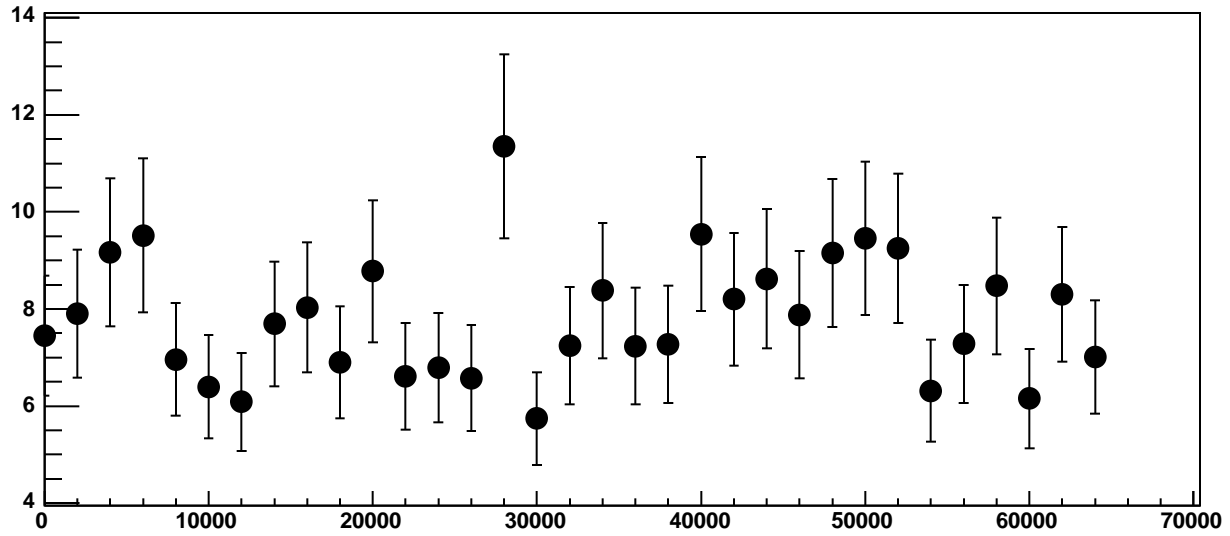


Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

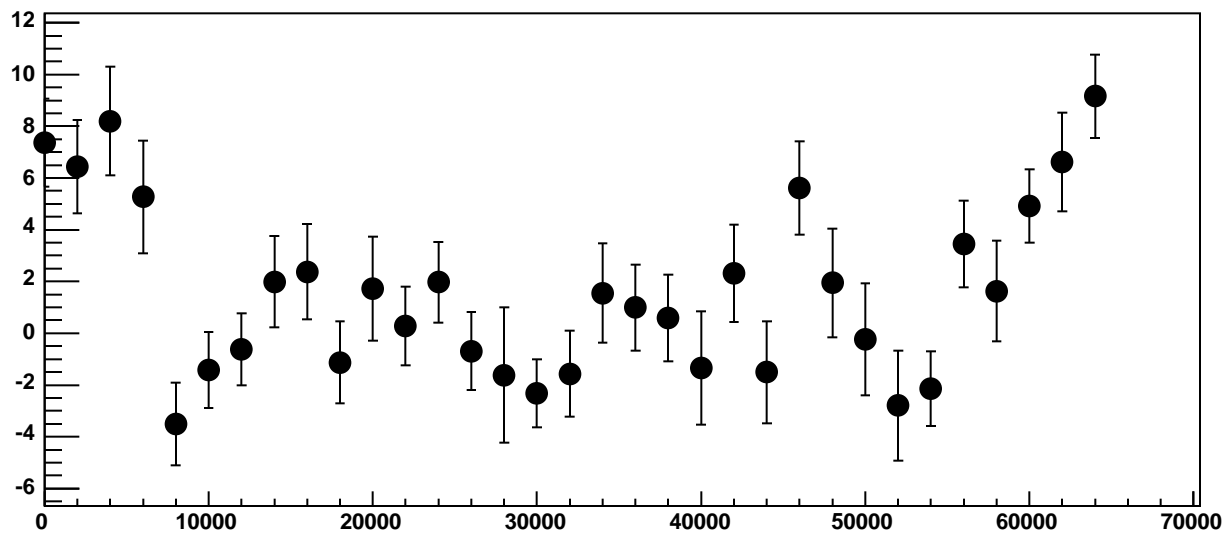


χ^2 / ndf 40.26 / 23
p0 451.7 ± 0.7774
p1 0.002885 ± 2.431e-05

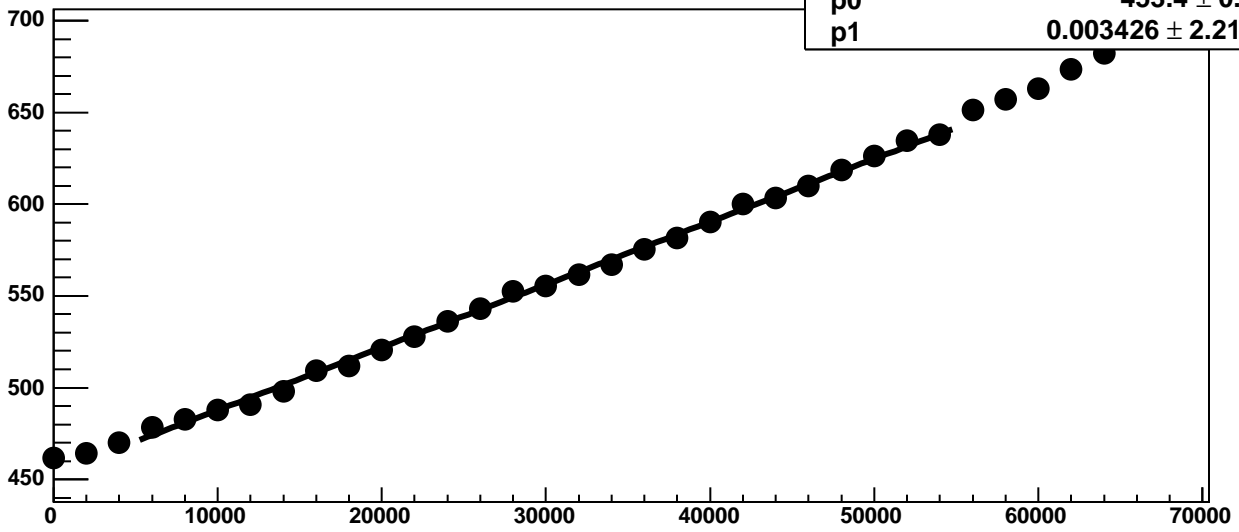
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.01 / 23

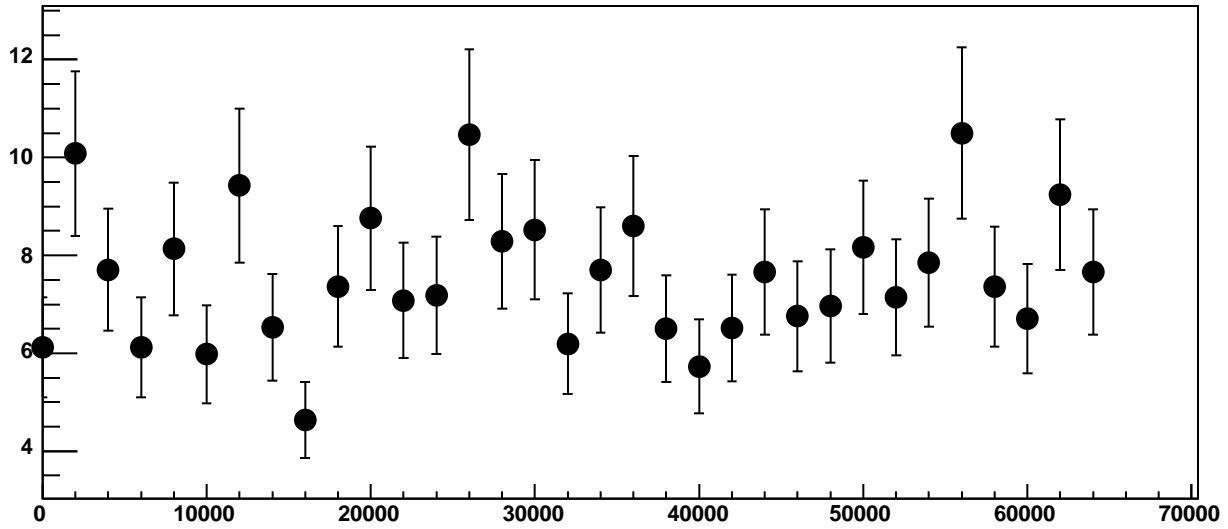
p0

453.4 ± 0.7221

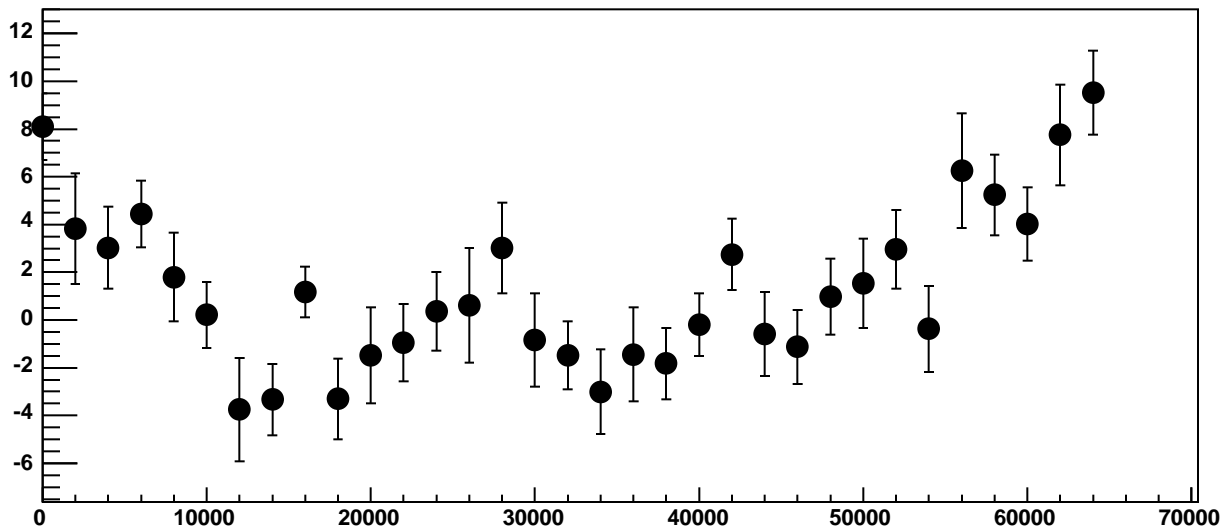
p1

$0.003426 \pm 2.21e-05$

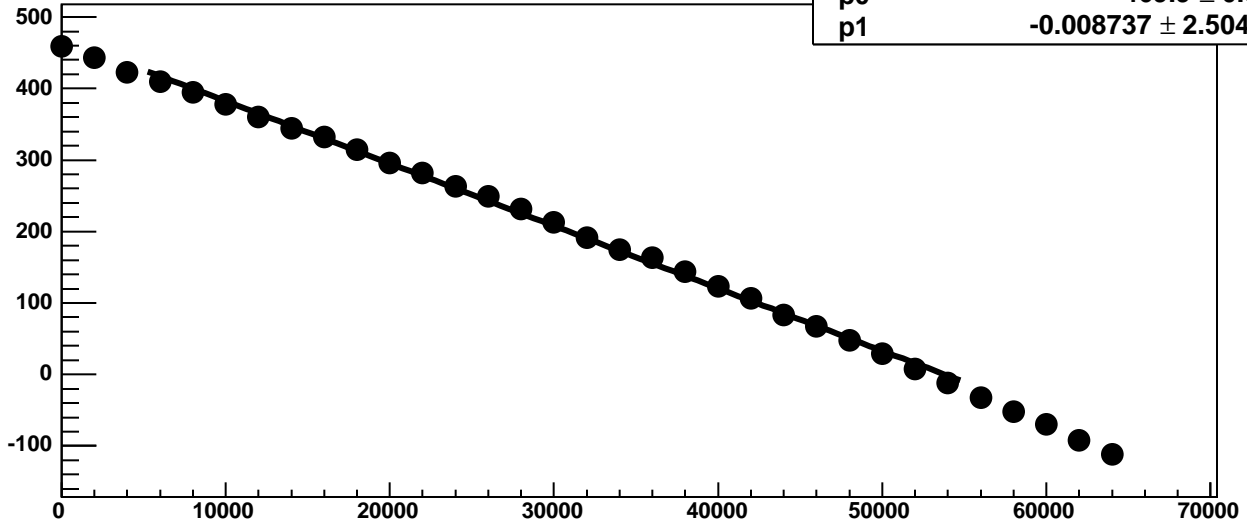
Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



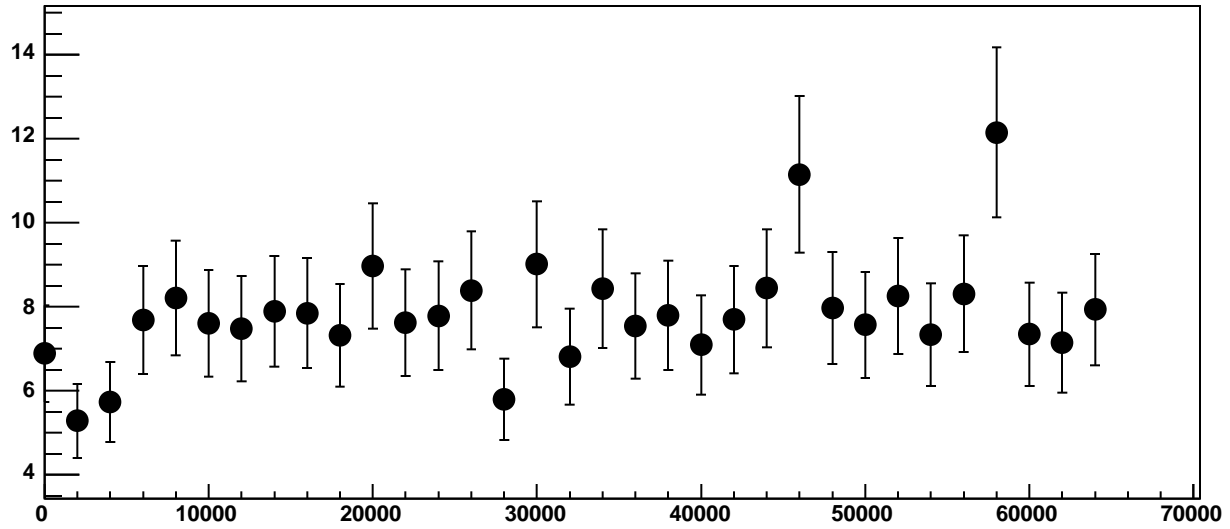
Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



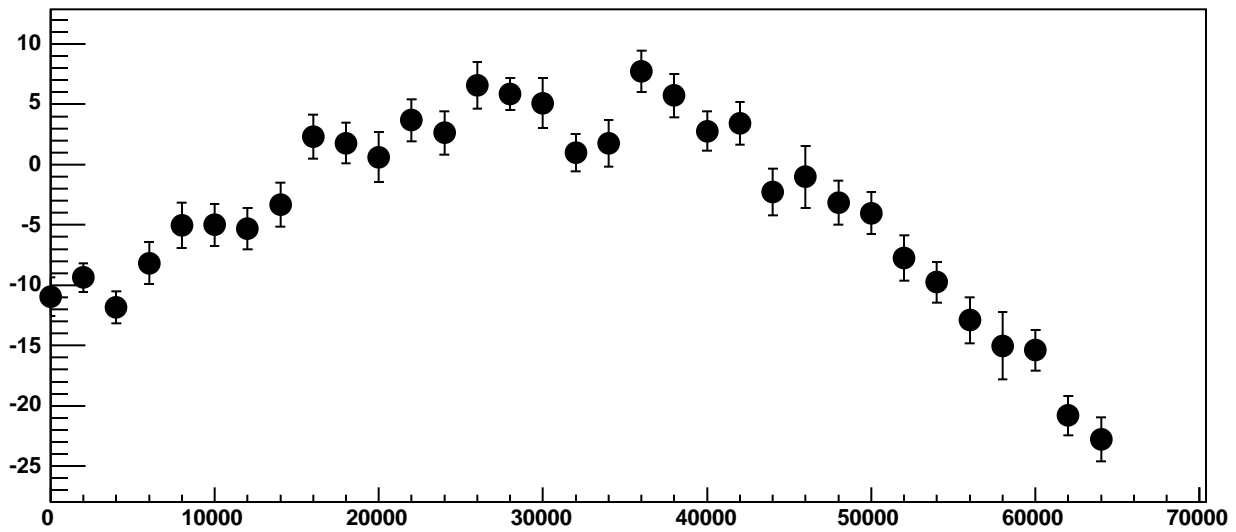
Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



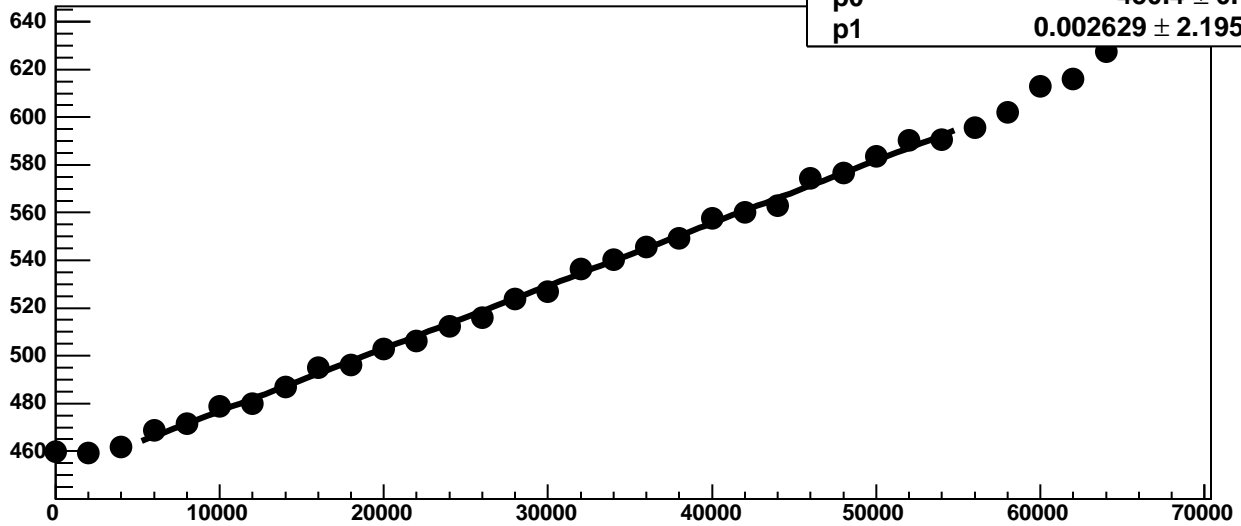
Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.6 / 23

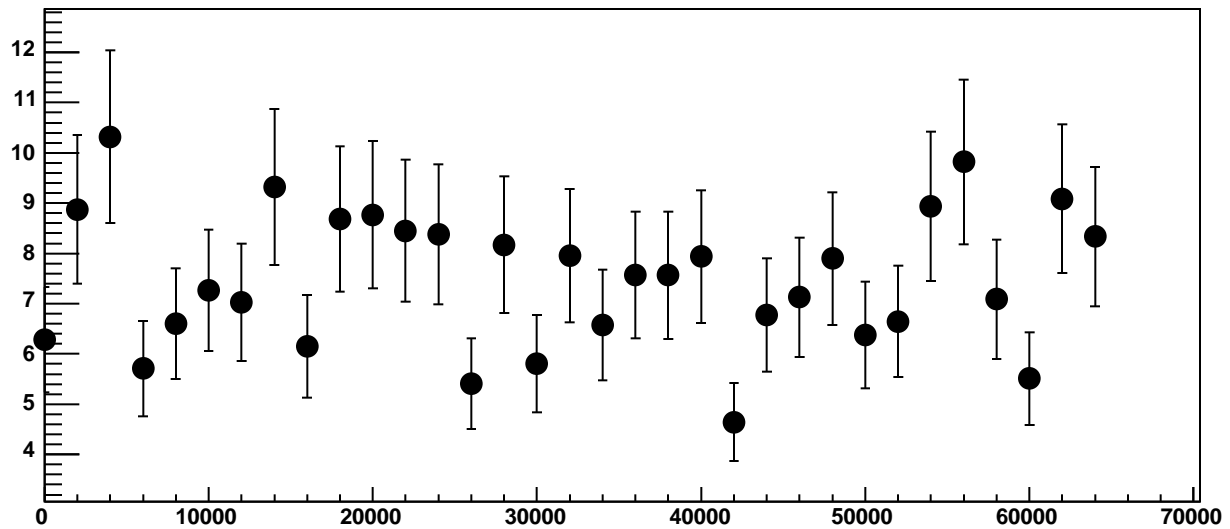
p0

450.4 ± 0.7352

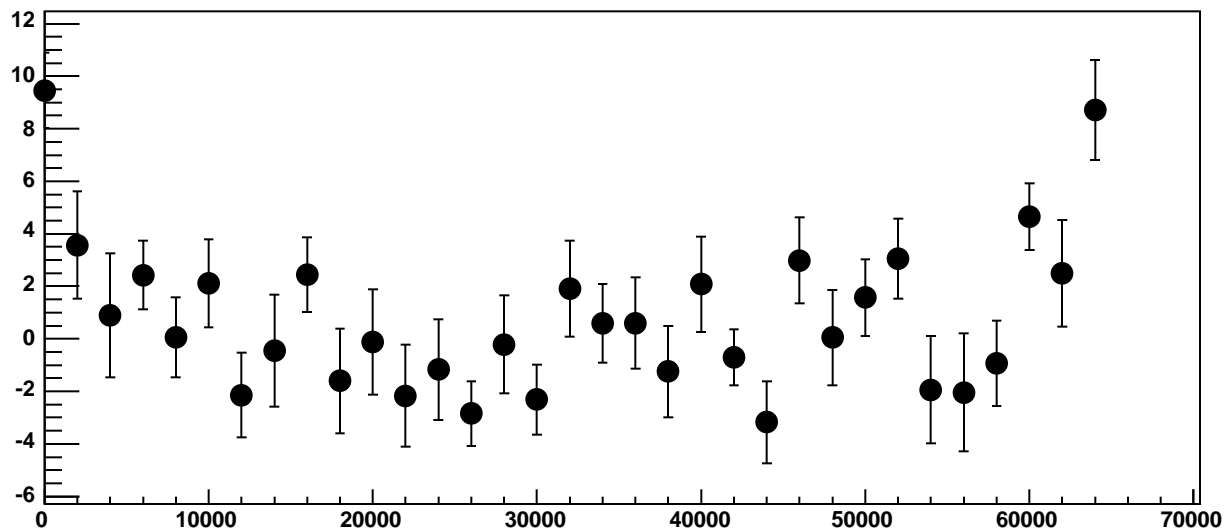
p1

$0.002629 \pm 2.195e-05$

Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

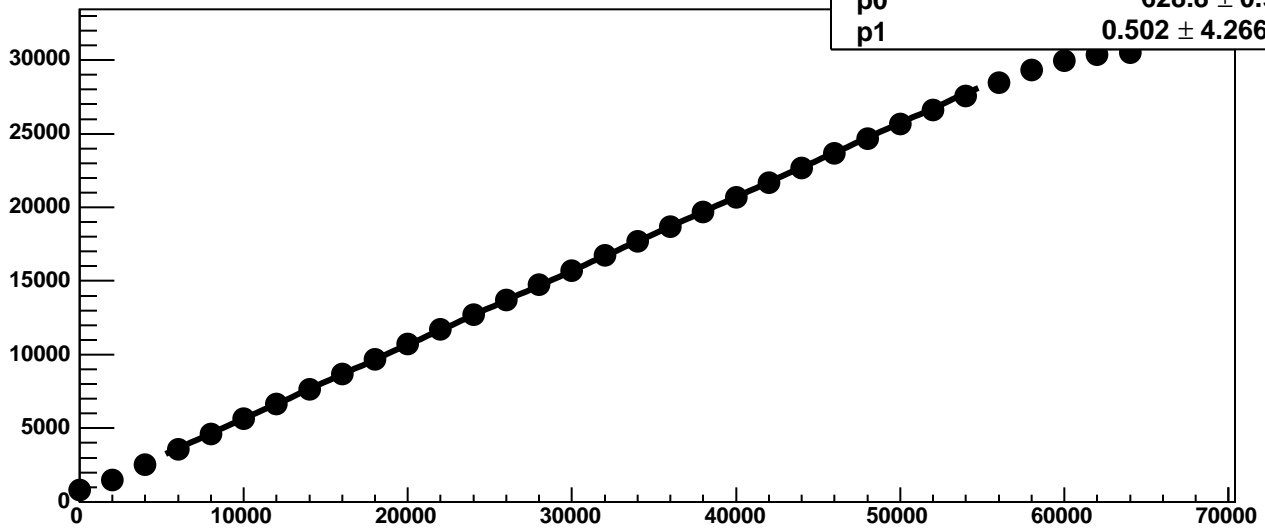


Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

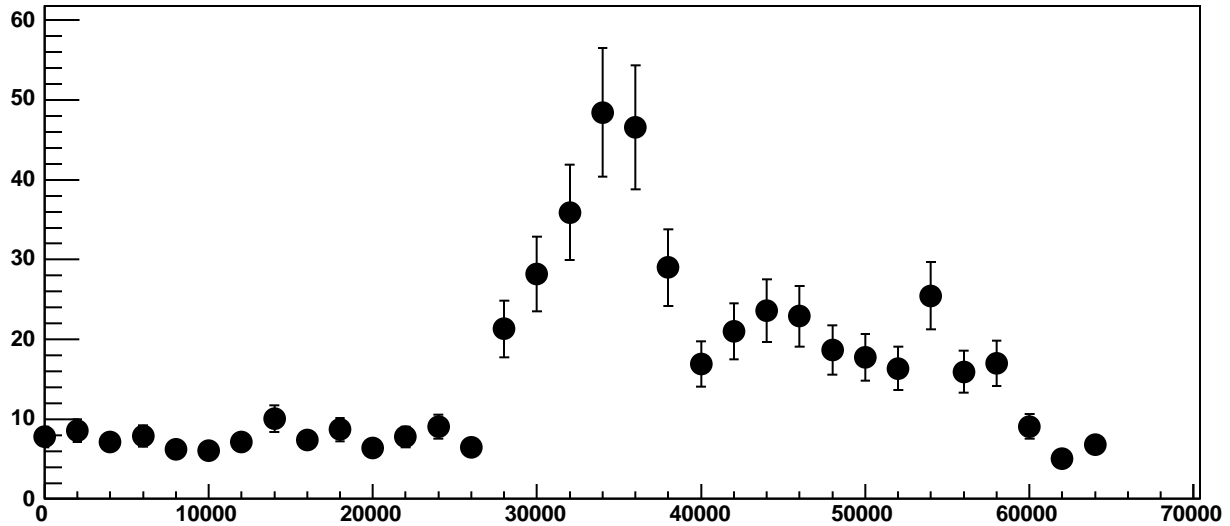


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

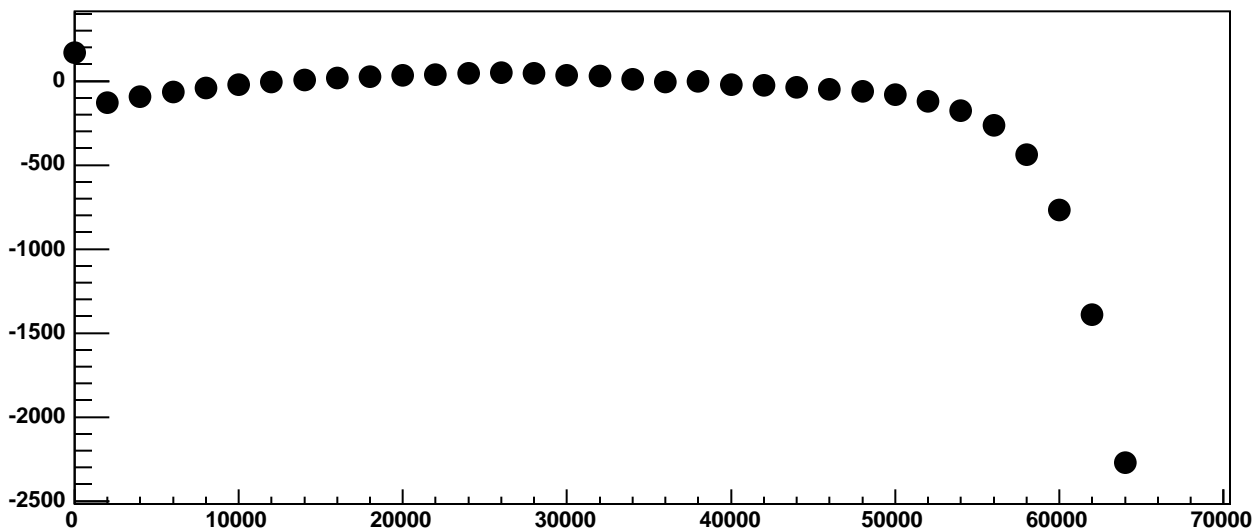
χ^2 / ndf 8208 / 23
p0 628.8 ± 0.9333
p1 $0.502 \pm 4.266e-05$



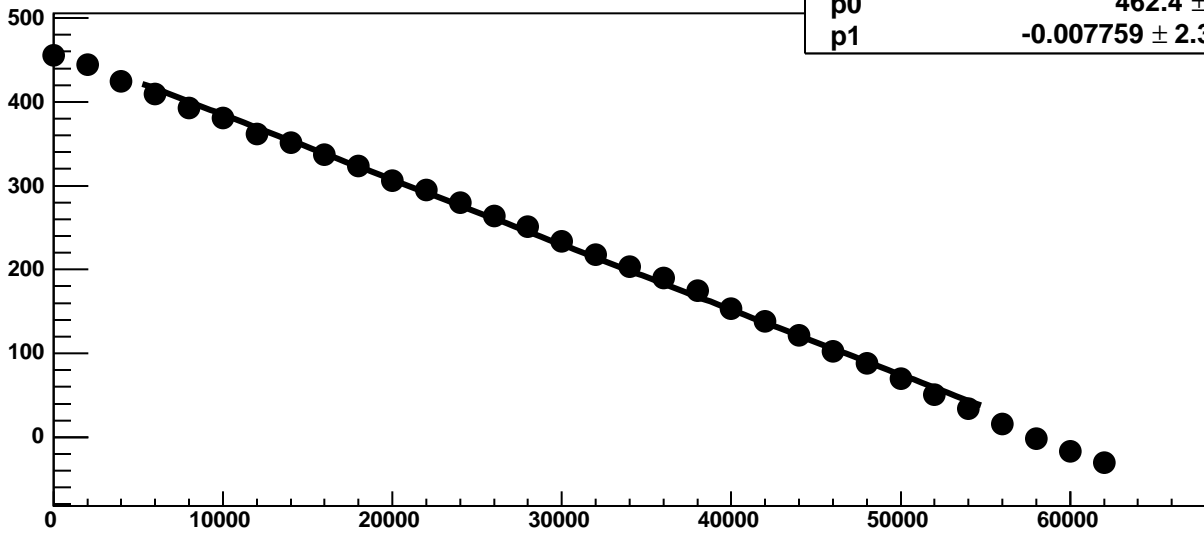
Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

199.1 / 23

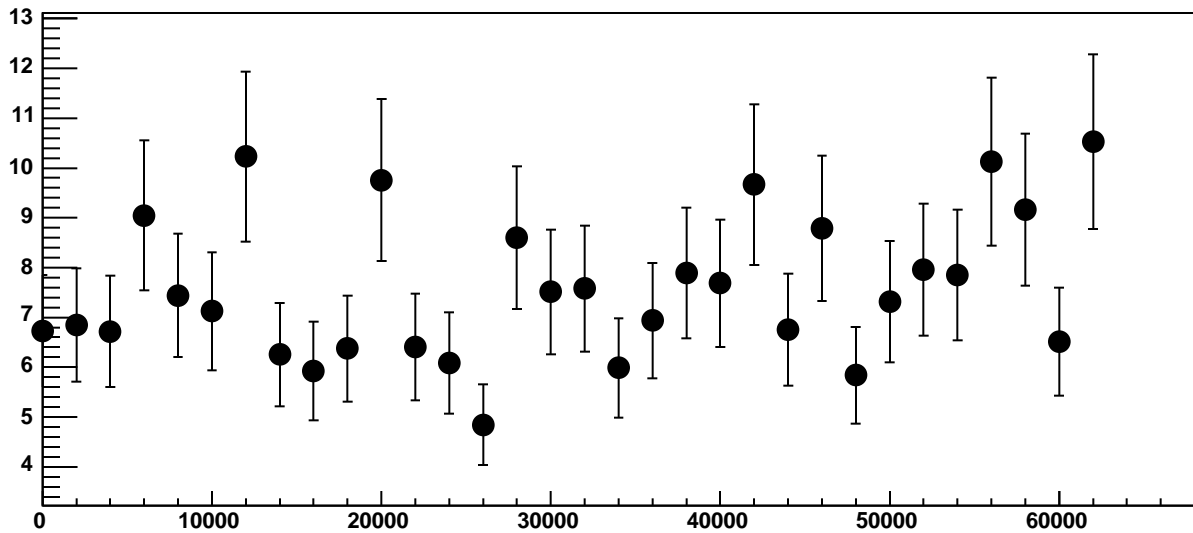
p0

462.4 ± 0.7739

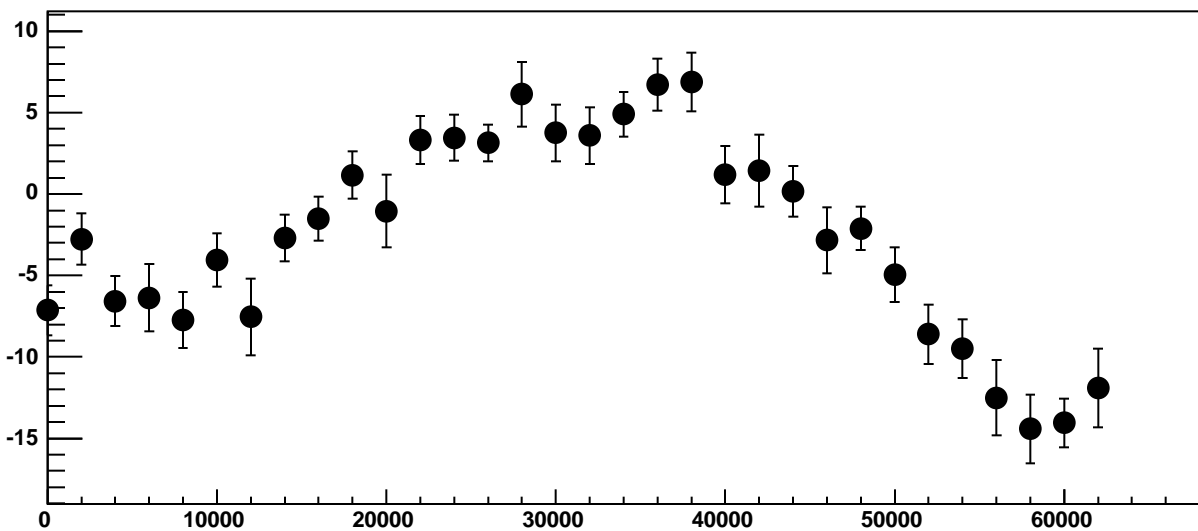
p1

$-0.007759 \pm 2.368e-05$

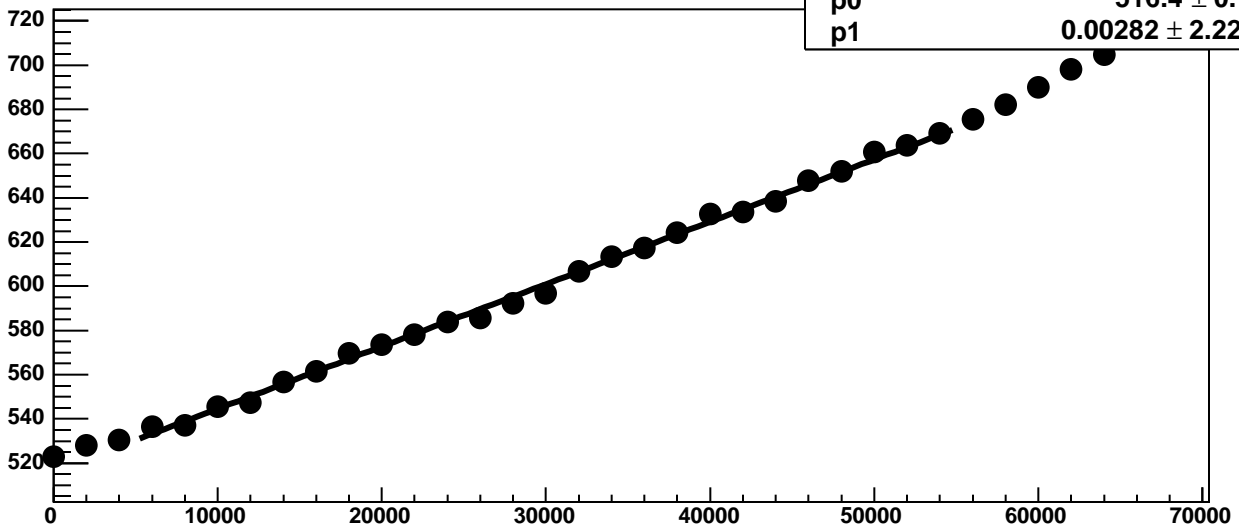
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.62 / 23

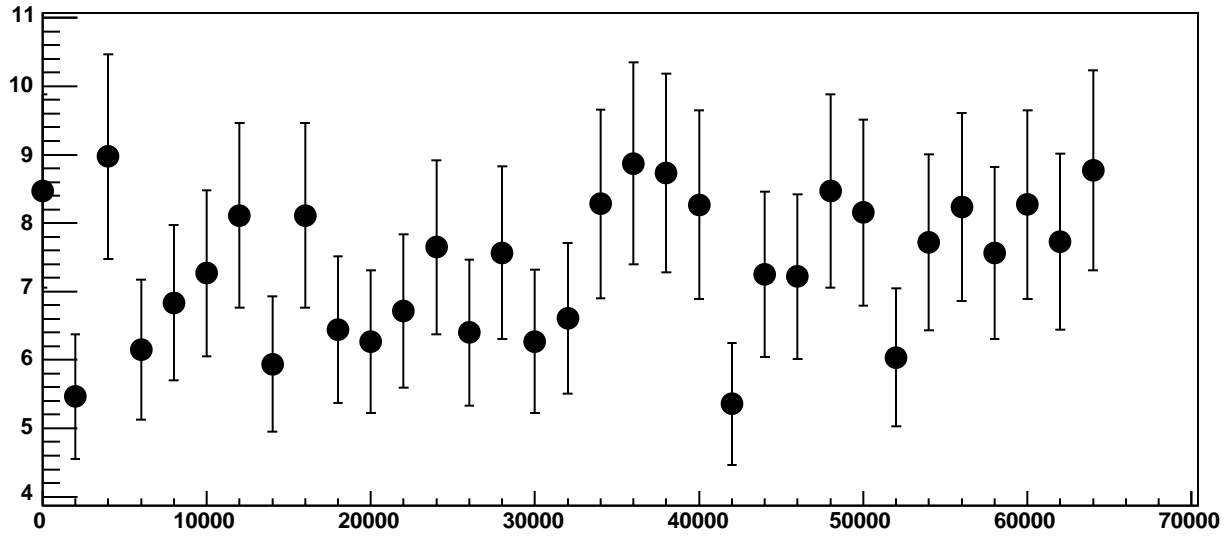
p0

516.4 ± 0.7244

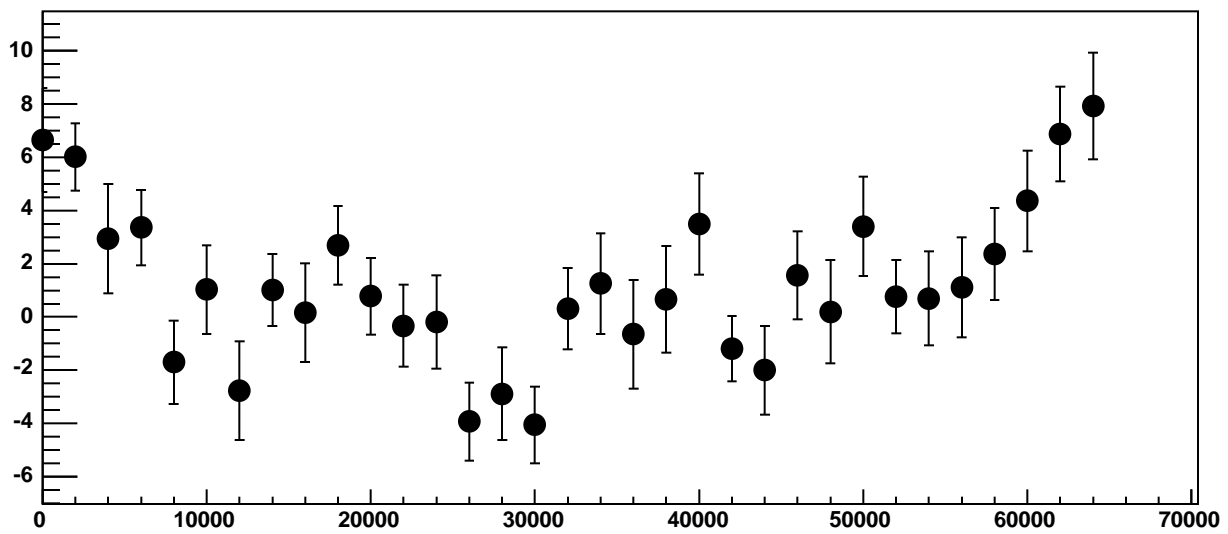
p1

$0.00282 \pm 2.22e-05$

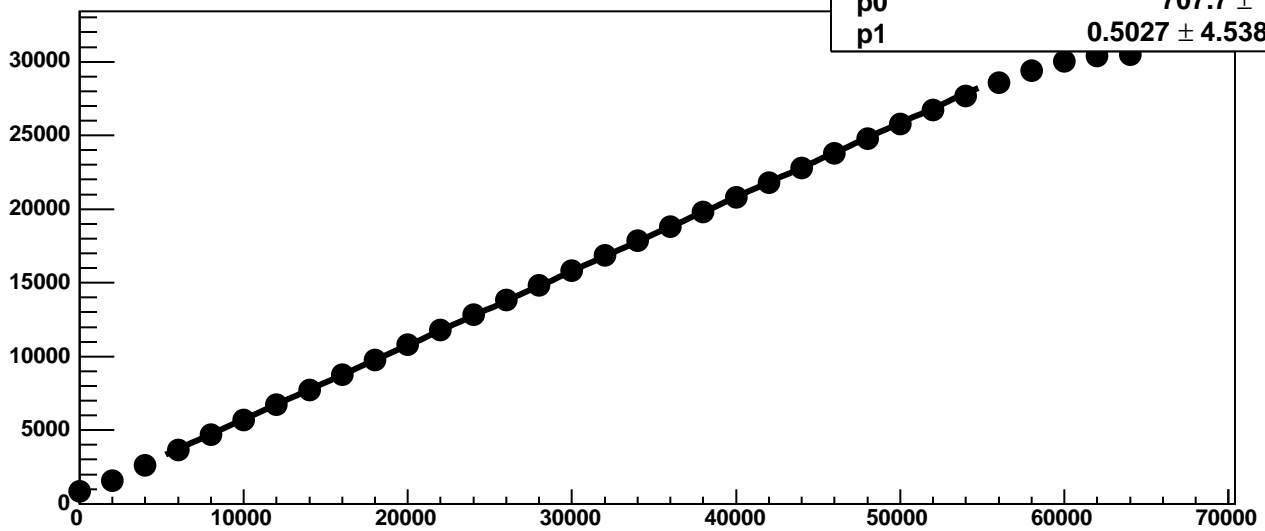
Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



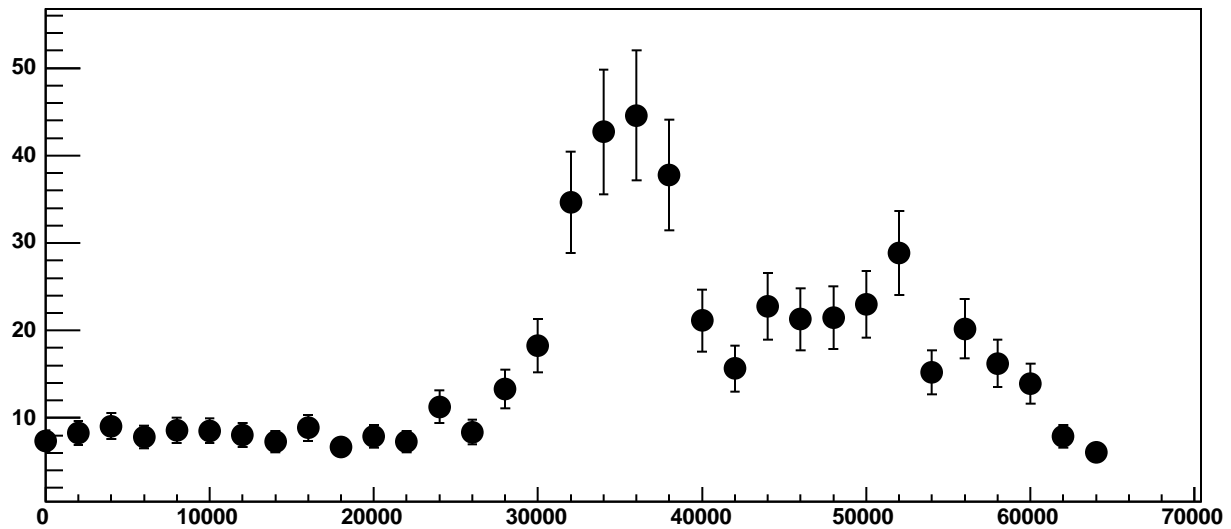
Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



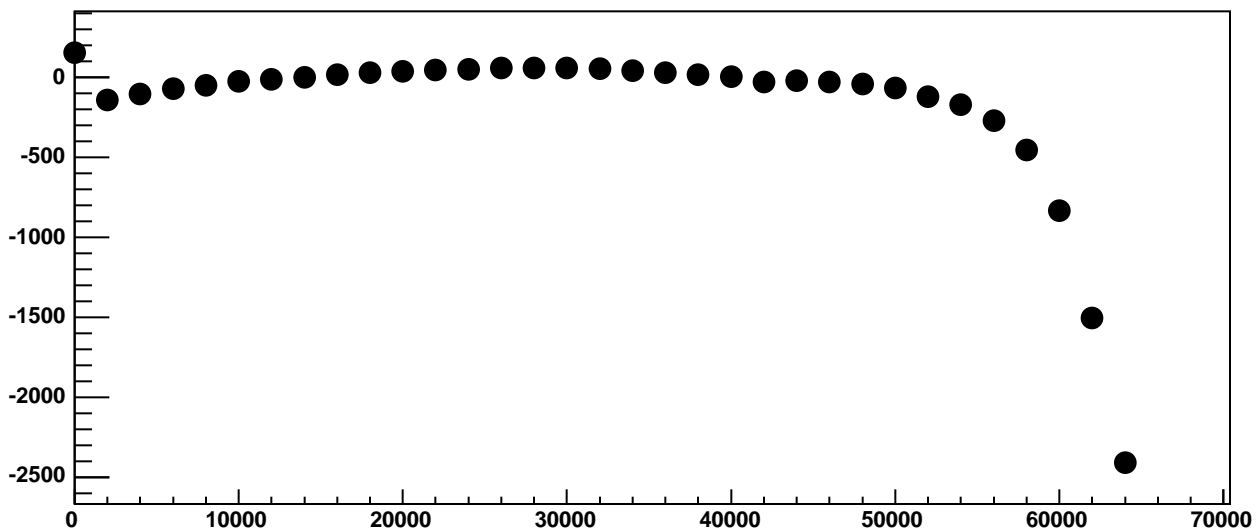
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



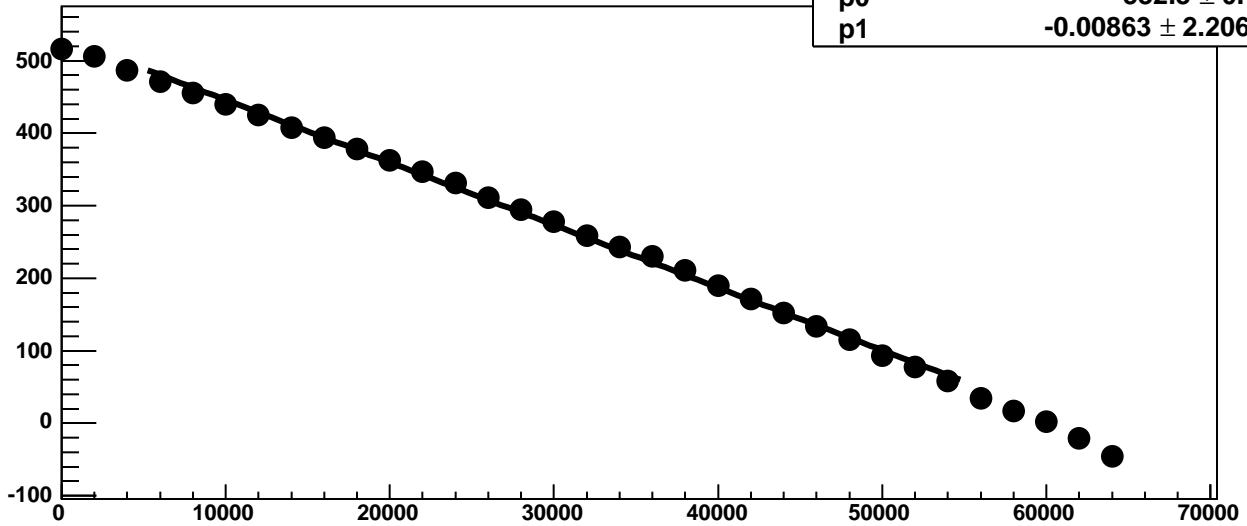
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

226.6 / 23

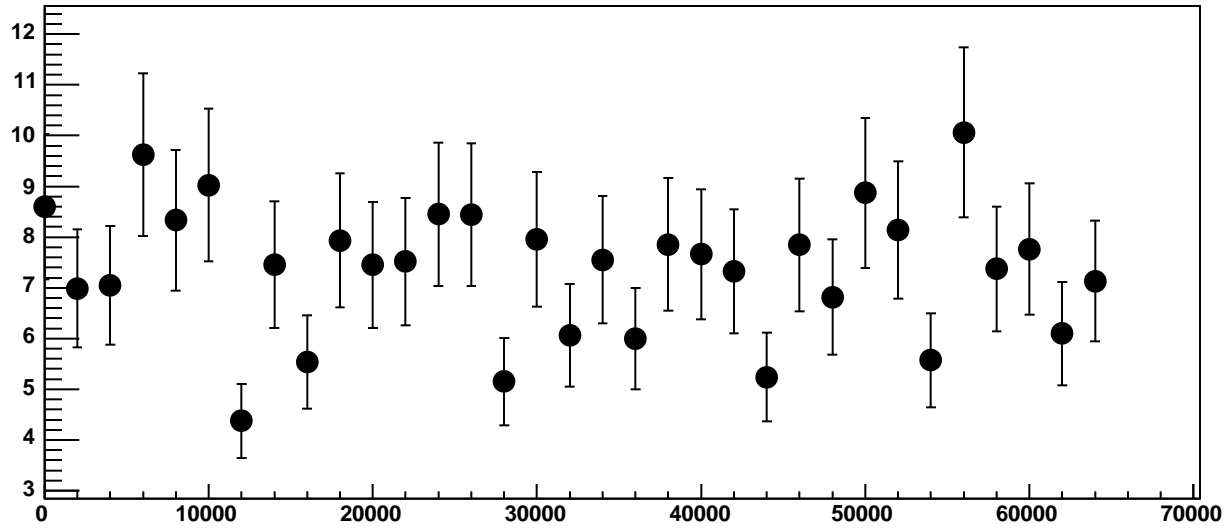
p0

532.3 ± 0.7365

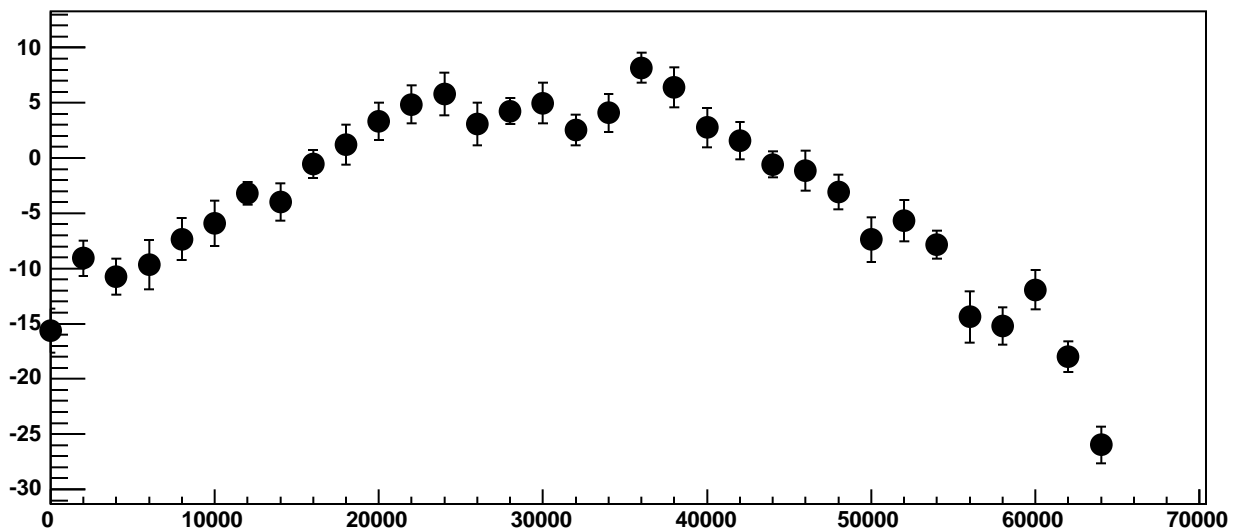
p1

$-0.00863 \pm 2.206e-05$

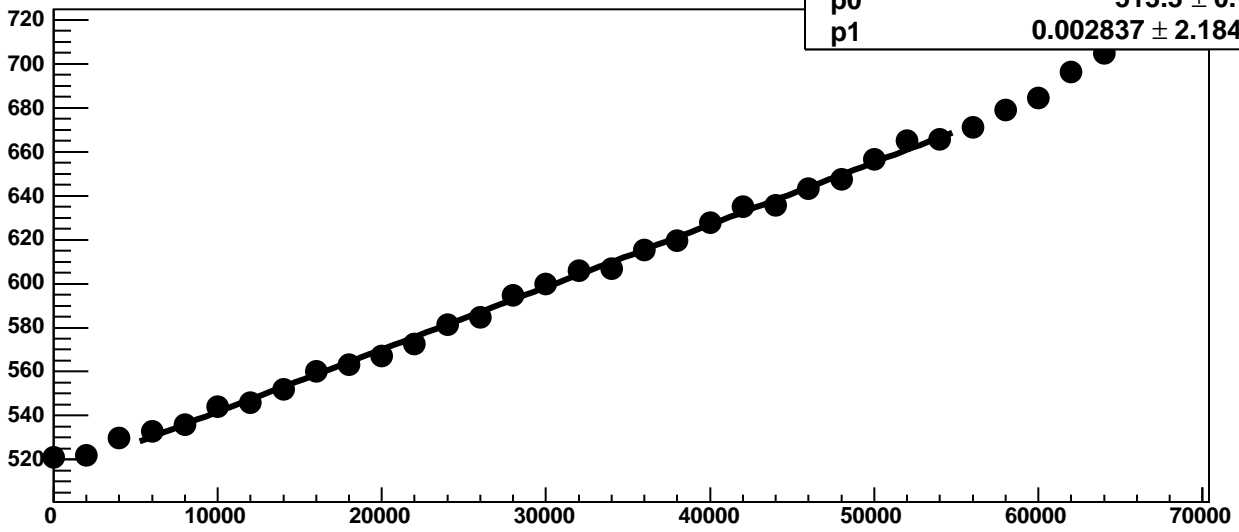
Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

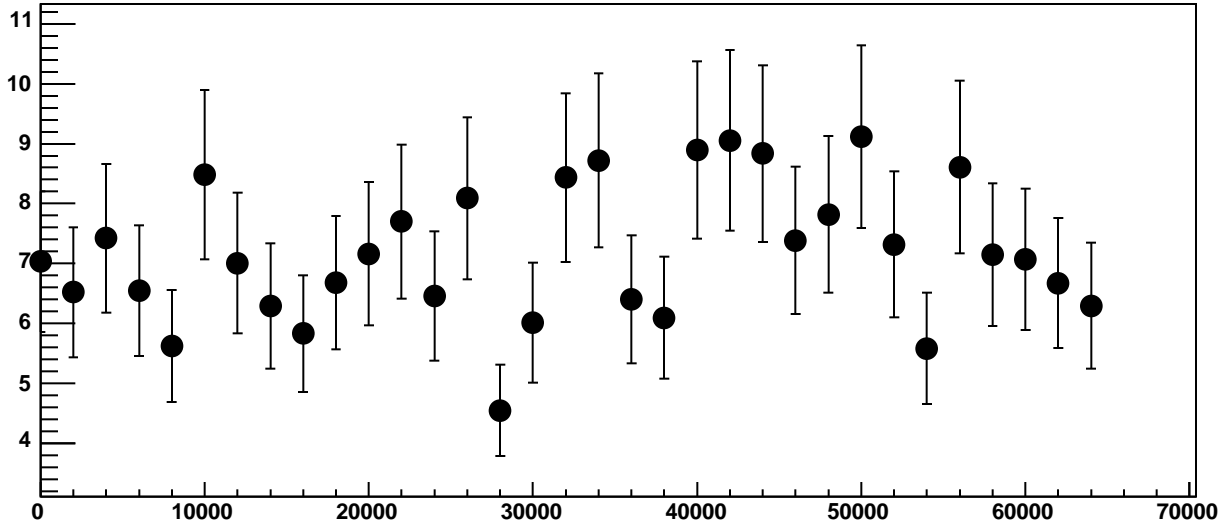


Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

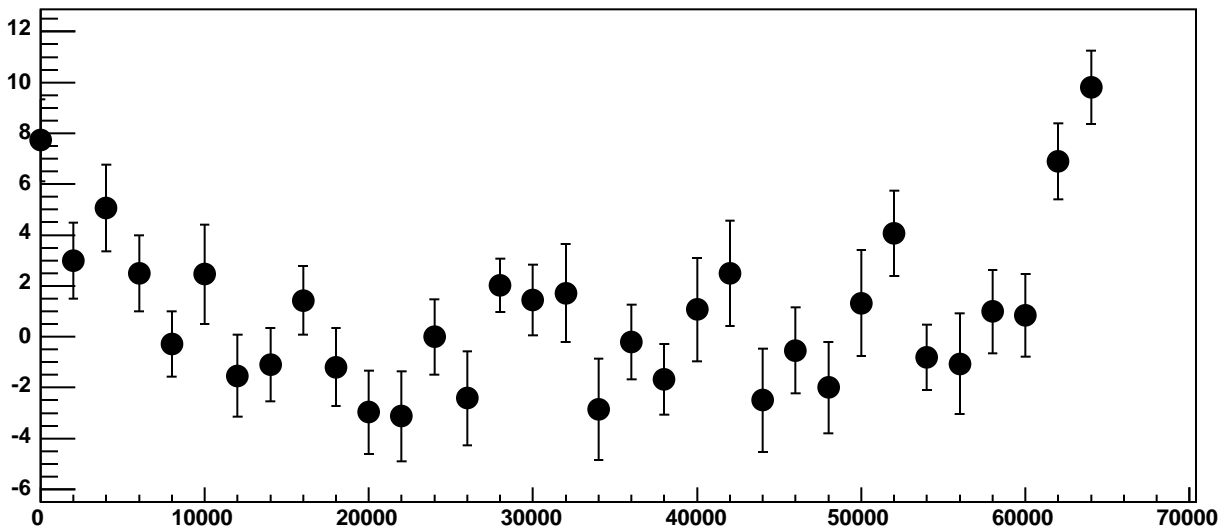


χ^2 / ndf 36.06 / 23
p0 513.3 ± 0.7005
p1 $0.002837 \pm 2.184e-05$

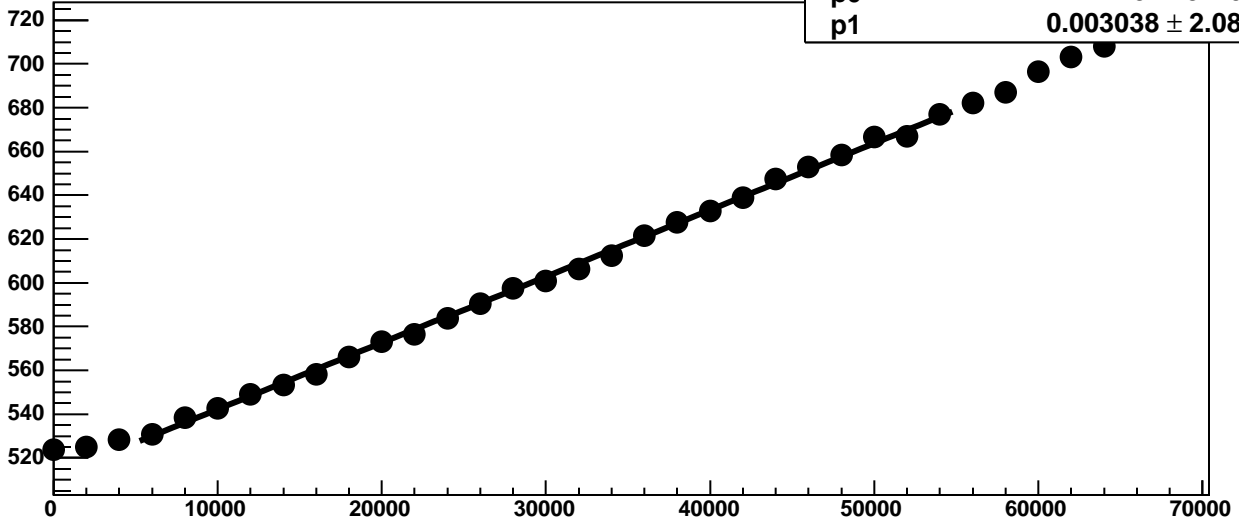
Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



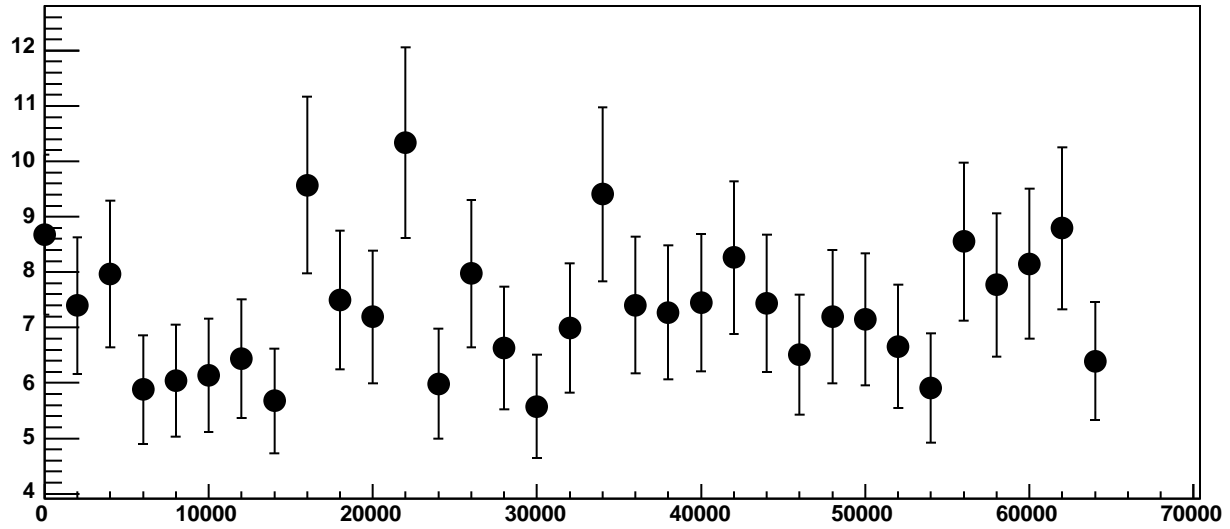
Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



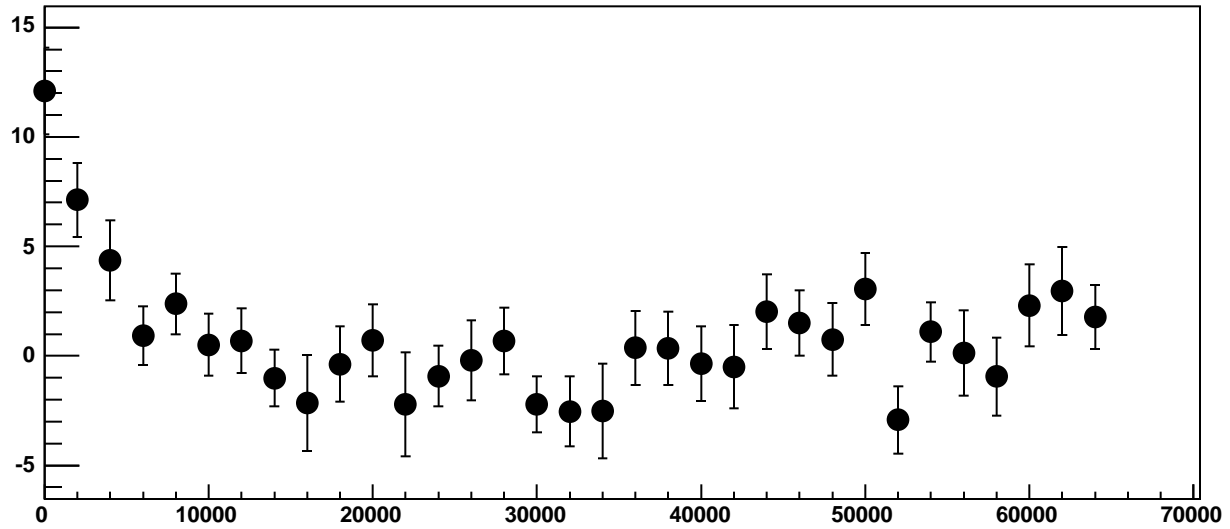
Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

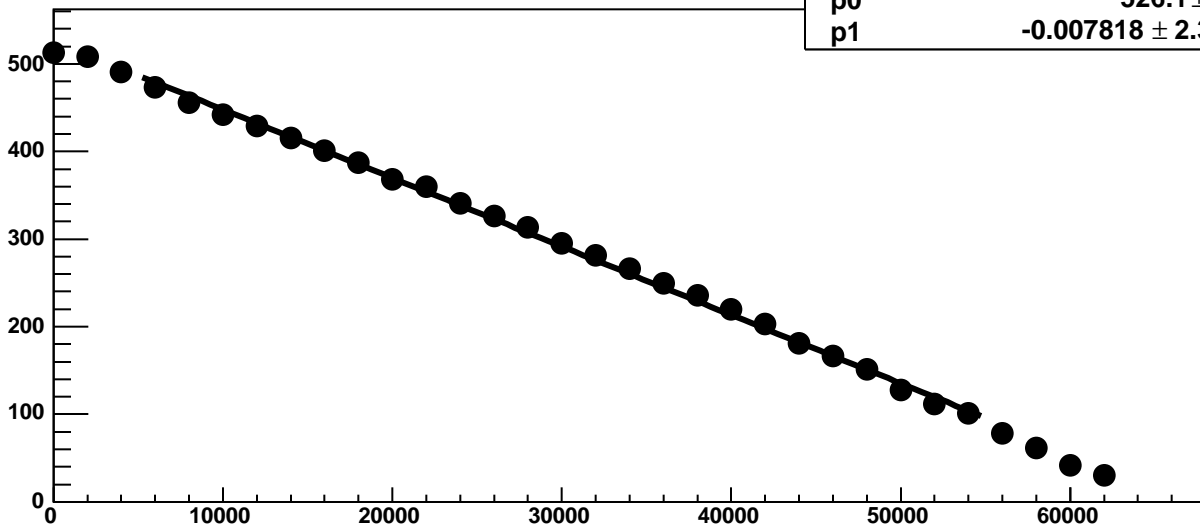


Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

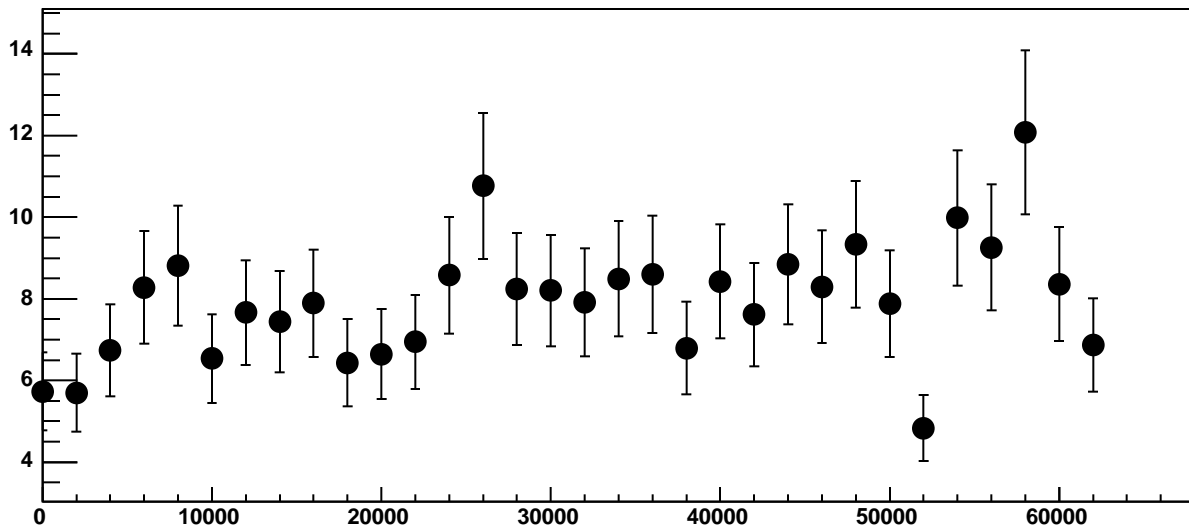


Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

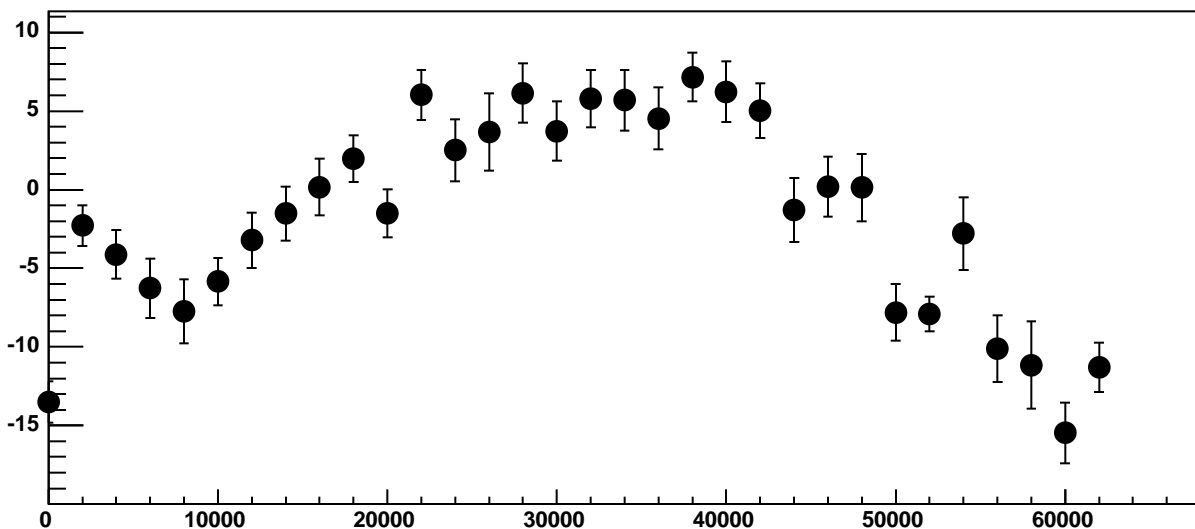
χ^2 / ndf 215.4 / 23
p0 526.1 ± 0.7954
p1 $-0.007818 \pm 2.366e-05$



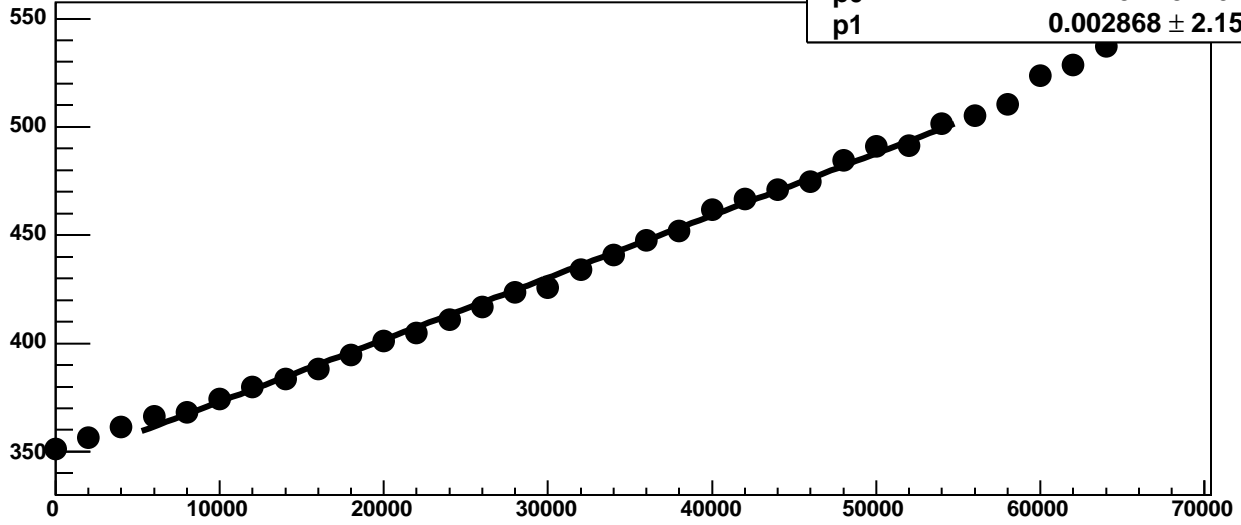
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

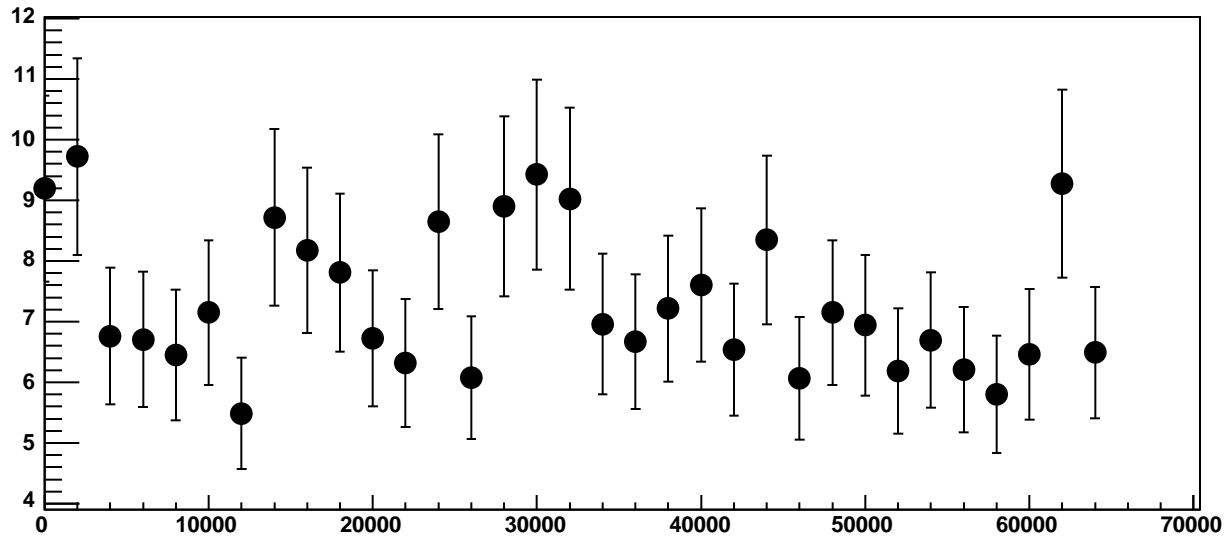


Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

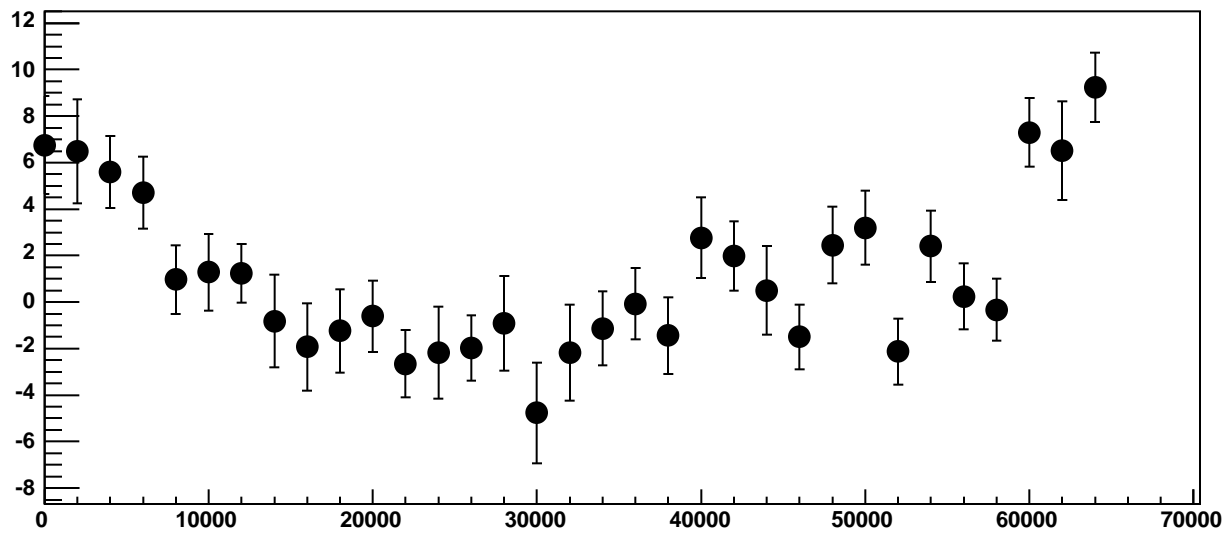


χ^2 / ndf 43.63 / 23
p0 344.3 ± 0.7242
p1 $0.002868 \pm 2.15e-05$

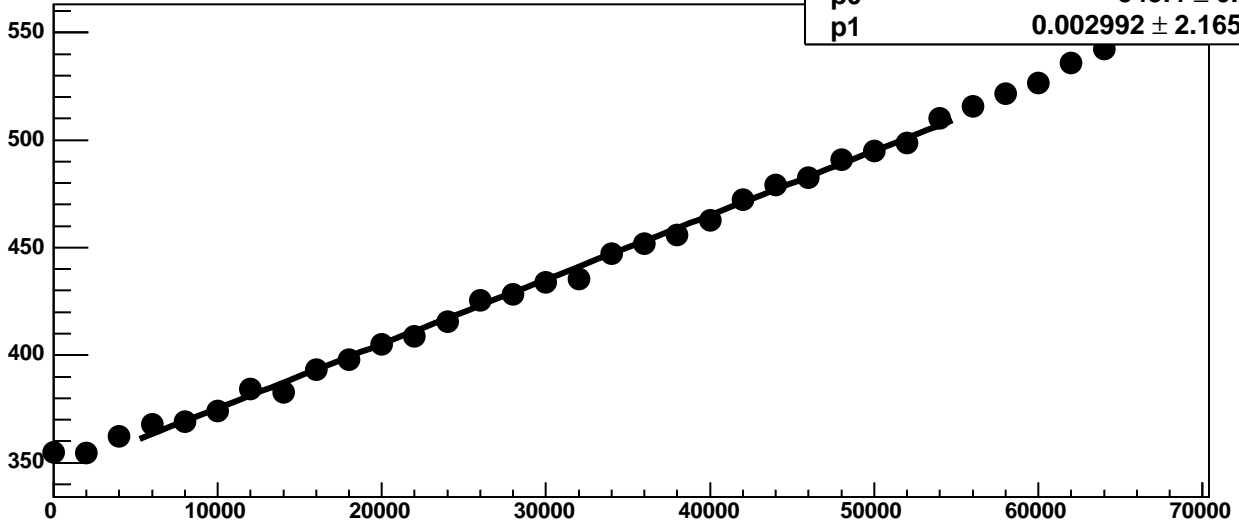
Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.09 / 23

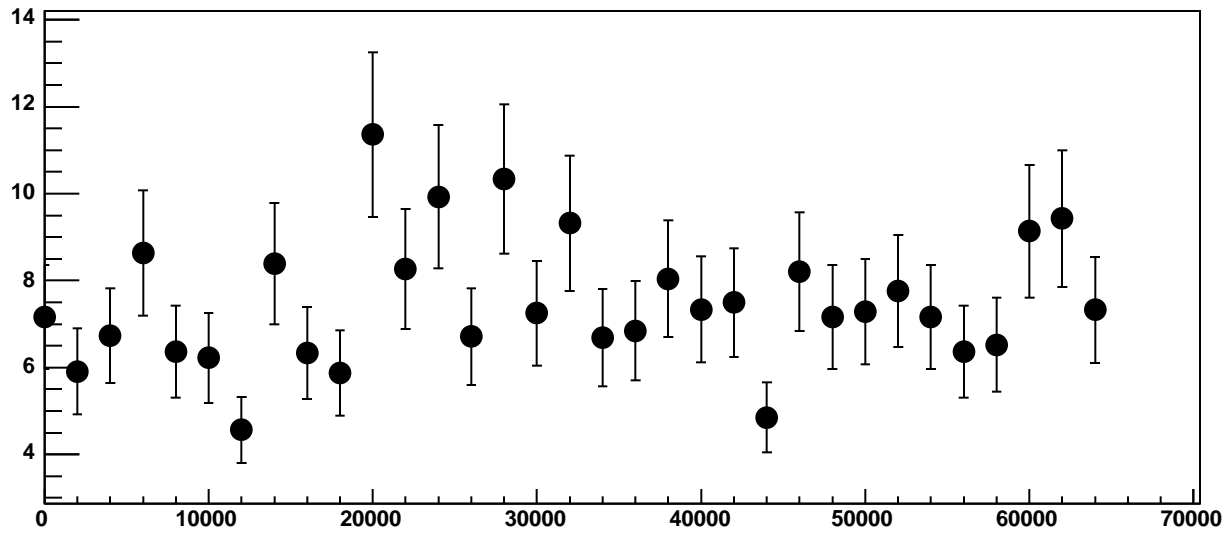
p0

345.4 ± 0.7113

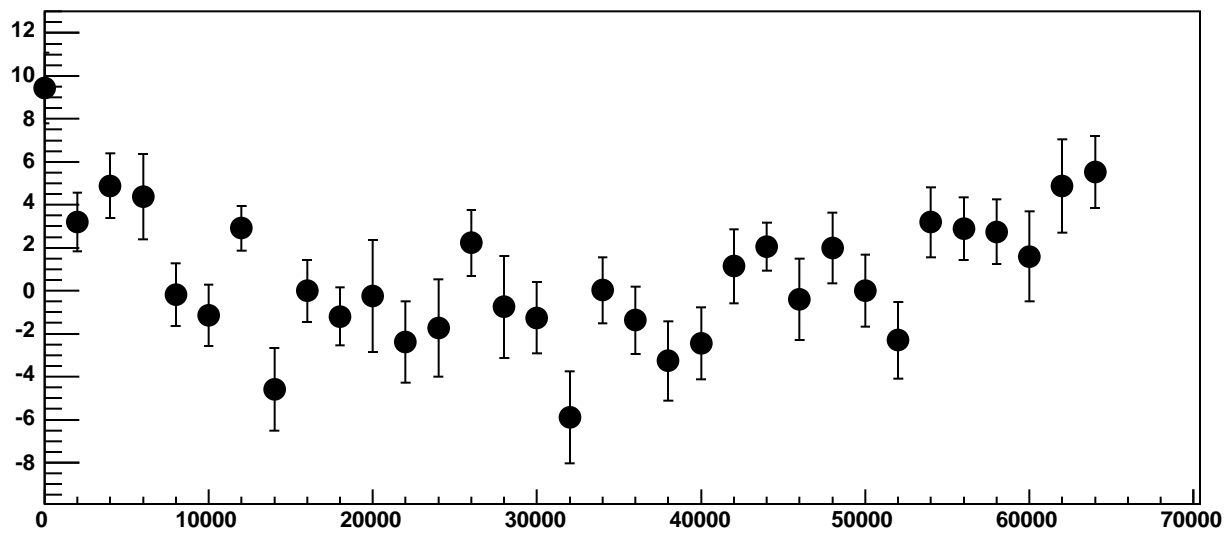
p1

$0.002992 \pm 2.165e-05$

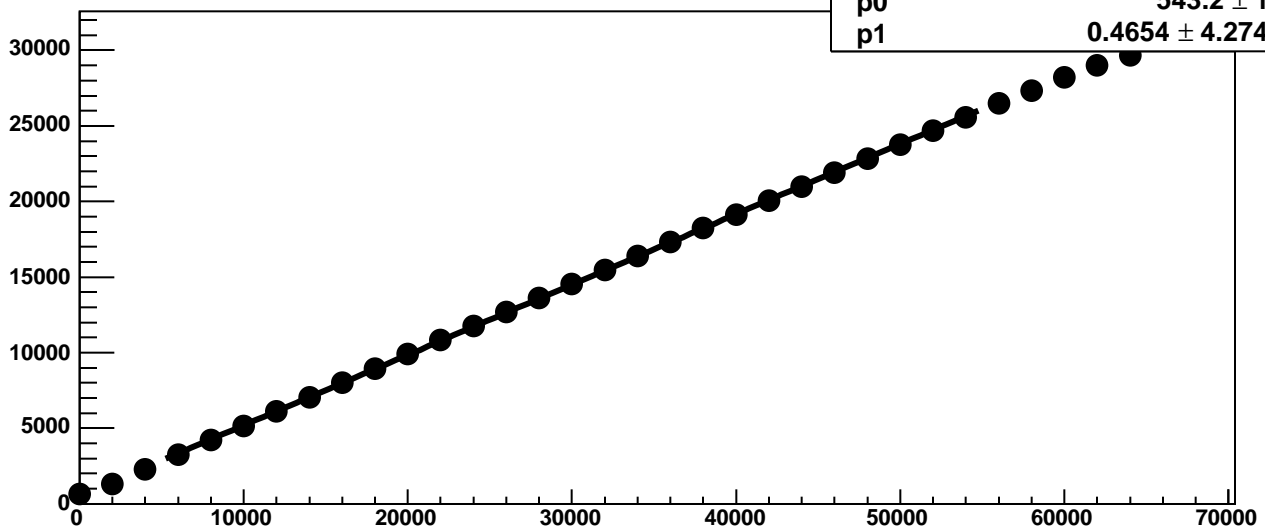
Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



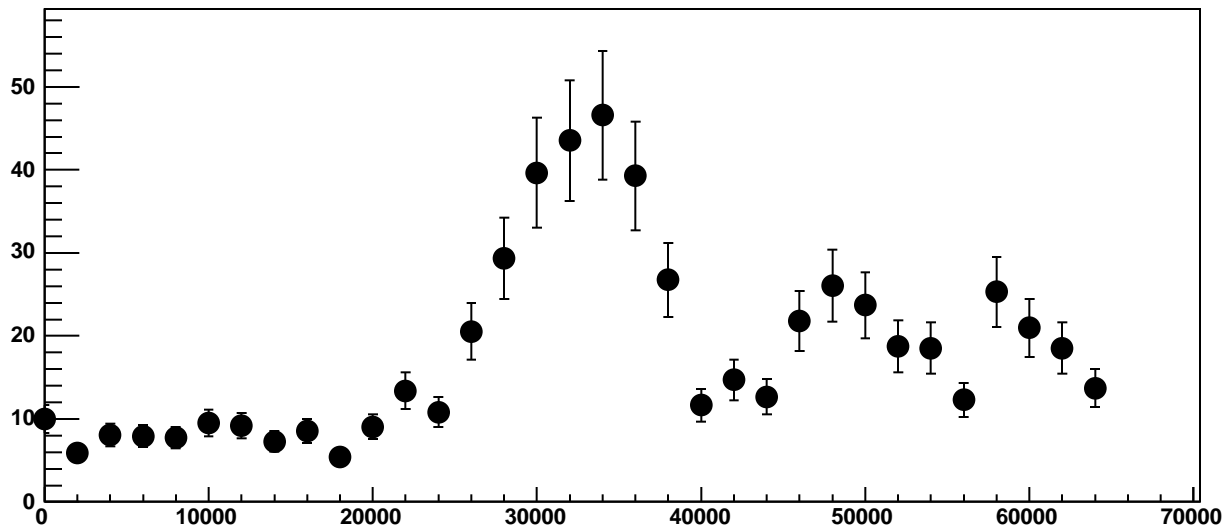
Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



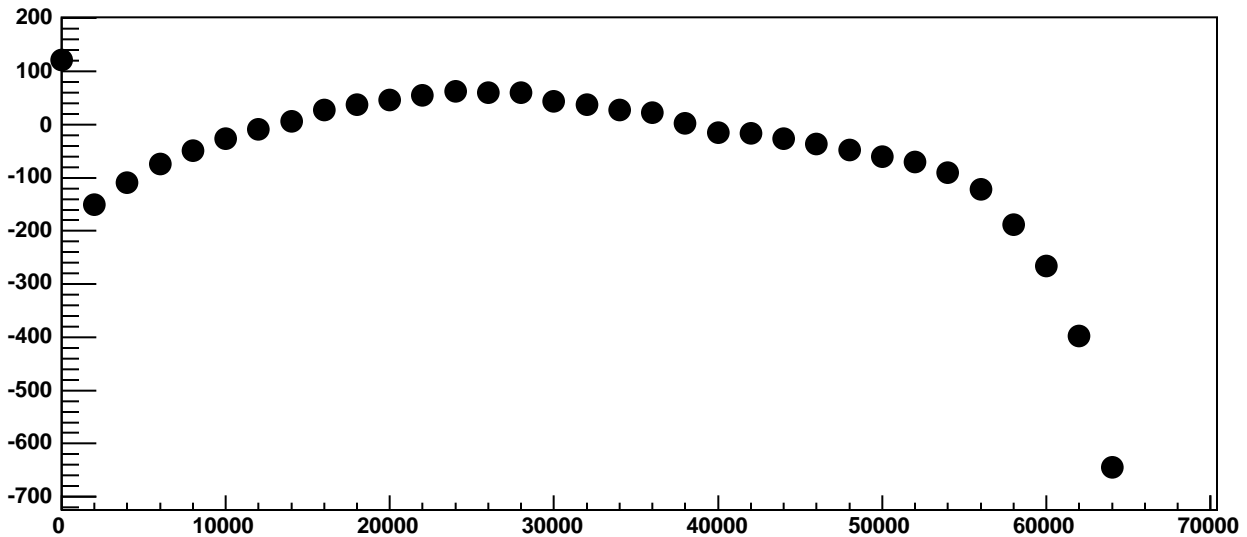
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



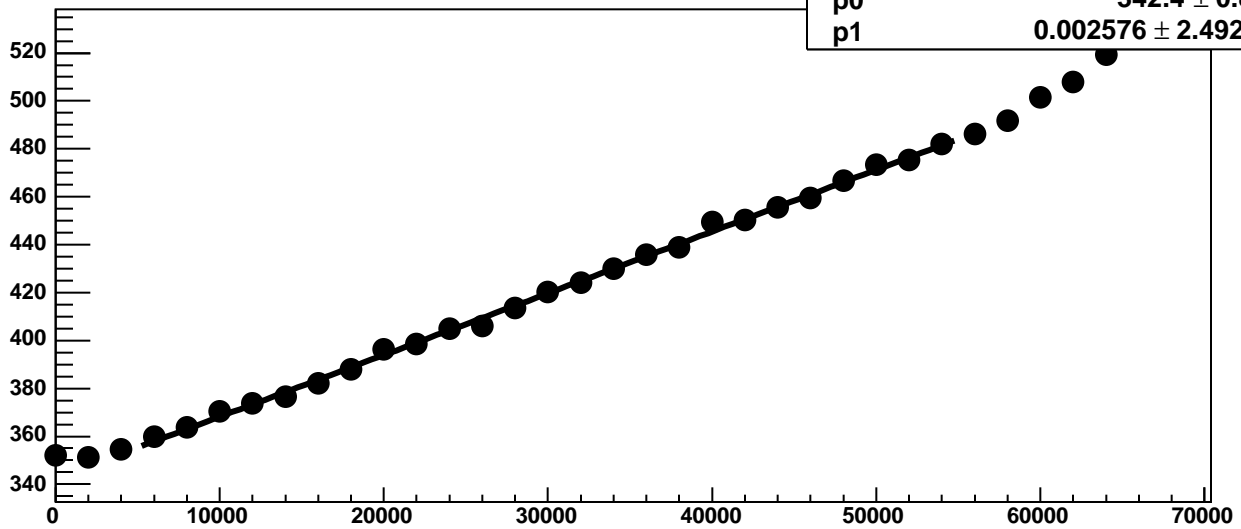
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

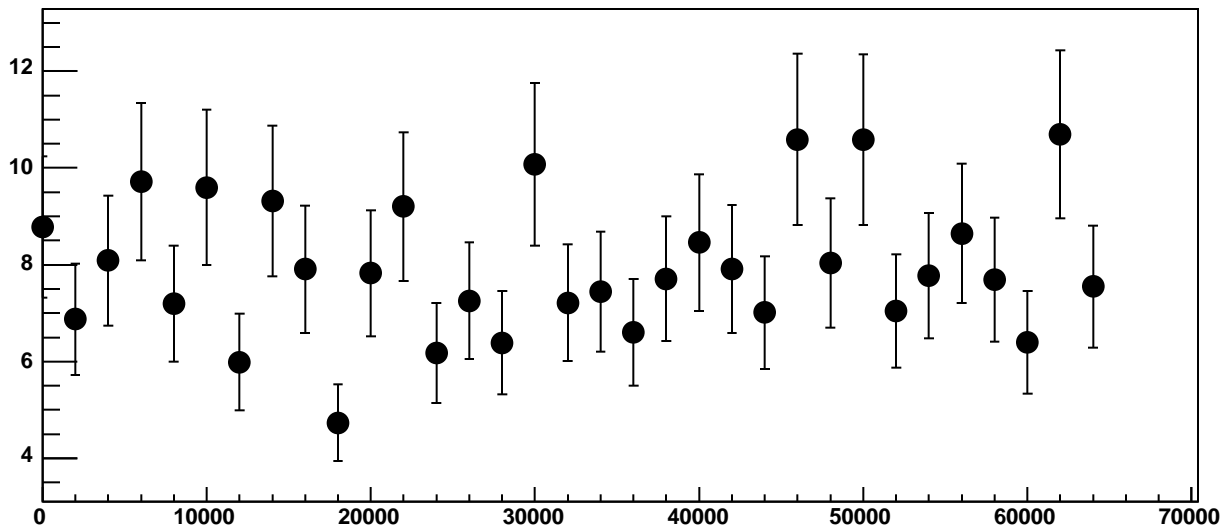


Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

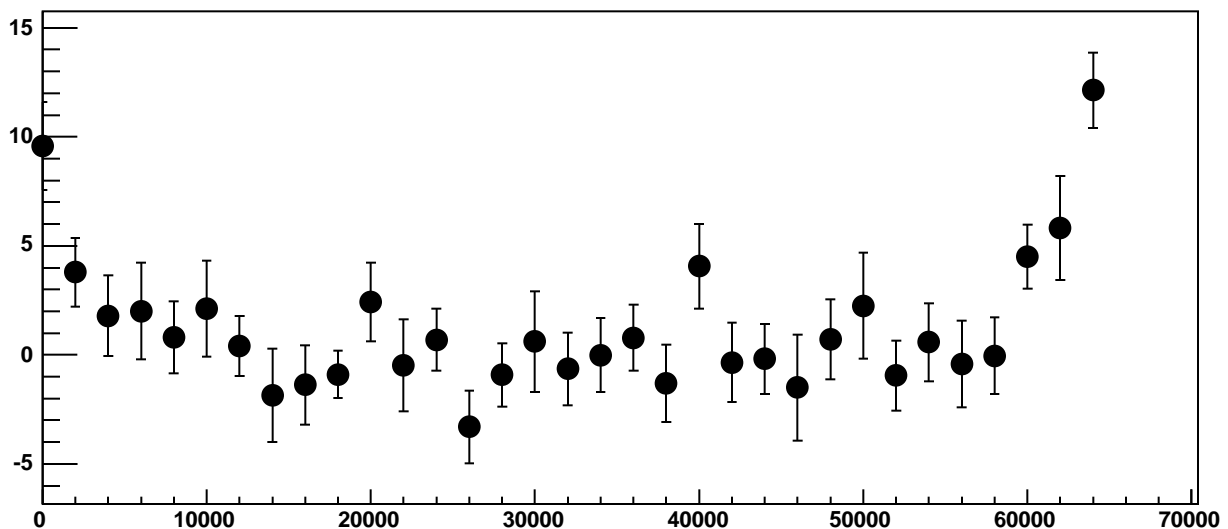


χ^2 / ndf 17.84 / 23
p0 342.4 ± 0.8005
p1 $0.002576 \pm 2.492e-05$

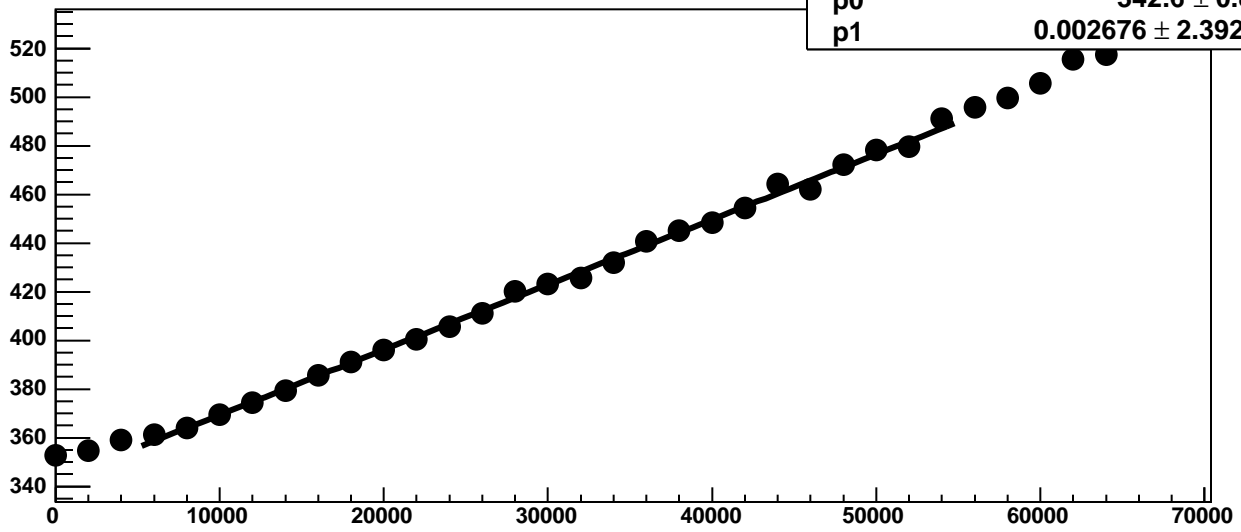
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.23 / 23

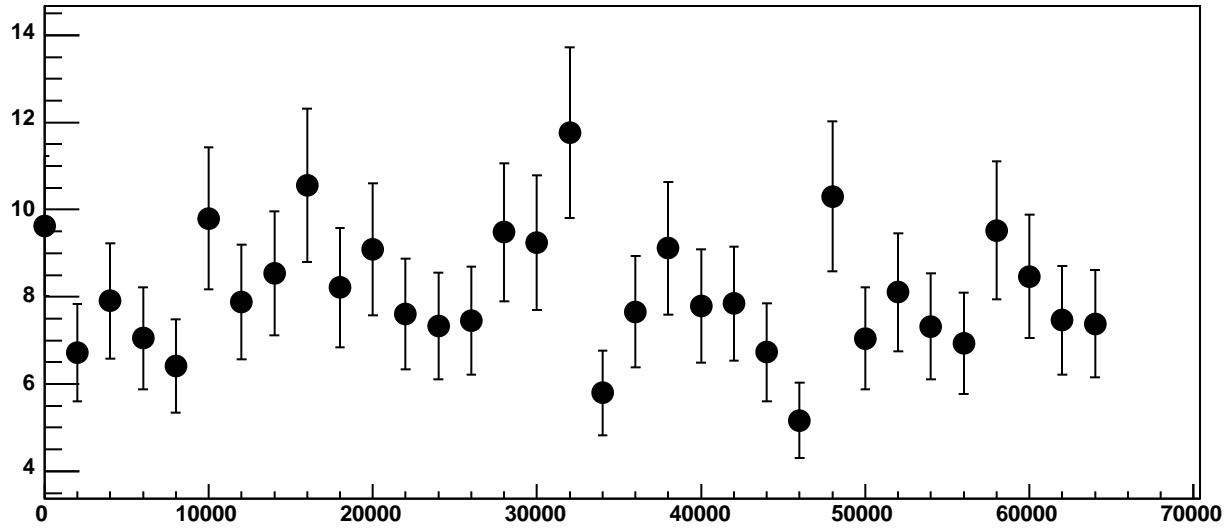
p0

342.6 ± 0.8234

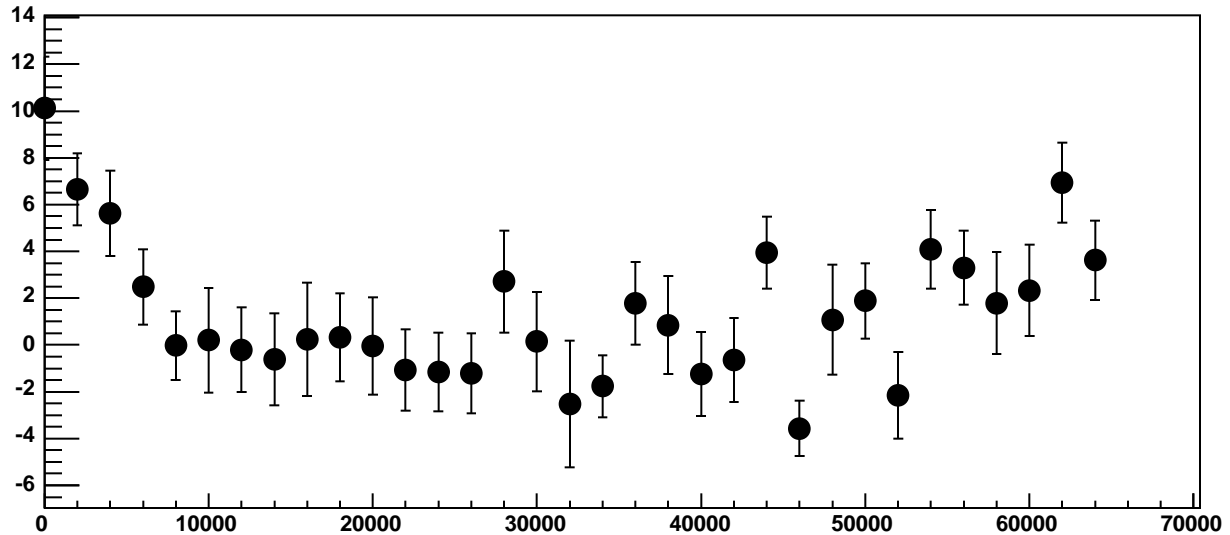
p1

$0.002676 \pm 2.392e-05$

Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

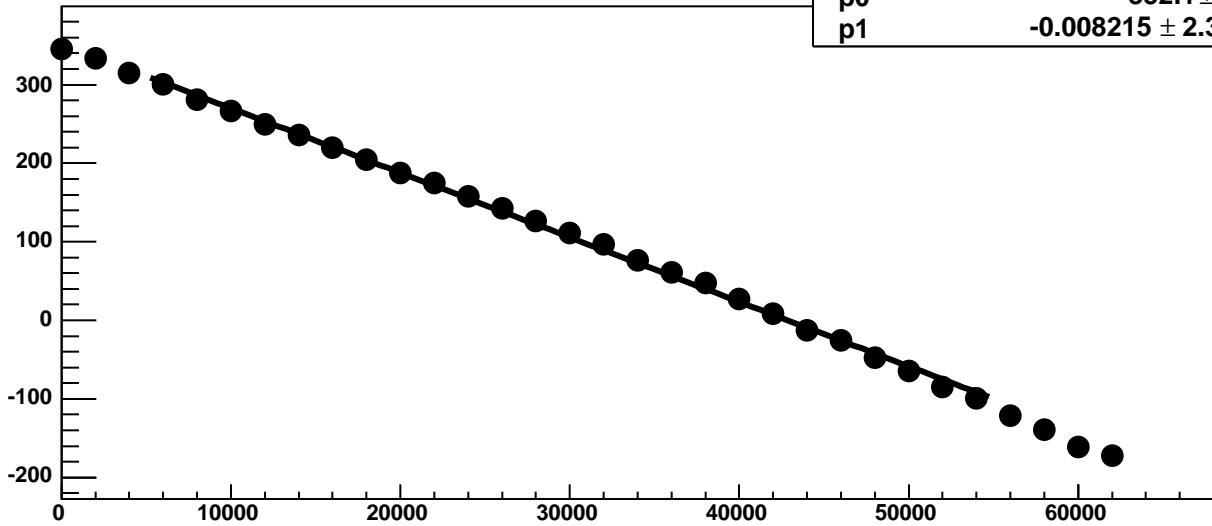


Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

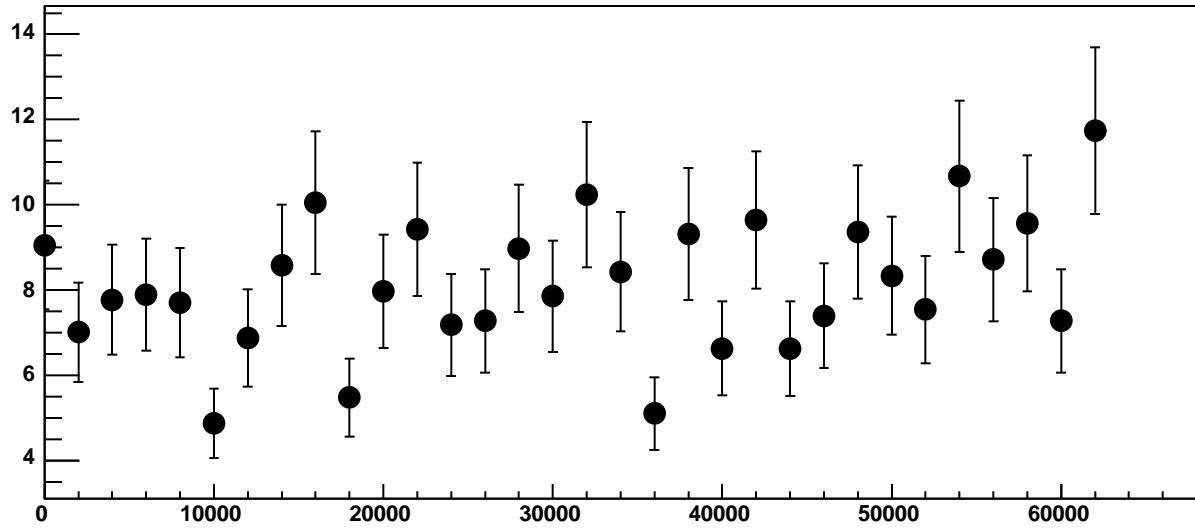


Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

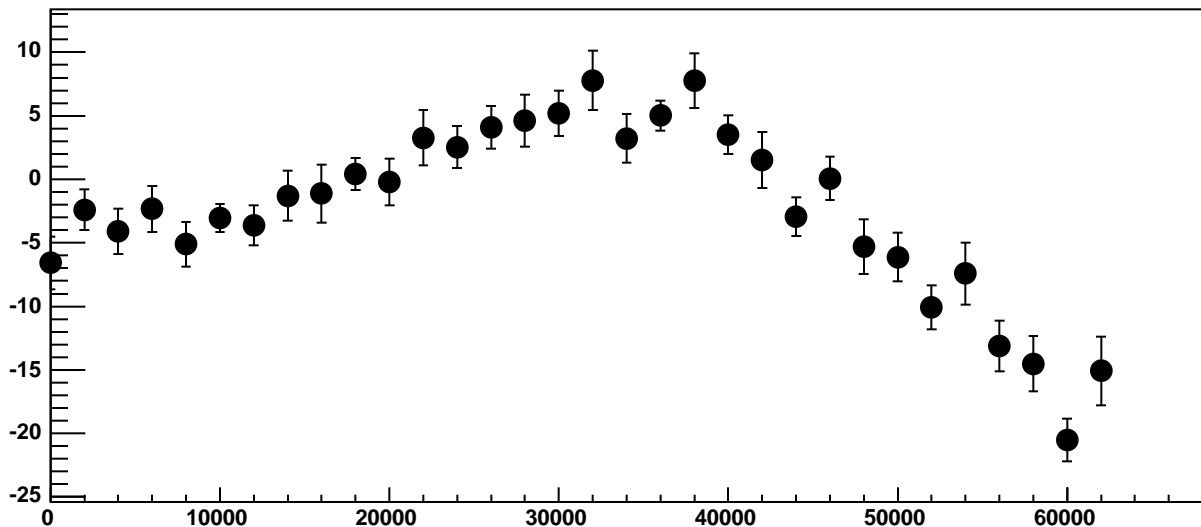
χ^2 / ndf 161.8 / 23
p0 352.1 ± 0.7602
p1 -0.008215 ± 2.394e-05



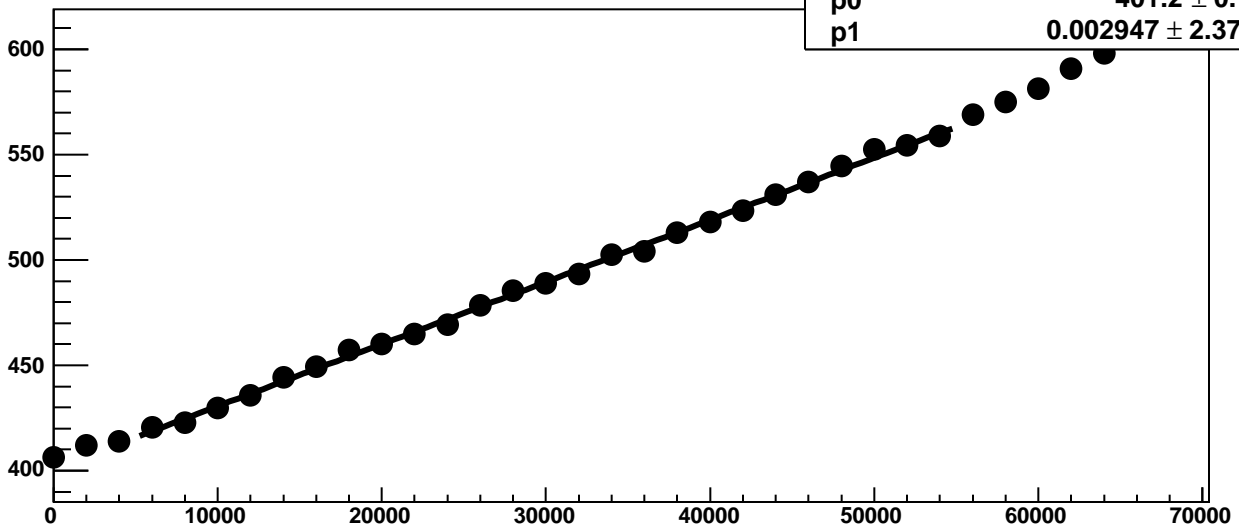
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

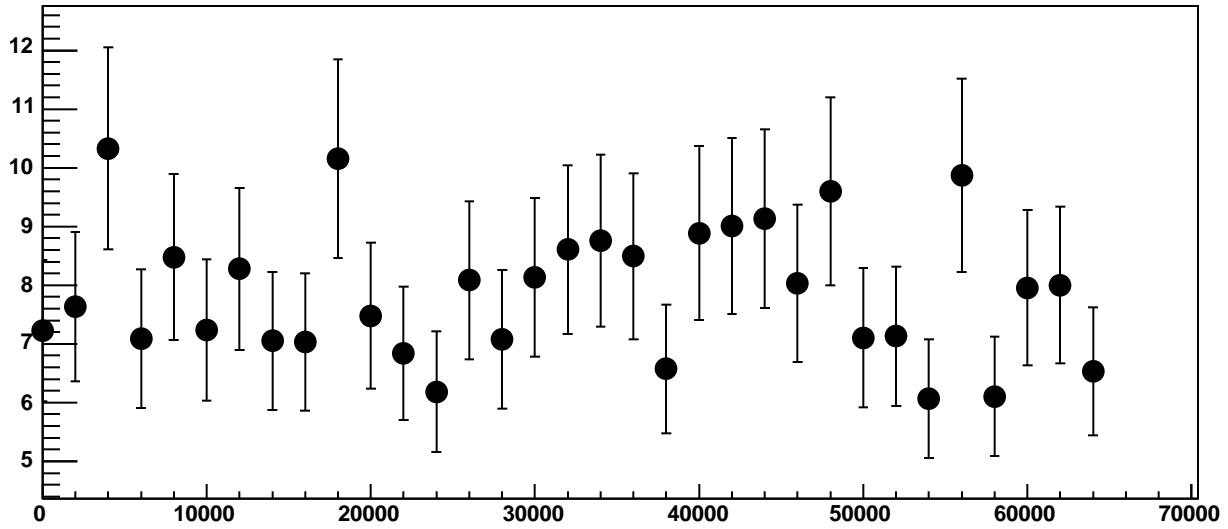


Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

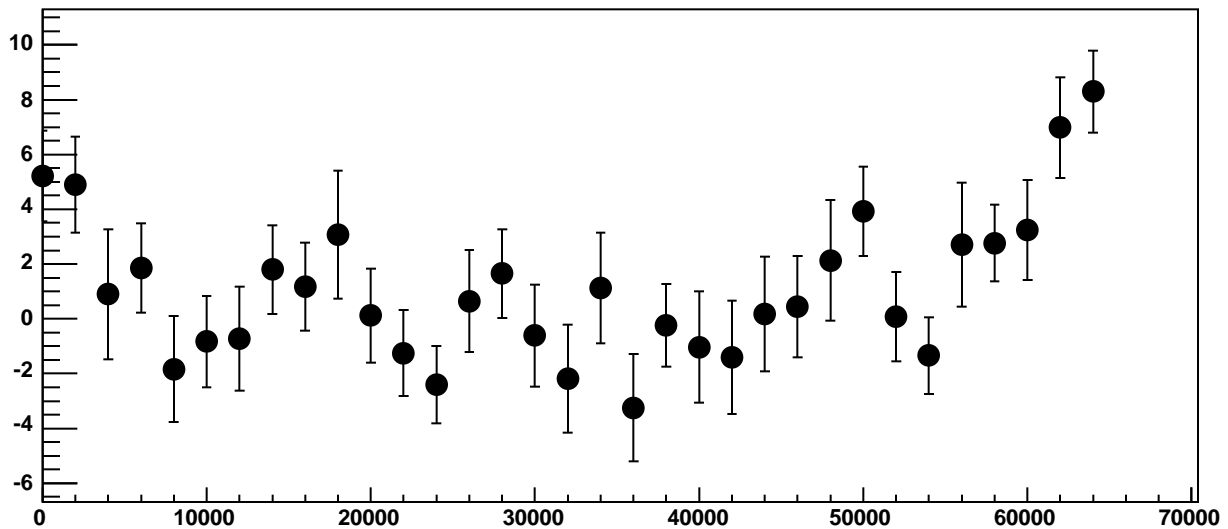


χ^2 / ndf 23.62 / 23
p0 401.2 ± 0.7909
p1 $0.002947 \pm 2.37\text{e-}05$

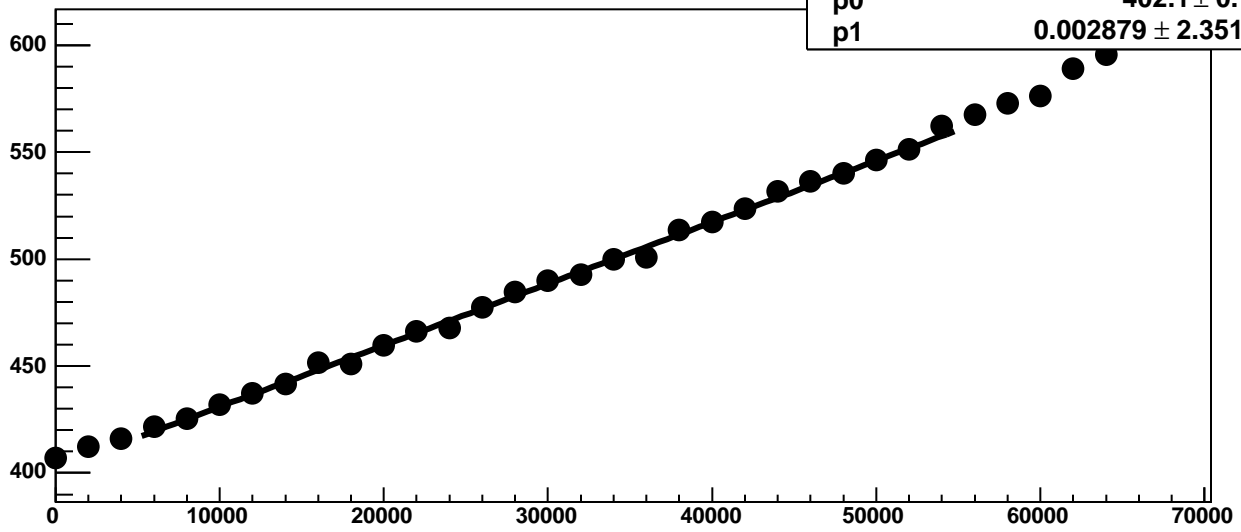
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



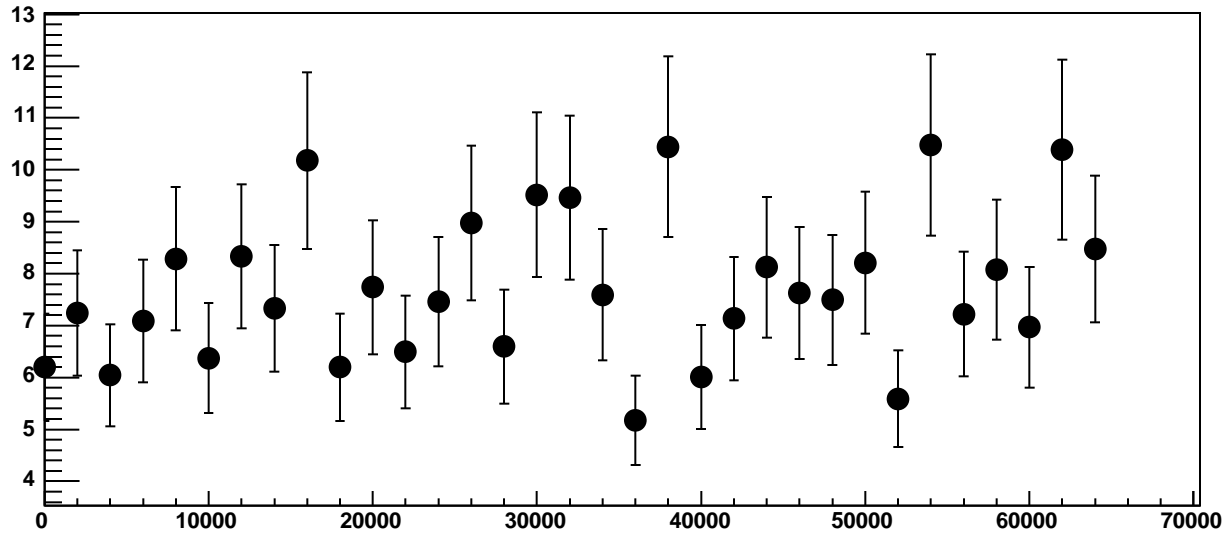
Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



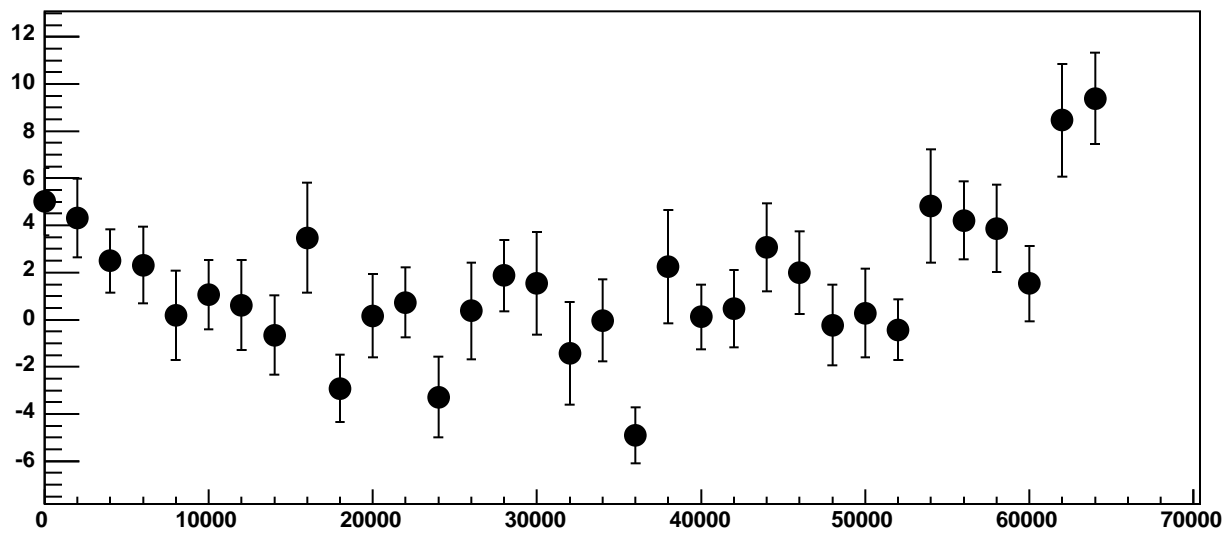
Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



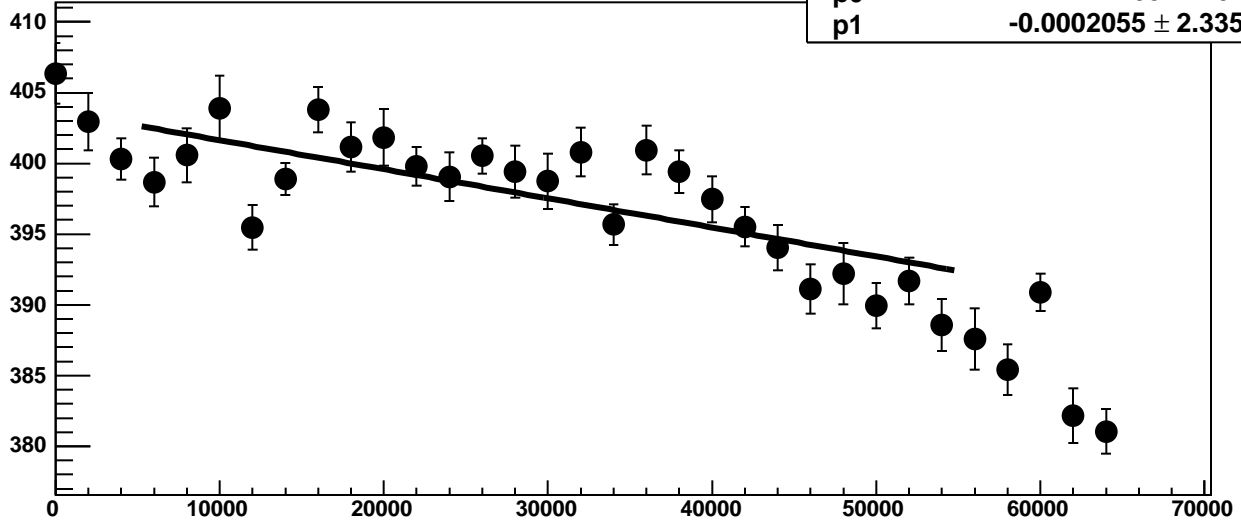
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

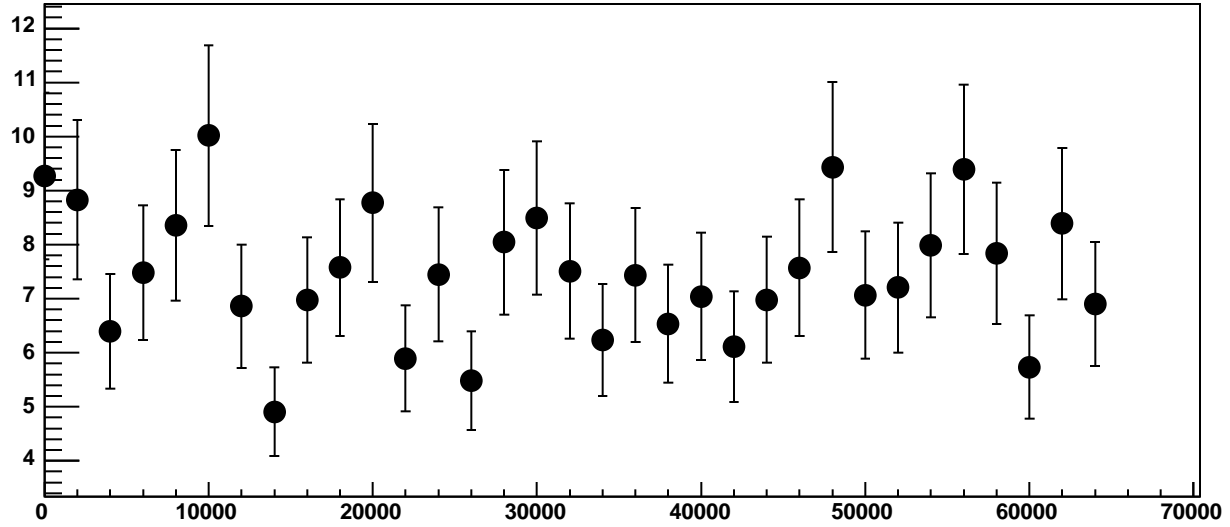


Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

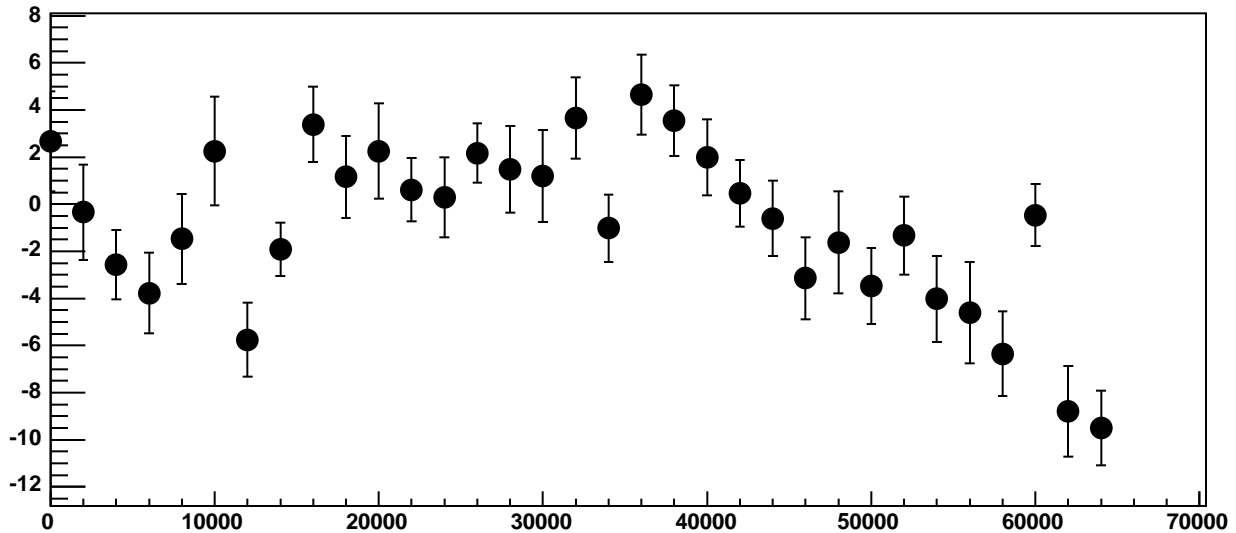


χ^2 / ndf 66.81 / 23
p0 403.7 ± 0.7635
p1 $-0.0002055 \pm 2.335e-05$

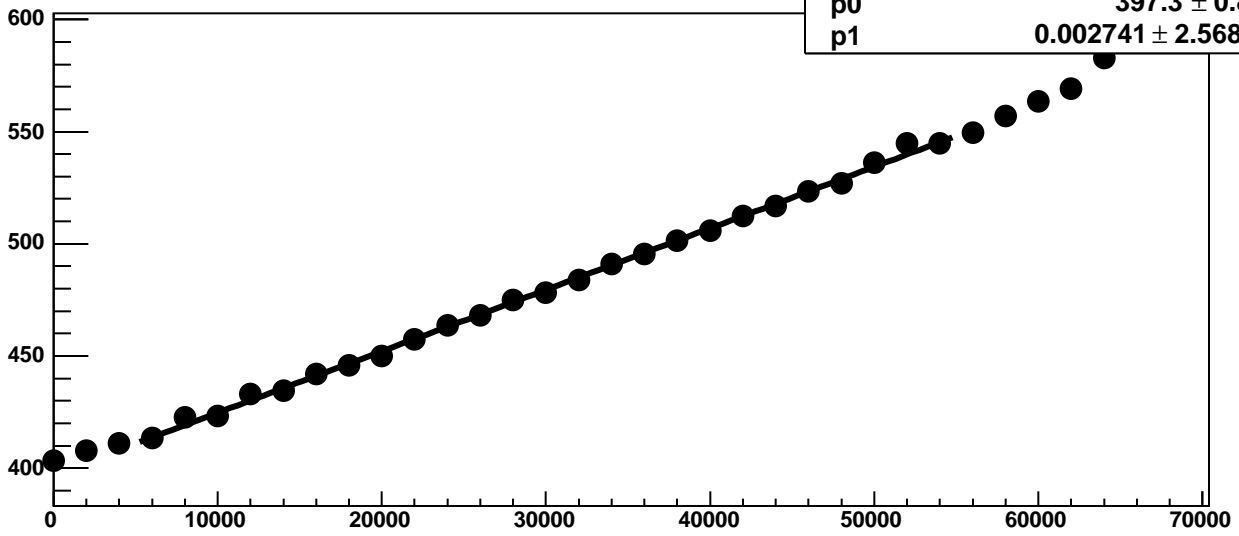
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

21.24 / 23

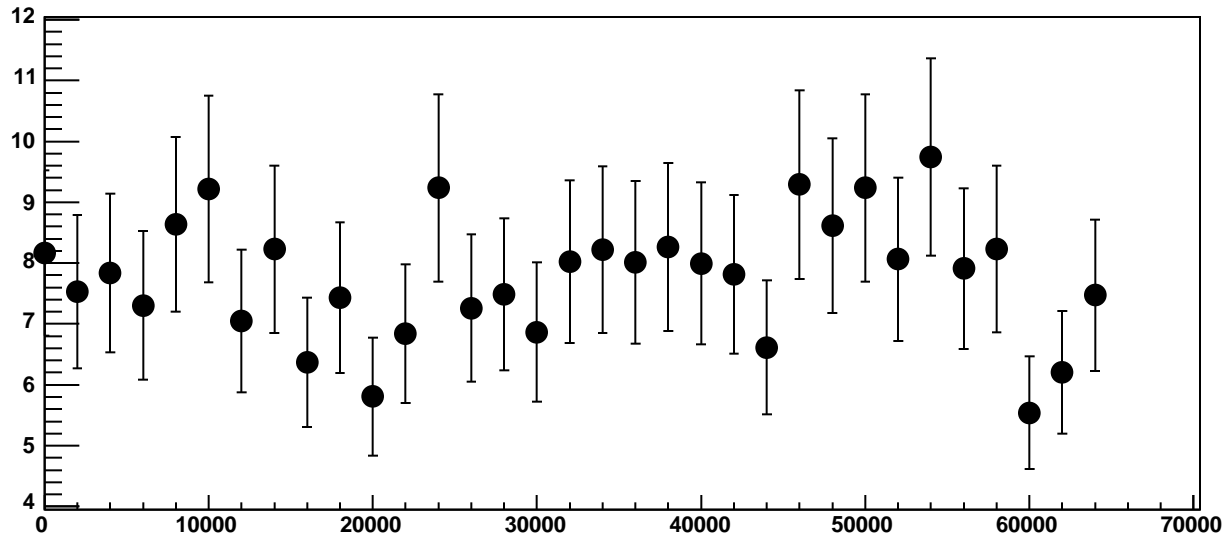
p0

397.3 ± 0.8165

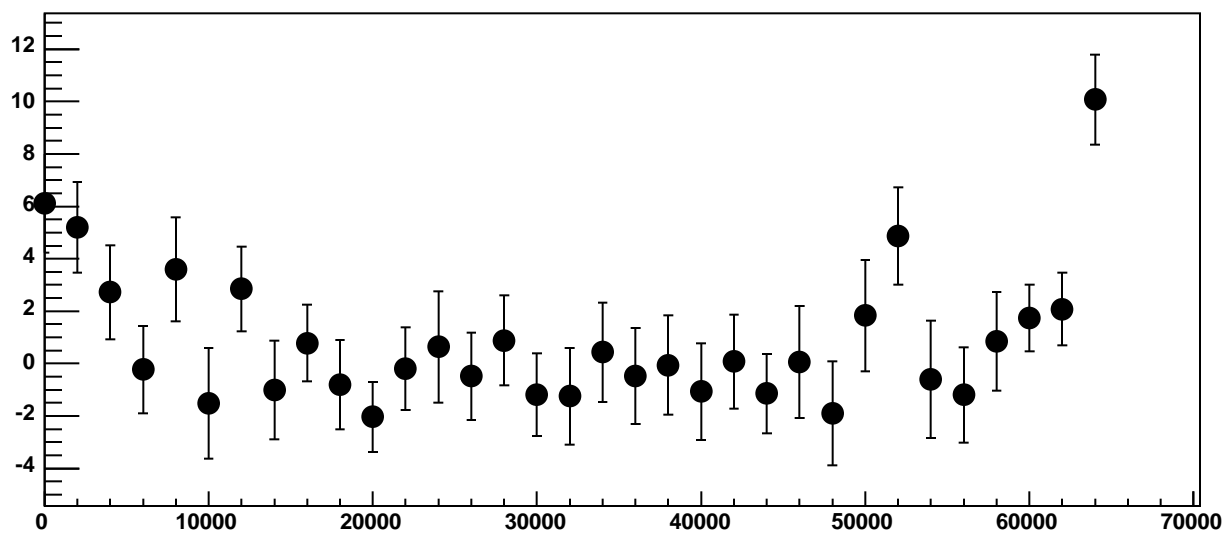
p1

$0.002741 \pm 2.568e-05$

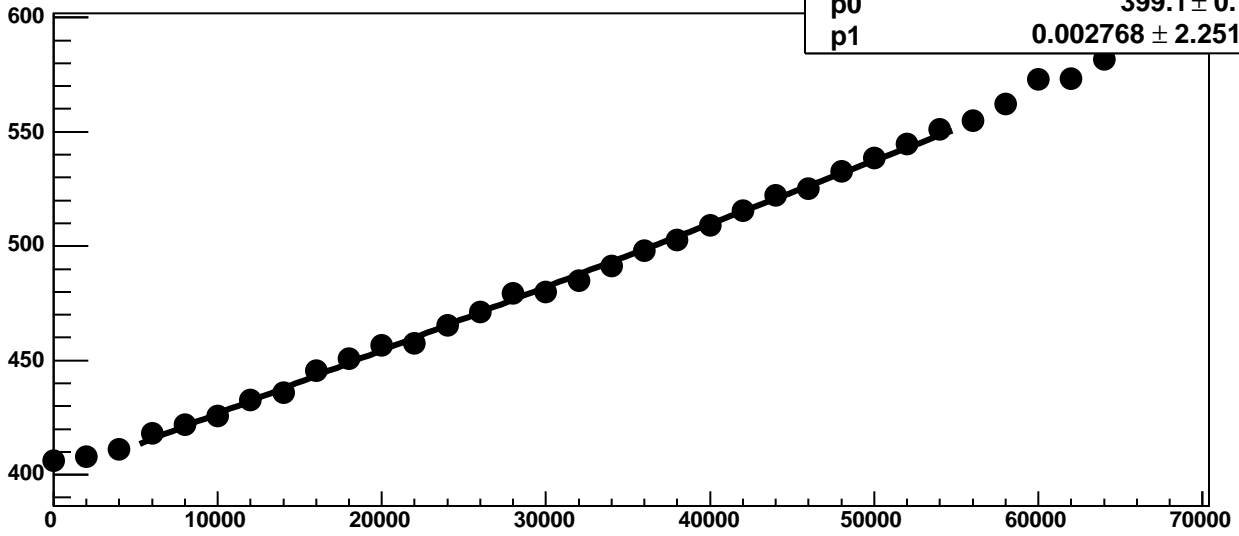
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



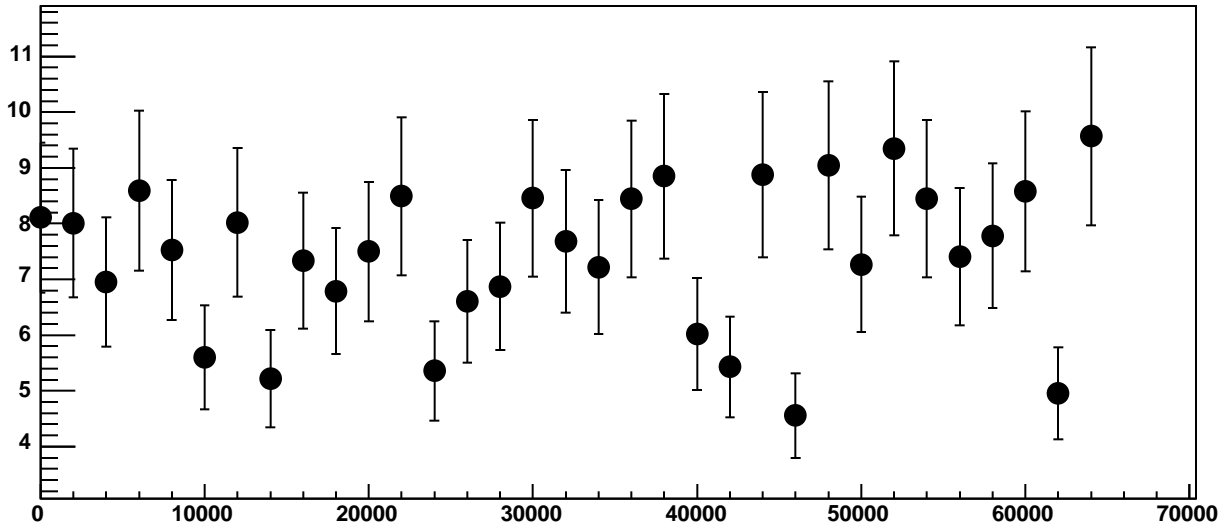
Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



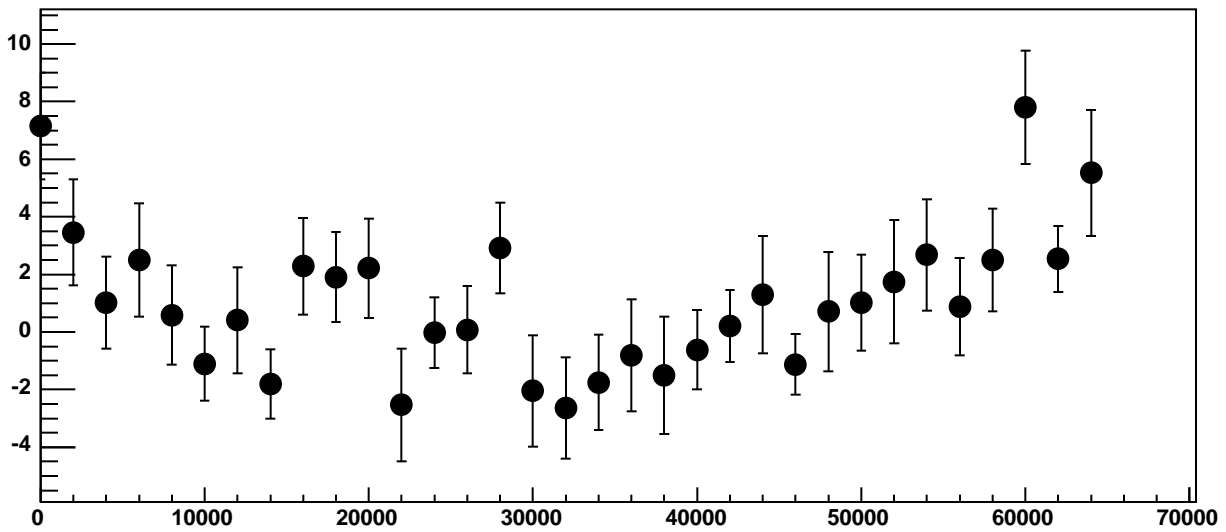
Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



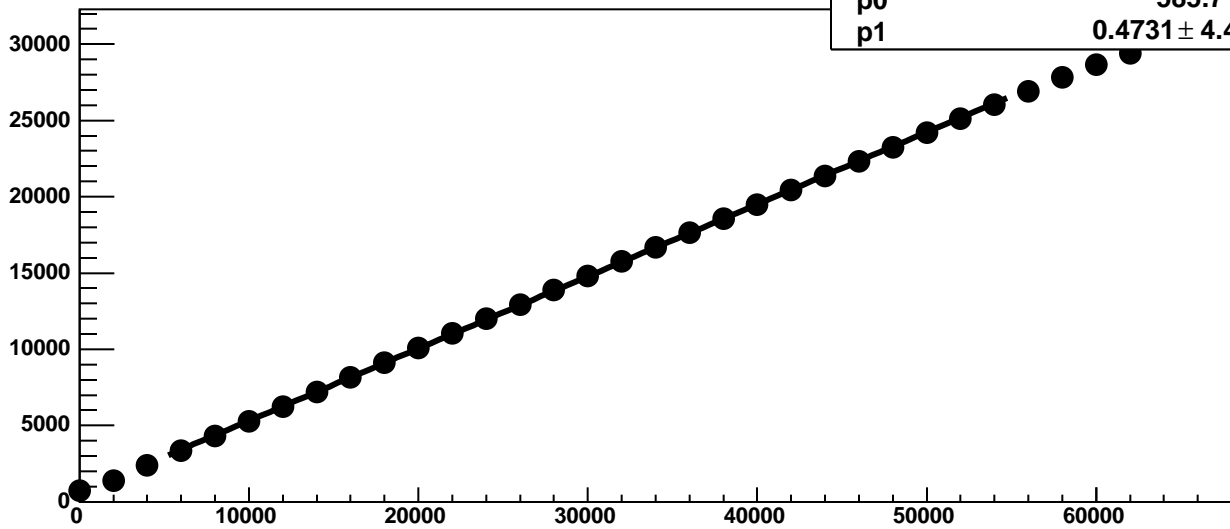
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



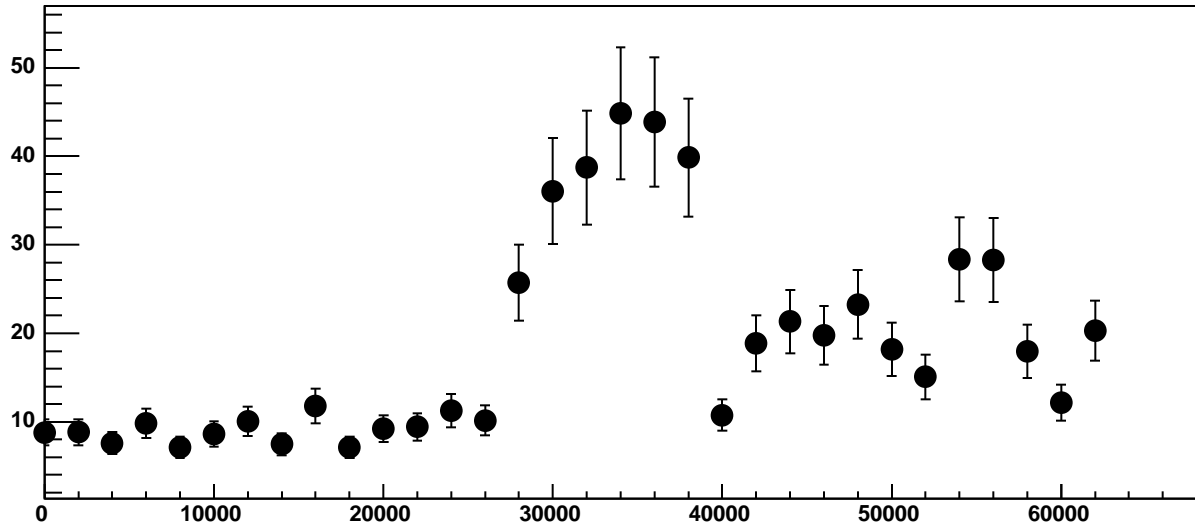
Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



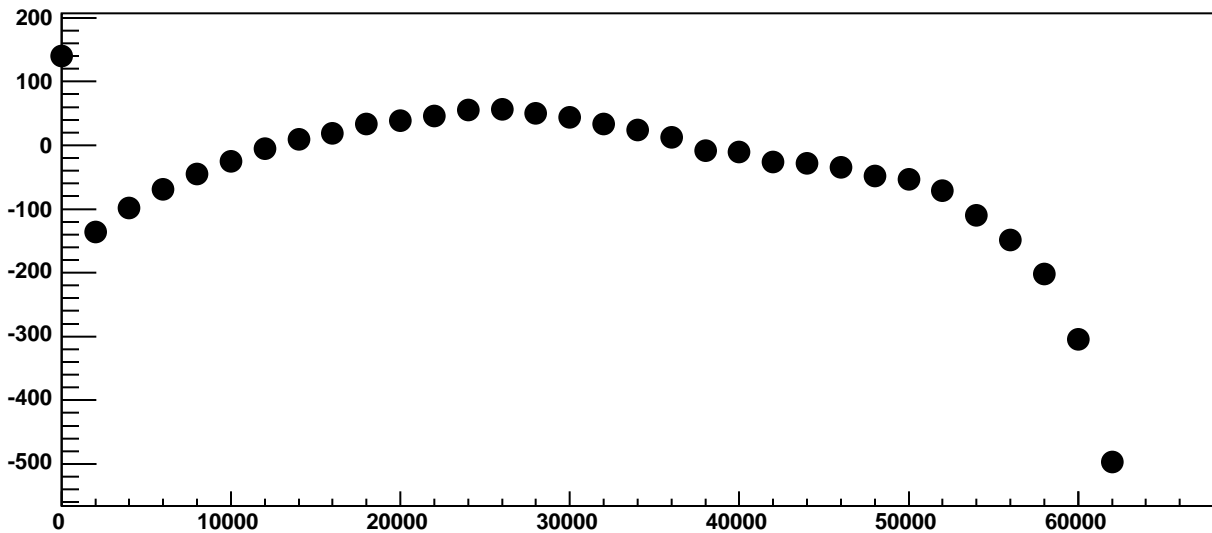
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



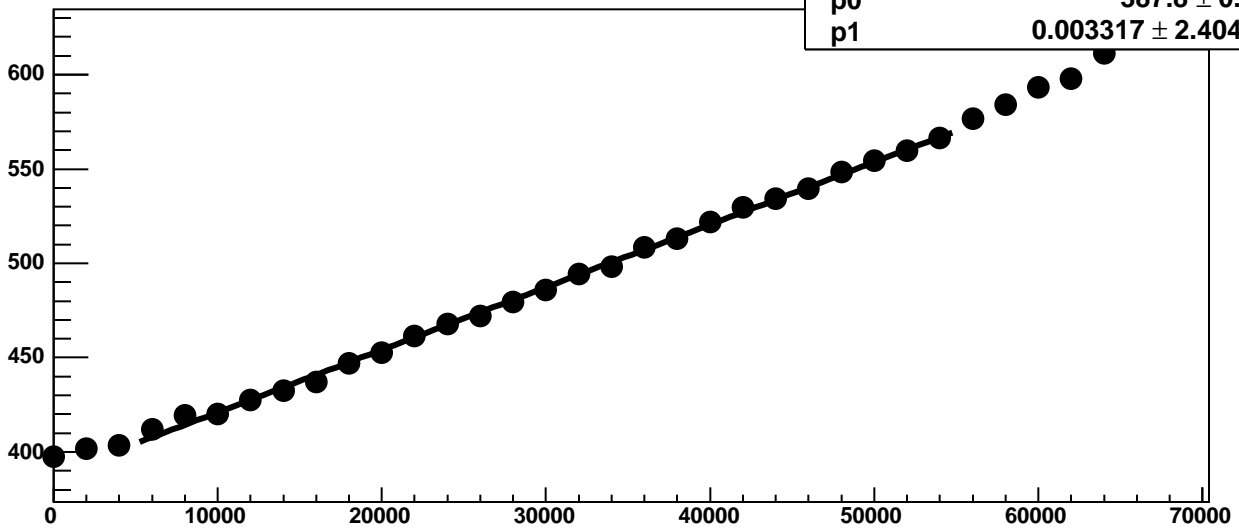
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.16 / 23

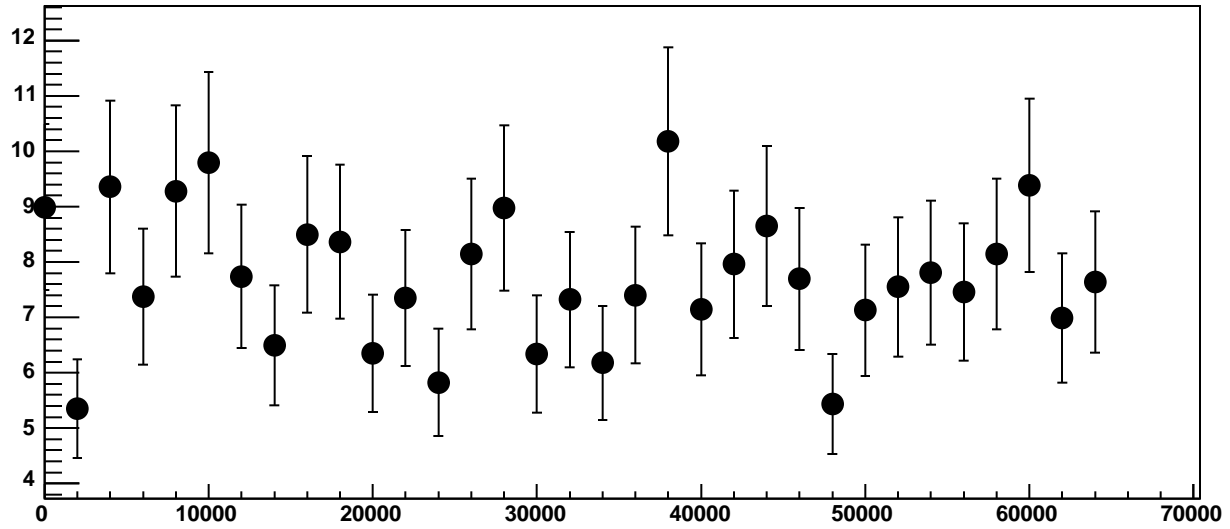
p0

387.8 ± 0.8131

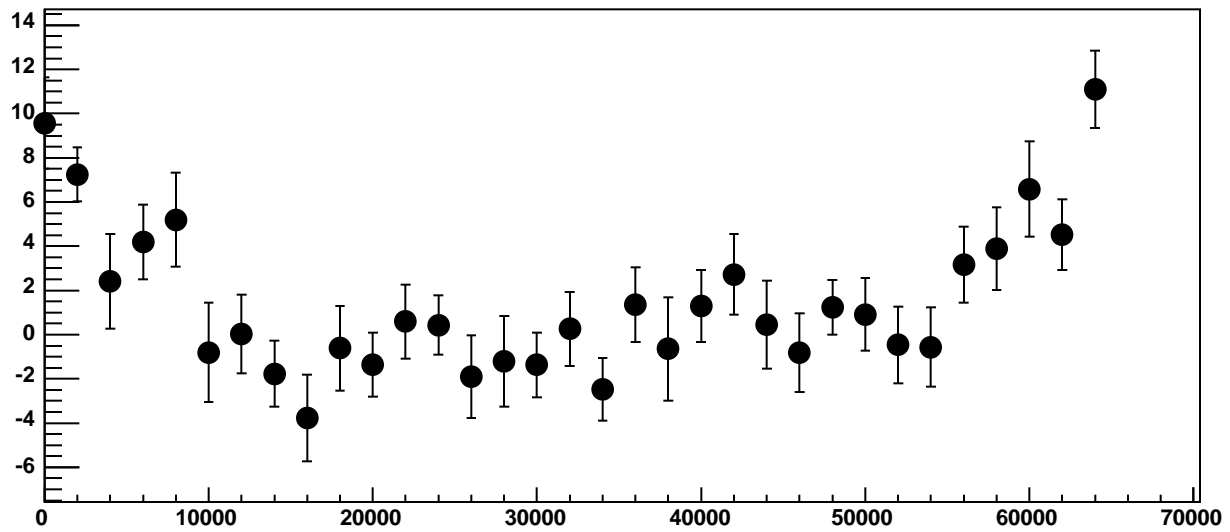
p1

$0.003317 \pm 2.404e-05$

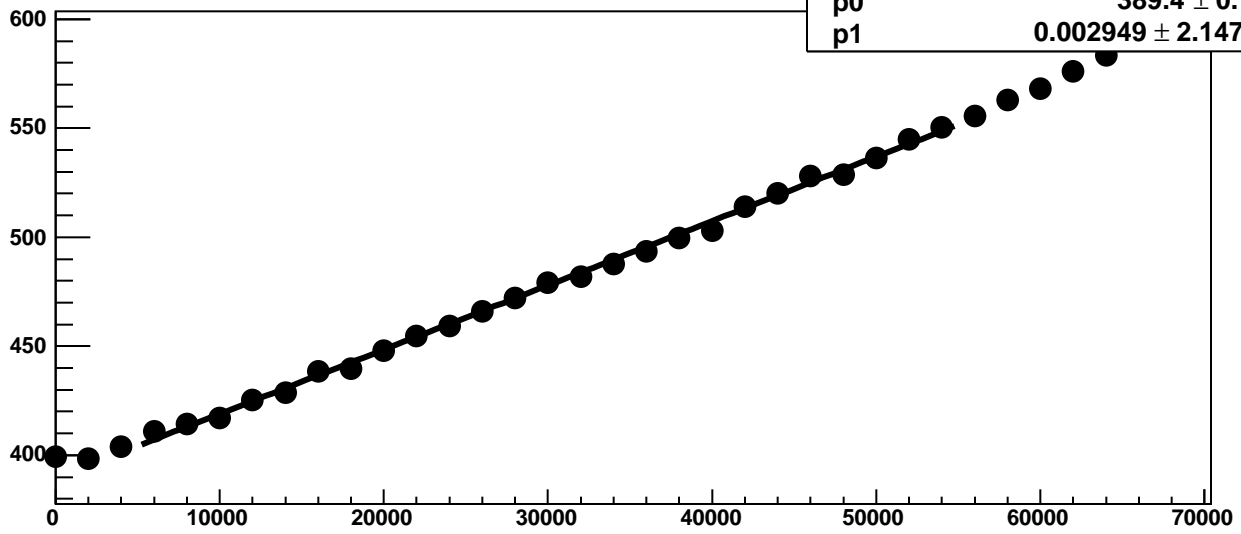
Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36 / 23

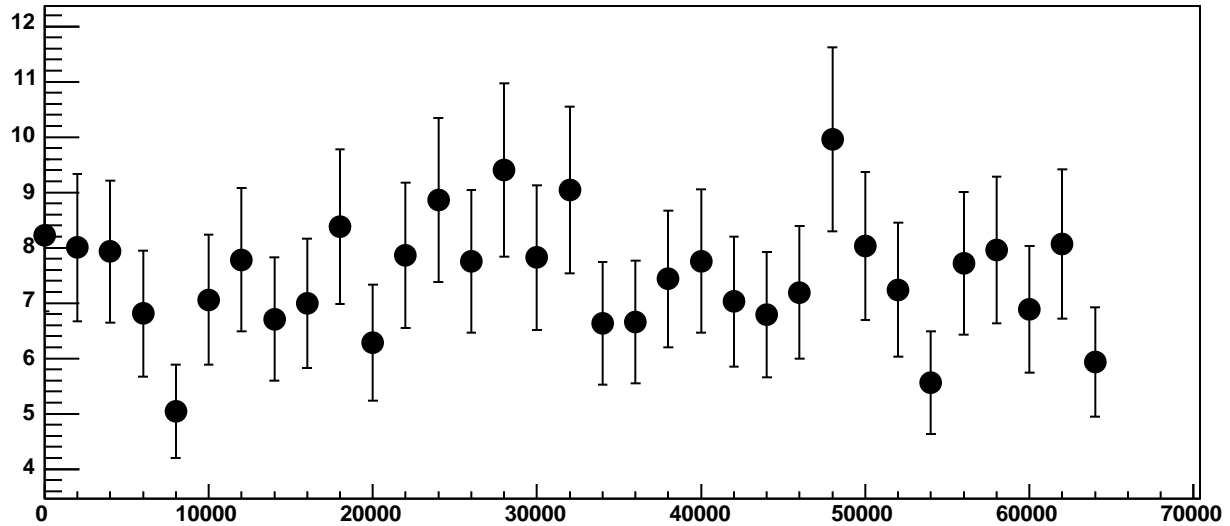
p0

389.4 ± 0.7105

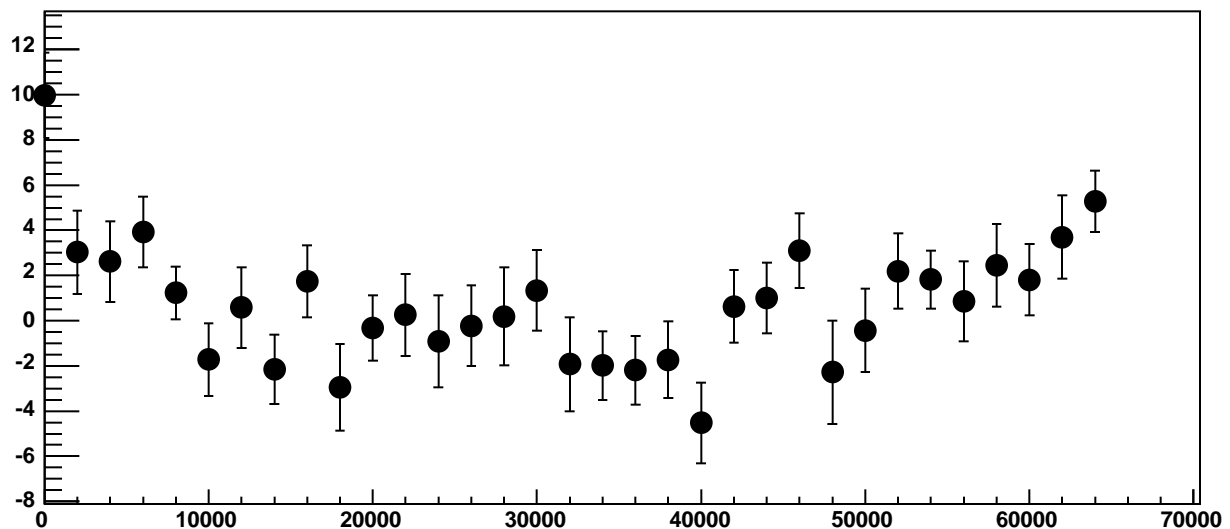
p1

$0.002949 \pm 2.147\text{e-}05$

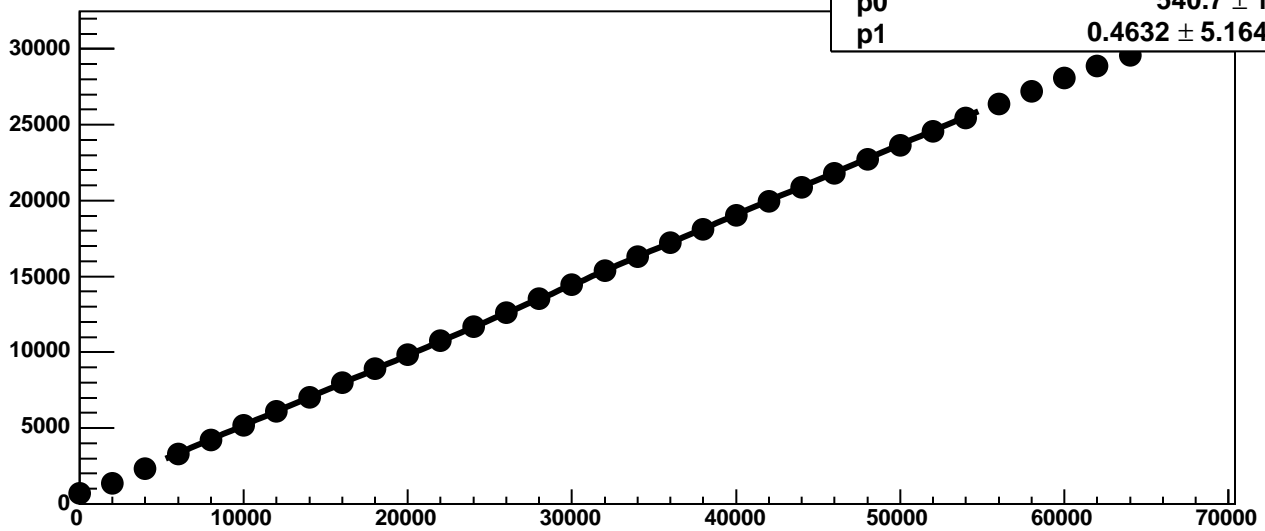
Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



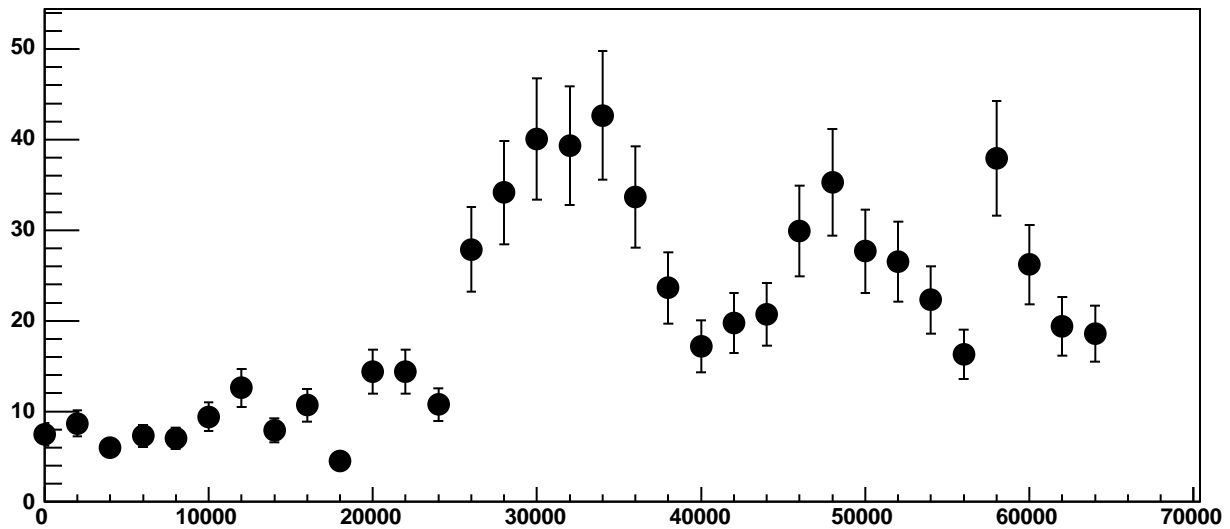
Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



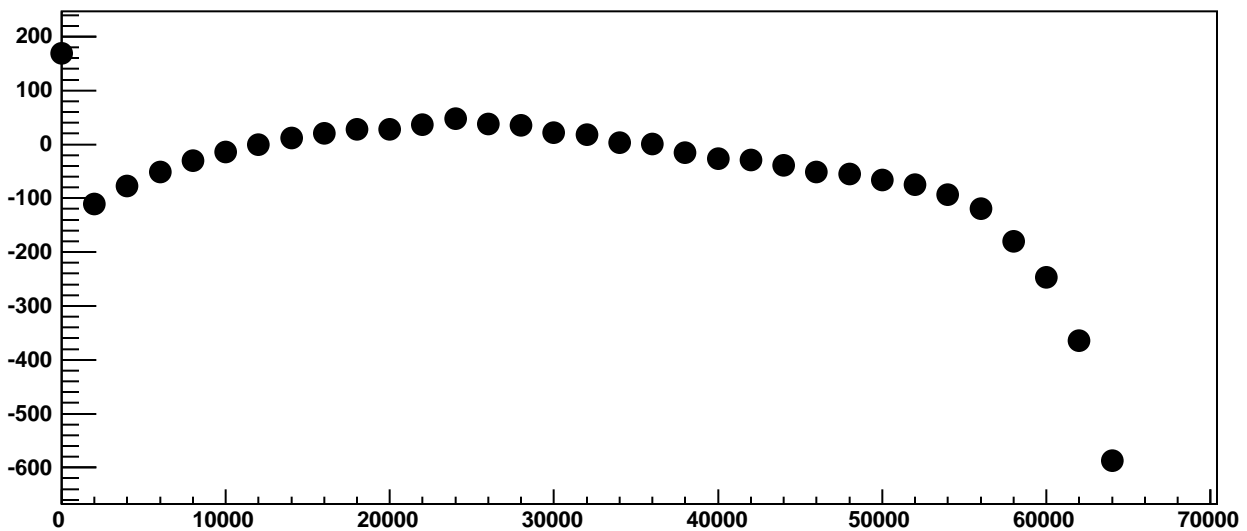
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



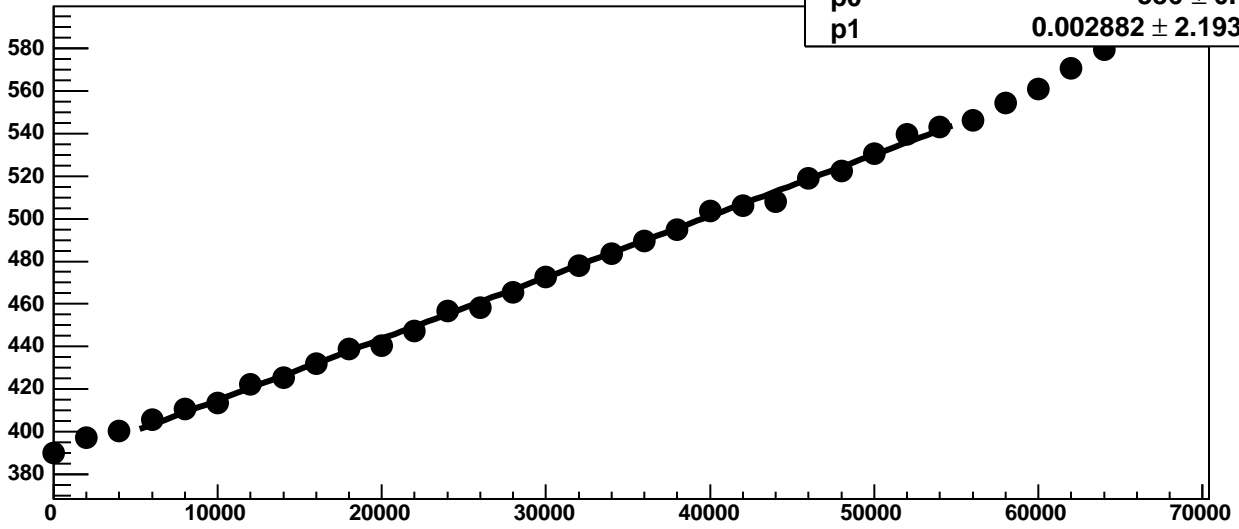
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

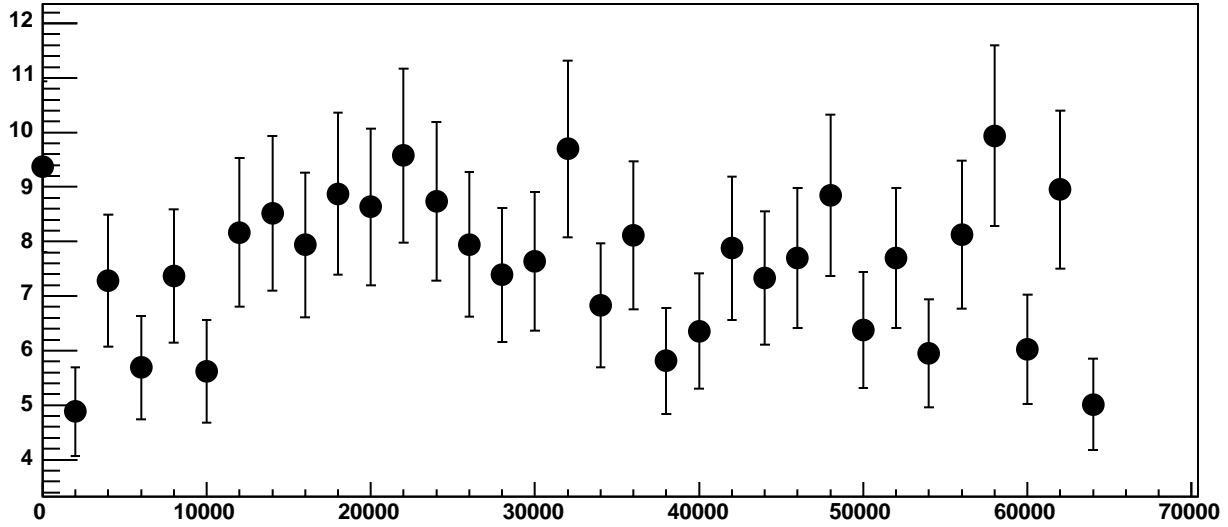


Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

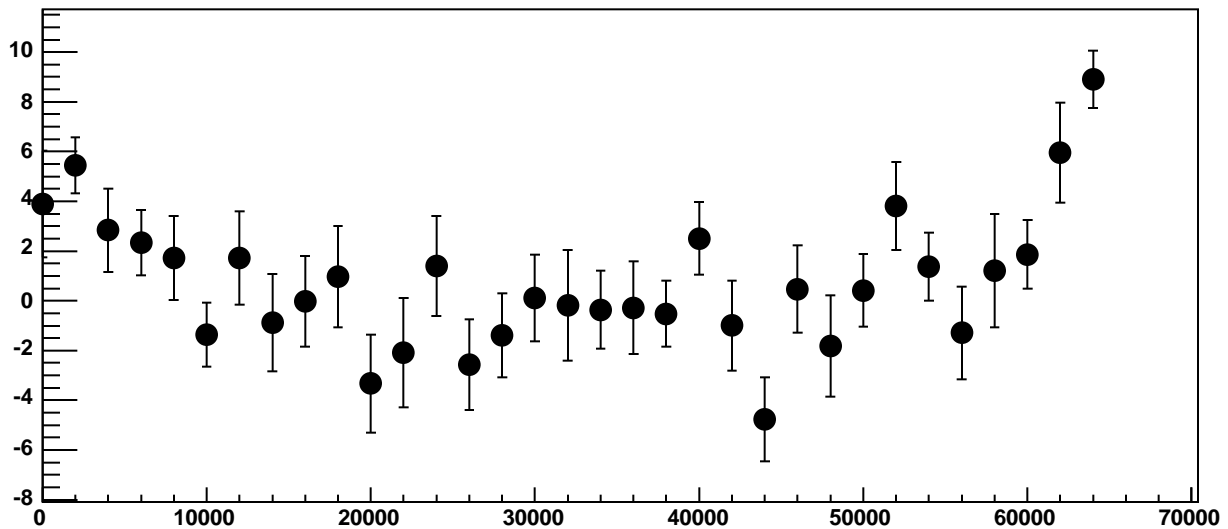


χ^2 / ndf 31.63 / 23
p0 386 ± 0.7447
p1 $0.002882 \pm 2.193e-05$

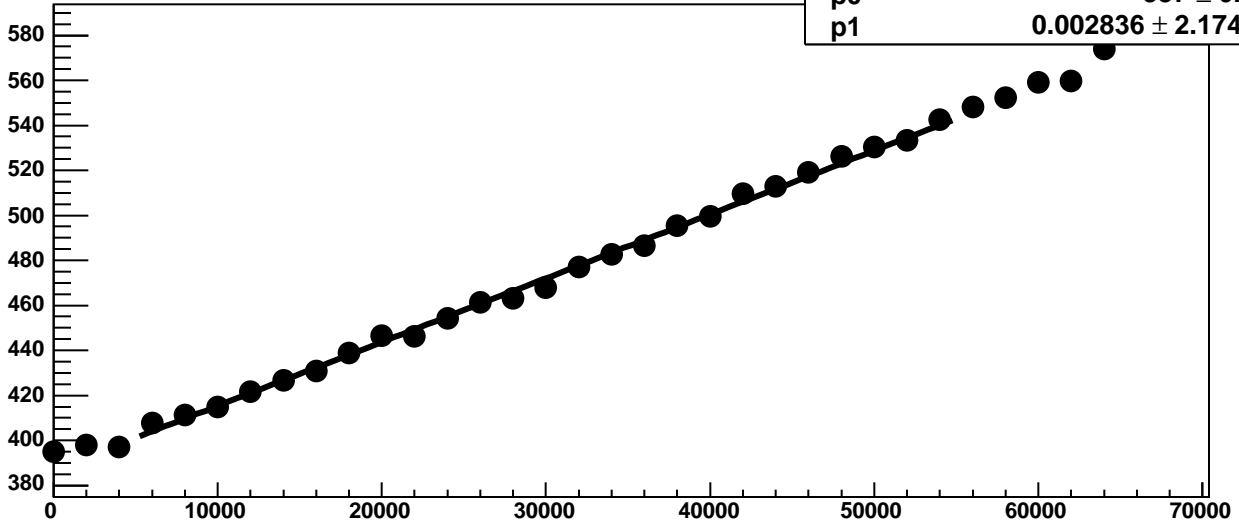
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

45.32 / 23

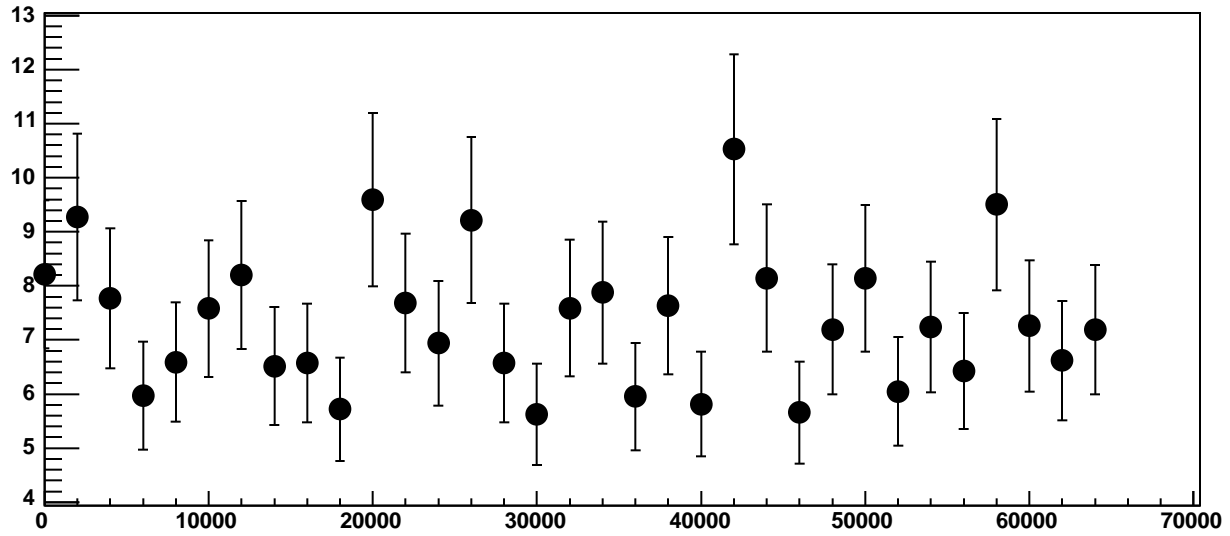
p0

387 ± 0.7221

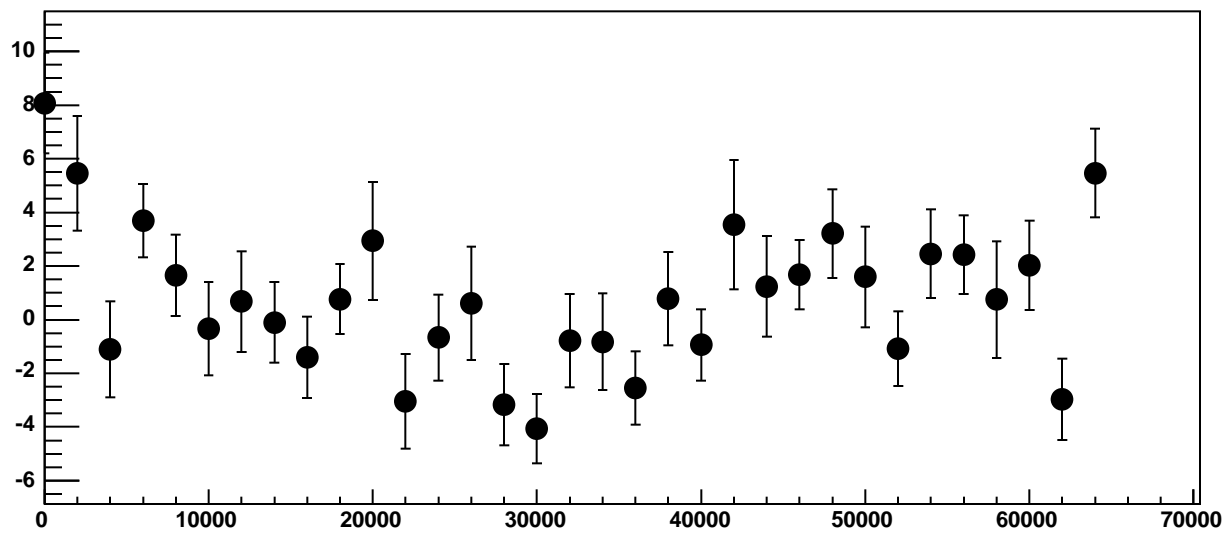
p1

$0.002836 \pm 2.174e-05$

Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

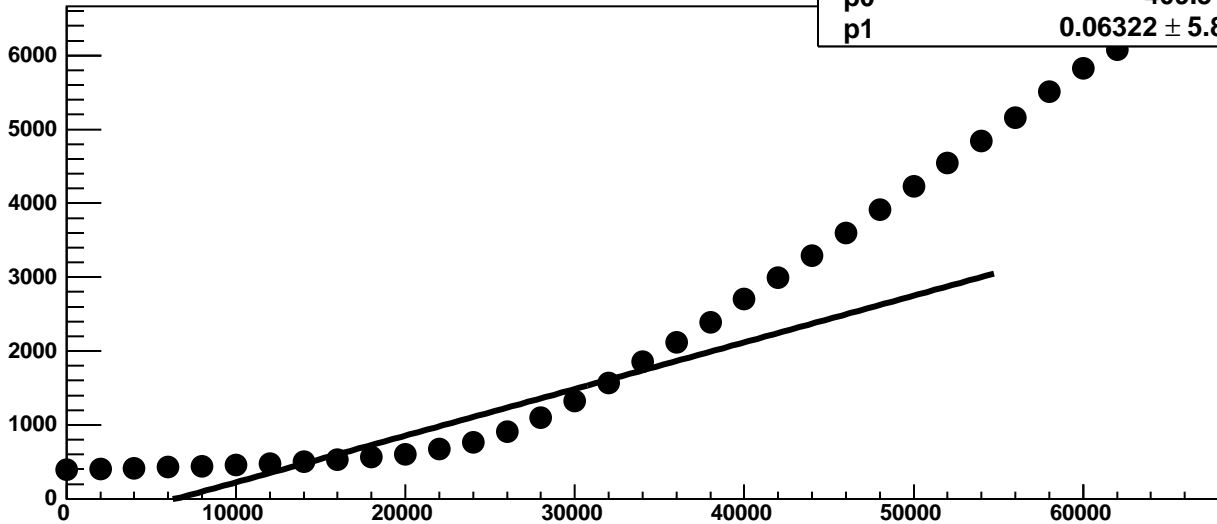


Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

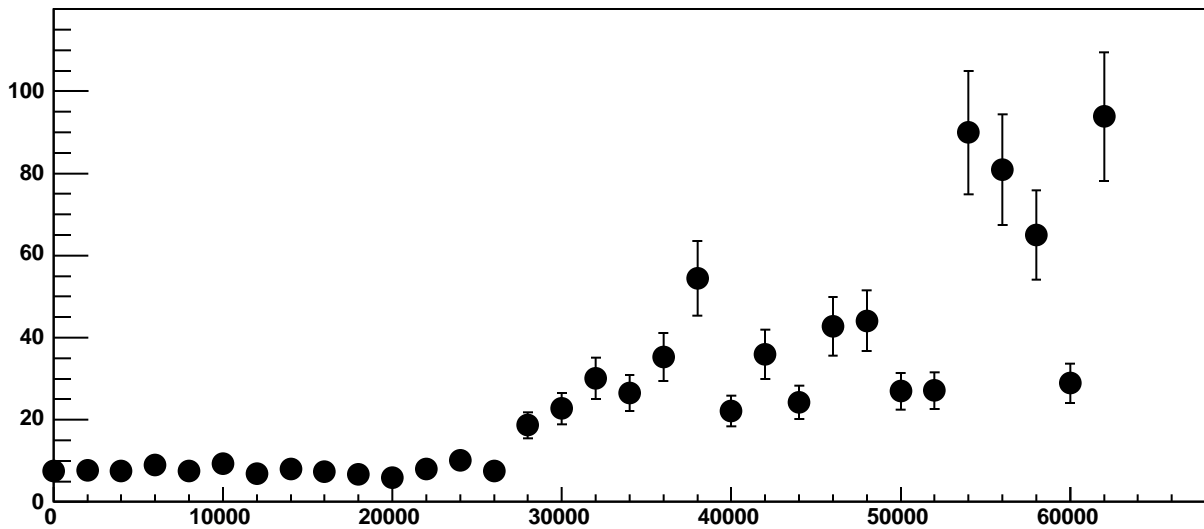


Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

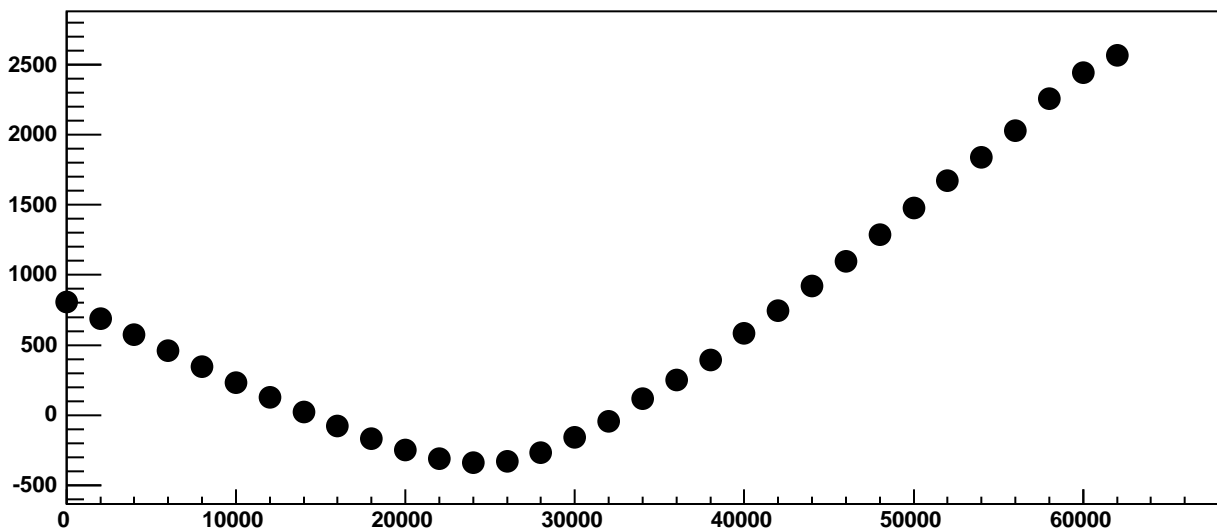
χ^2 / ndf 4.627e+05 / 23
p0 -409.5 ± 1.173
p1 0.06322 ± 5.856e-05



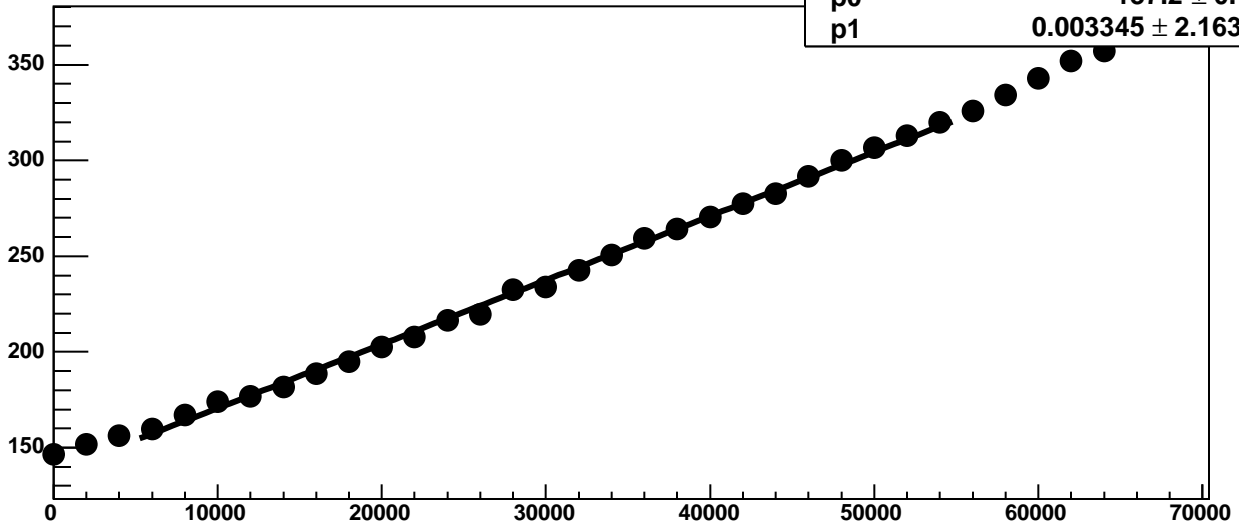
Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.74 / 23

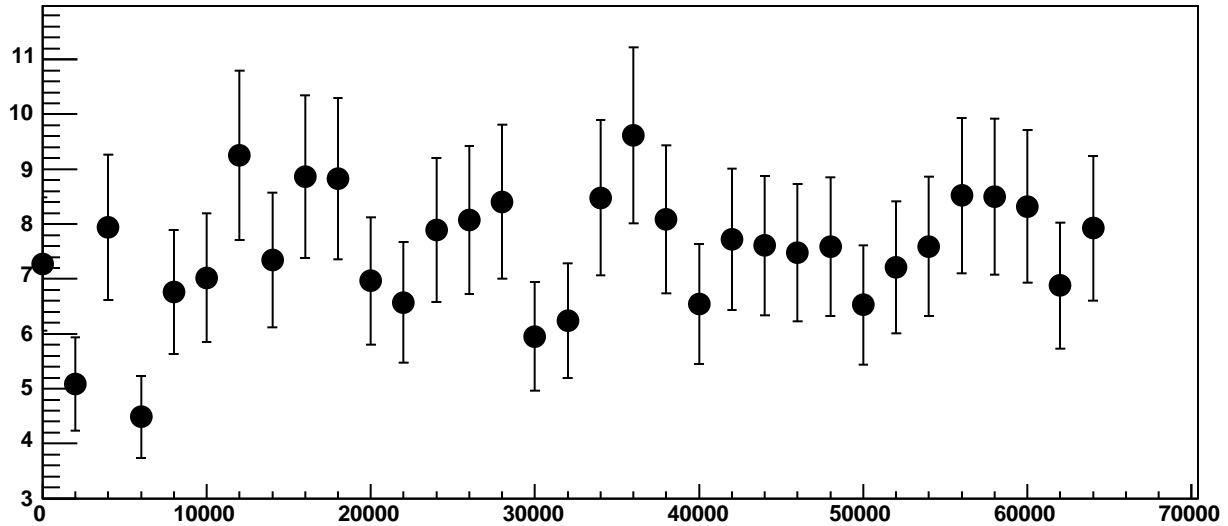
p0

137.2 ± 0.7034

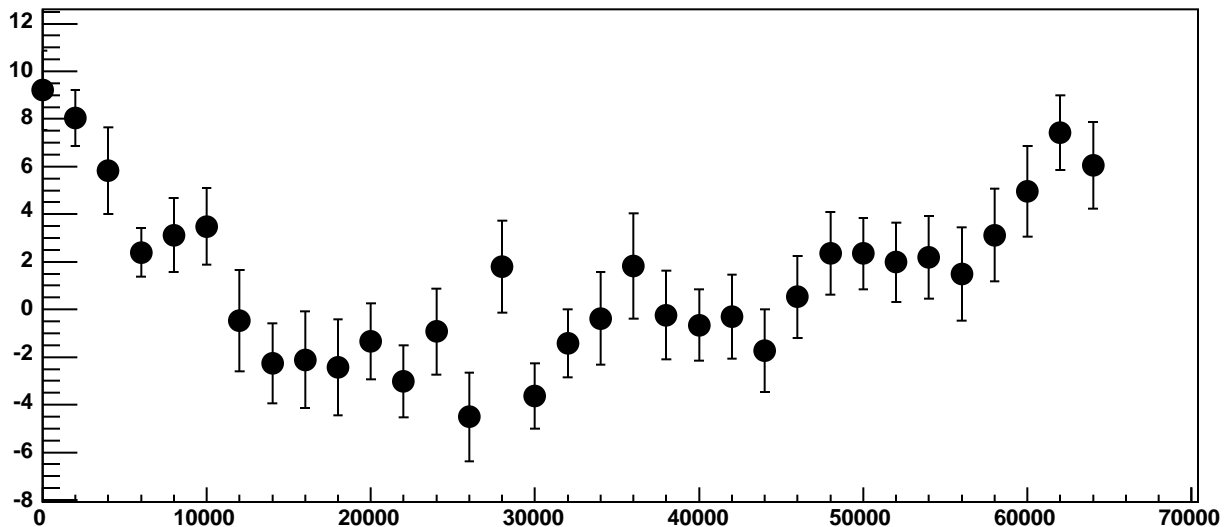
p1

$0.003345 \pm 2.163e-05$

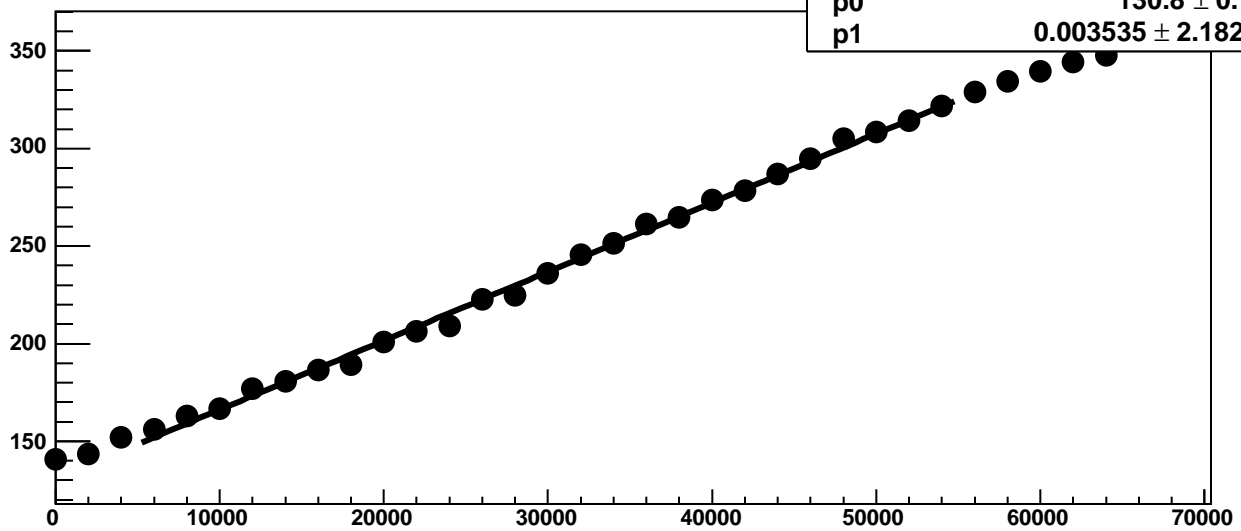
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



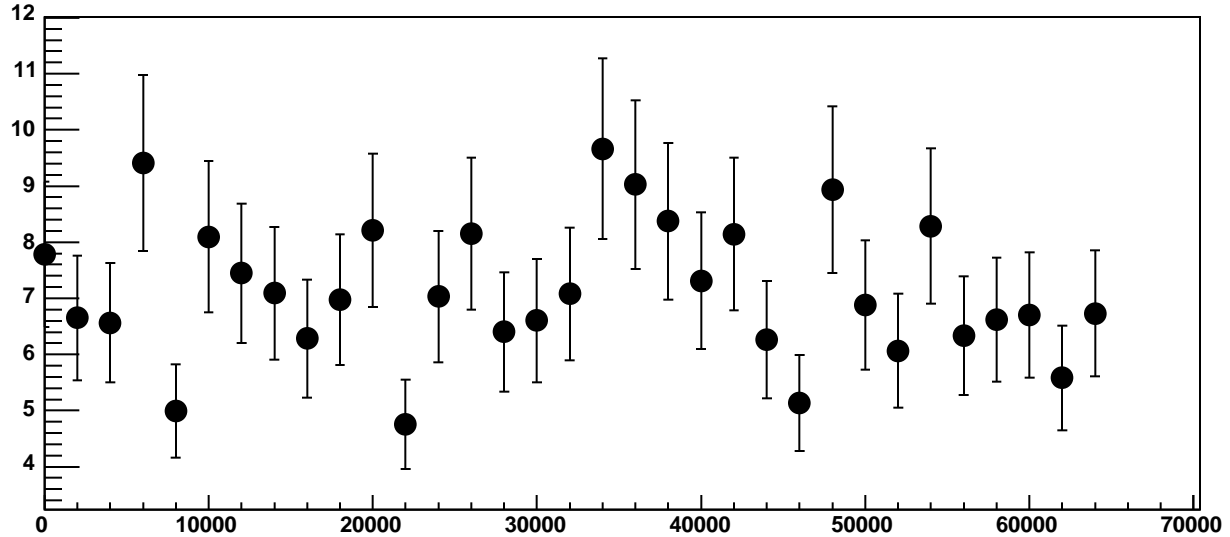
Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



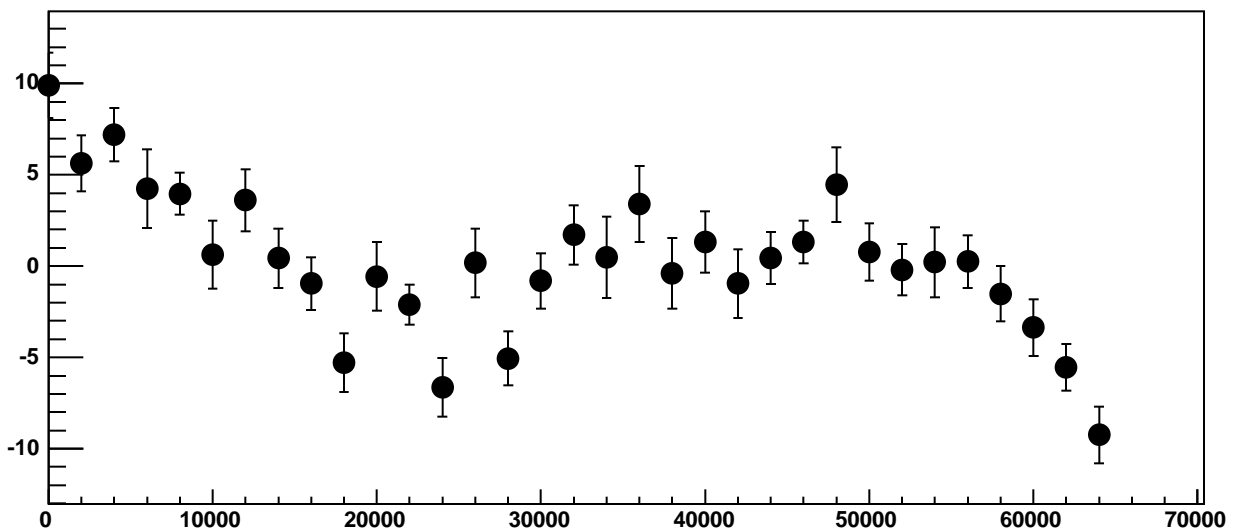
Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



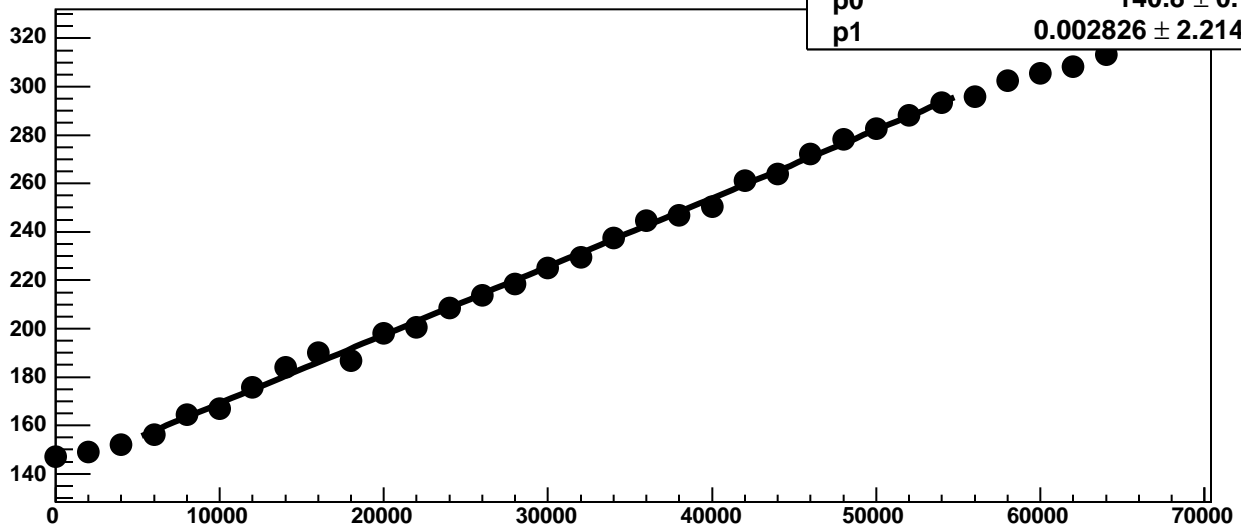
Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

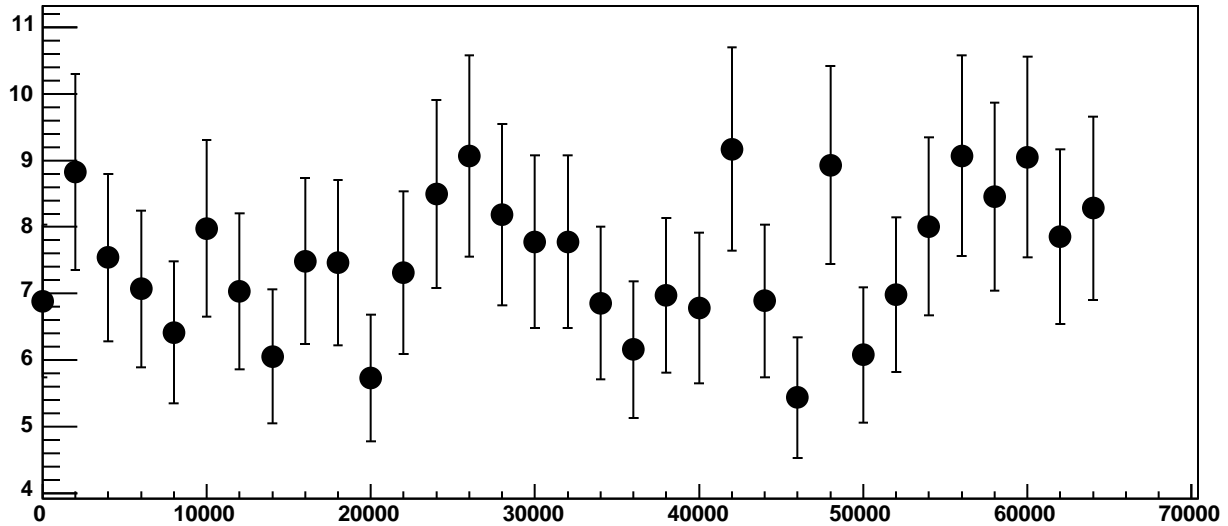


Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

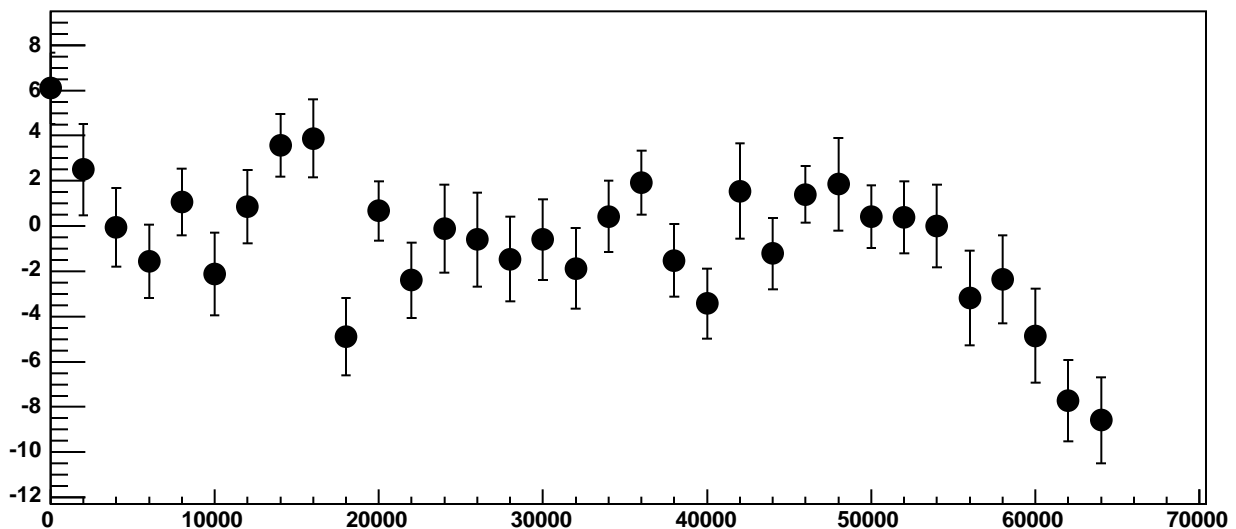


χ^2 / ndf 38.17 / 23
p0 140.8 ± 0.7403
p1 $0.002826 \pm 2.214e-05$

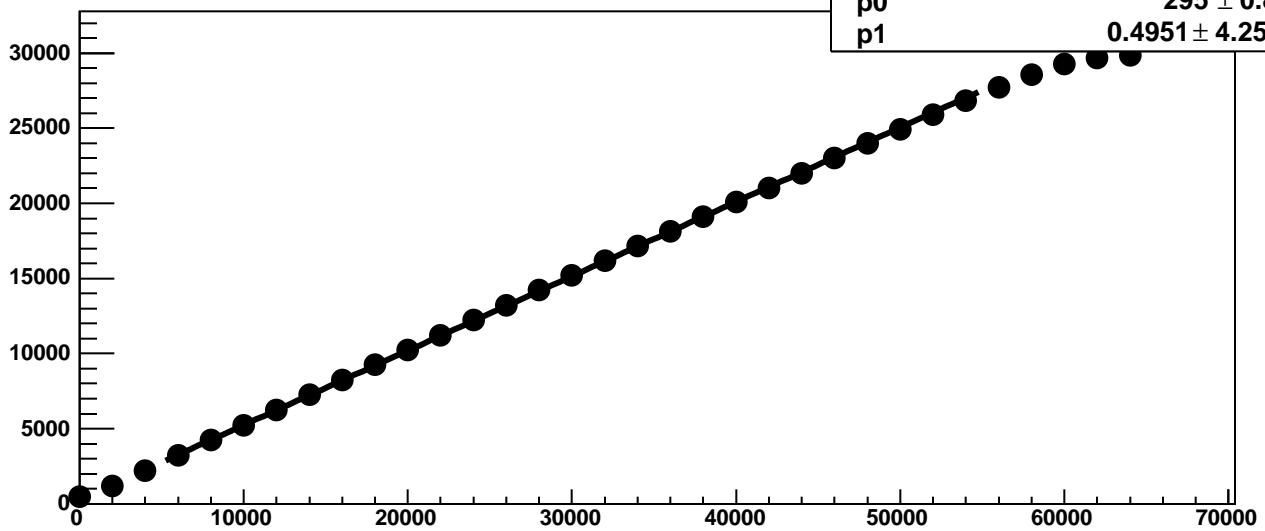
Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



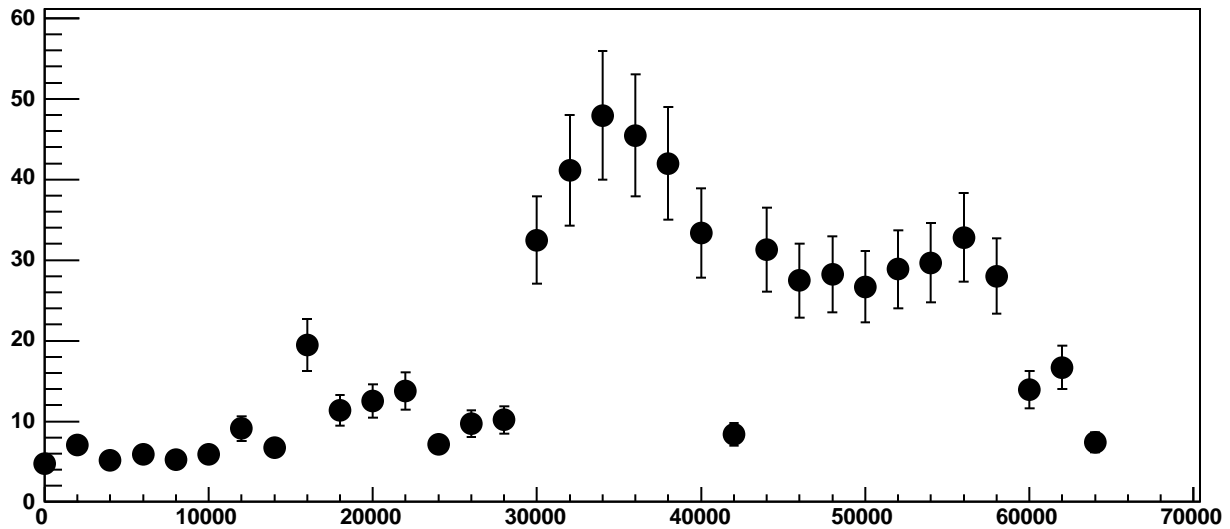
Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



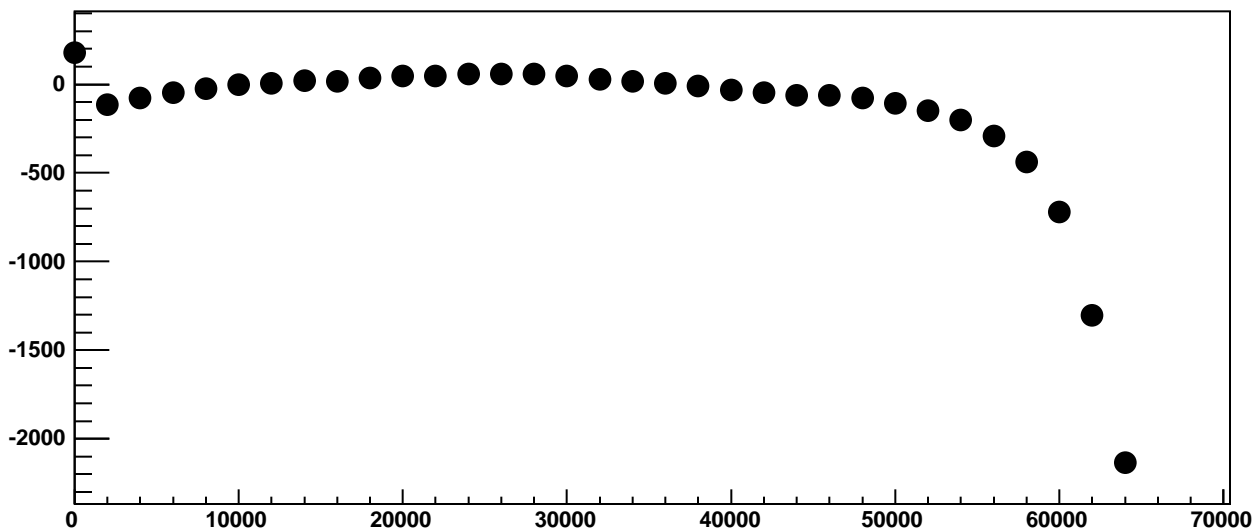
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



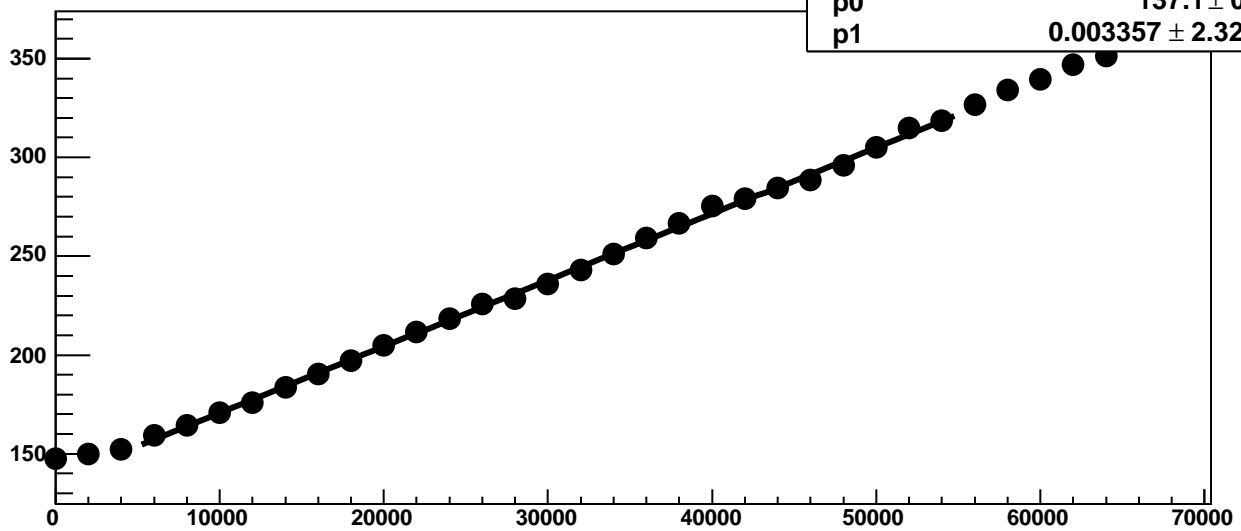
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.39 / 23

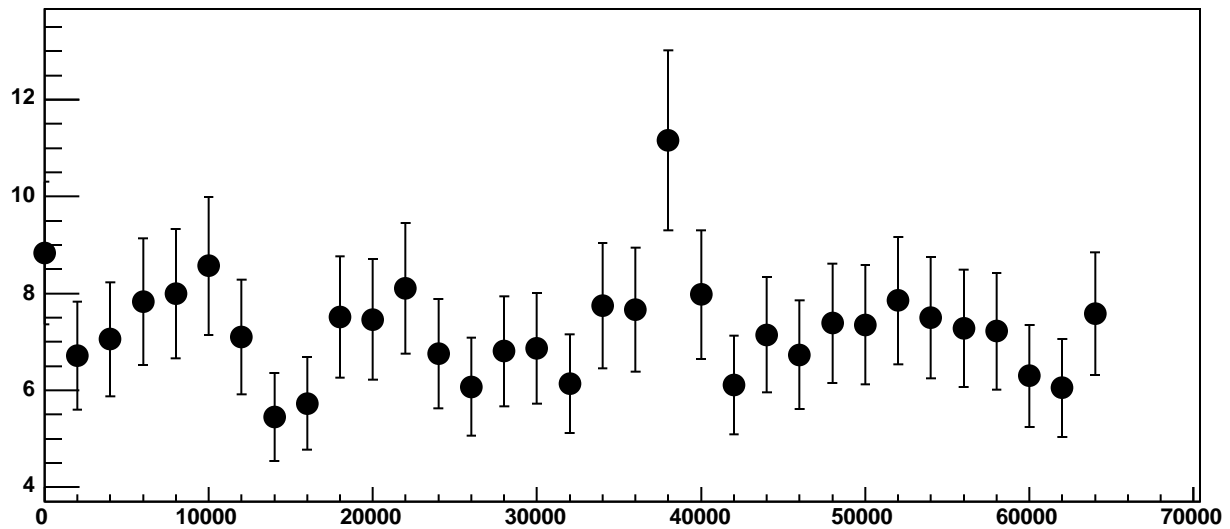
p0

137.1 ± 0.758

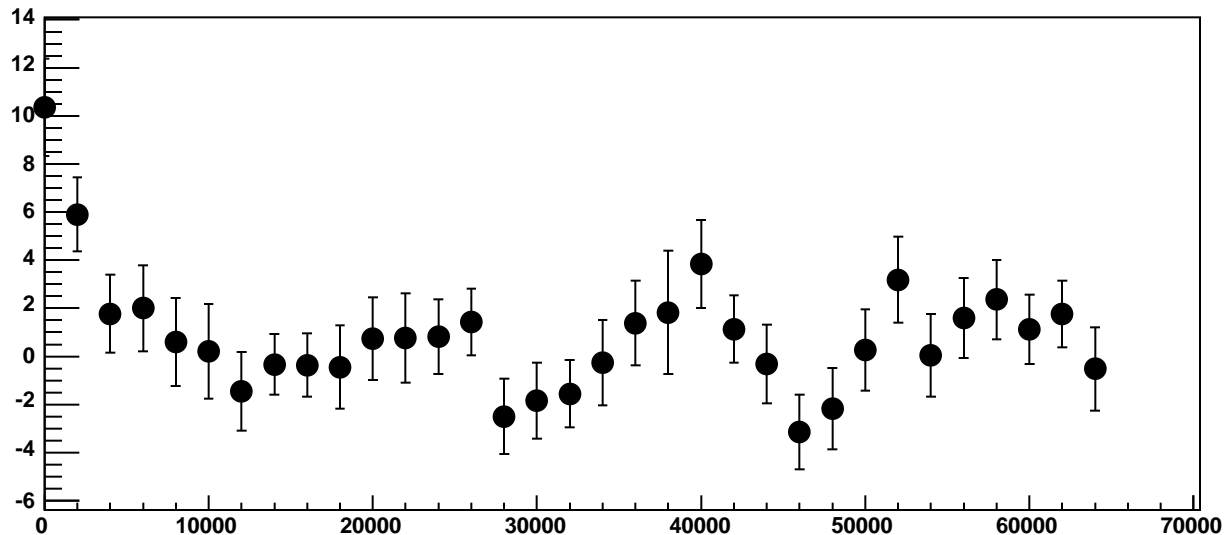
p1

$0.003357 \pm 2.32e-05$

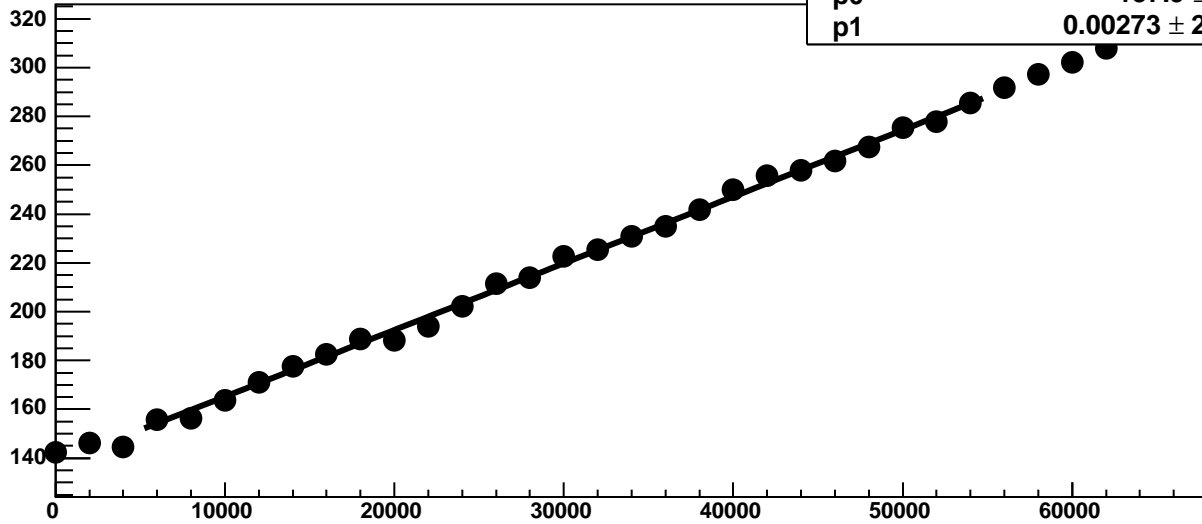
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.34 / 23

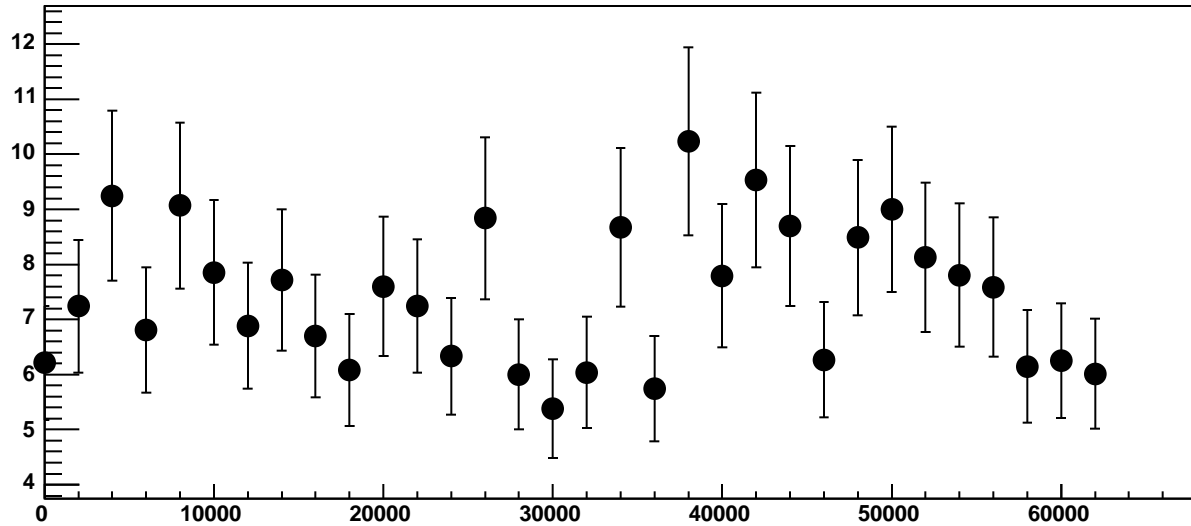
p0

137.9 ± 0.7807

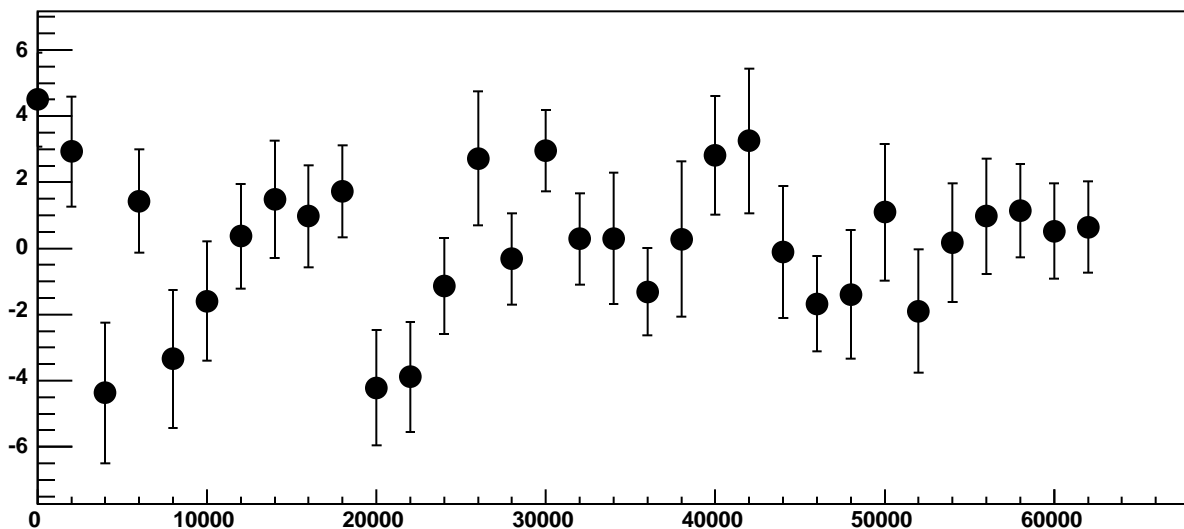
p1

$0.00273 \pm 2.44e-05$

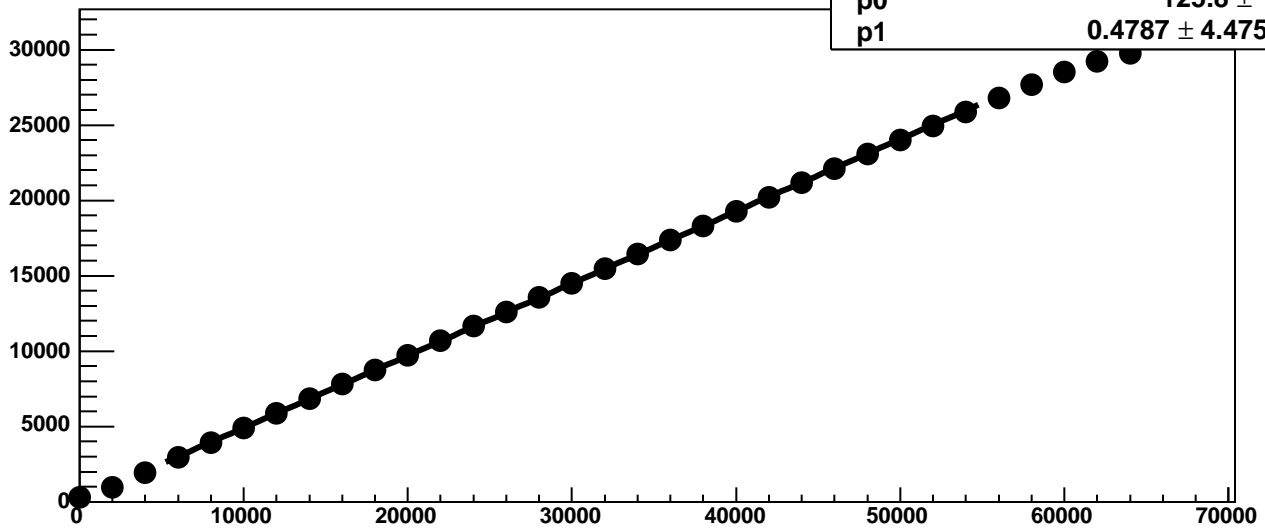
Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



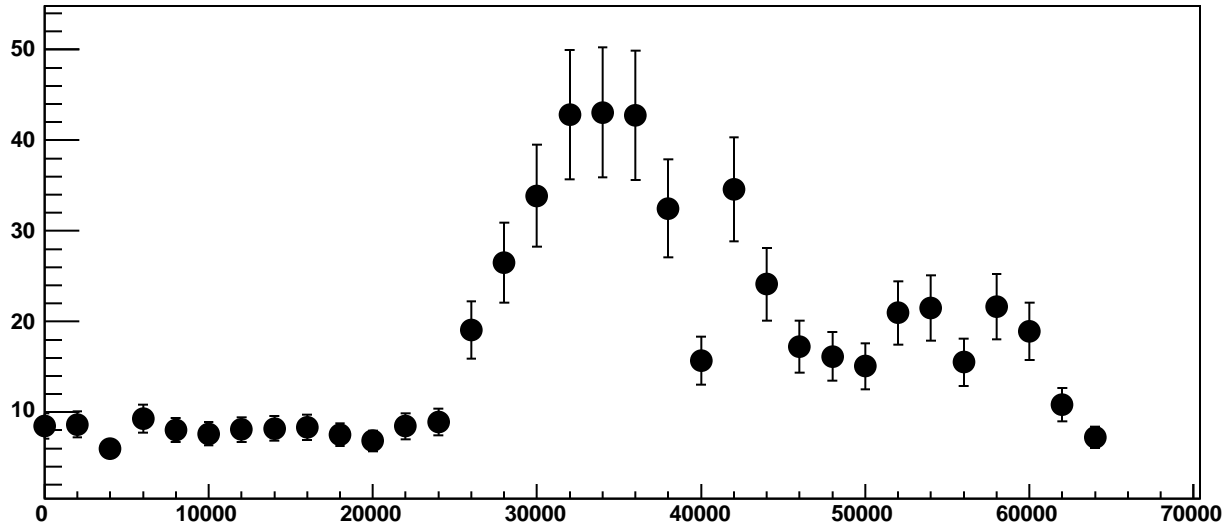
Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



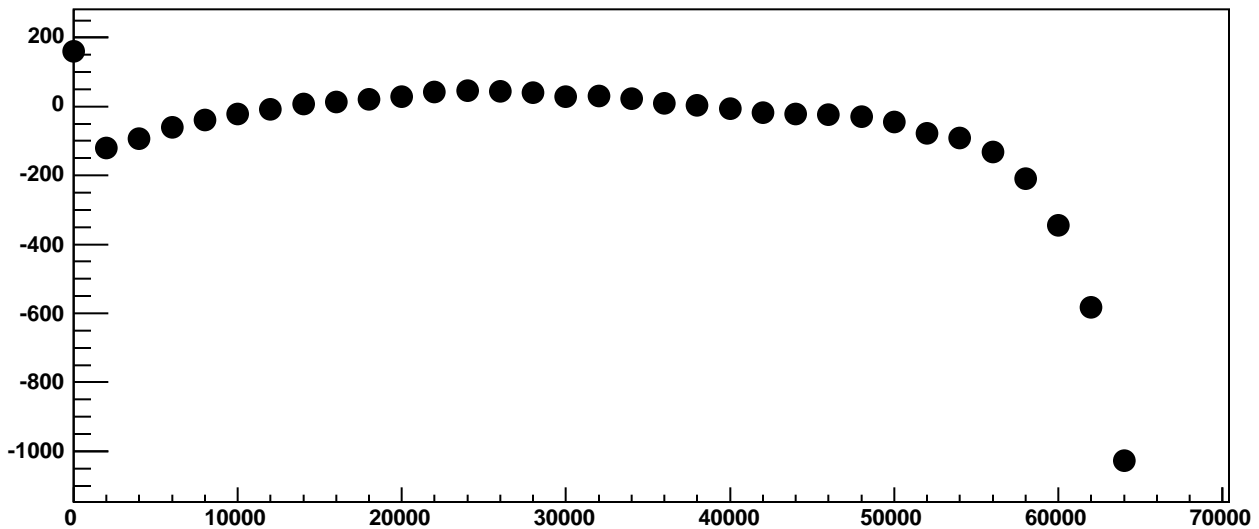
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



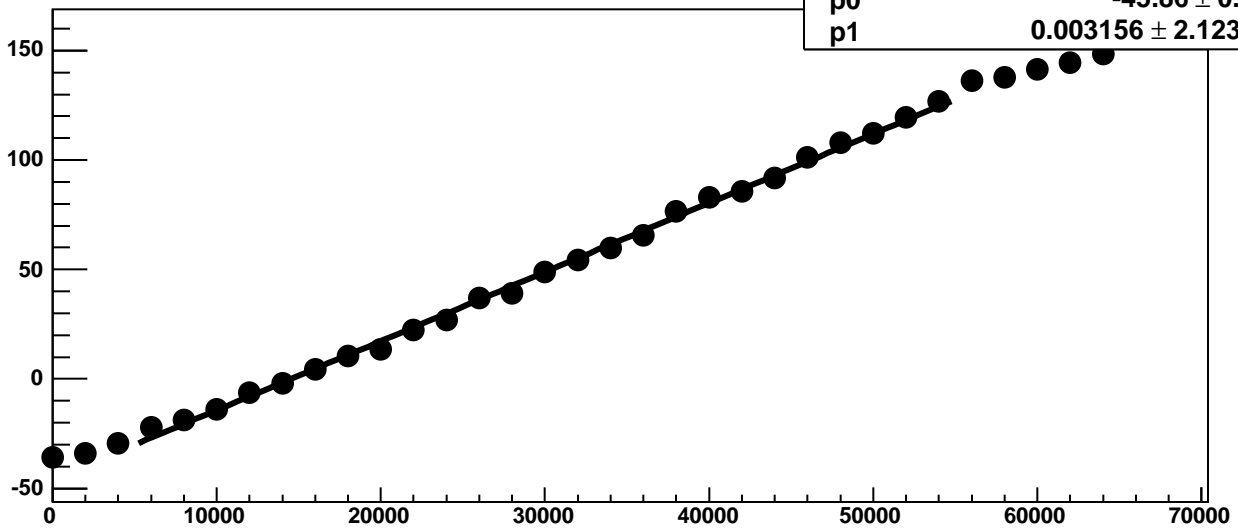
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



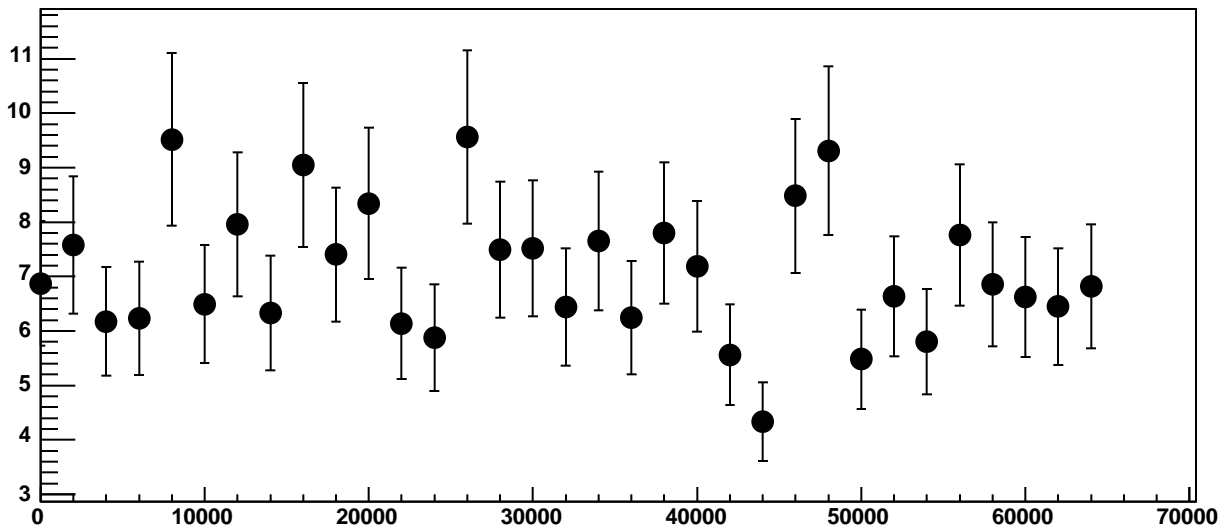
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



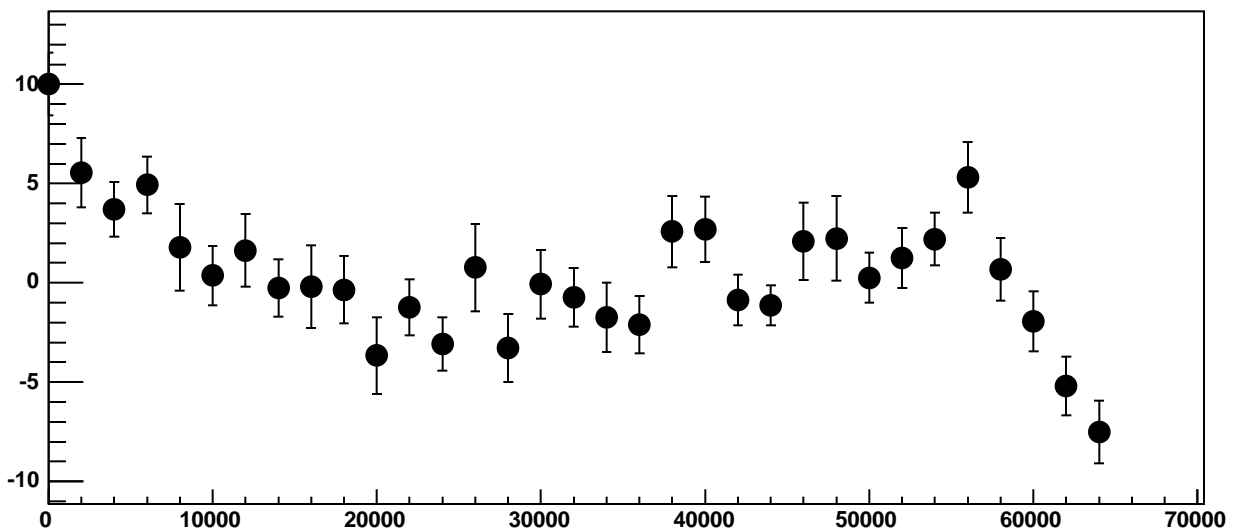
Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



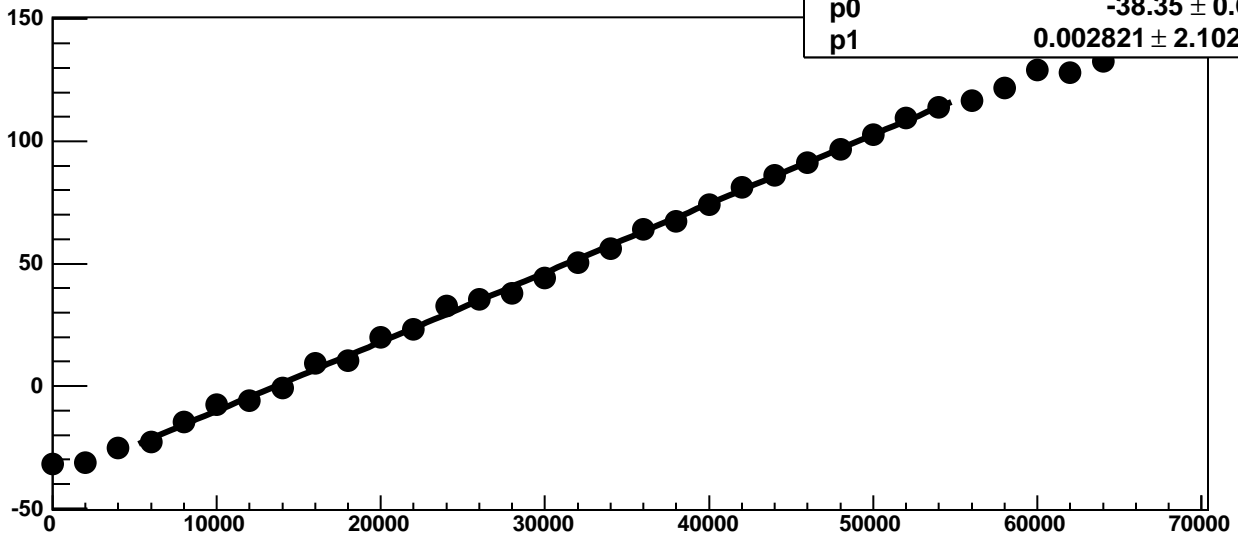
Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



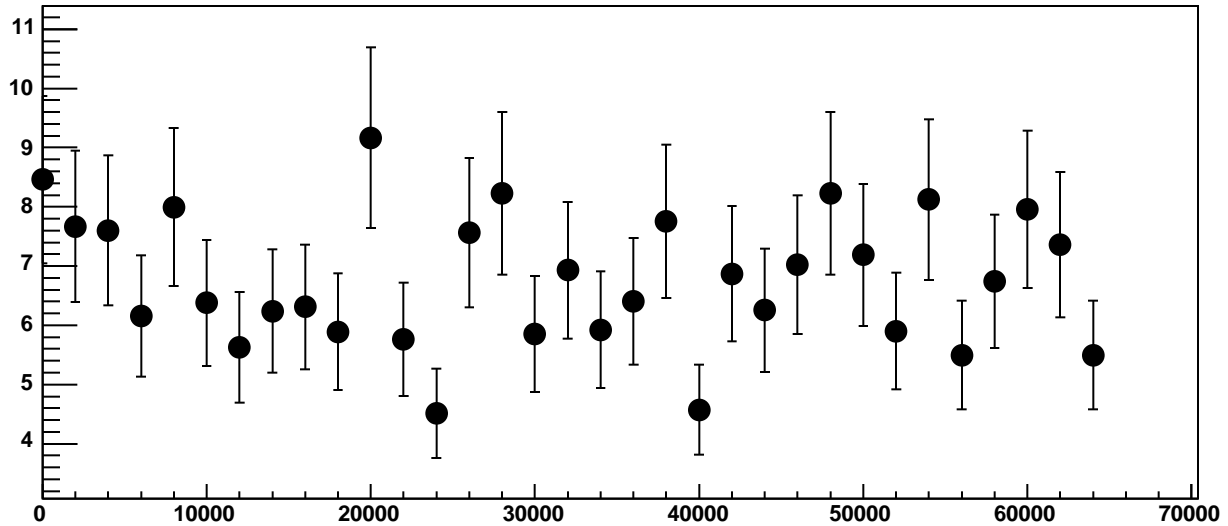
Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



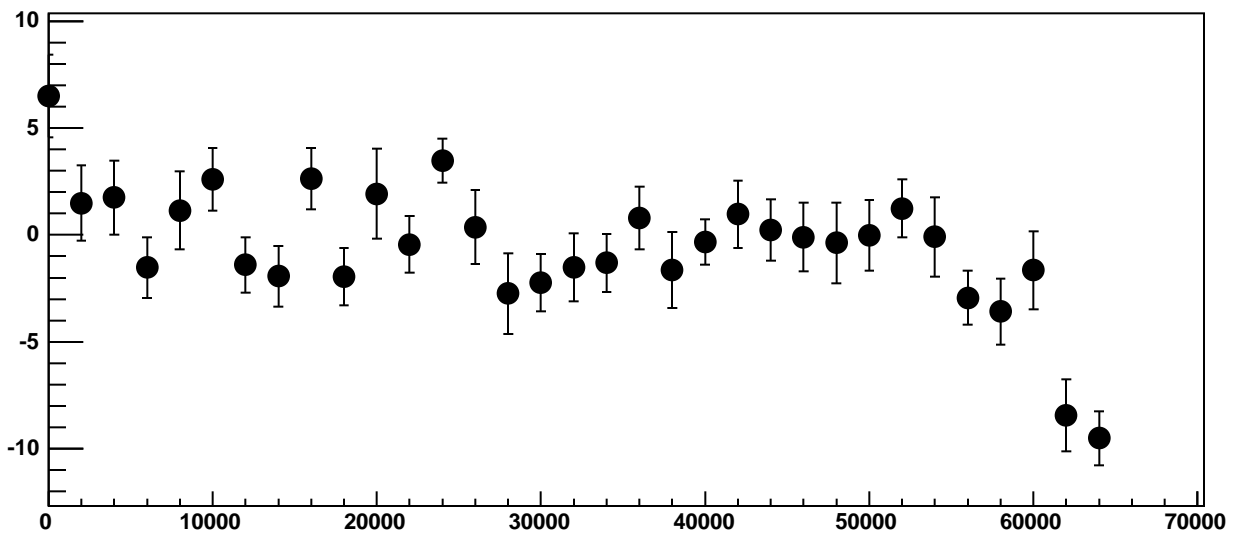
Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



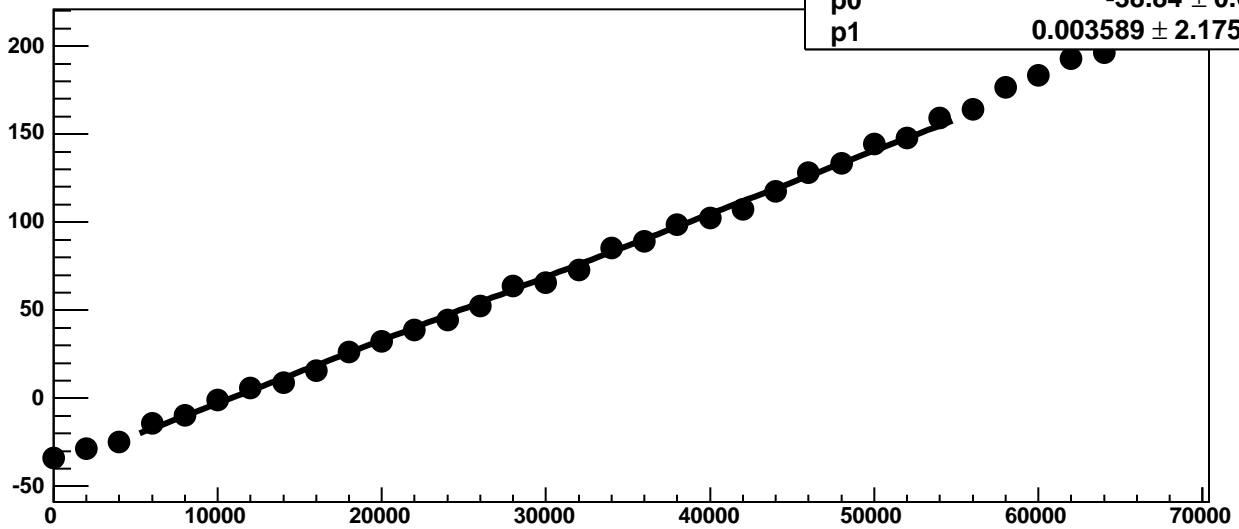
Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

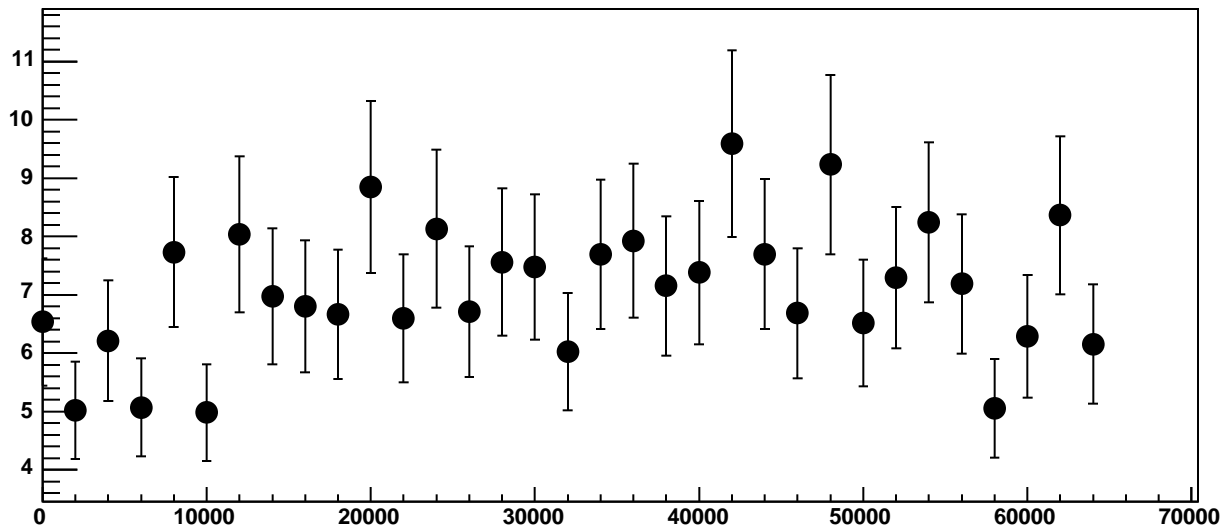


Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

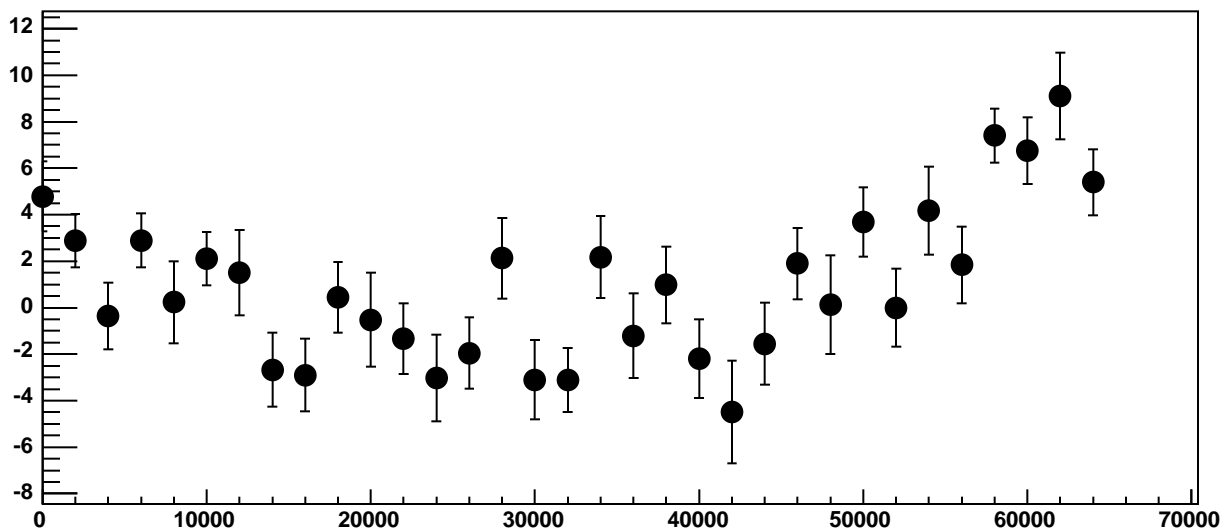


χ^2 / ndf 53.01 / 23
p0 -38.84 ± 0.6868
p1 $0.003589 \pm 2.175e-05$

Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC

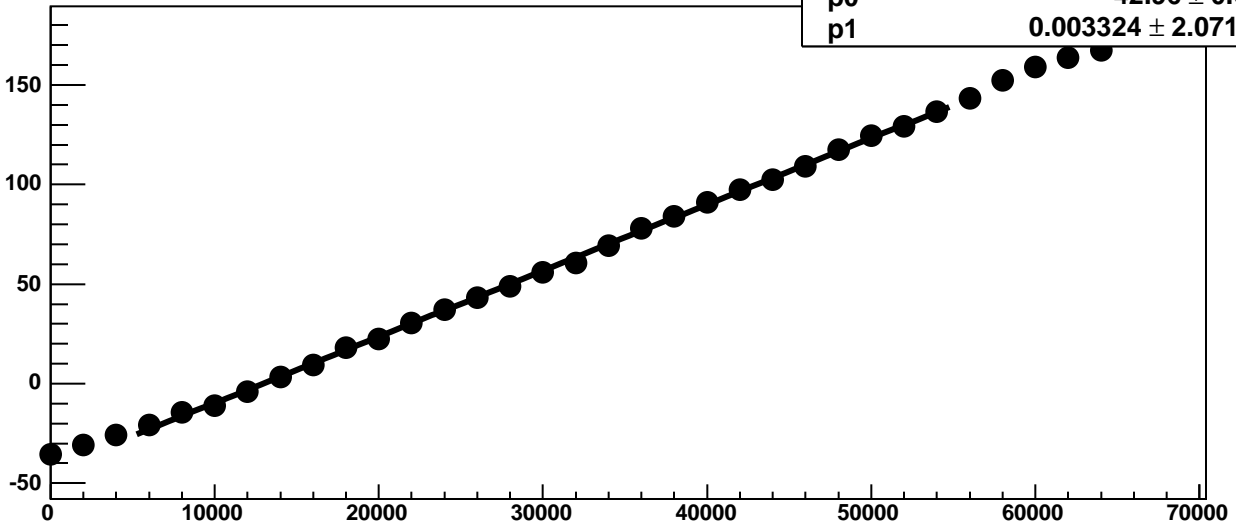


Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

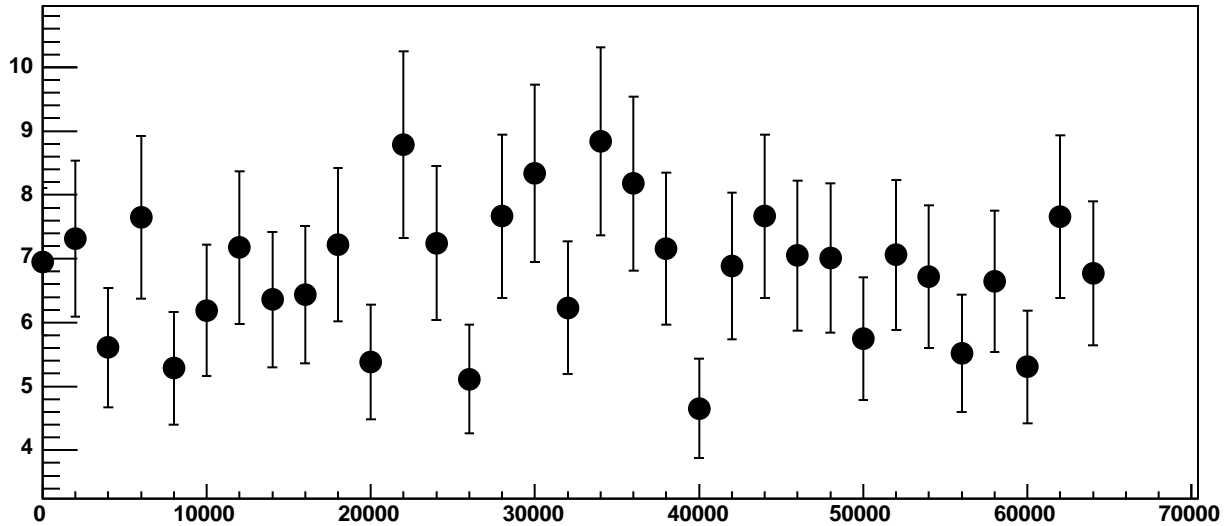


Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

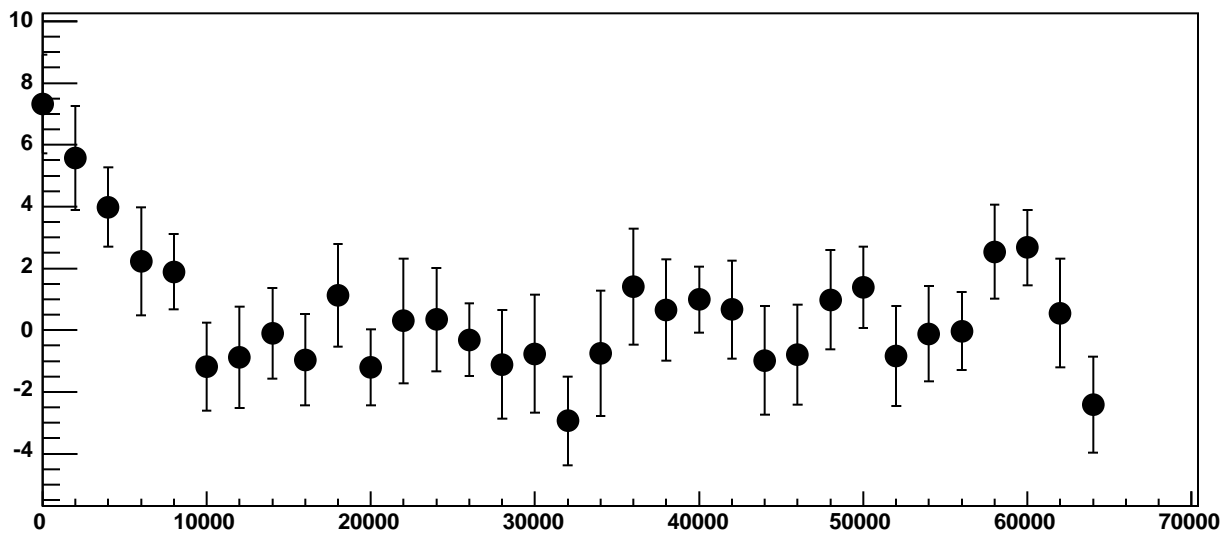
χ^2 / ndf 15.98 / 23
p0 -42.96 ± 0.6844
p1 $0.003324 \pm 2.071\text{e-}05$



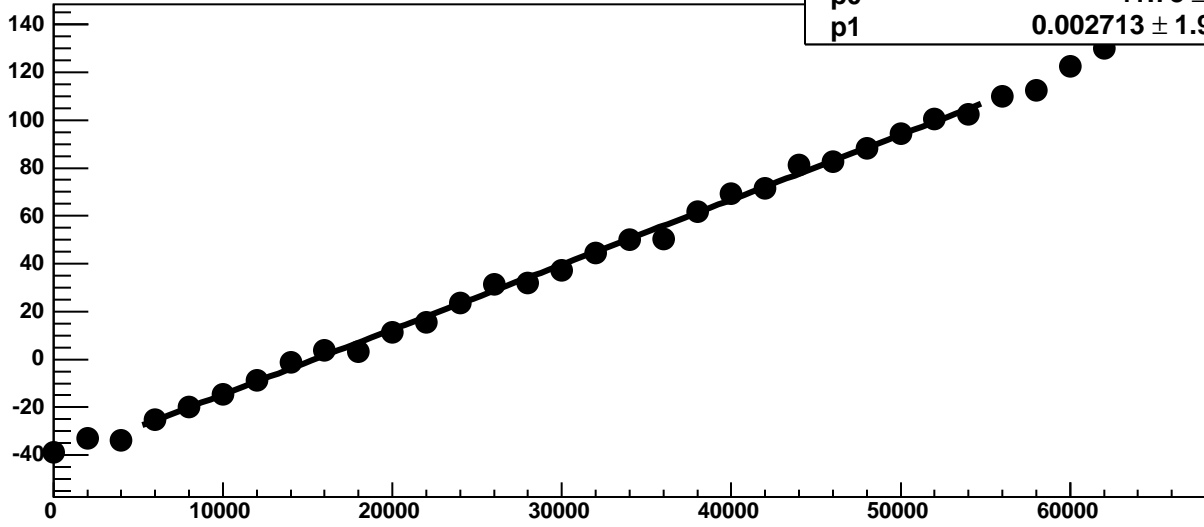
Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

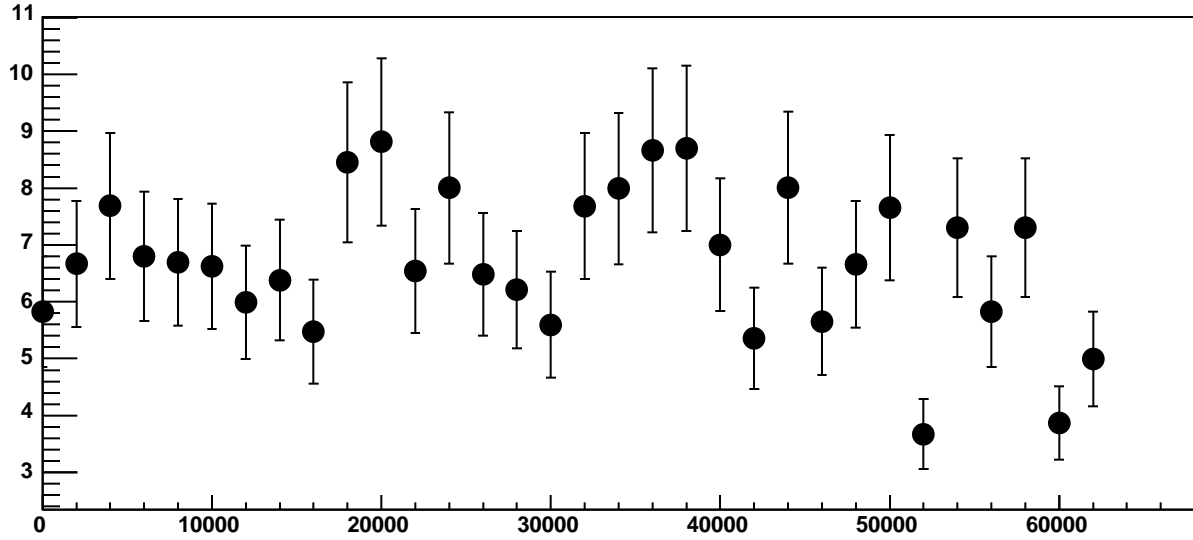


Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

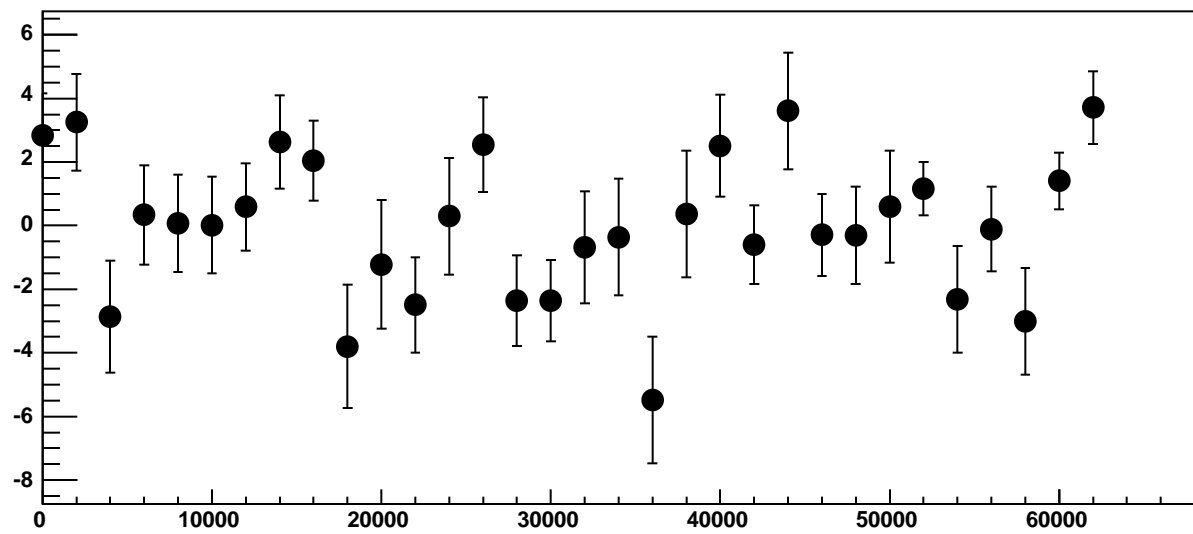


χ^2 / ndf 40.52 / 23
p0 -41.78 ± 0.6812
p1 $0.002713 \pm 1.944e-05$

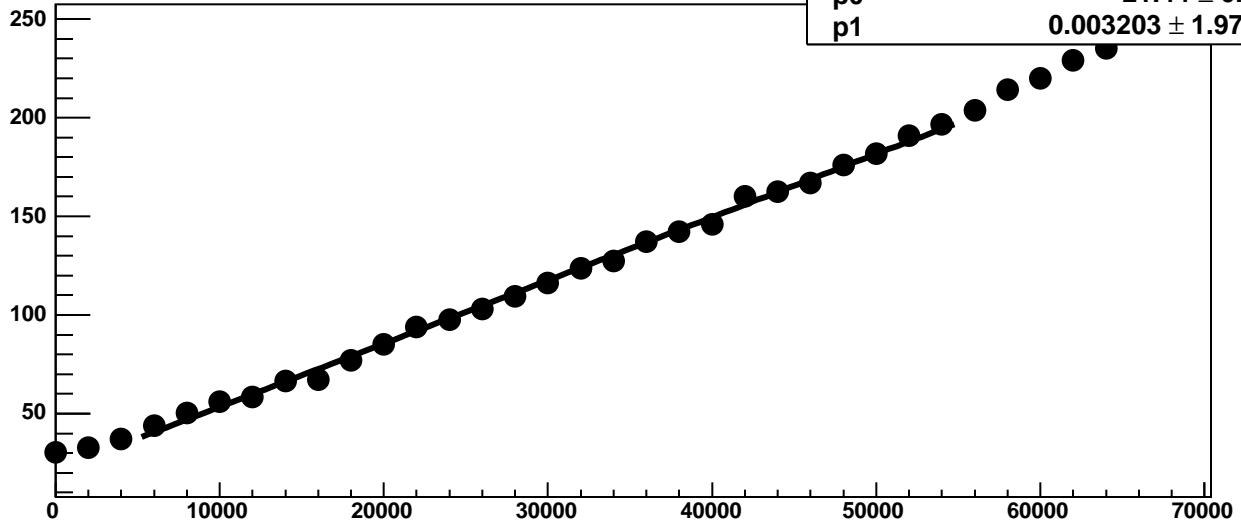
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

73.1 / 23

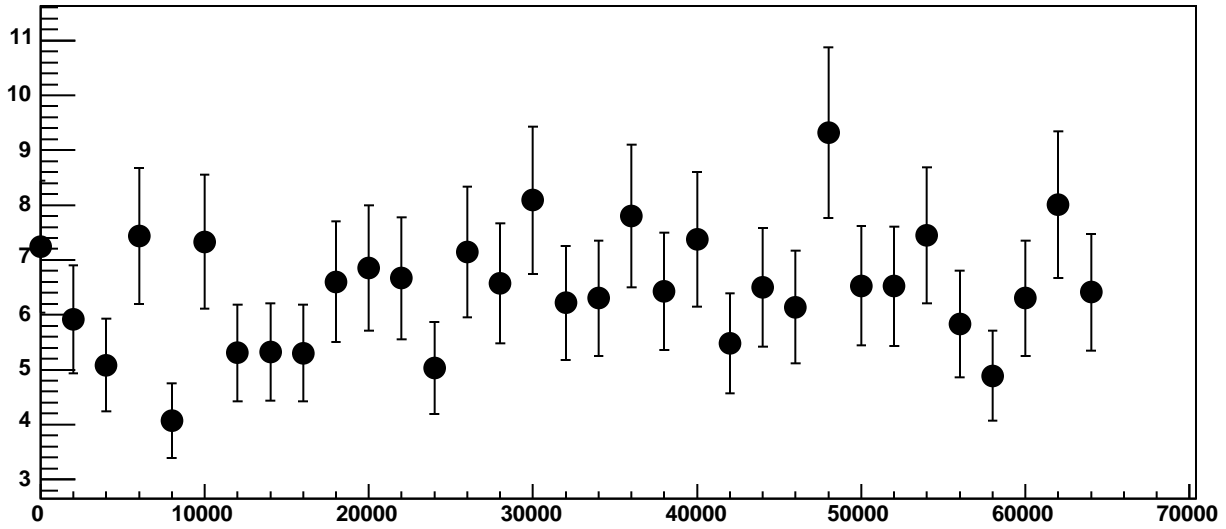
p0

21.44 ± 0.6171

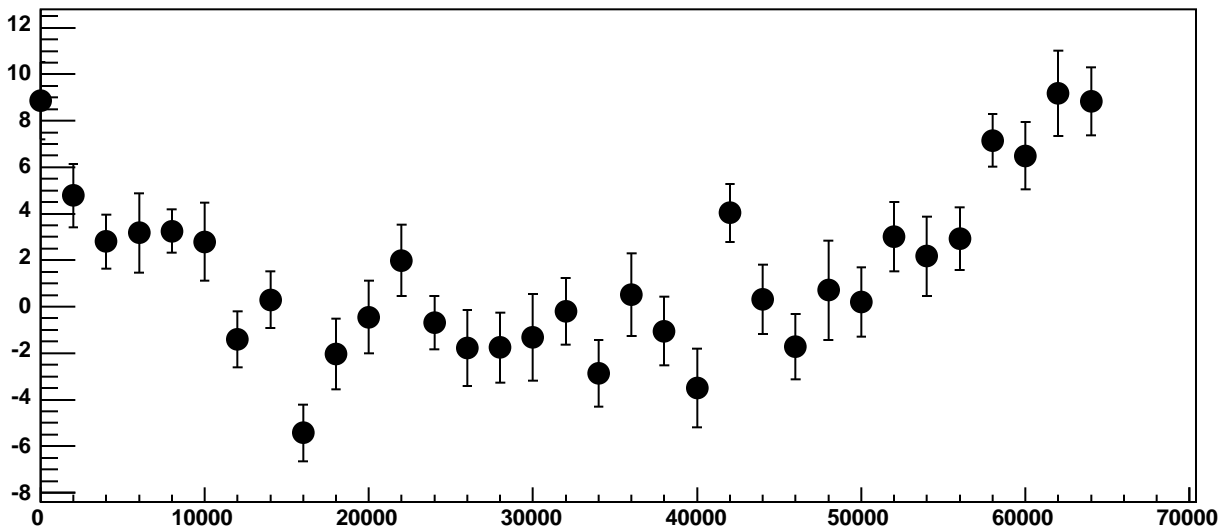
p1

$0.003203 \pm 1.97e-05$

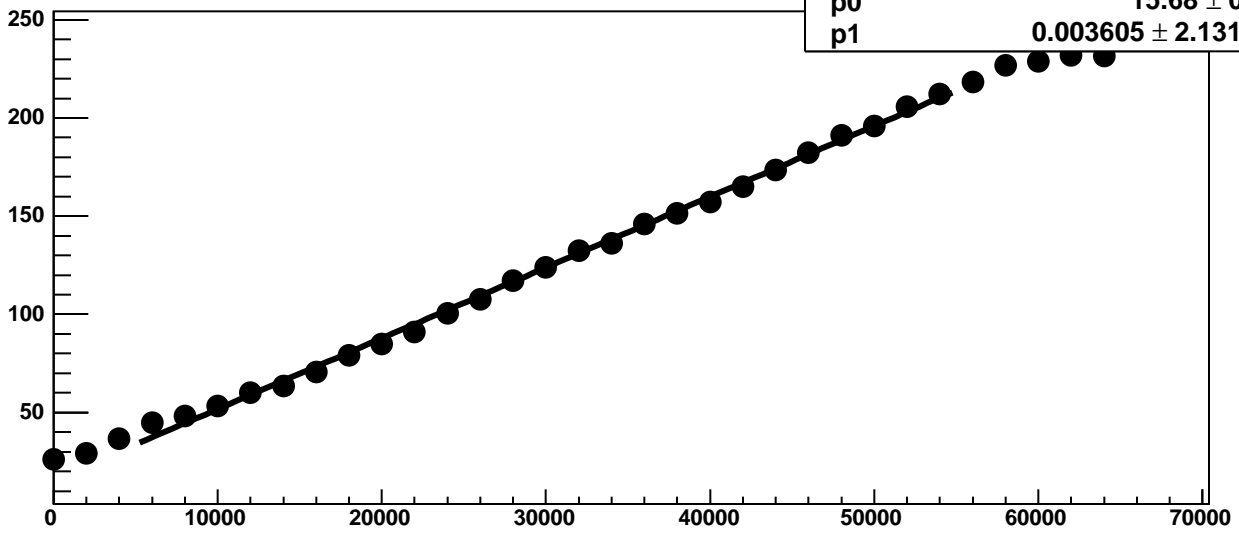
Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

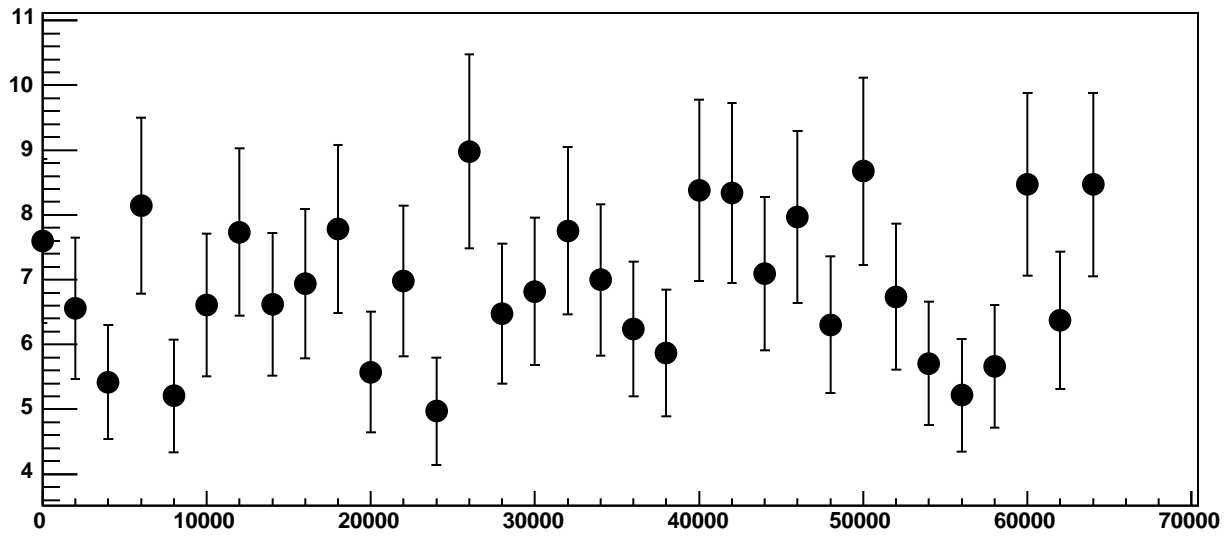


Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

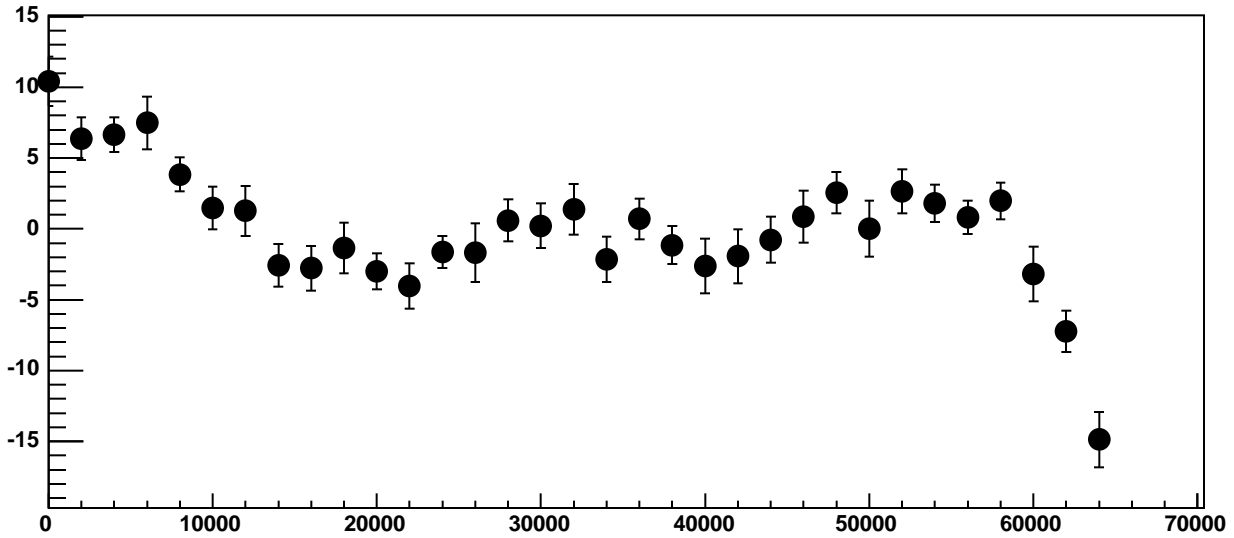


χ^2 / ndf 63.67 / 23
p0 15.68 ± 0.699
p1 $0.003605 \pm 2.131\text{e-}05$

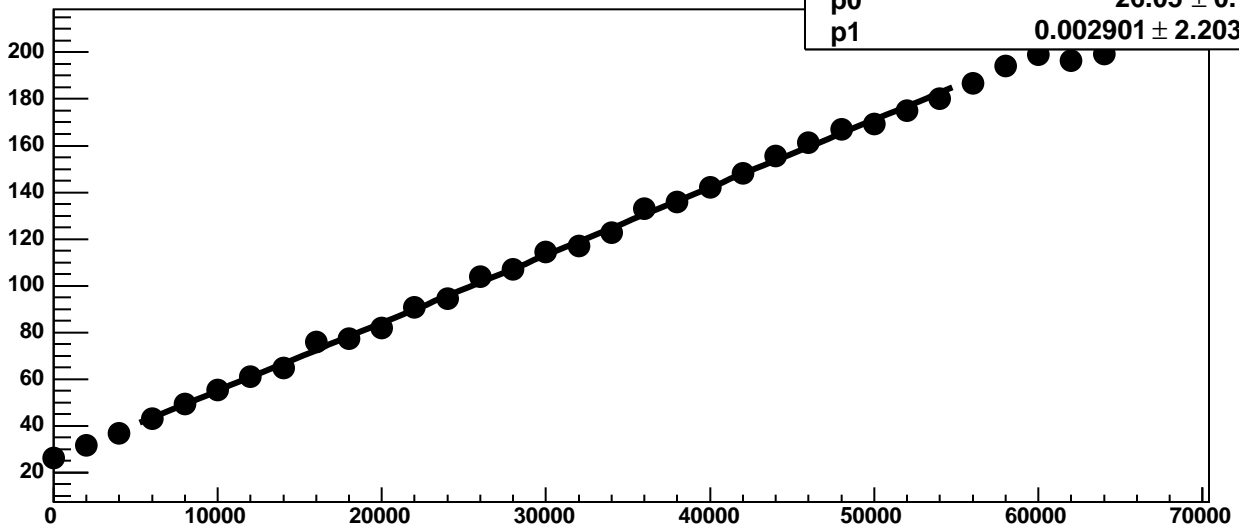
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

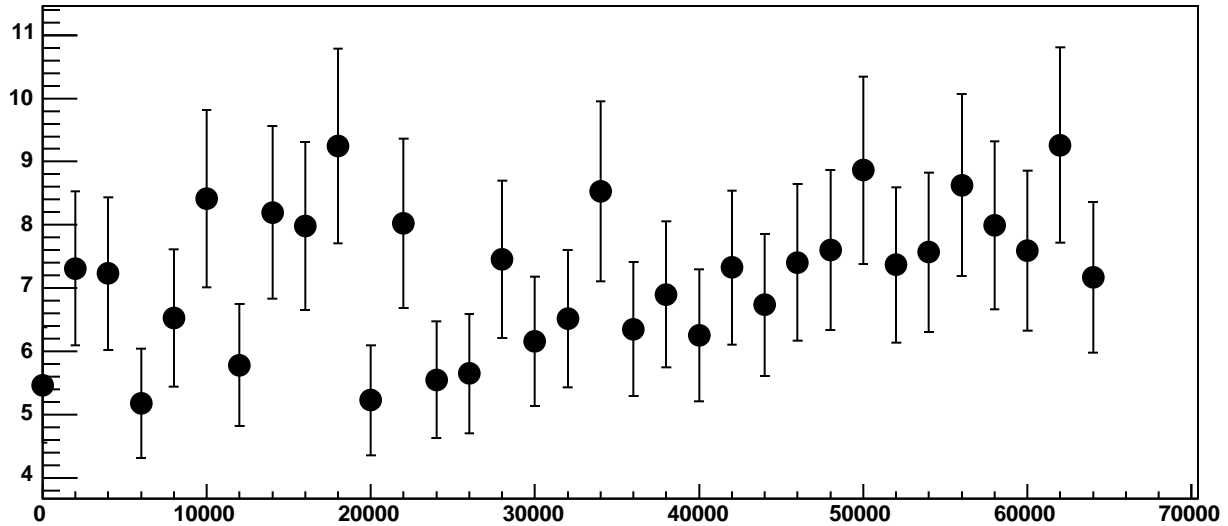


Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

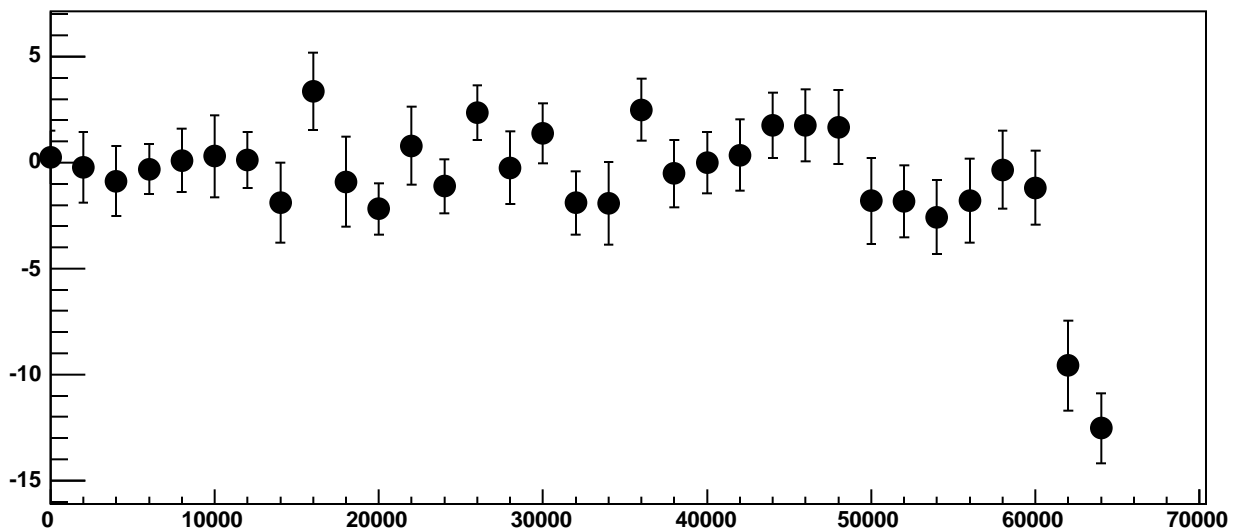


χ^2 / ndf 26.28 / 23
p0 26.05 ± 0.7019
p1 $0.002901 \pm 2.203e-05$

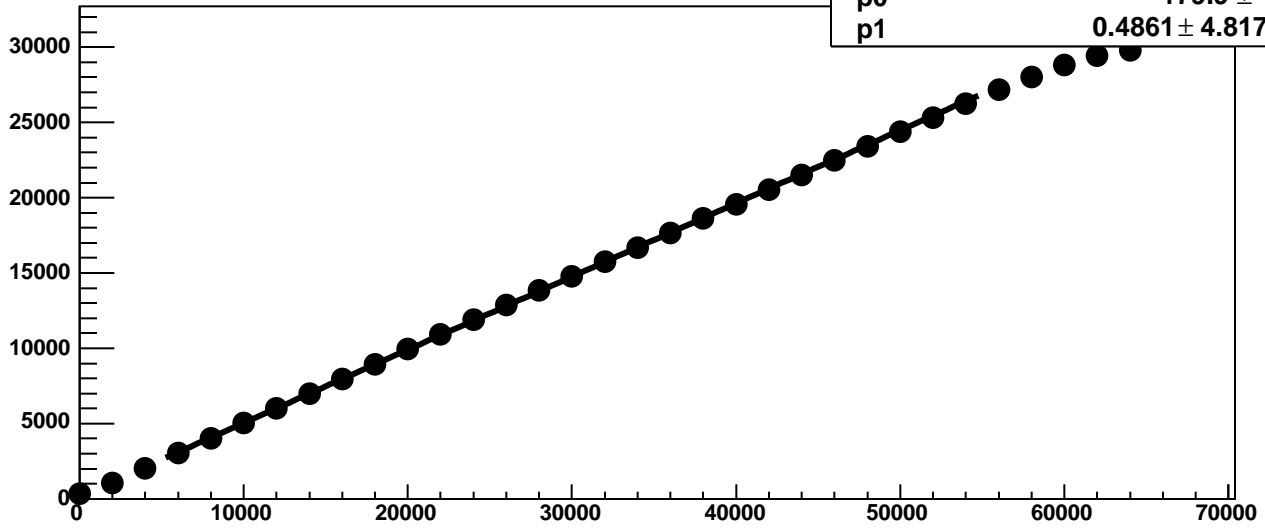
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



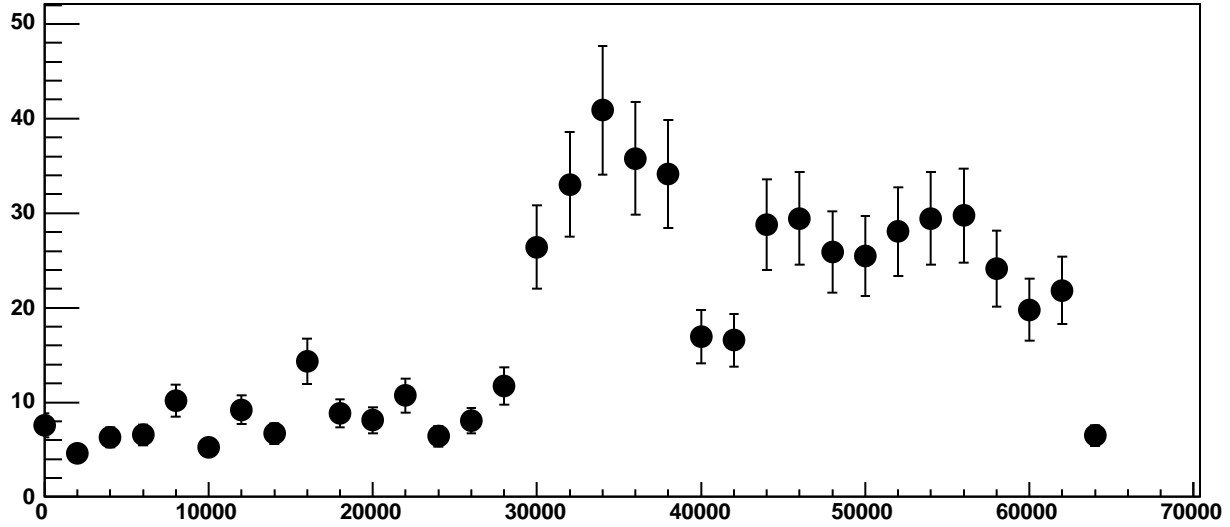
Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



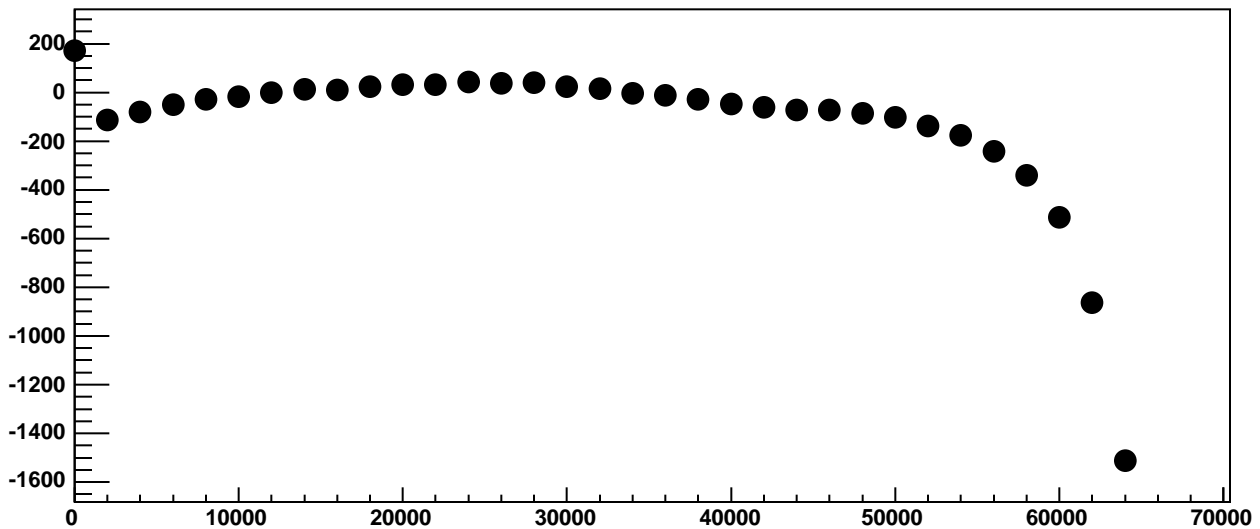
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



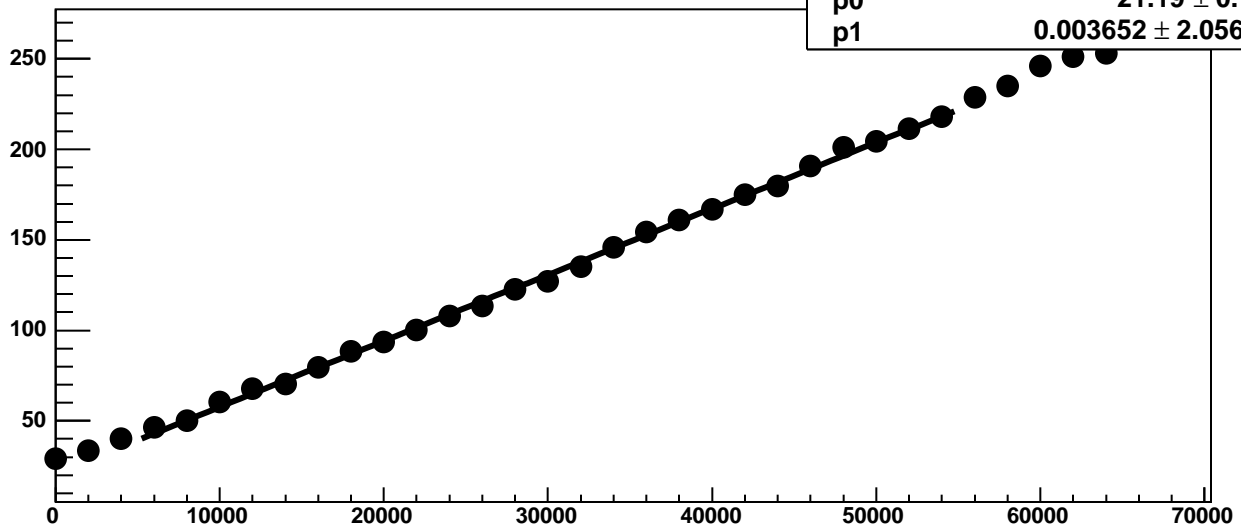
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

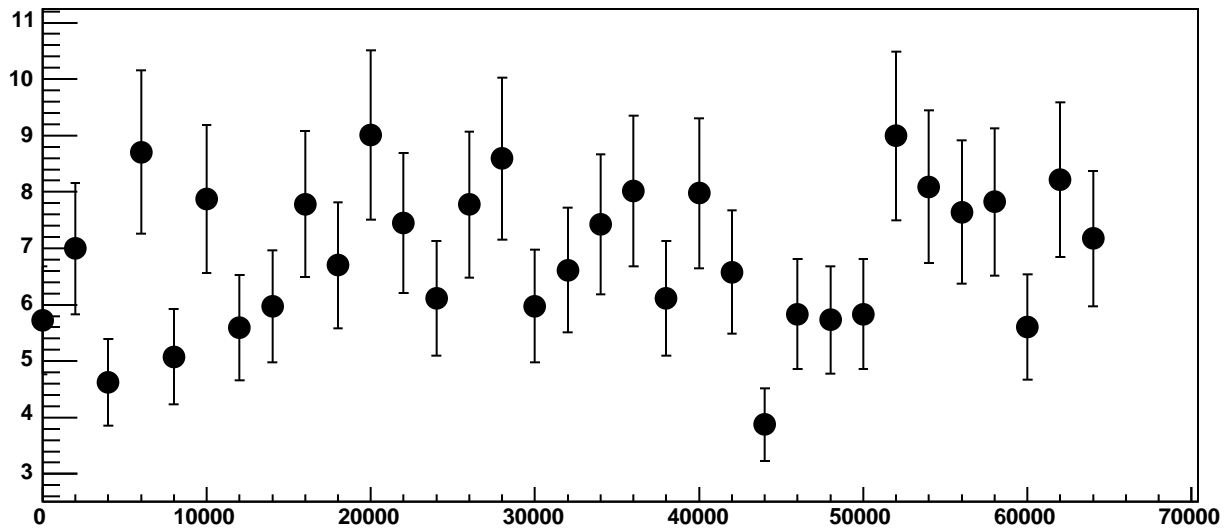


Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

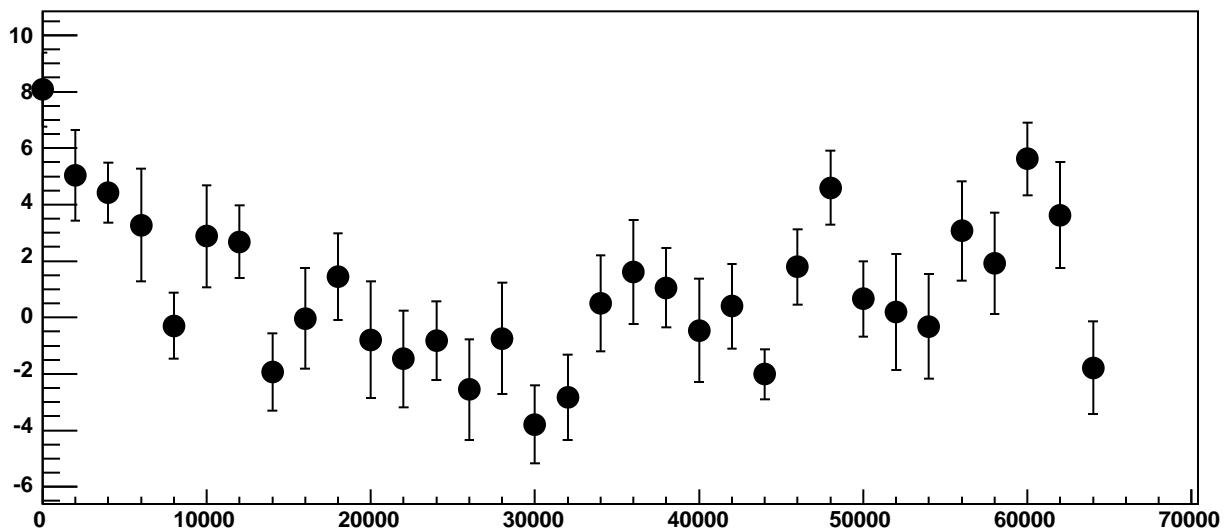


χ^2 / ndf 47.99 / 23
p0 21.19 ± 0.7013
p1 $0.003652 \pm 2.056e-05$

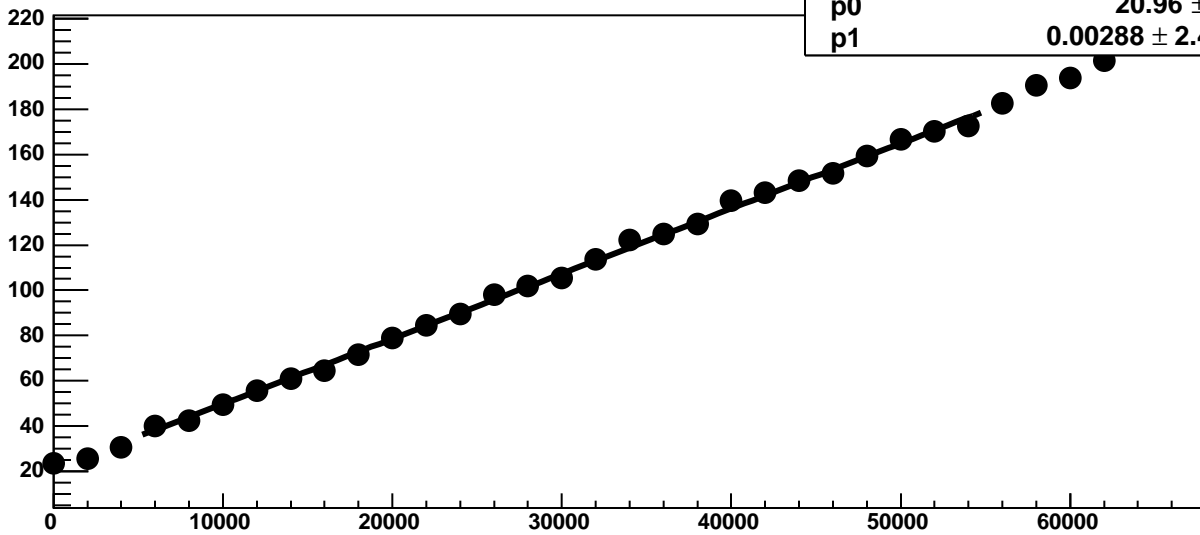
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

25.71 / 23

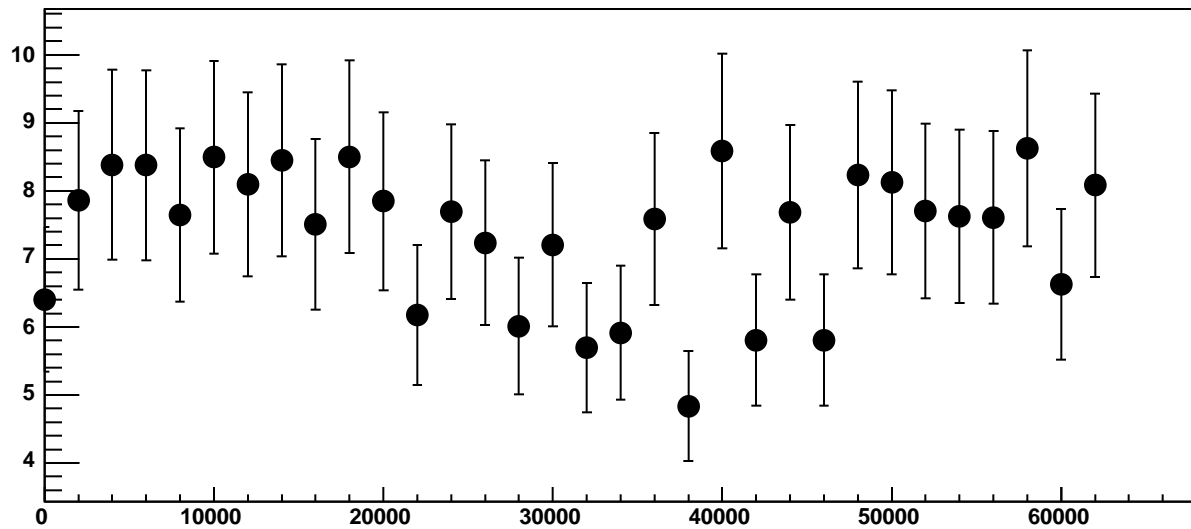
p0

20.96 ± 0.8277

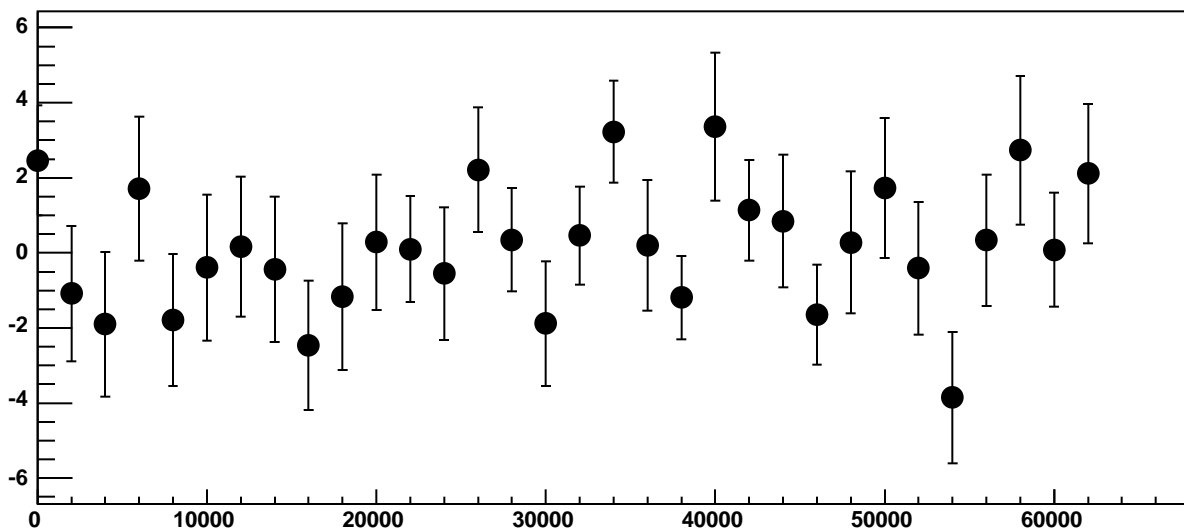
p1

$0.00288 \pm 2.434e-05$

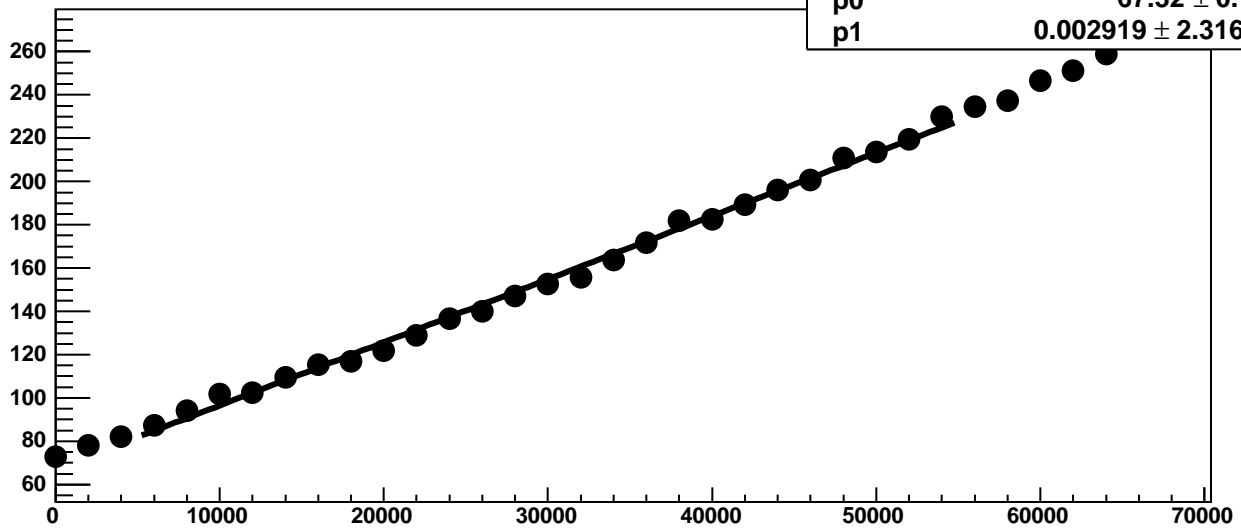
Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

77.68 / 23

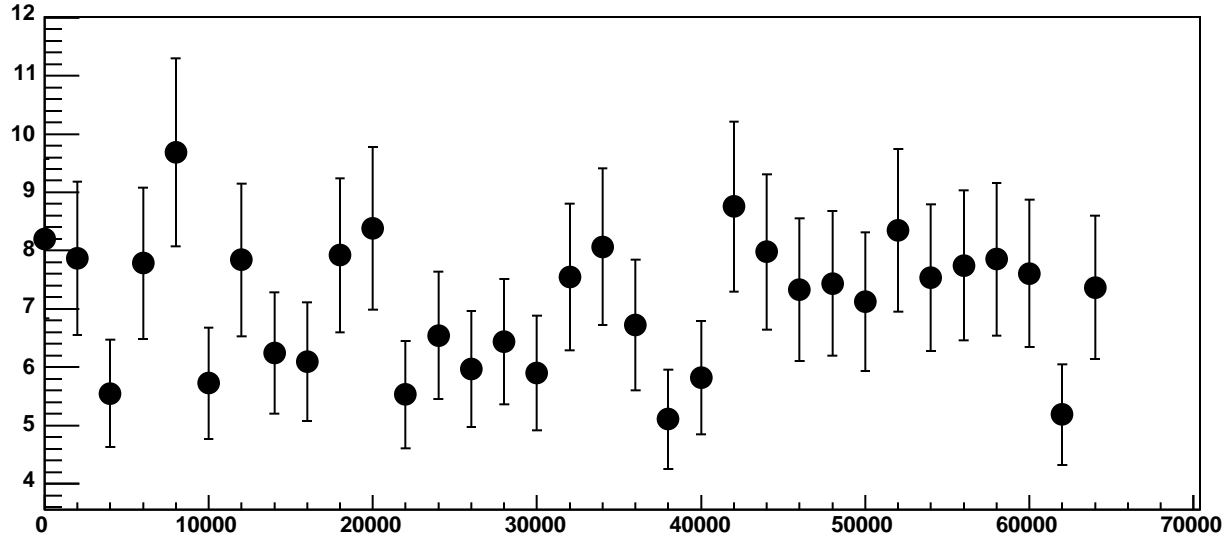
p0

67.32 ± 0.7524

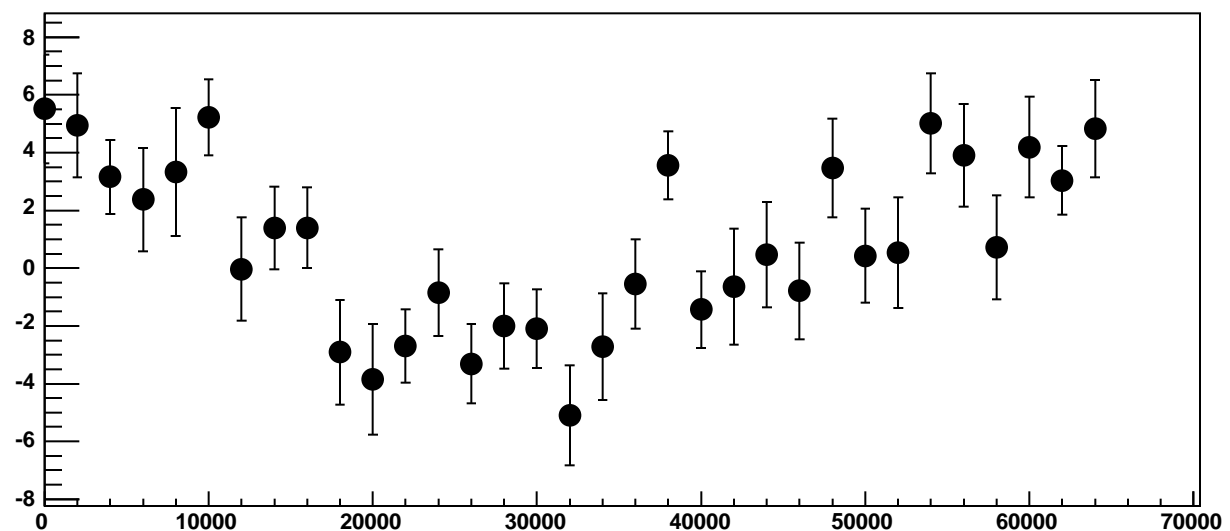
p1

$0.002919 \pm 2.316e-05$

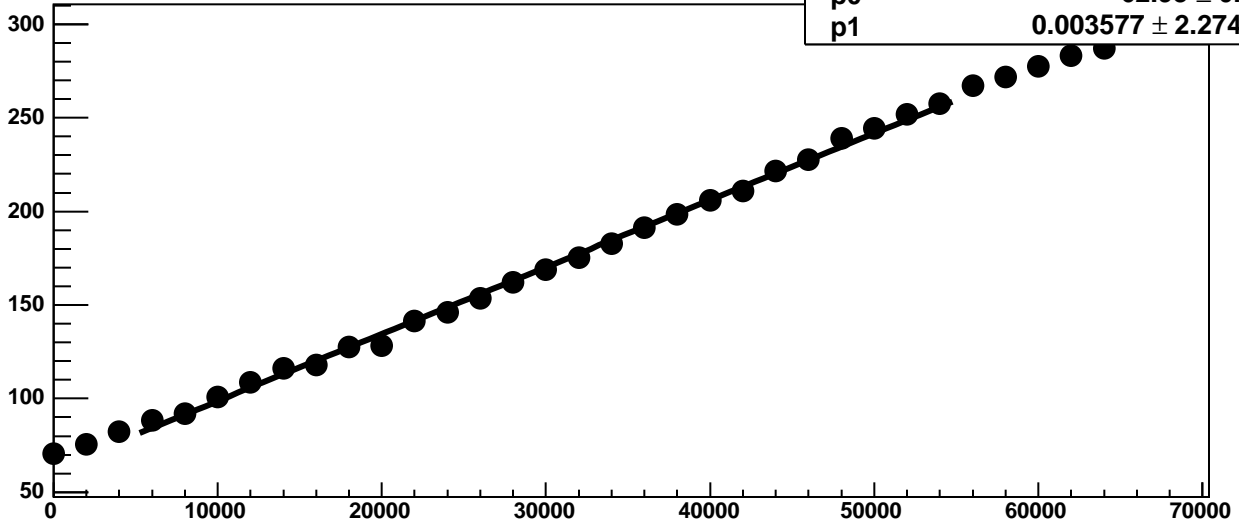
Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

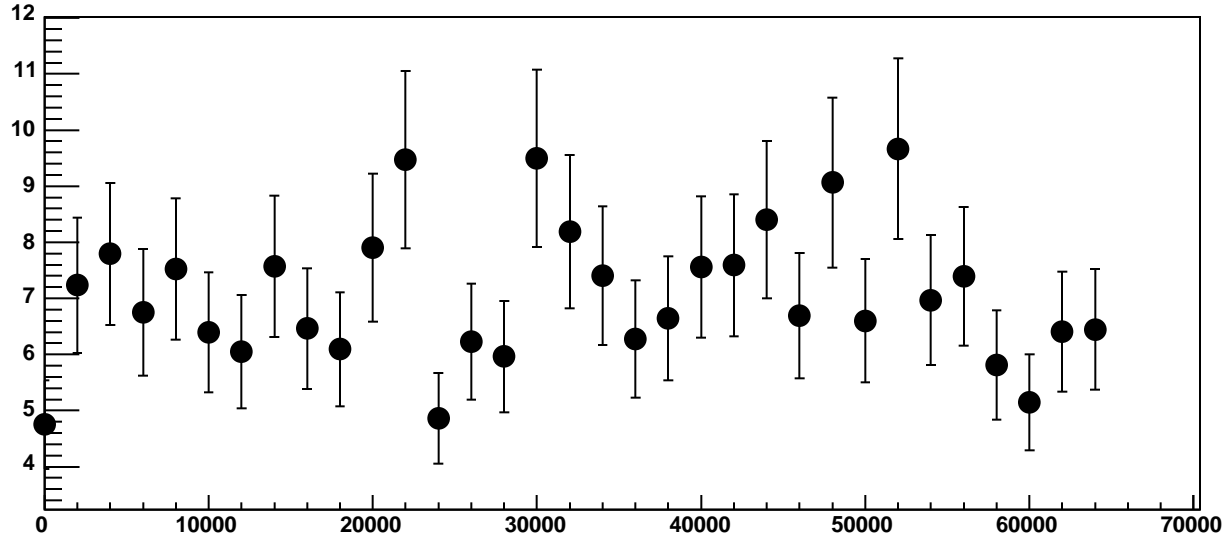


Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

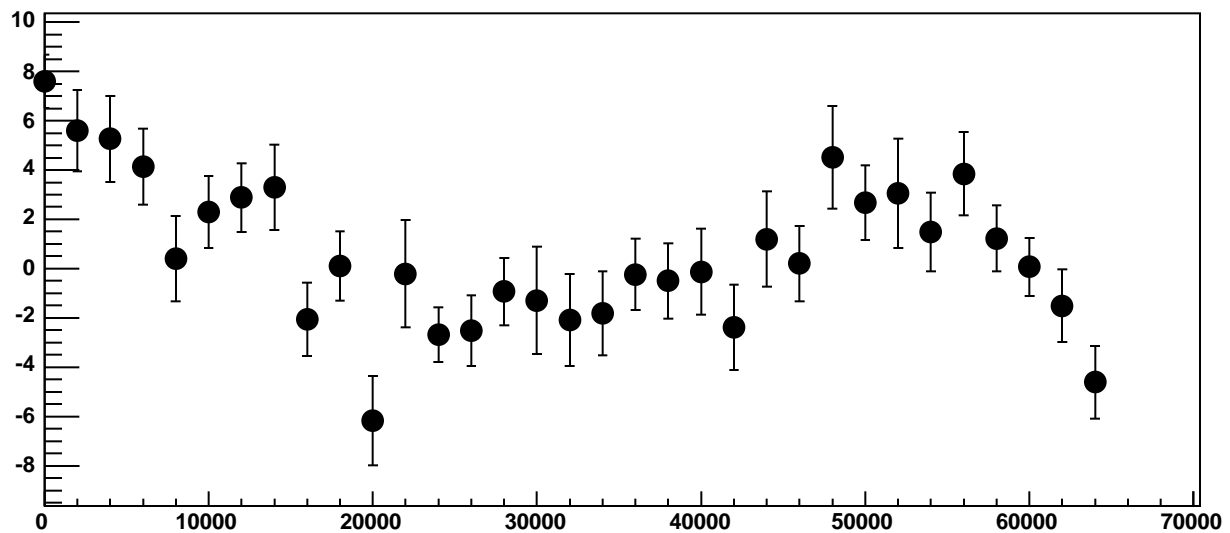


χ^2 / ndf 56.12 / 23
p0 62.93 ± 0.7251
p1 $0.003577 \pm 2.274e-05$

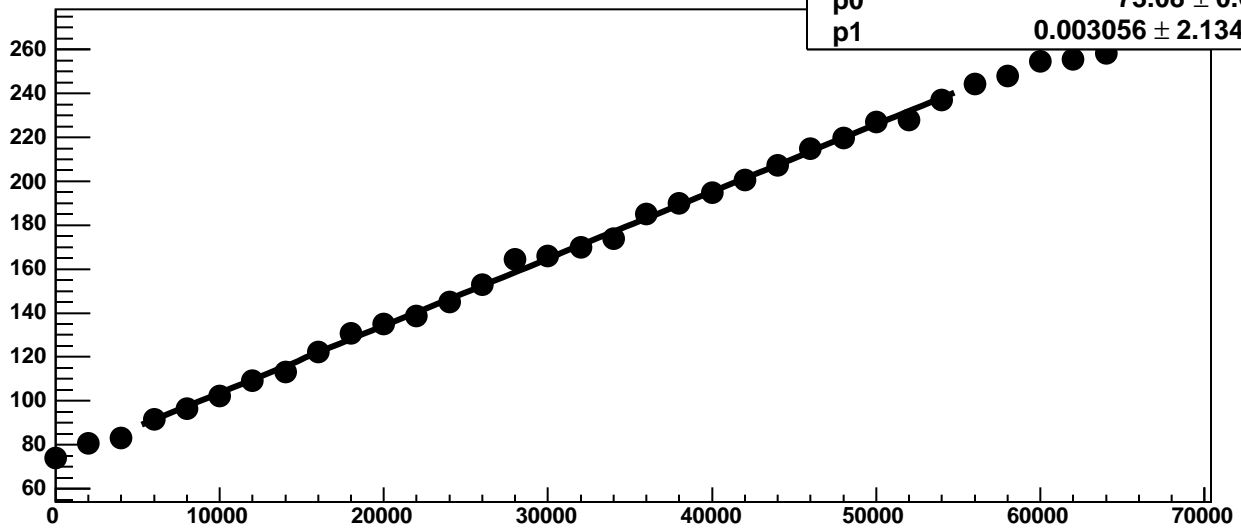
Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

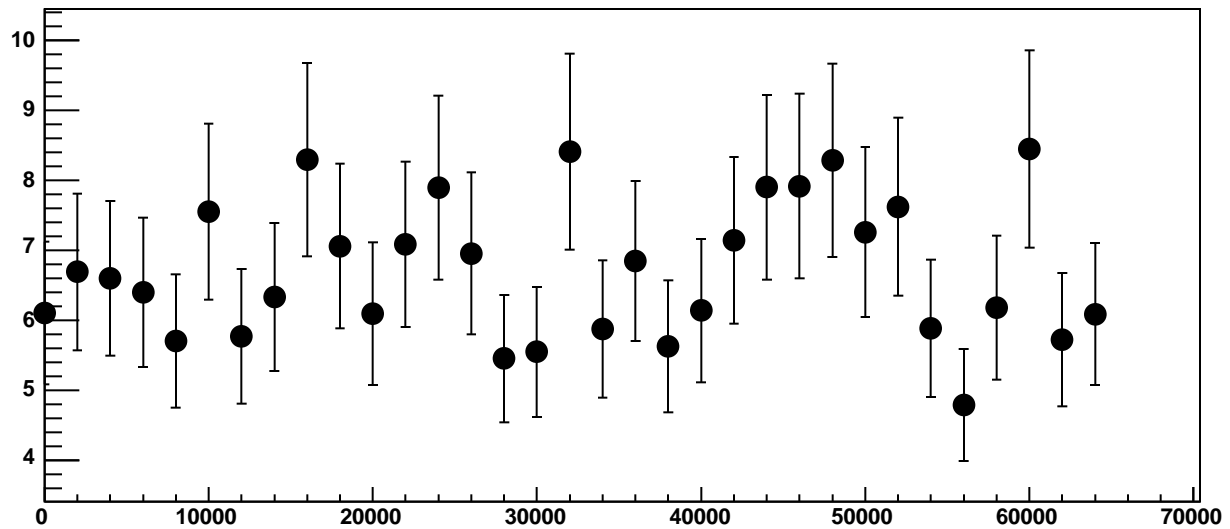


Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

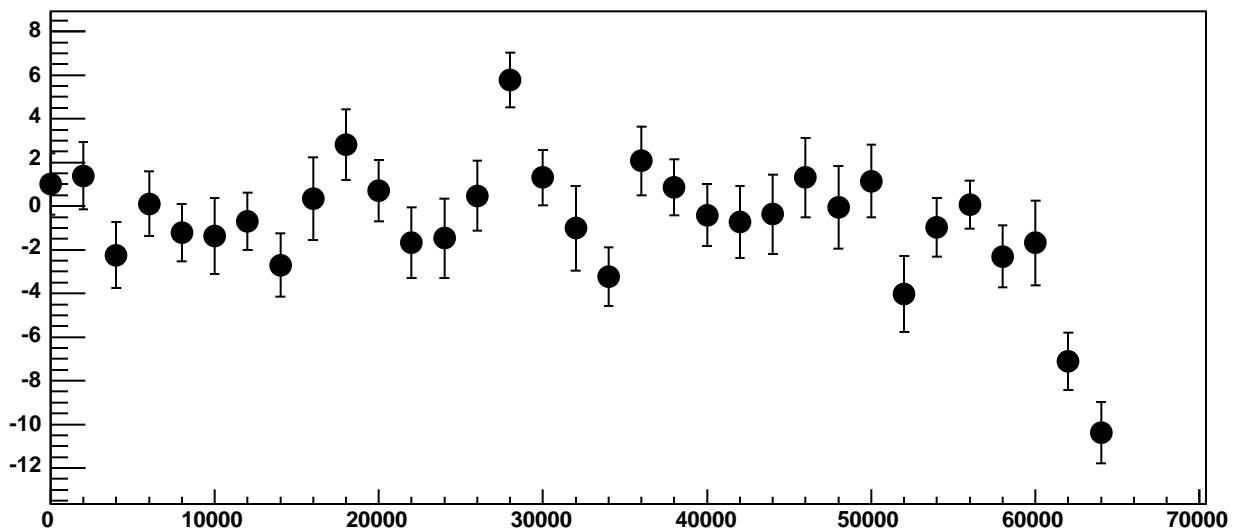


χ^2 / ndf 48.03 / 23
p0 73.08 ± 0.6939
p1 $0.003056 \pm 2.134e-05$

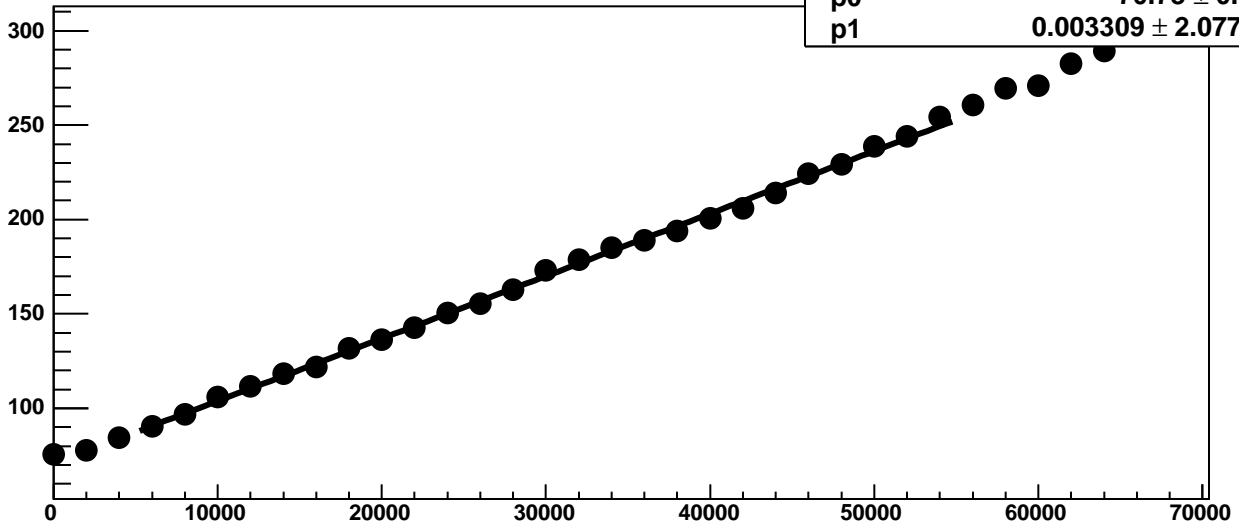
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

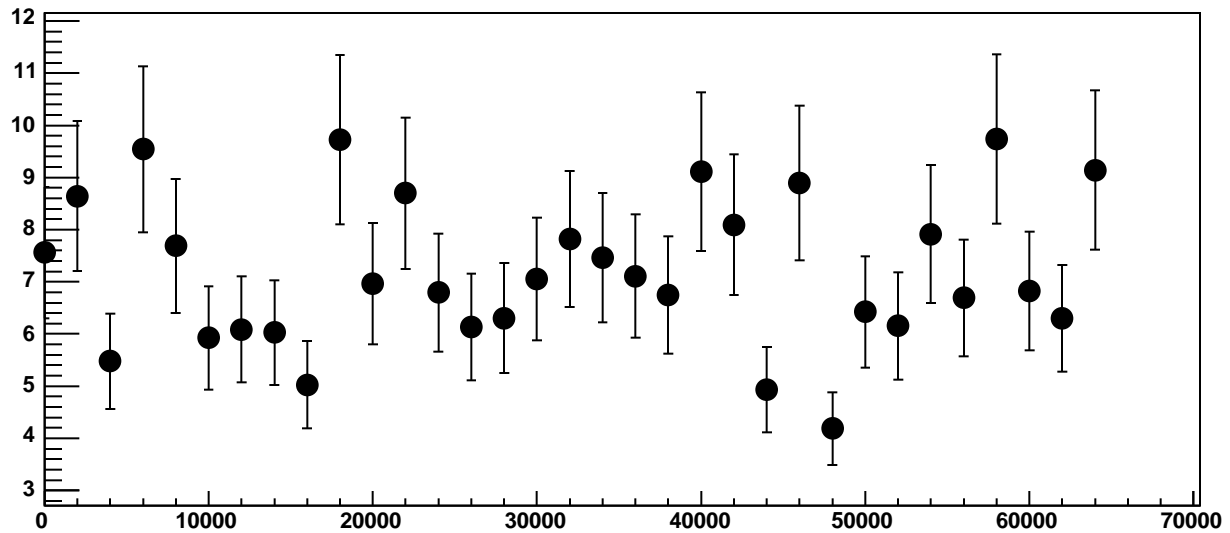


Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

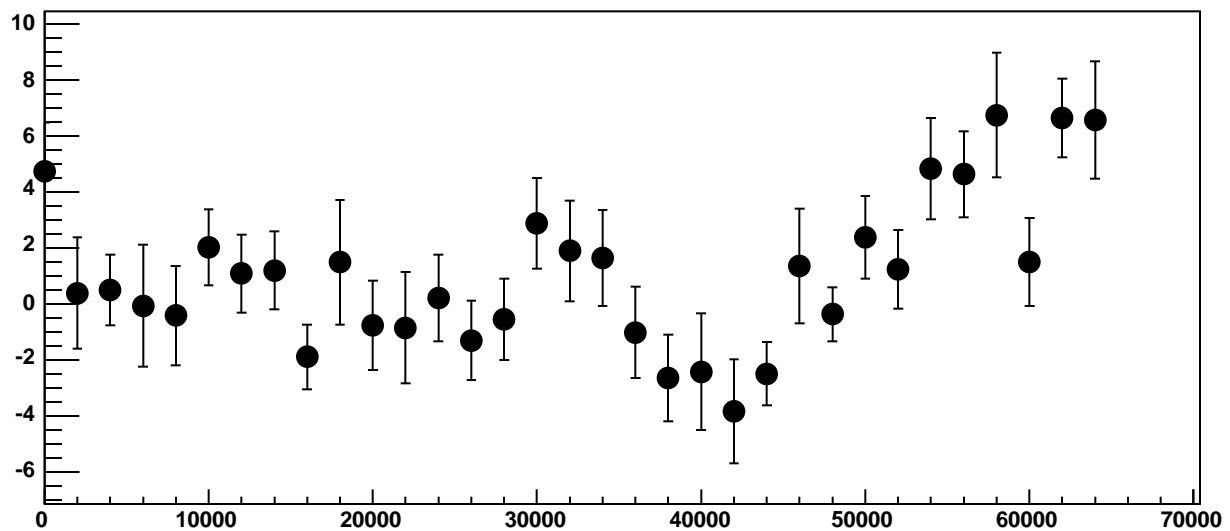


χ^2 / ndf 38.21 / 23
p0 70.78 ± 0.7113
p1 0.003309 ± 2.077e-05

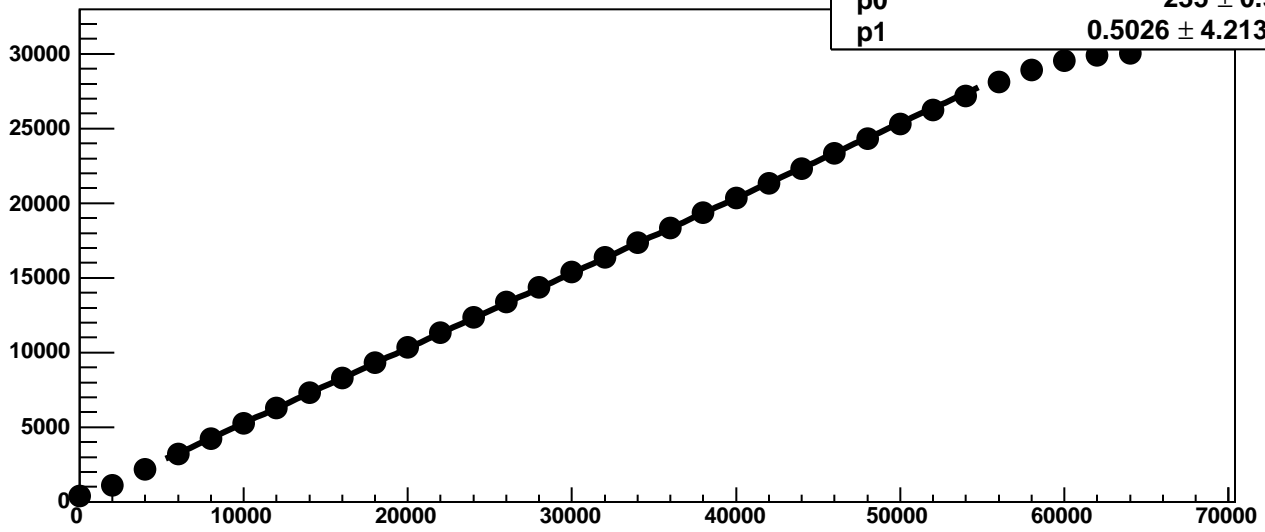
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



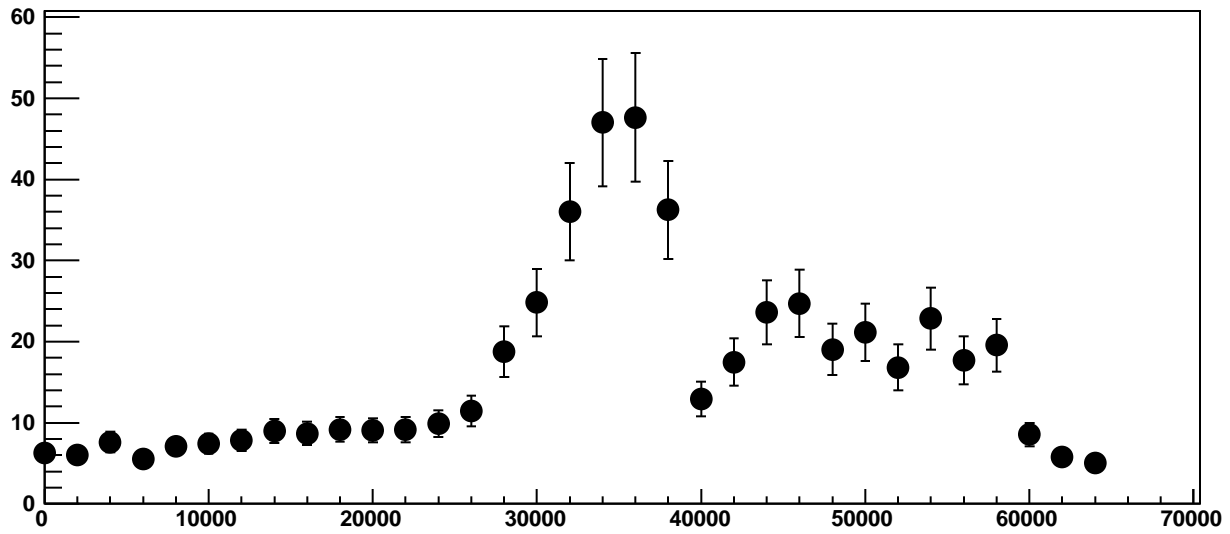
Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



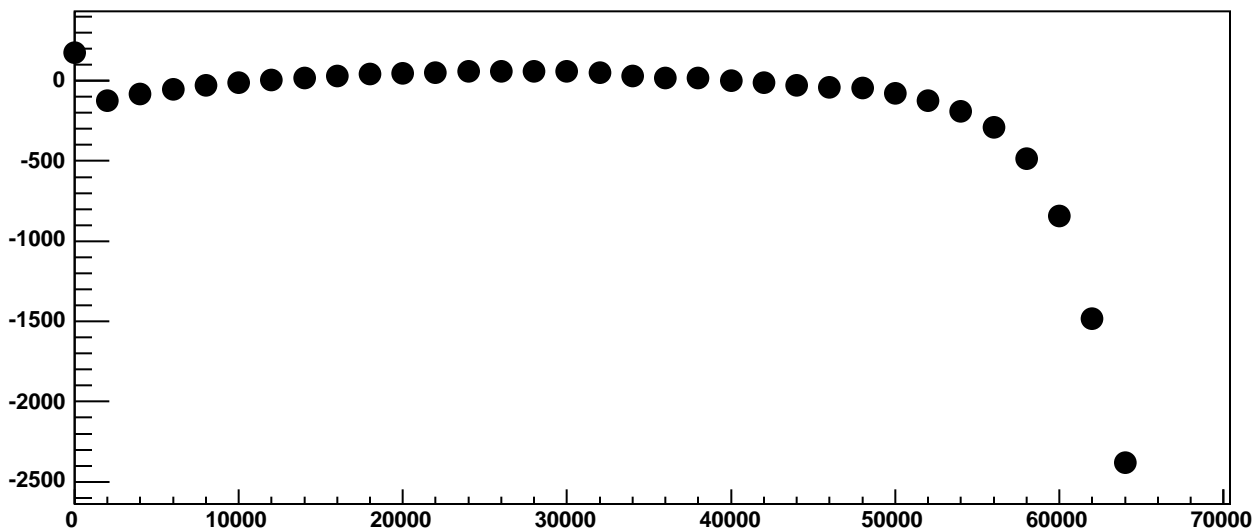
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



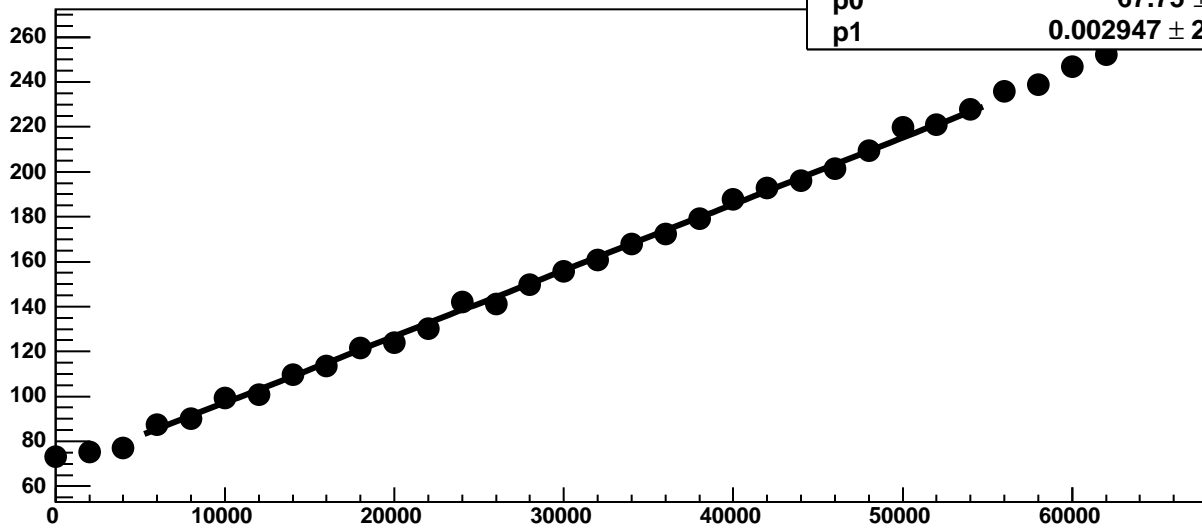
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

36.84 / 23

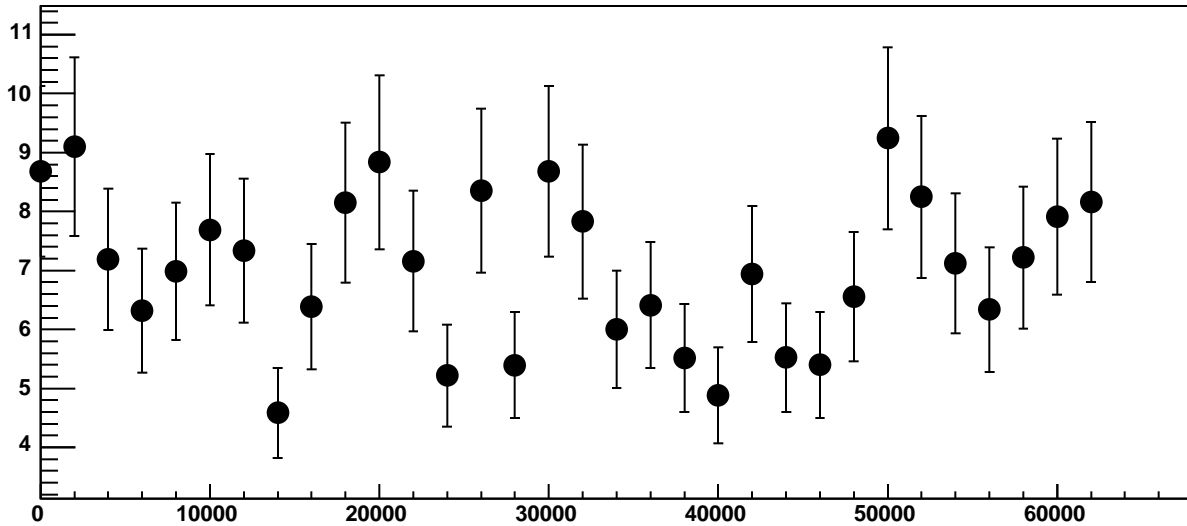
p0

67.75 ± 0.7029

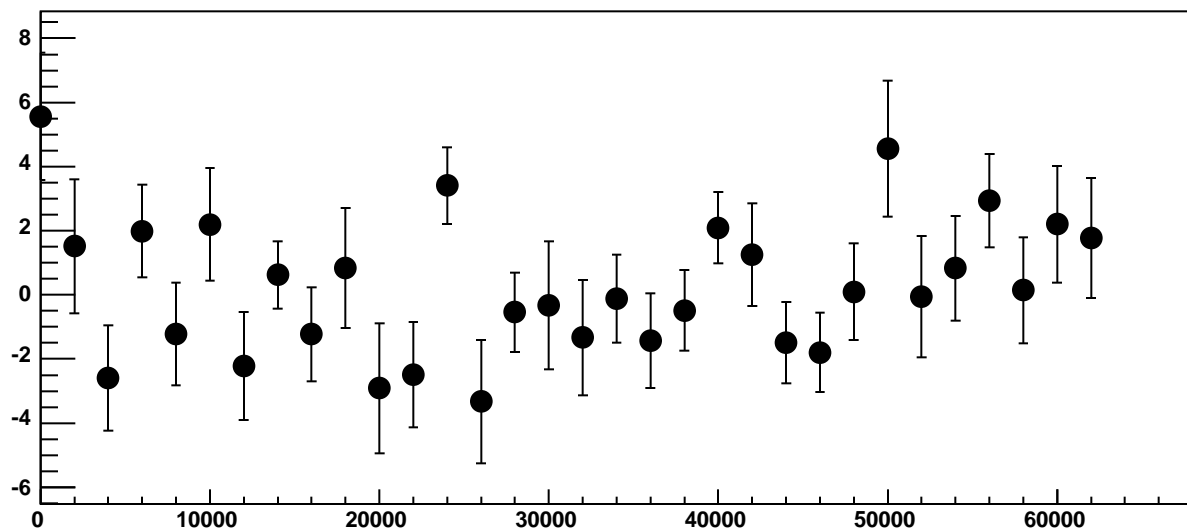
p1

$0.002947 \pm 2.12e-05$

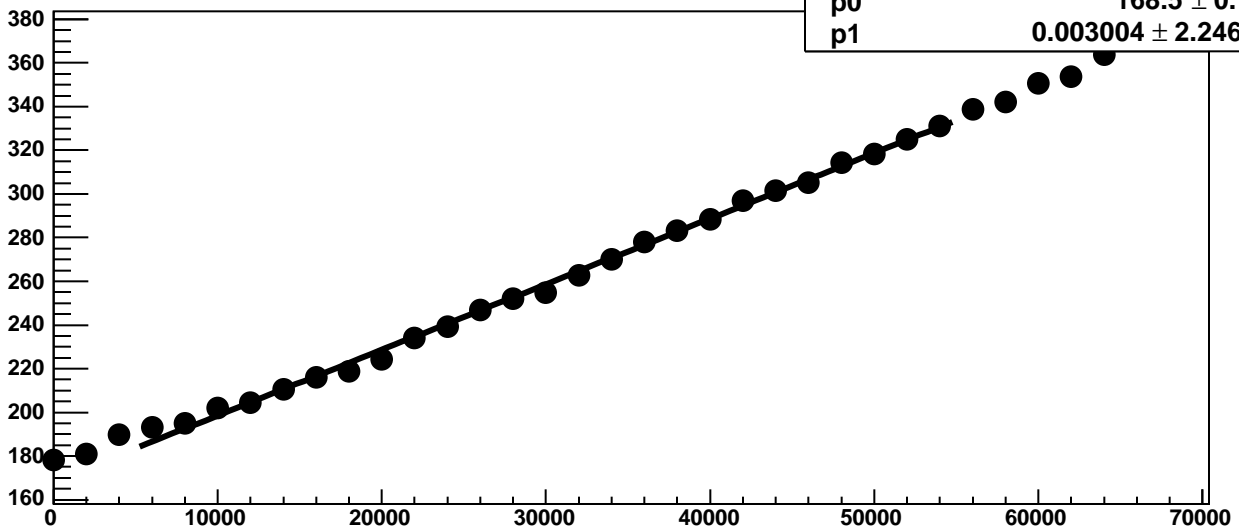
Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

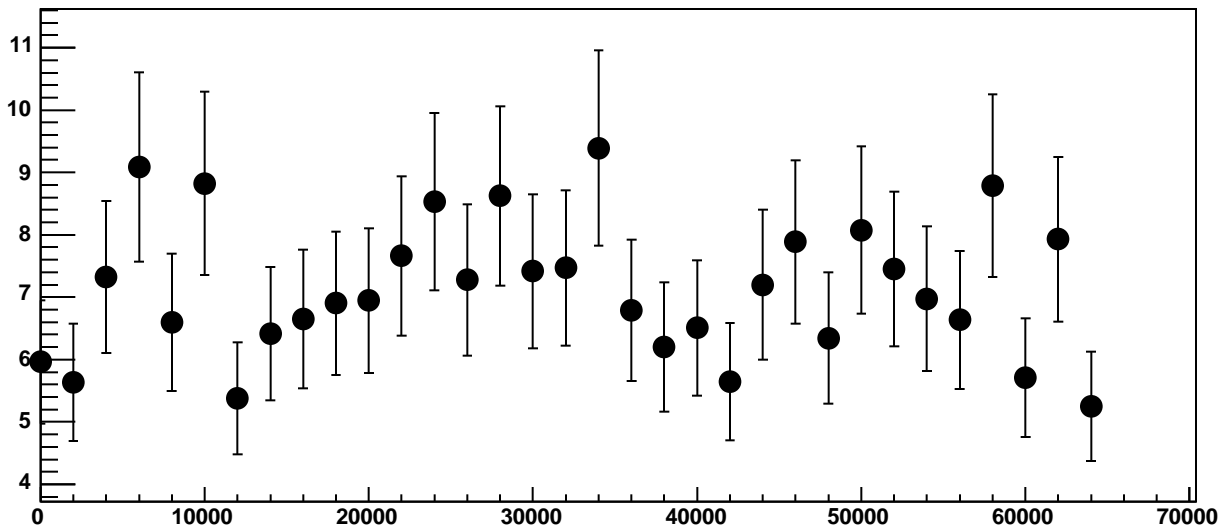


Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

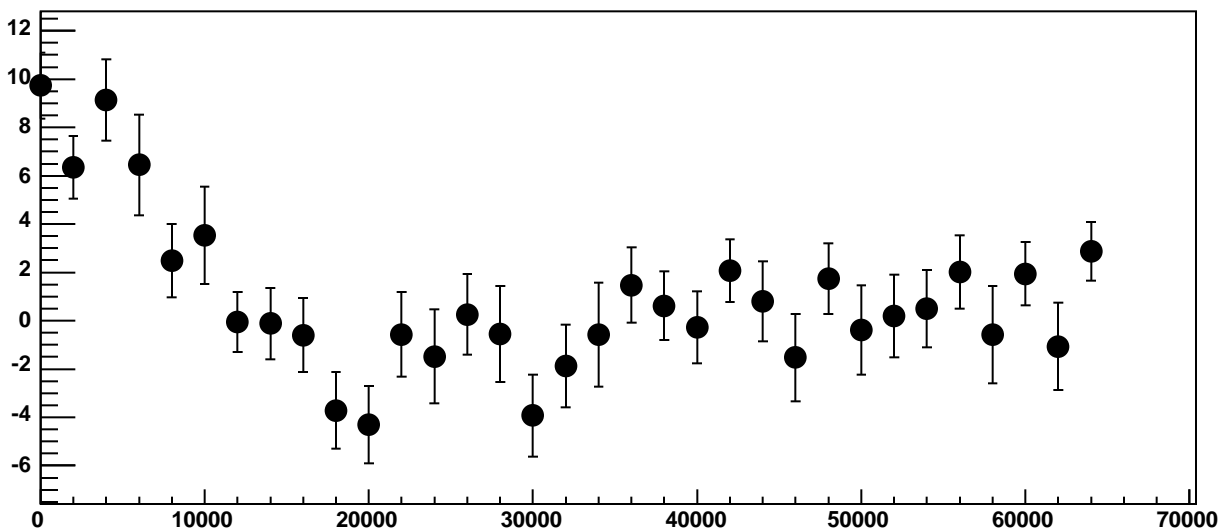


χ^2 / ndf 41.83 / 23
p0 168.5 ± 0.7497
p1 $0.003004 \pm 2.246e-05$

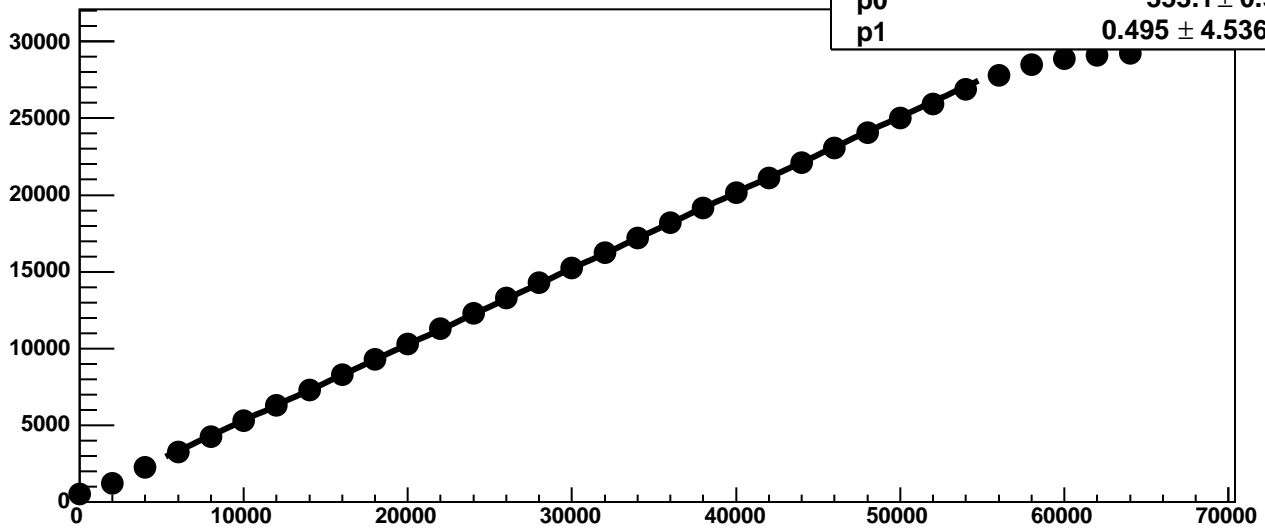
Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



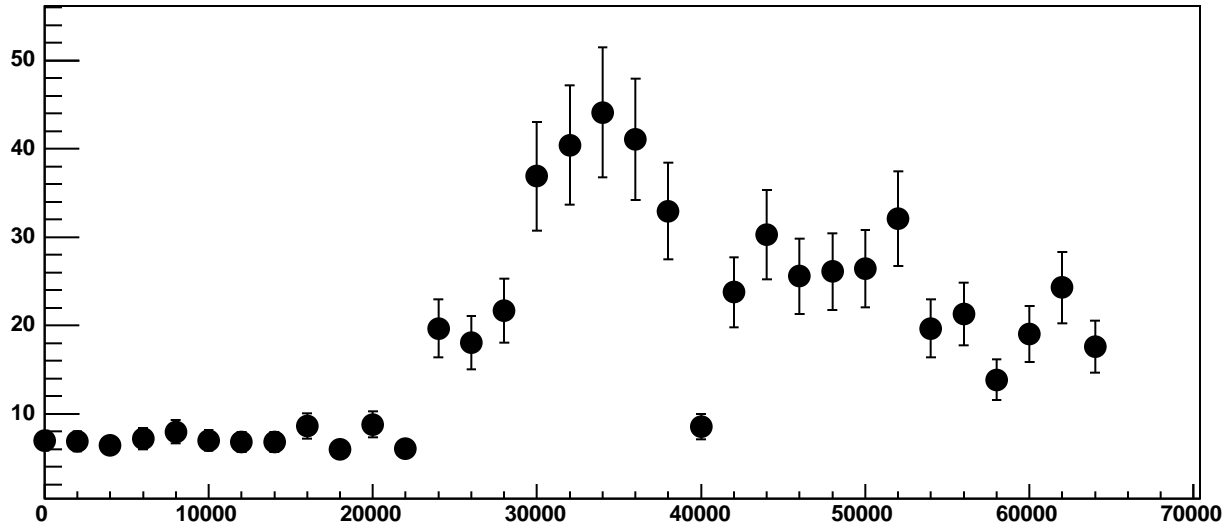
Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



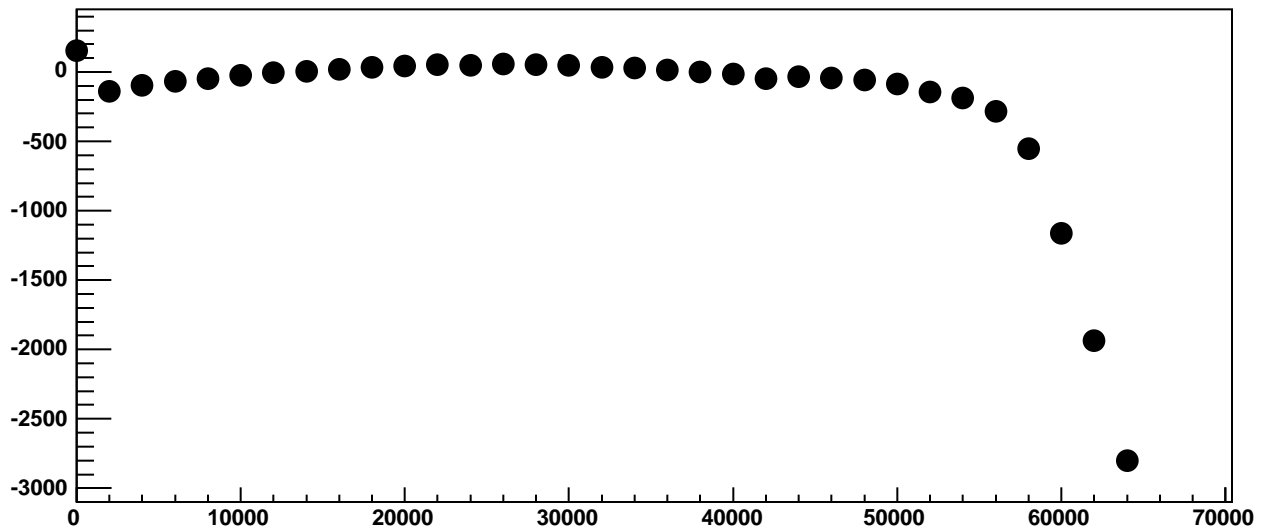
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



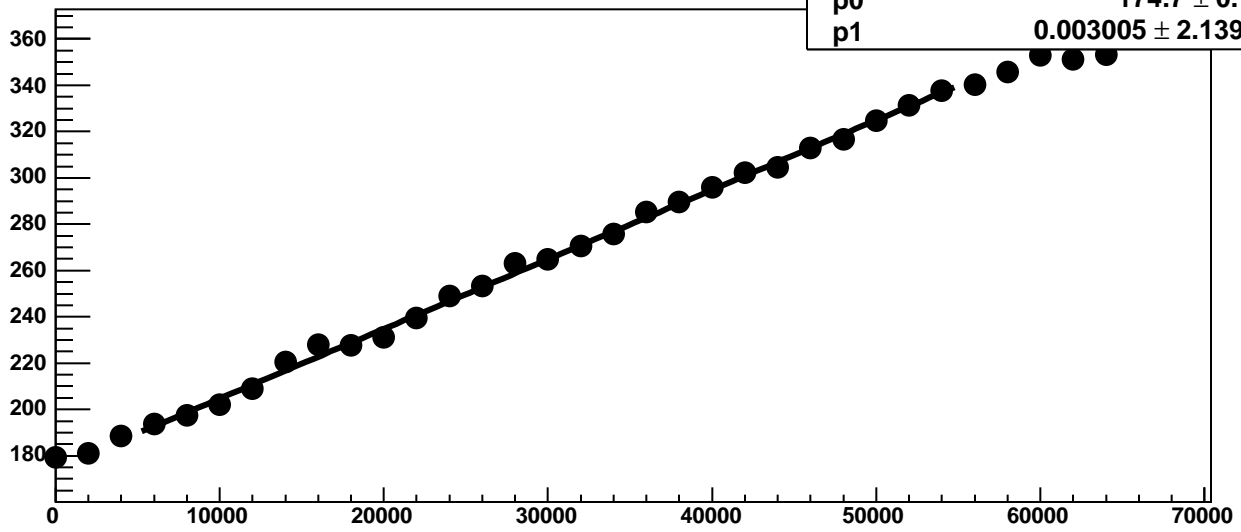
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

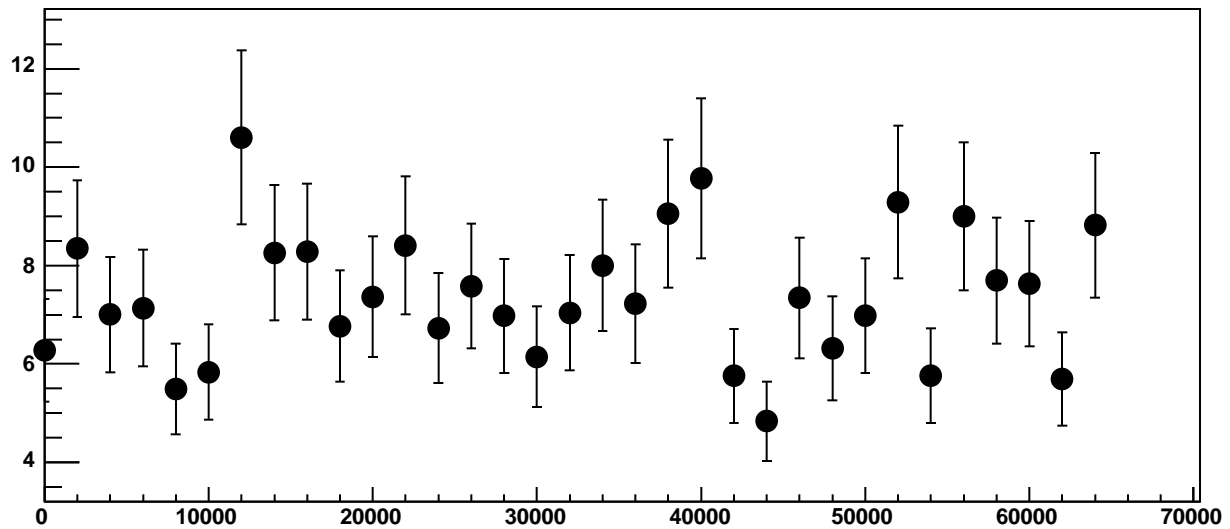


Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

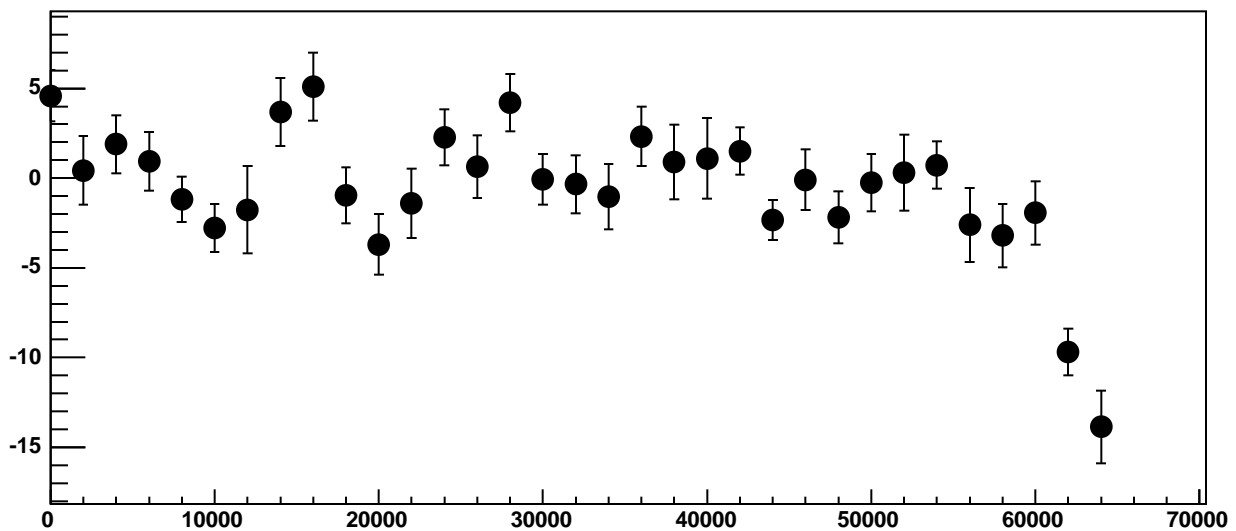


χ^2 / ndf 43.1 / 23
p0 174.7 ± 0.7287
p1 $0.003005 \pm 2.139\text{e-}05$

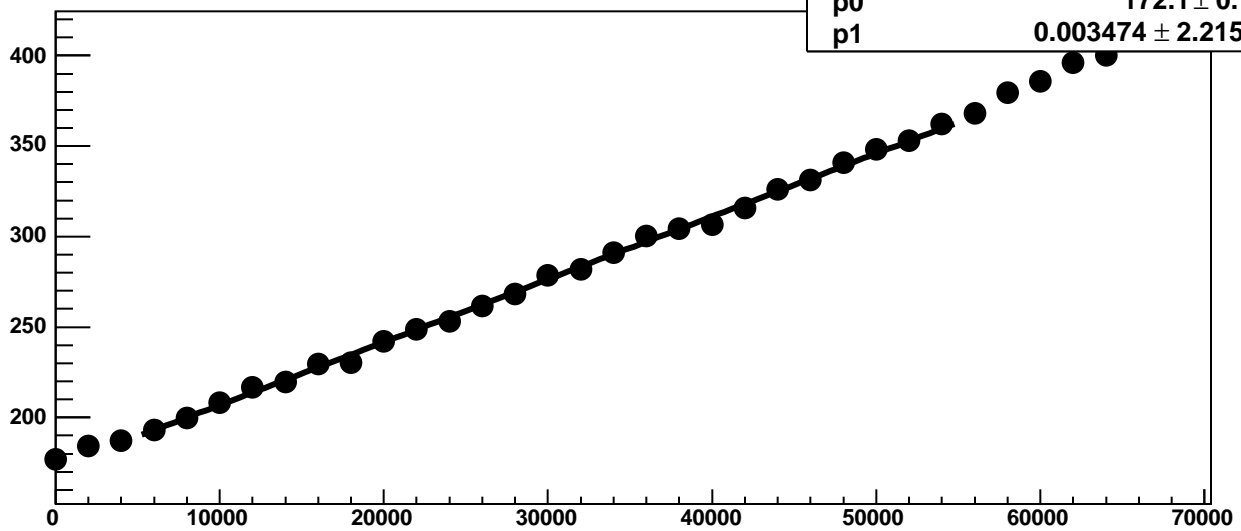
Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

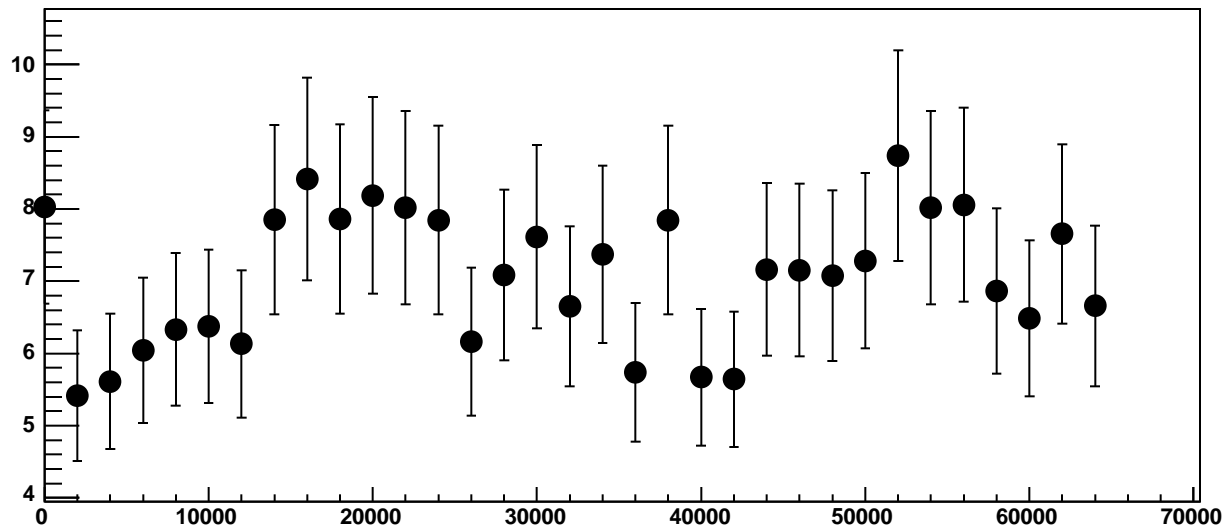


Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

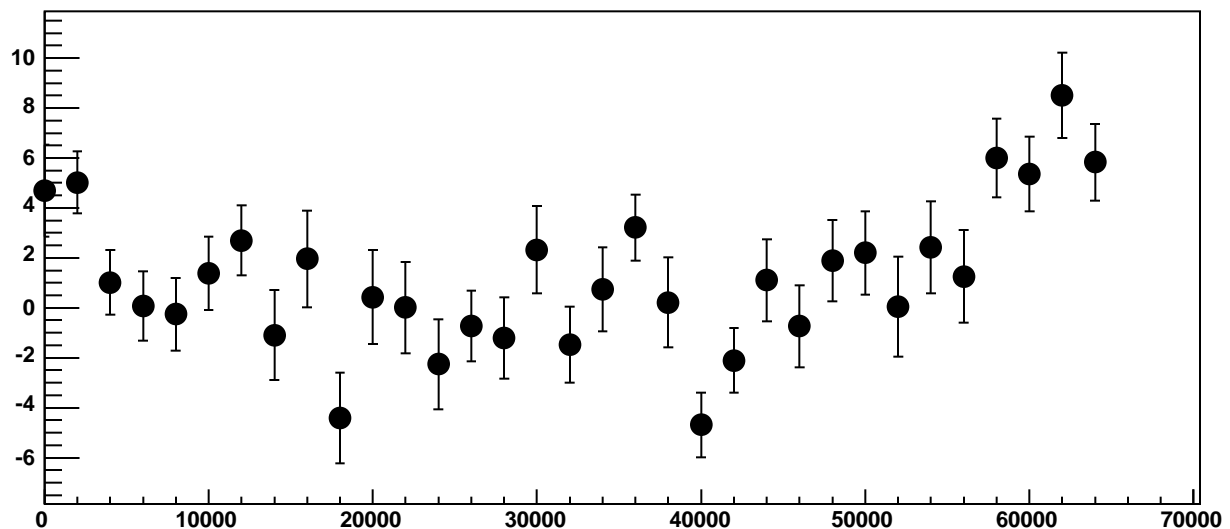


χ^2 / ndf 44.37 / 23
p0 172.1 ± 0.7277
p1 $0.003474 \pm 2.215e-05$

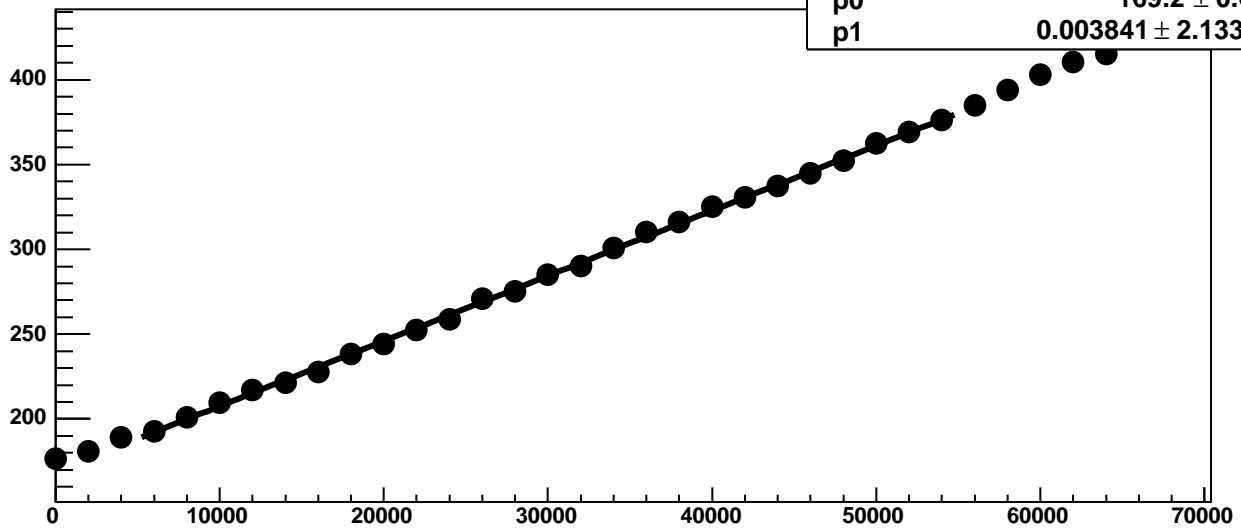
Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

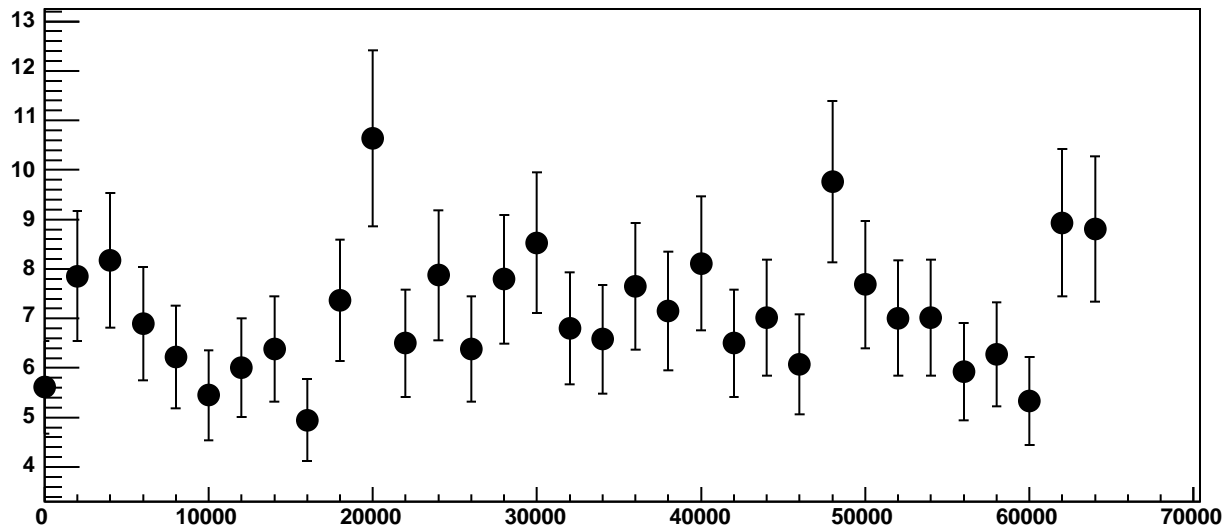


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

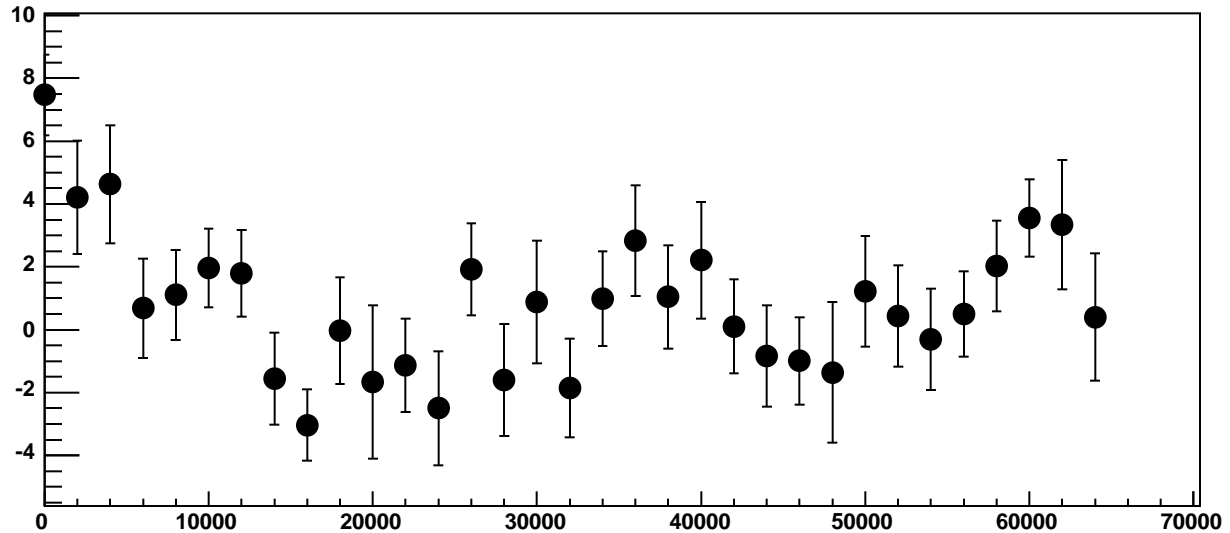


χ^2 / ndf 26.94 / 23
p0 169.2 ± 0.6799
p1 $0.003841 \pm 2.133e-05$

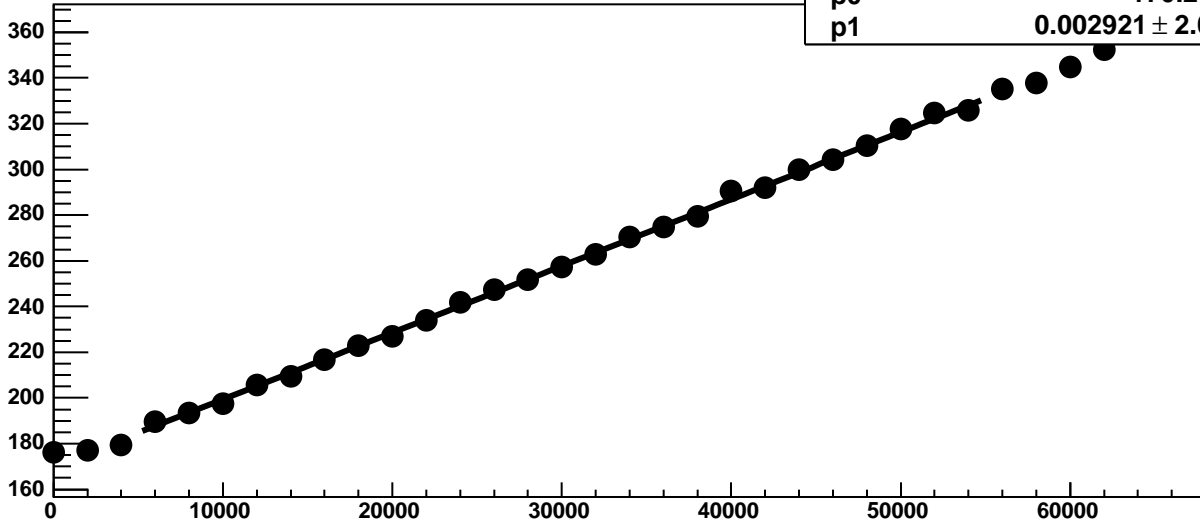
Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.7 / 23

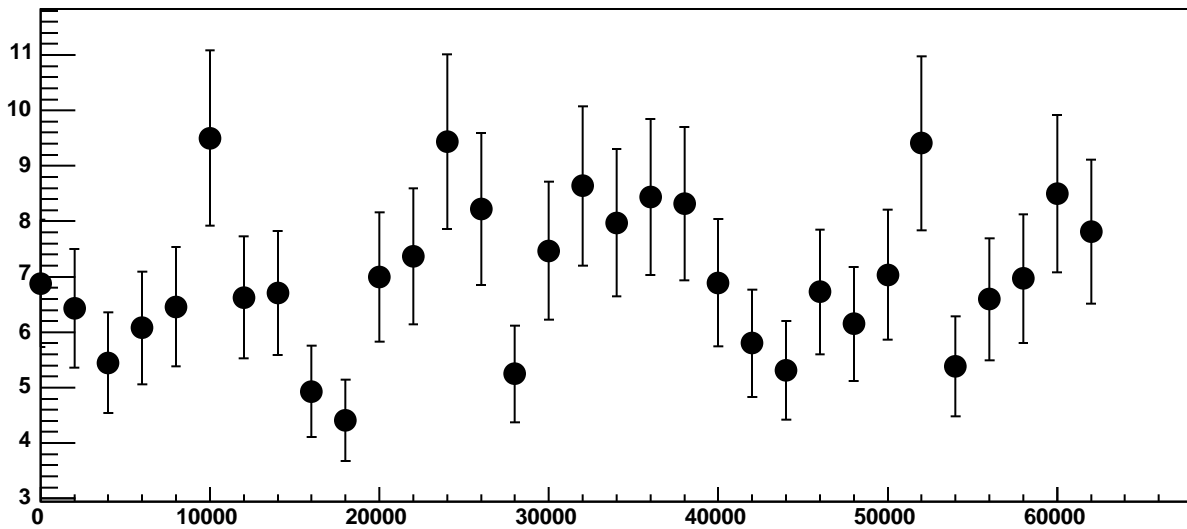
p0

170.2 ± 0.674

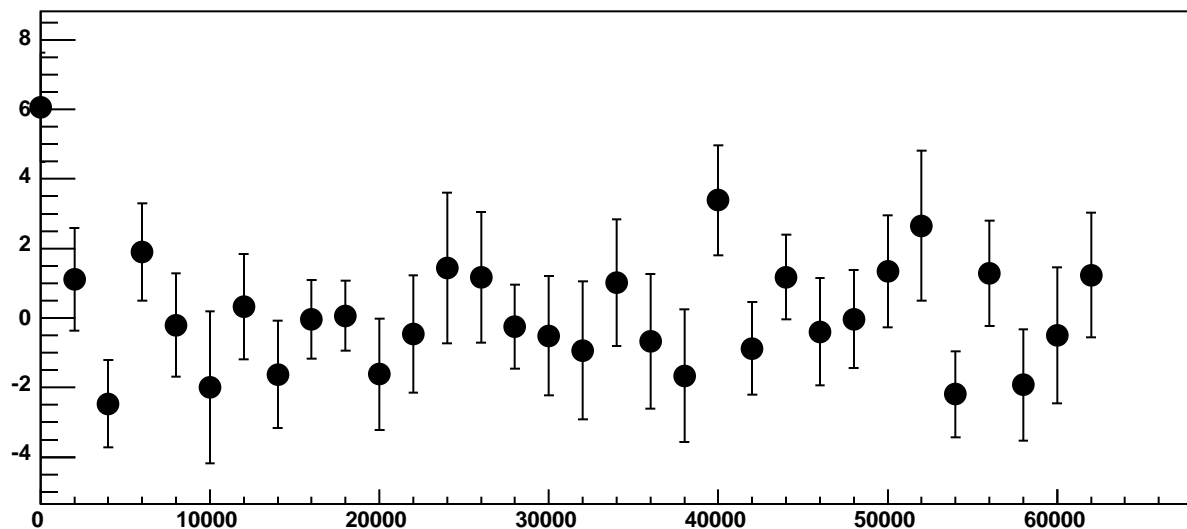
p1

$0.002921 \pm 2.037\text{e-}05$

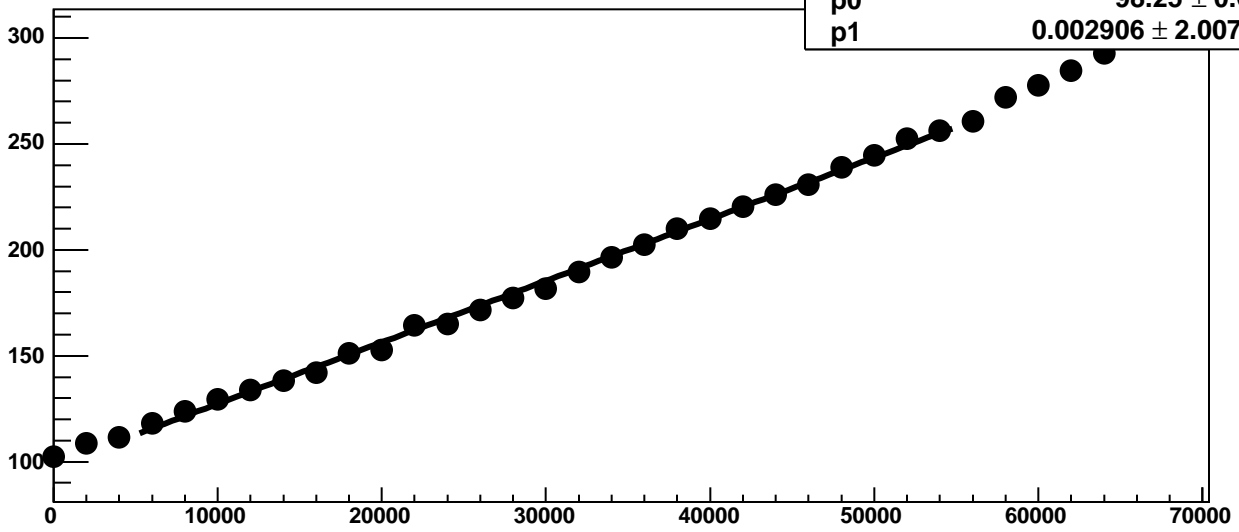
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

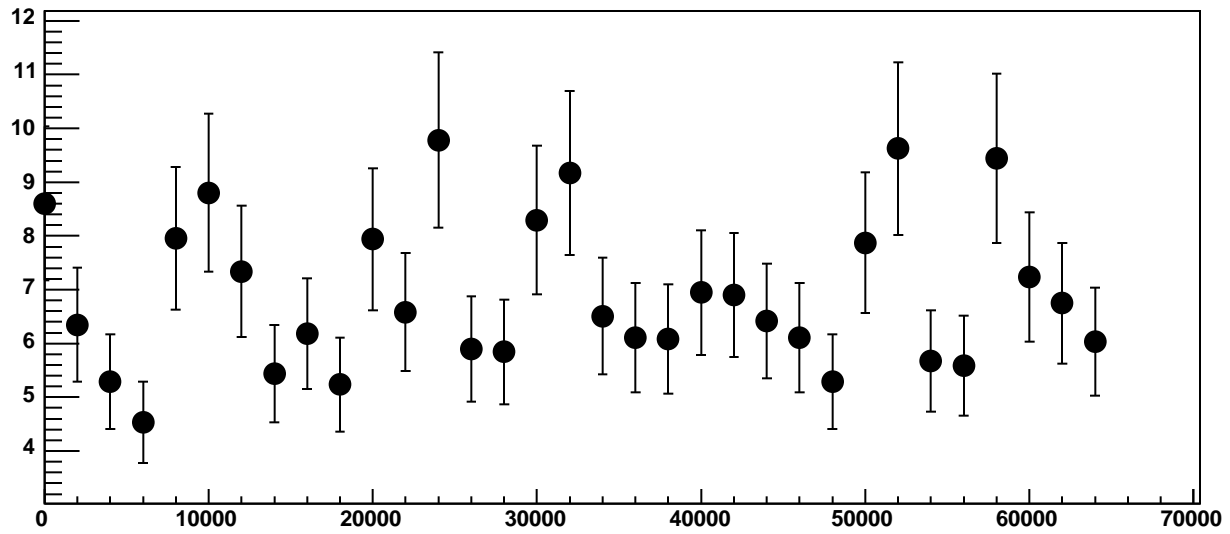


Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

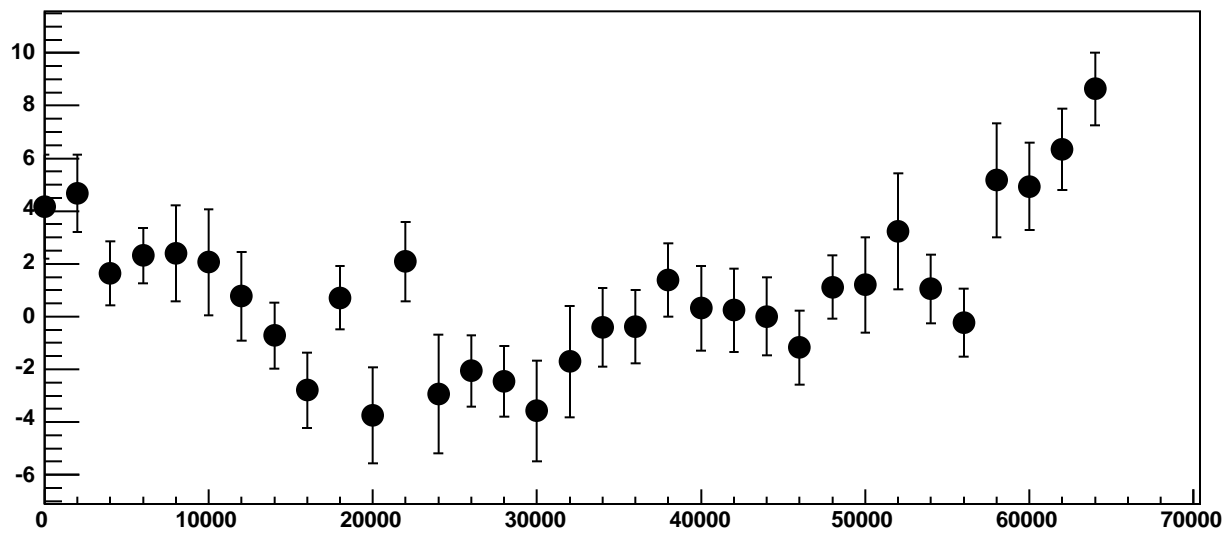


χ^2 / ndf 36.17 / 23
p0 98.25 ± 0.6612
p1 $0.002906 \pm 2.007e-05$

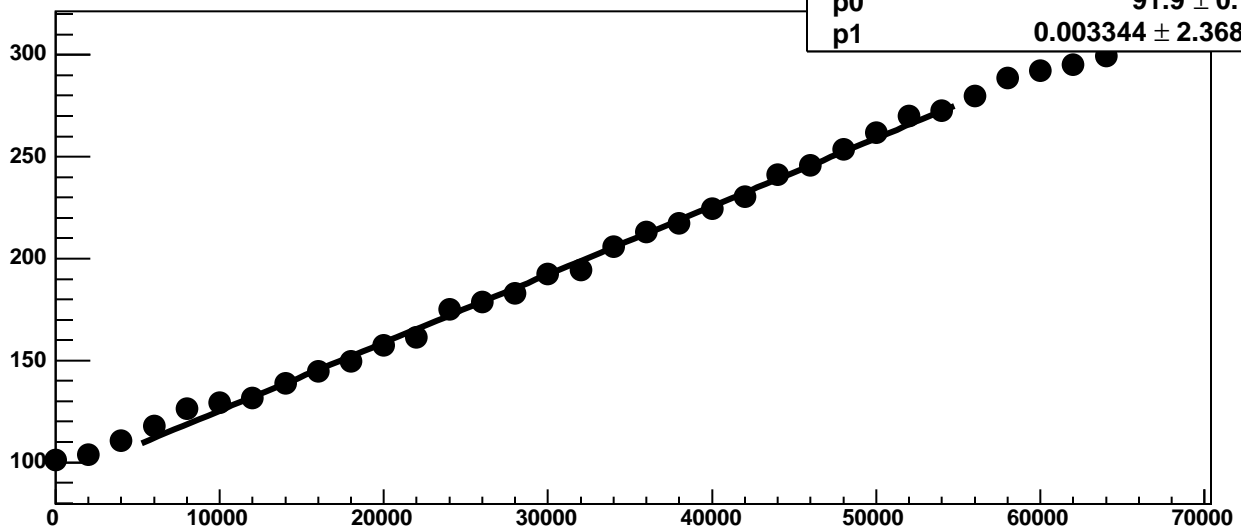
Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

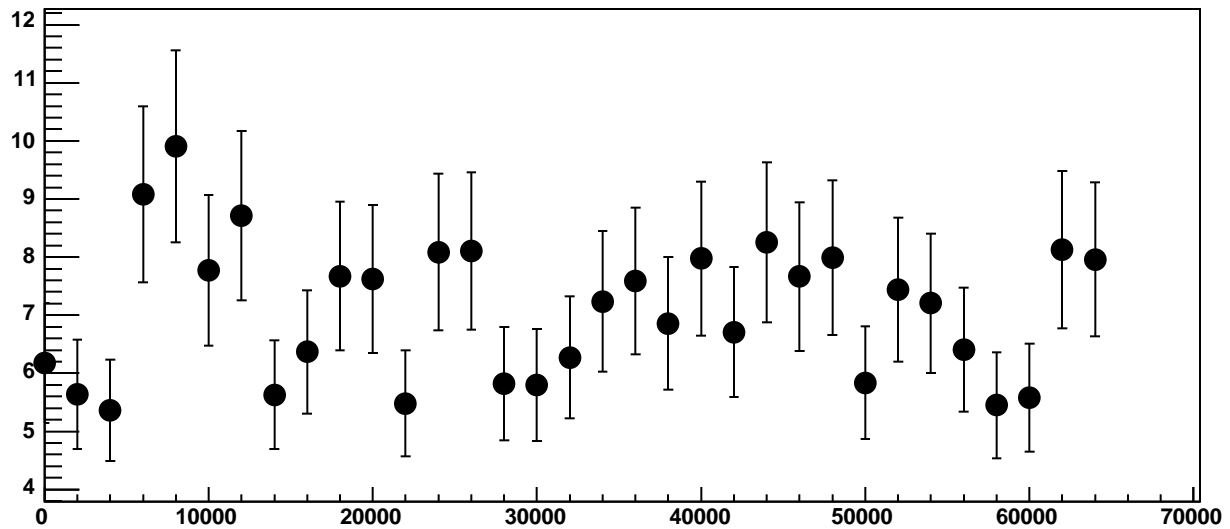


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

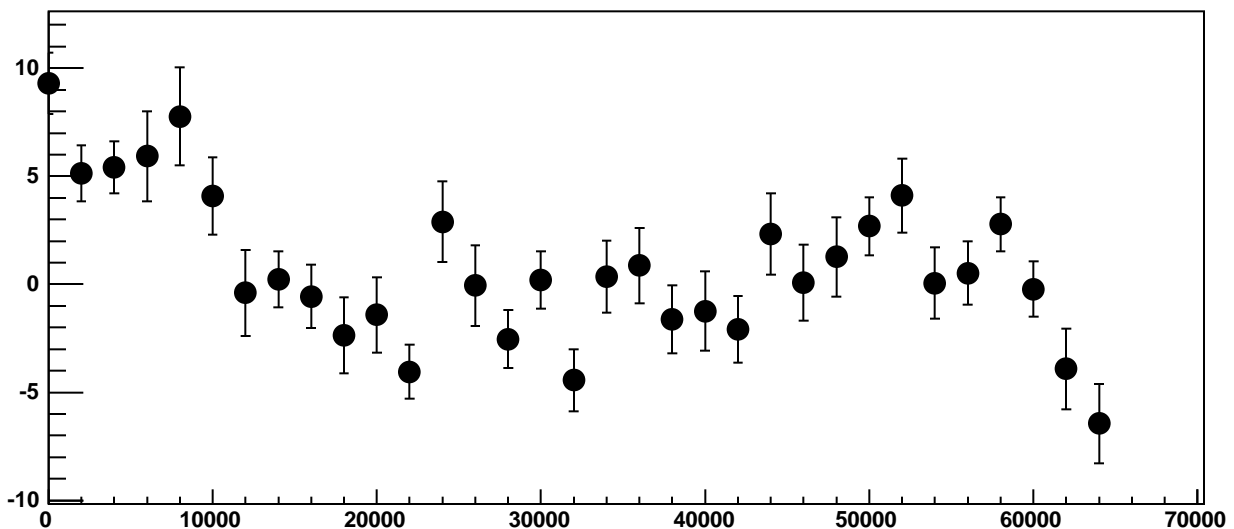


χ^2 / ndf 69.05 / 23
p0 91.9 ± 0.7915
p1 0.003344 ± 2.368e-05

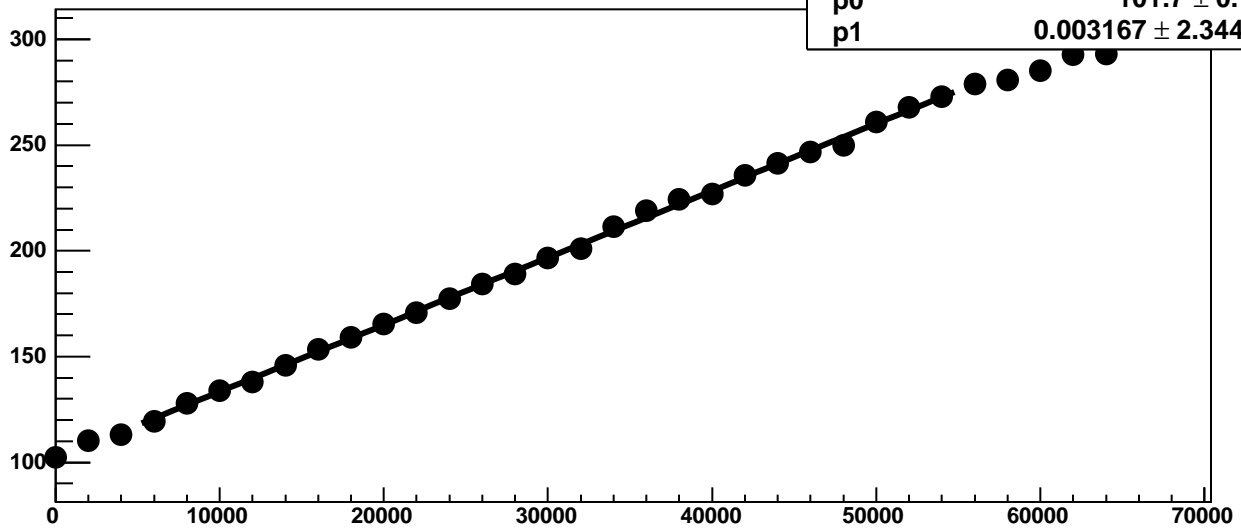
Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

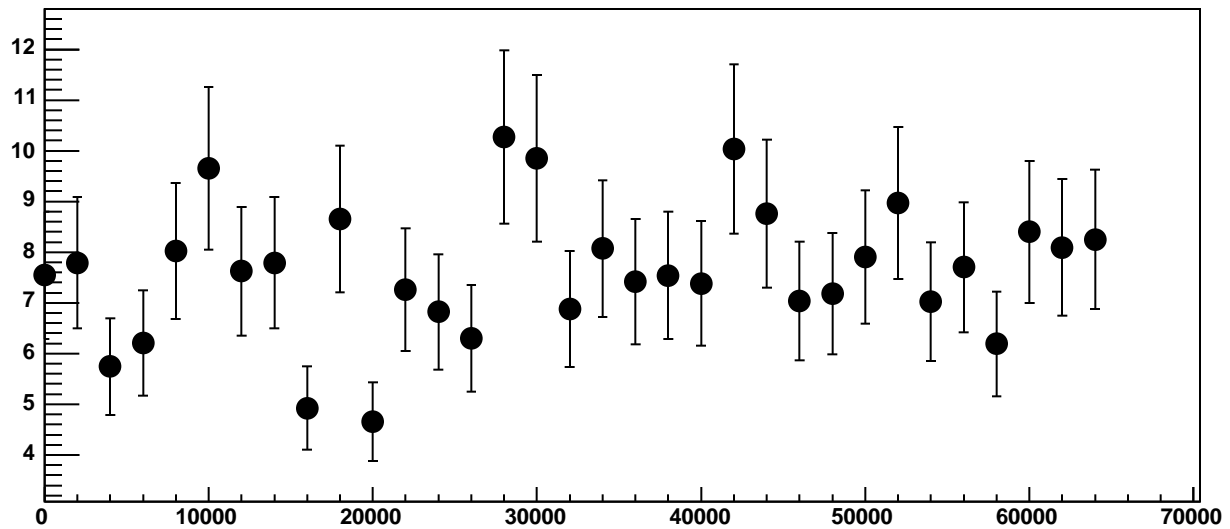


Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

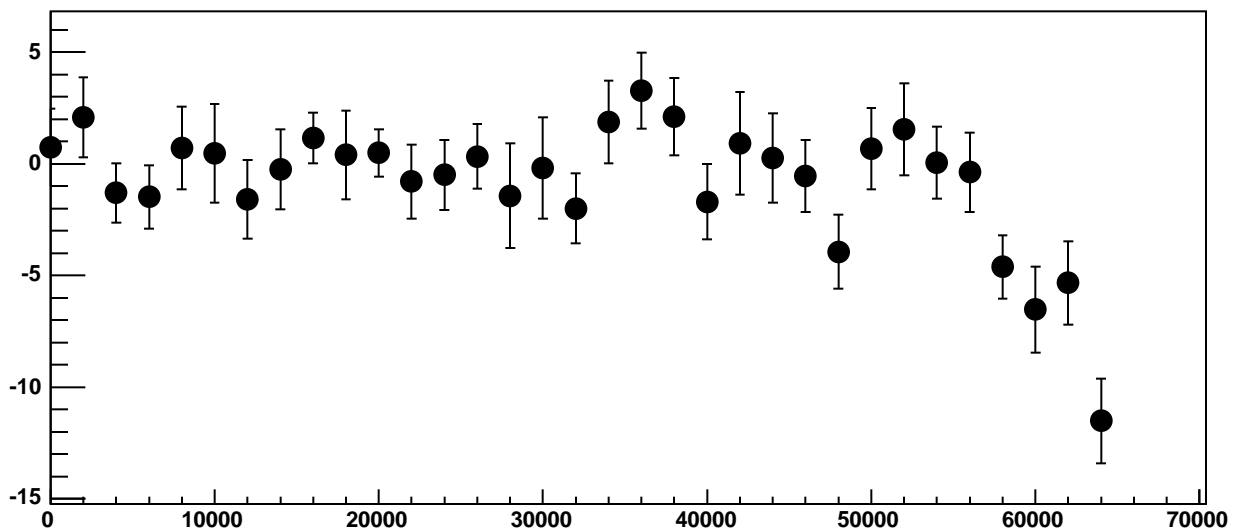


χ^2 / ndf 19.73 / 23
p0 101.7 ± 0.7443
p1 $0.003167 \pm 2.344e-05$

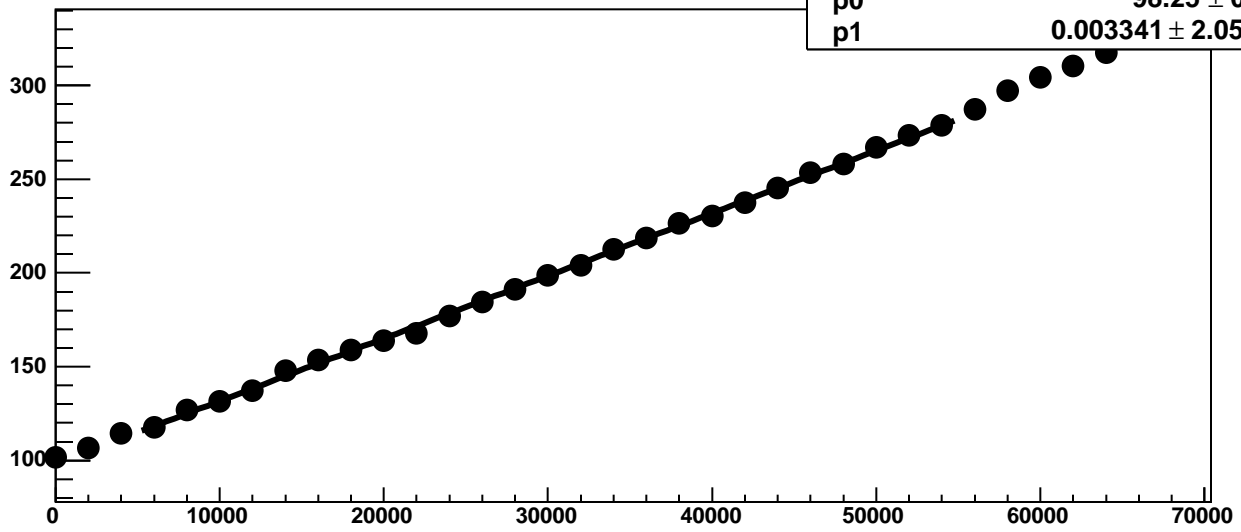
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

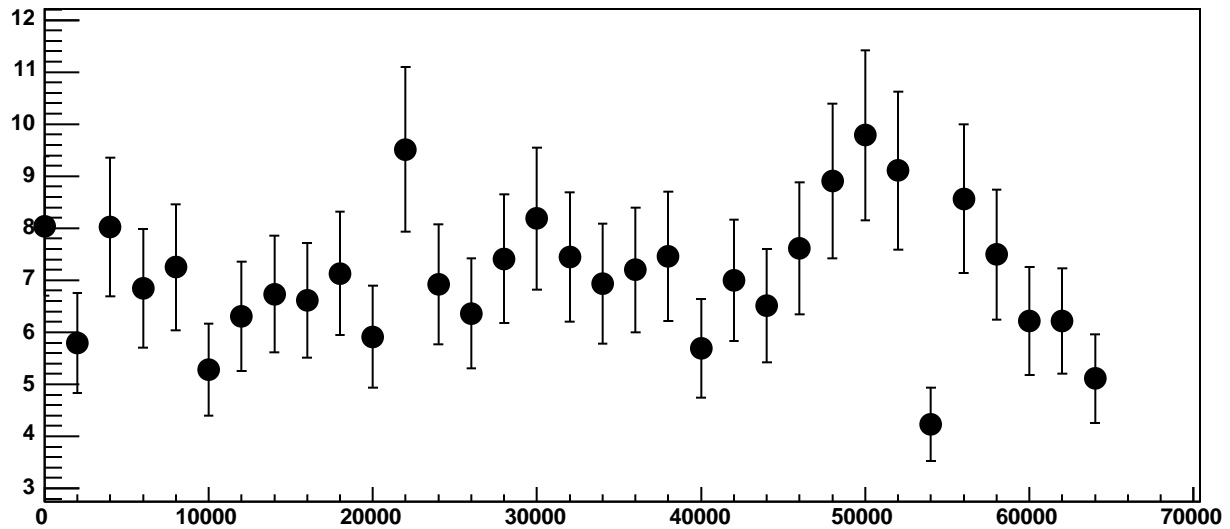


Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

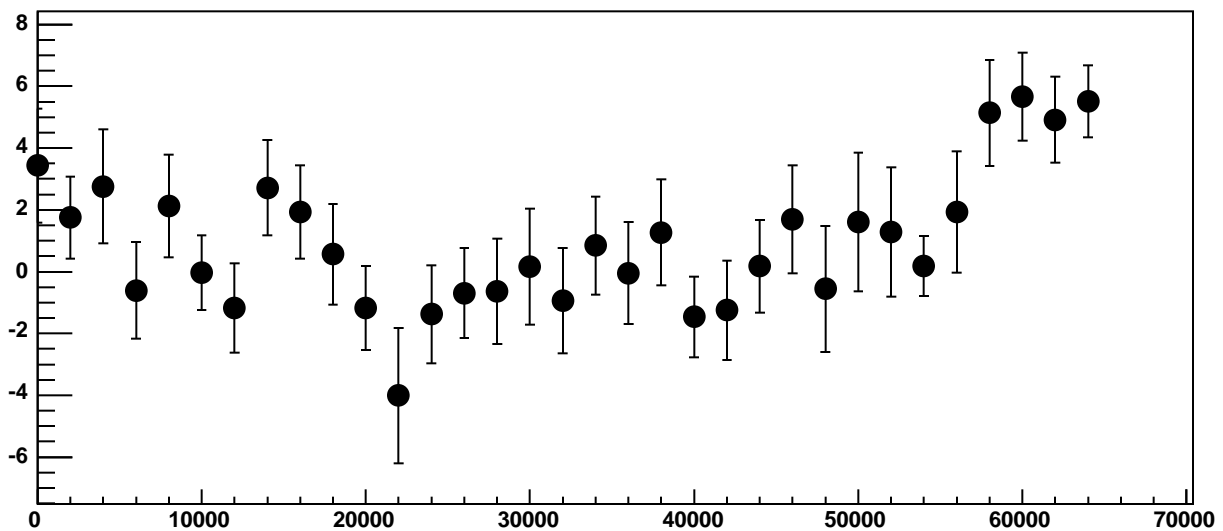


χ^2 / ndf 17.5 / 23
p0 98.25 ± 0.687
p1 $0.003341 \pm 2.05e-05$

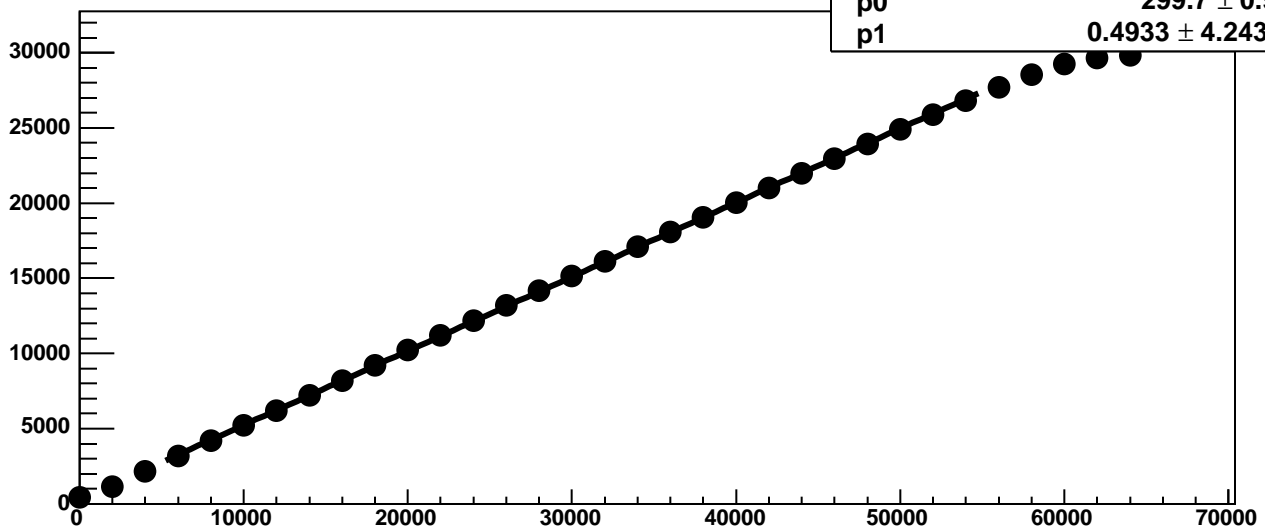
Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



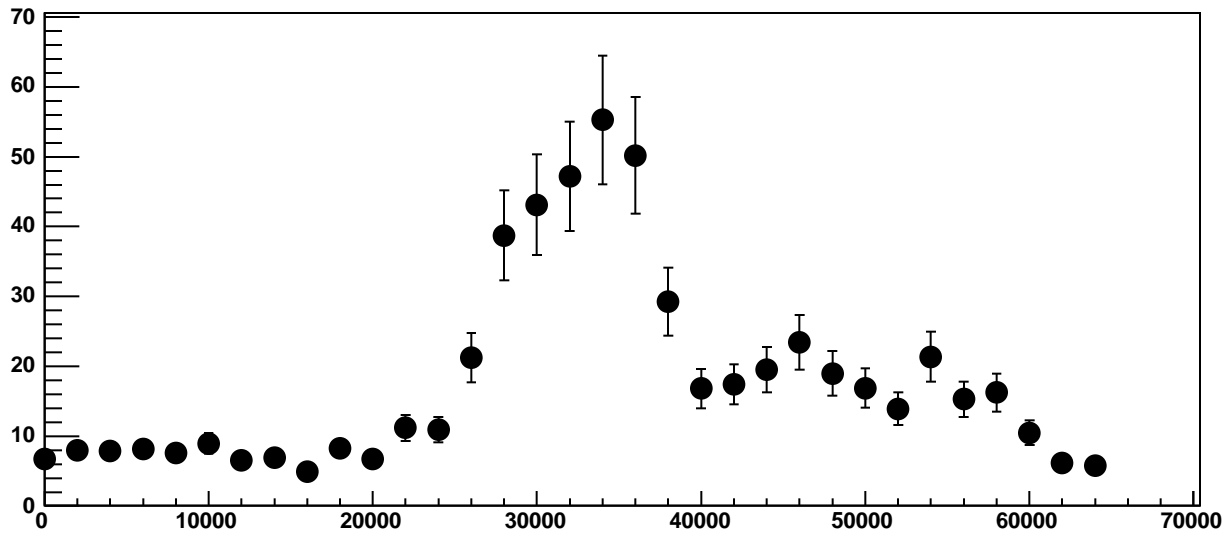
Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



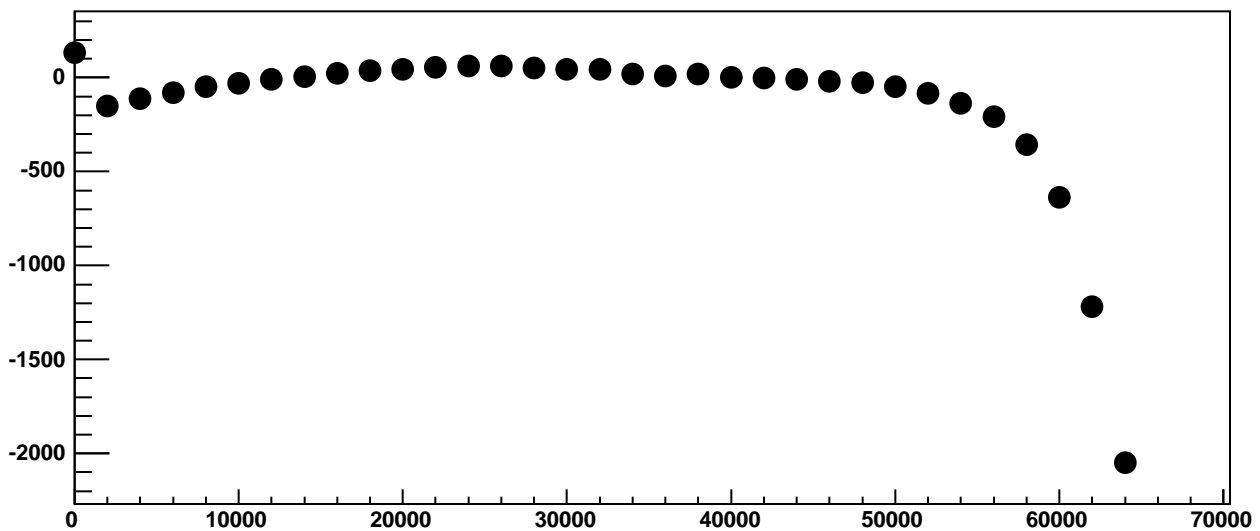
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



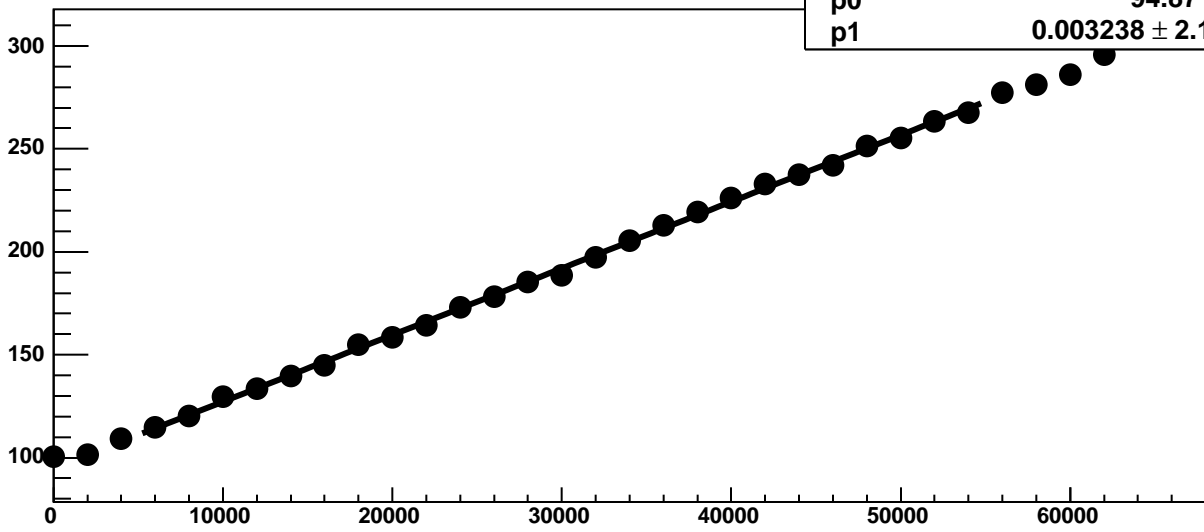
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

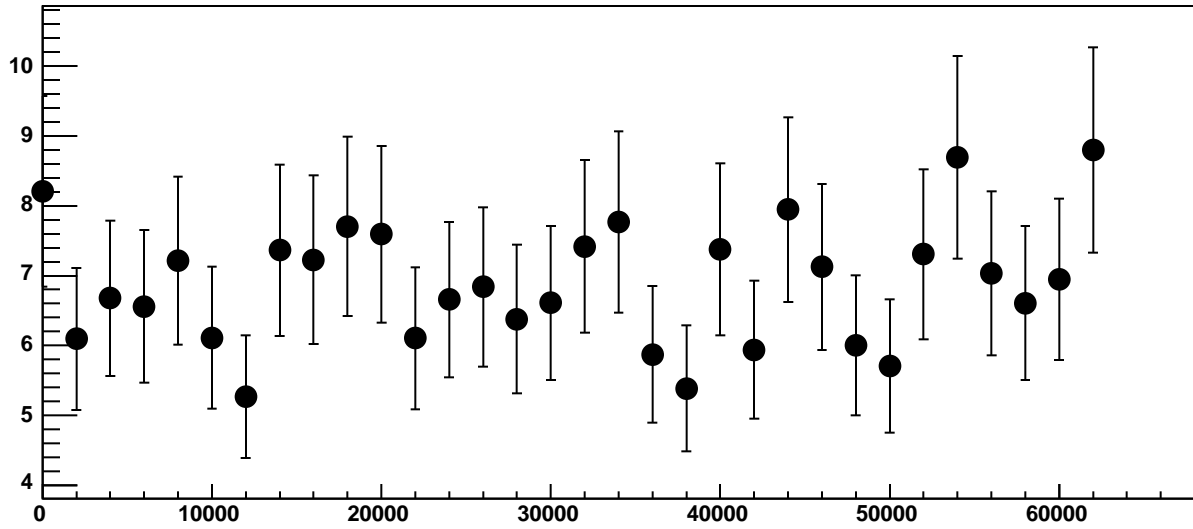


Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

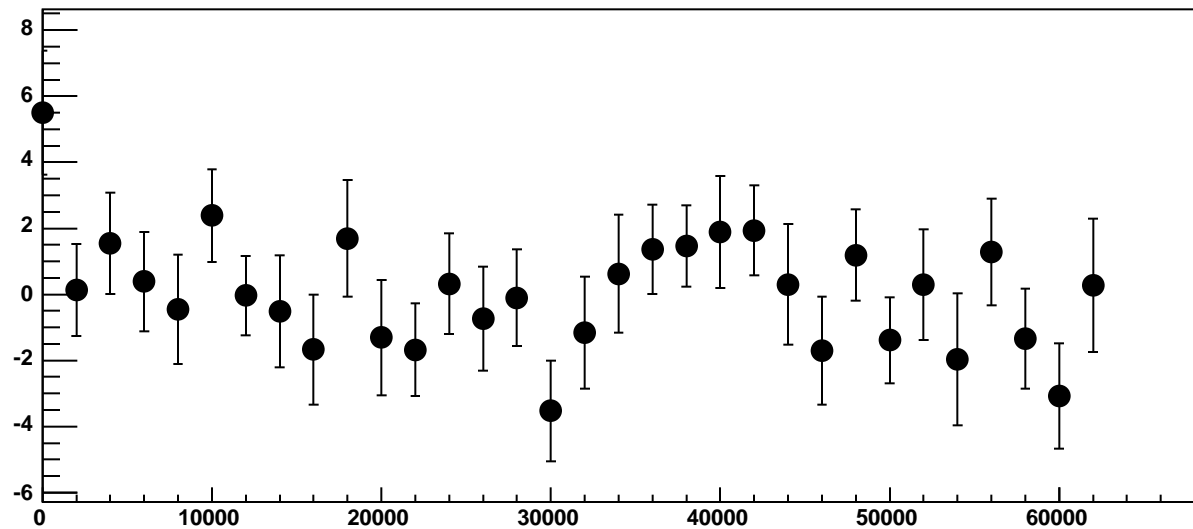


χ^2 / ndf 23 / 23
p0 94.87 ± 0.703
p1 $0.003238 \pm 2.128e-05$

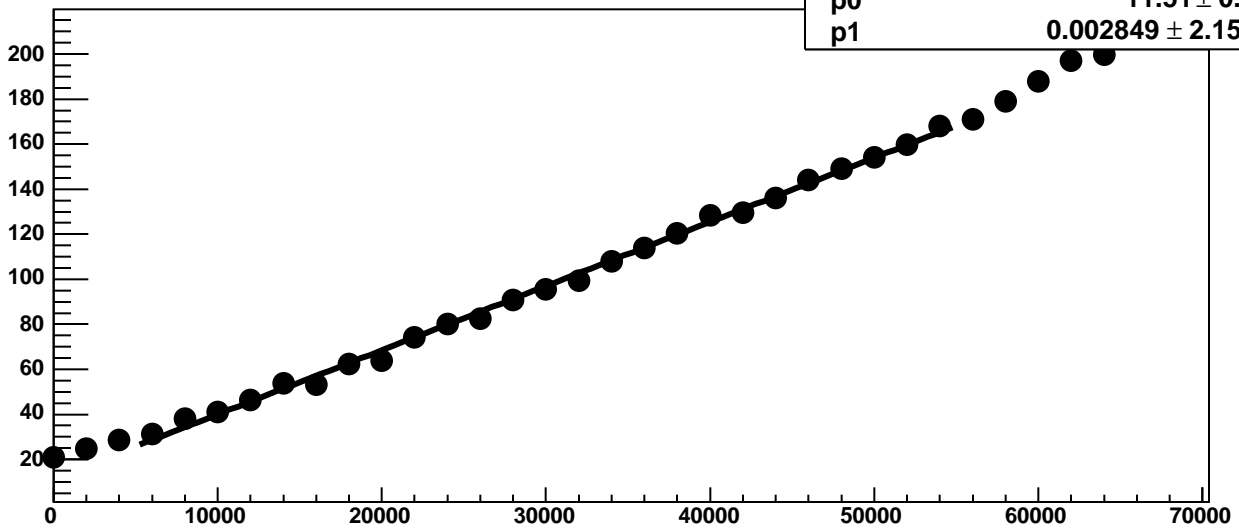
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

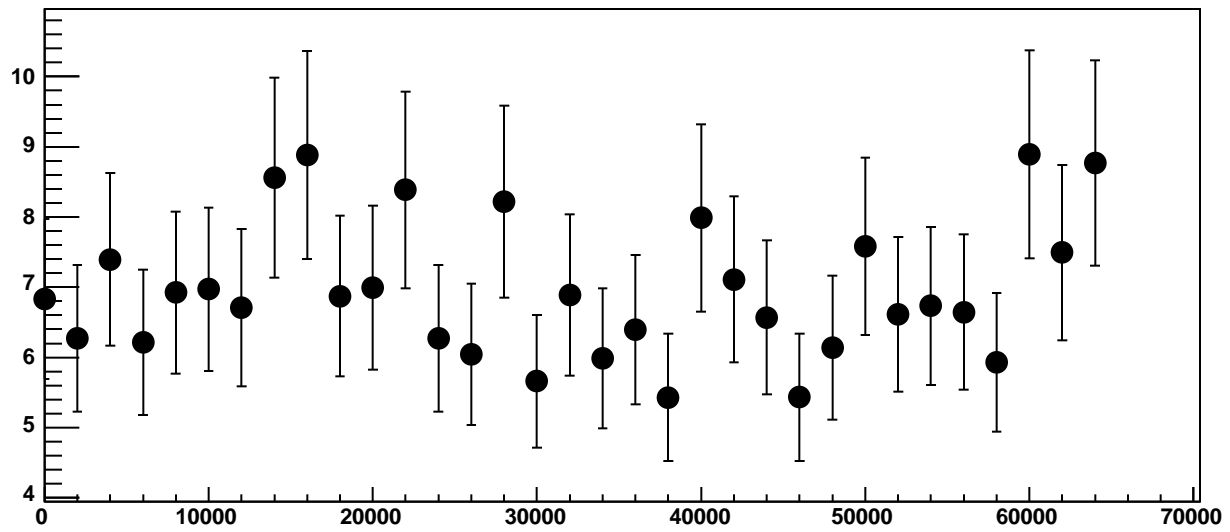


Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

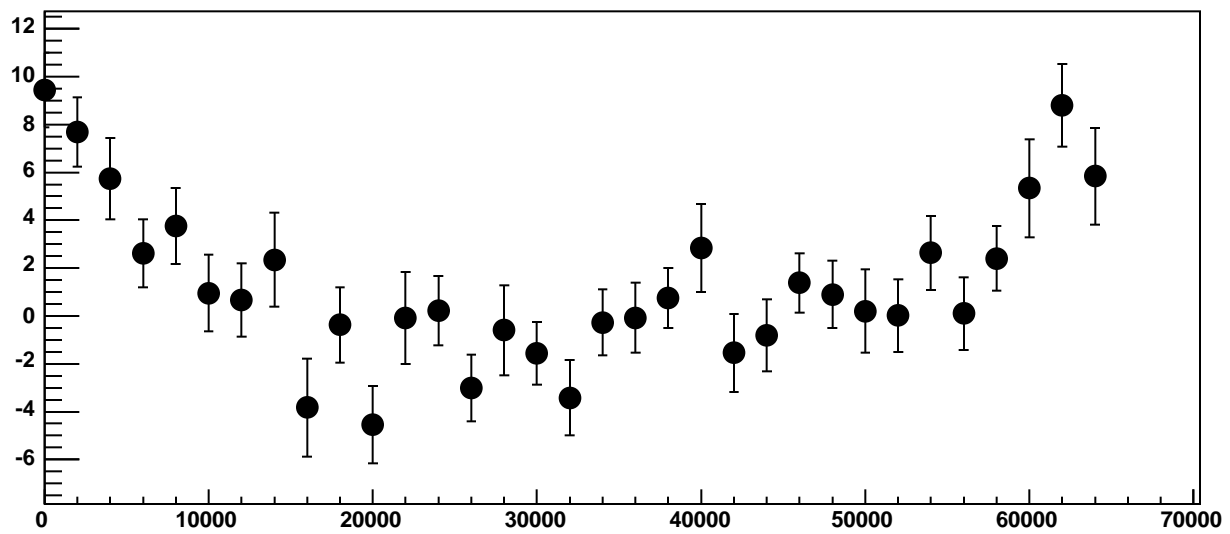


χ^2 / ndf 42 / 23
p0 11.51 ± 0.7331
p1 $0.002849 \pm 2.15e-05$

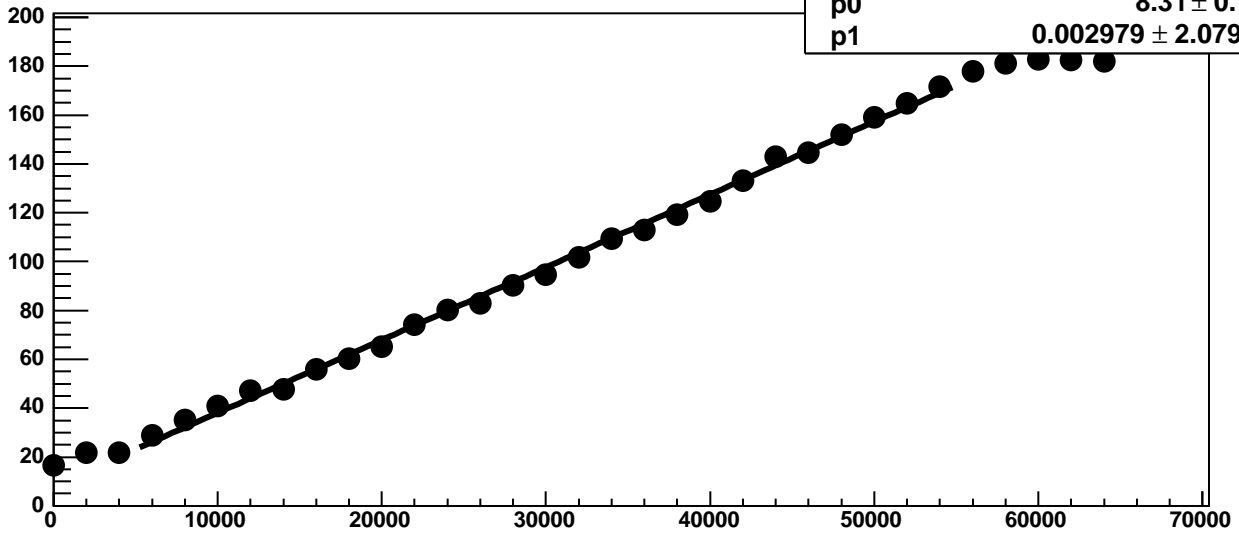
Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.4 / 23

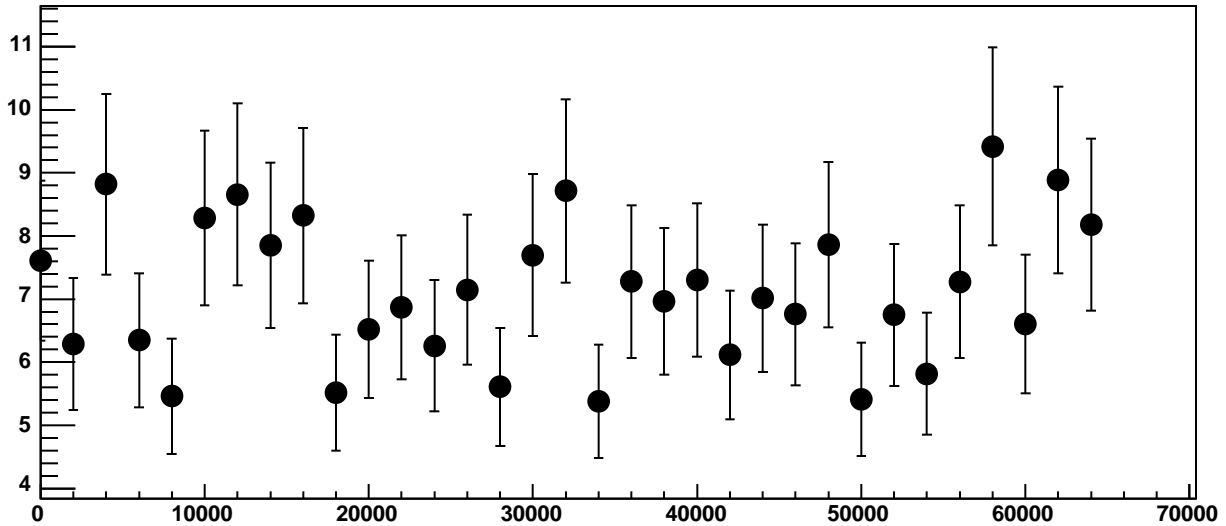
p0

8.31 ± 0.7055

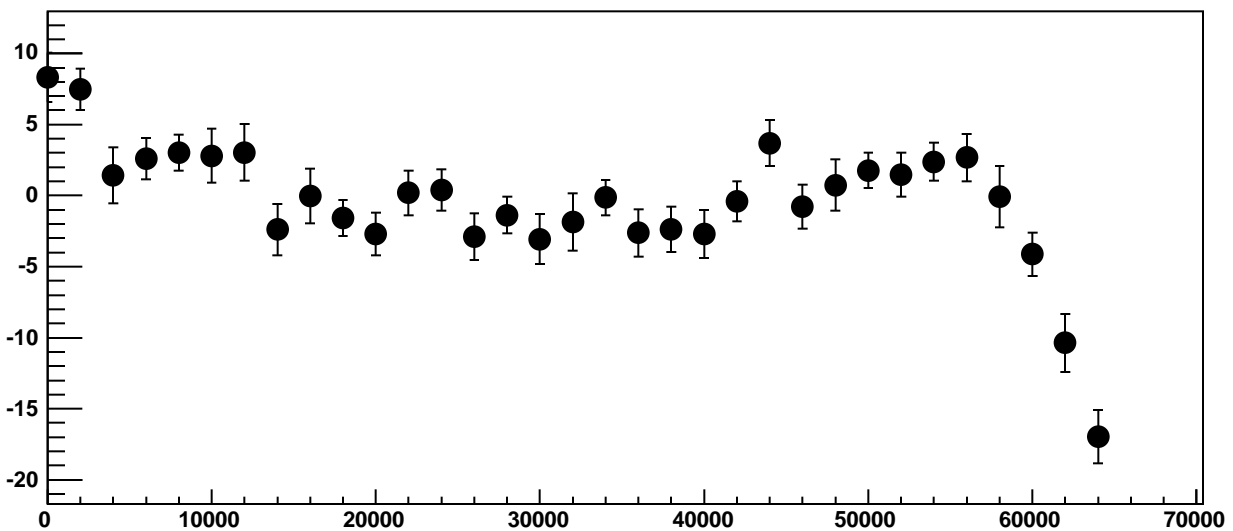
p1

$0.002979 \pm 2.079e-05$

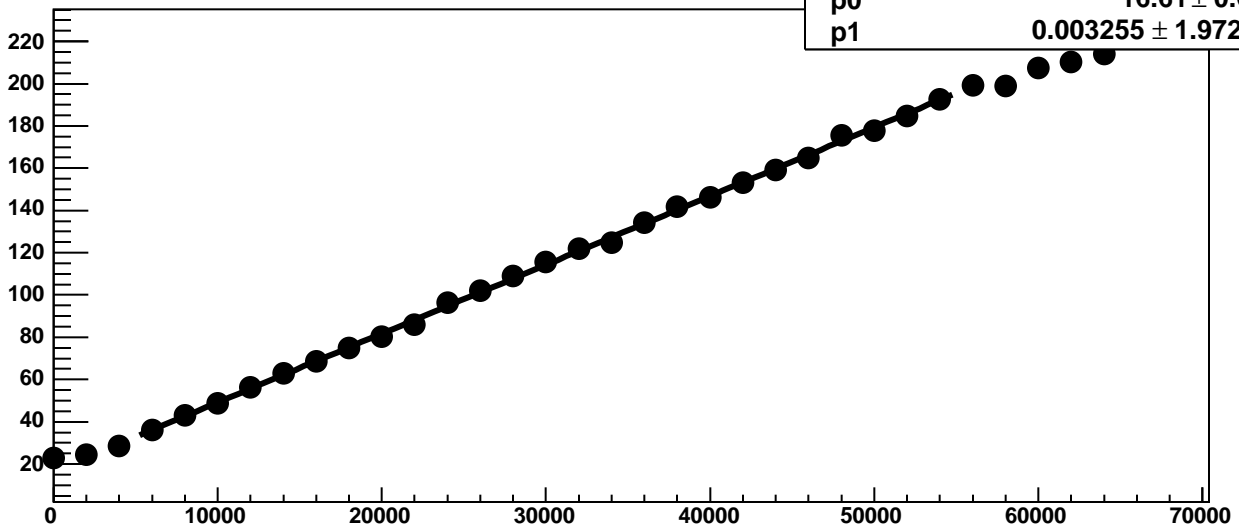
Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

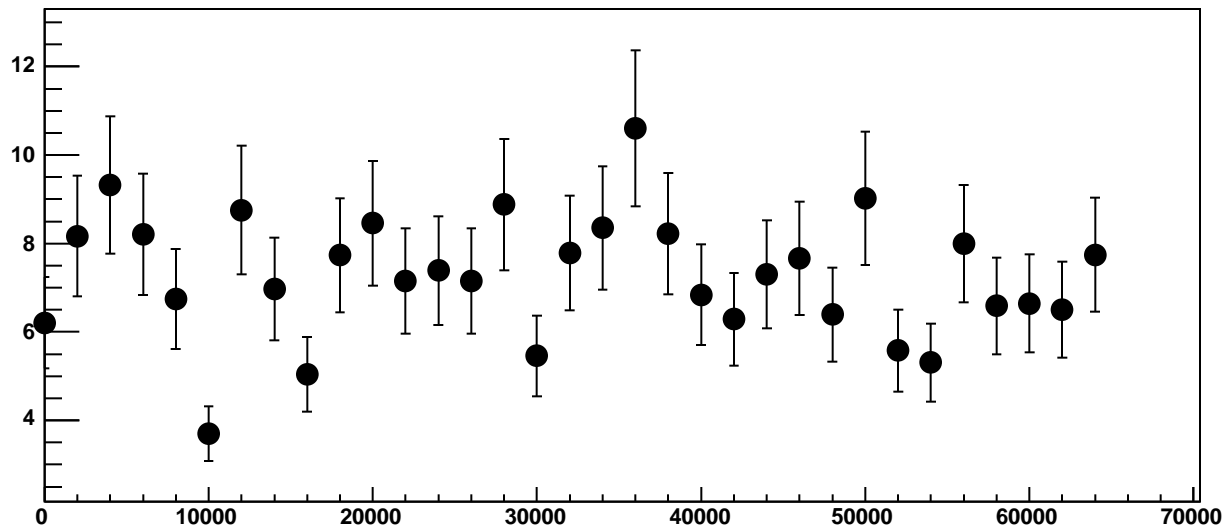


Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

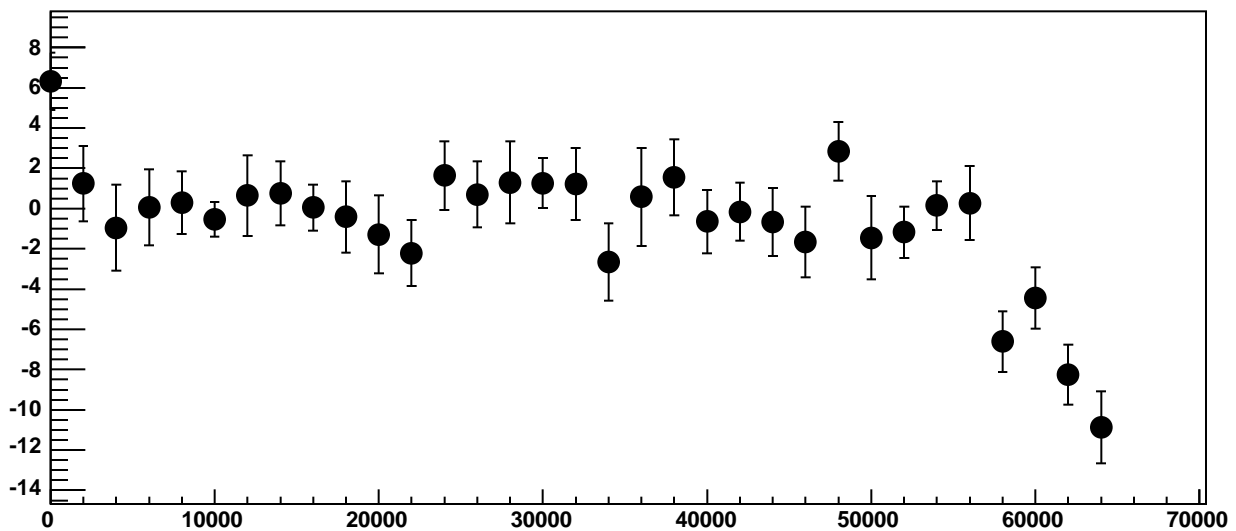


χ^2 / ndf 15.09 / 23
p0 16.61 ± 0.6488
p1 0.003255 ± 1.972e-05

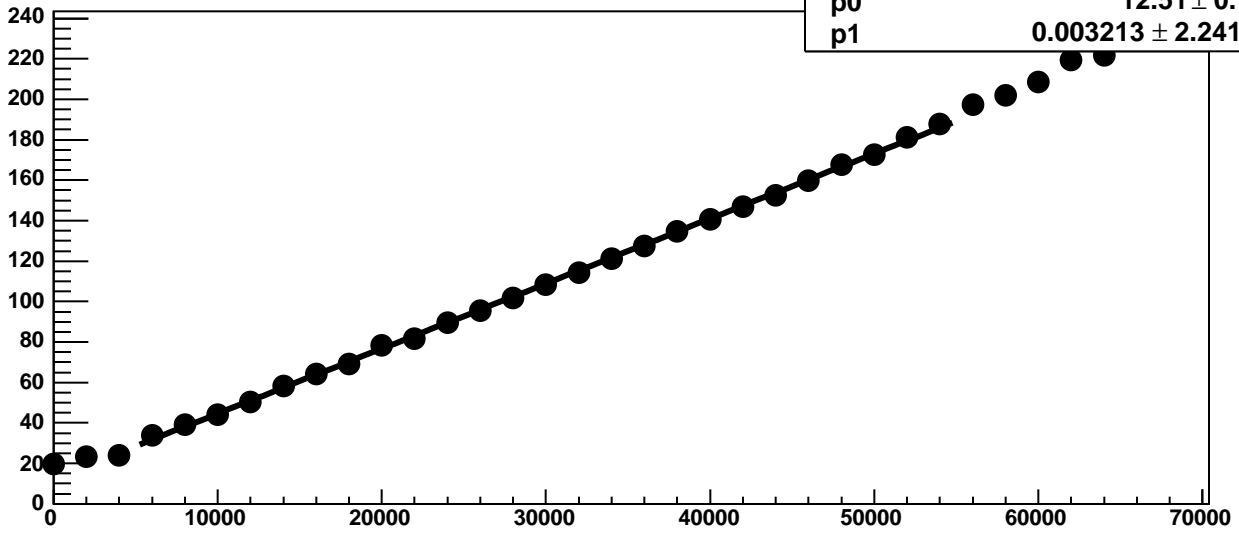
Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

10.01 / 23

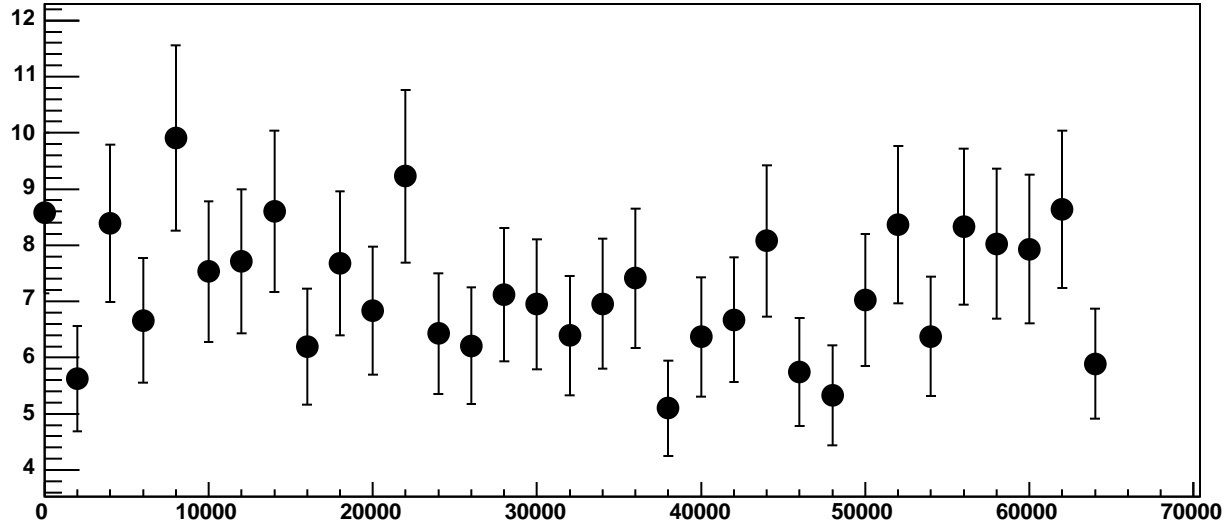
p0

12.51 ± 0.7763

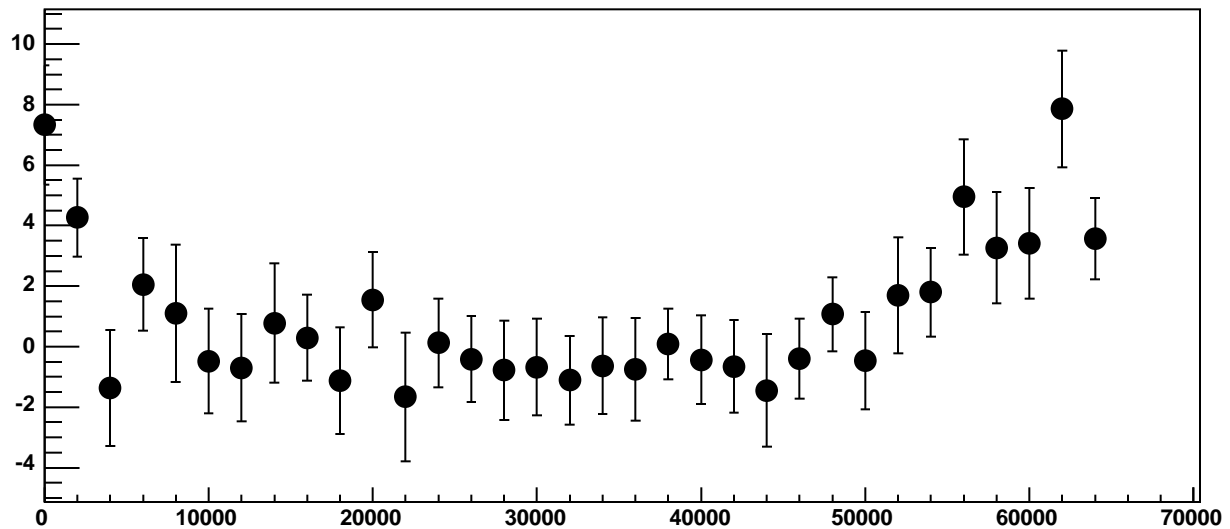
p1

$0.003213 \pm 2.241e-05$

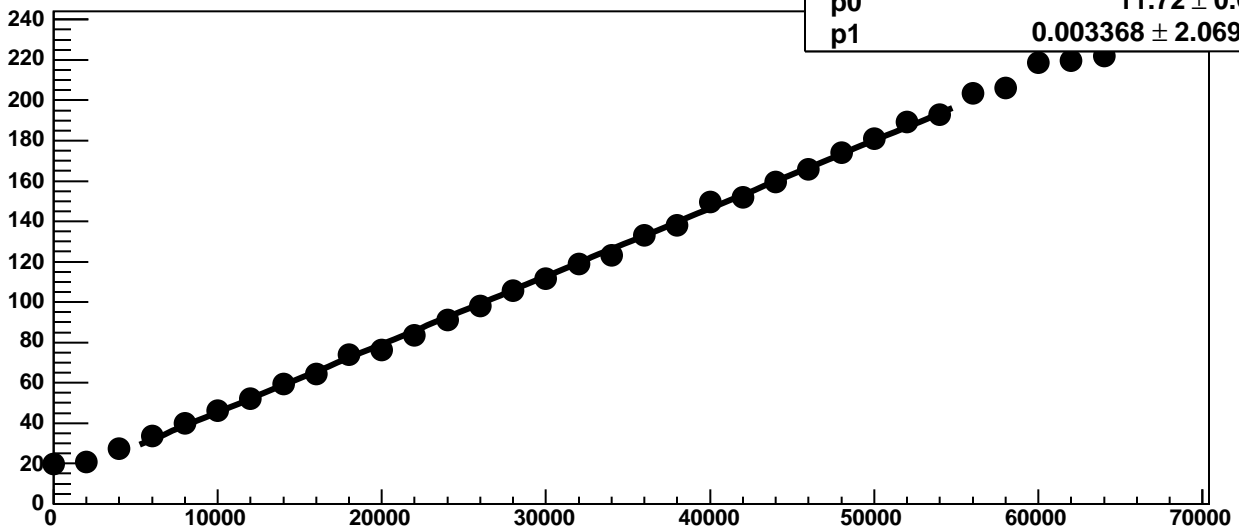
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

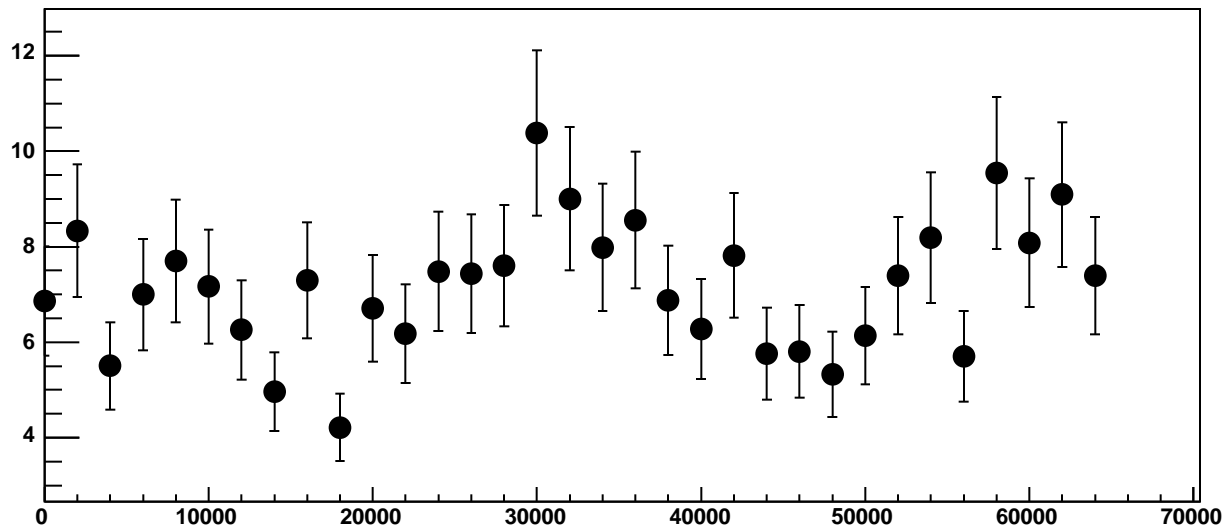


Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

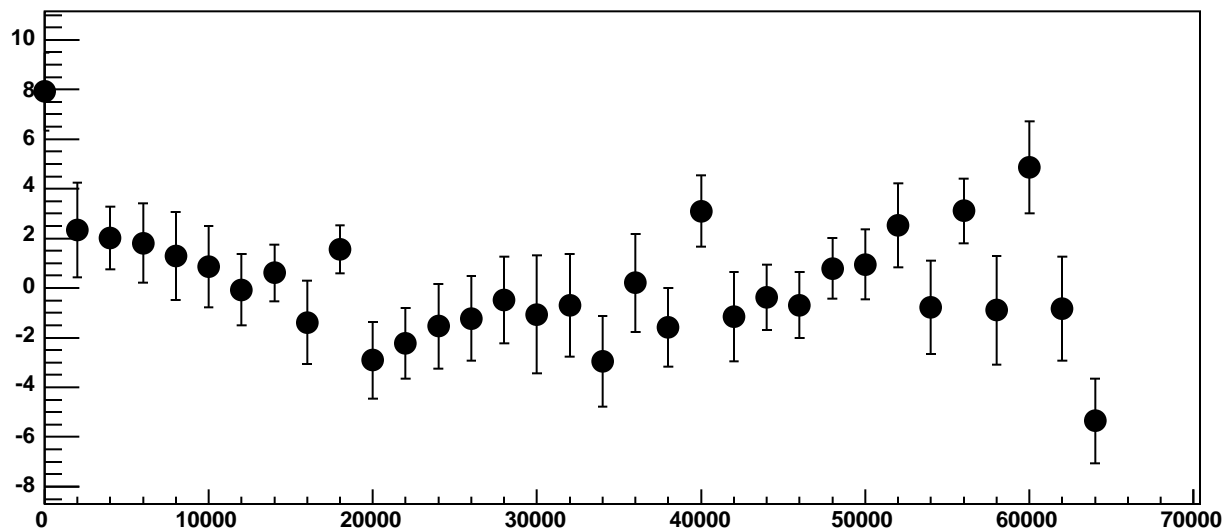


χ^2 / ndf 25.73 / 23
p0 11.72 ± 0.6804
p1 0.003368 ± 2.069e-05

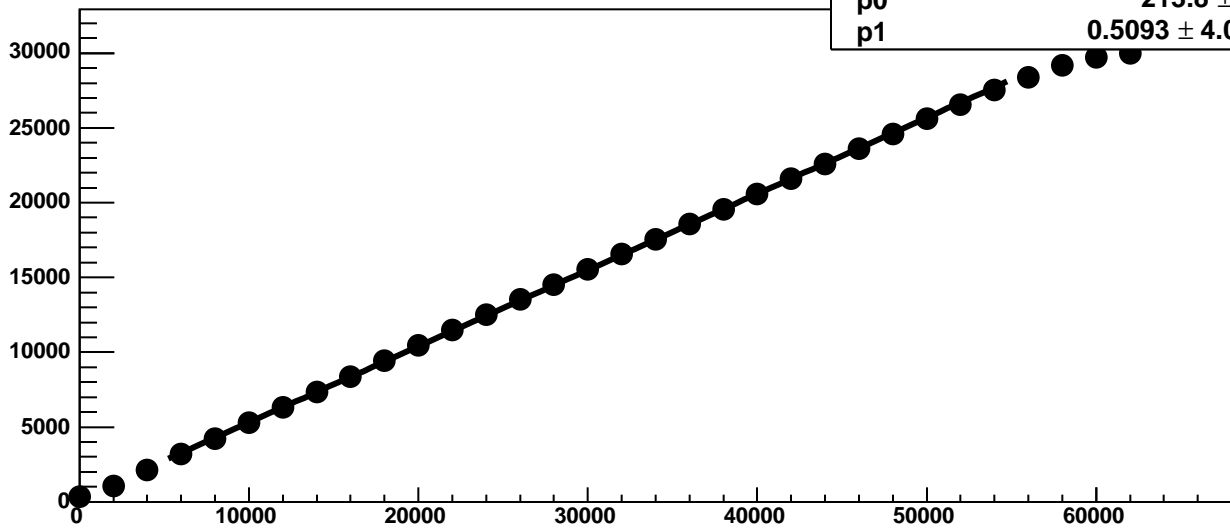
Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



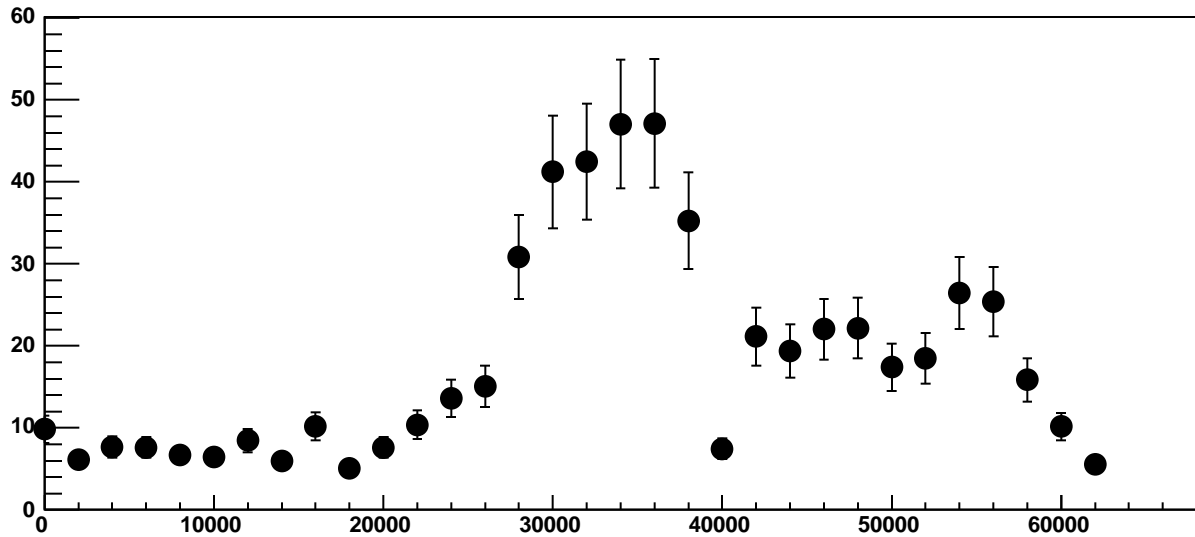
Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



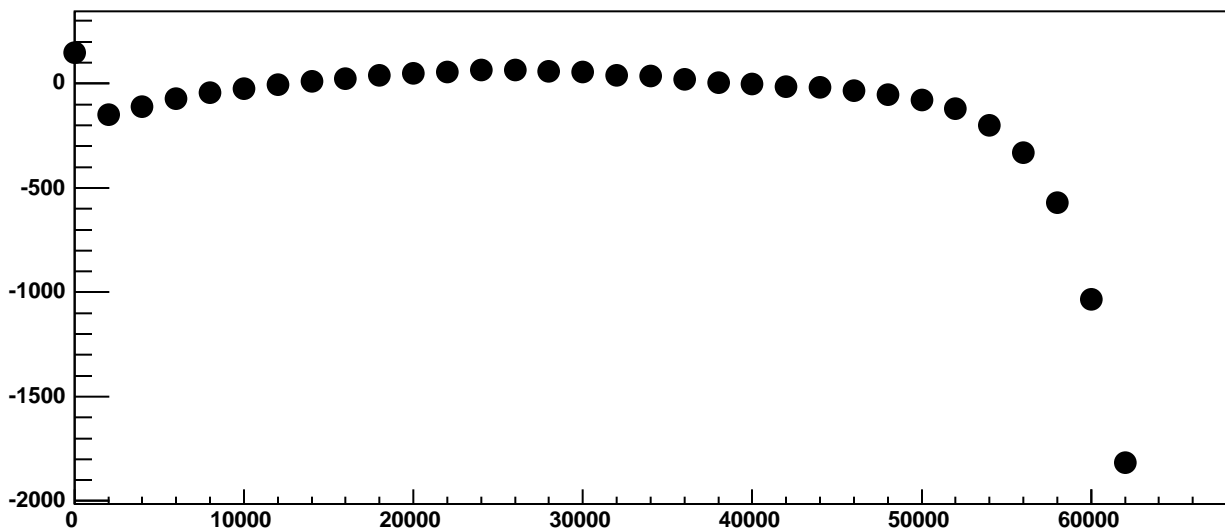
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



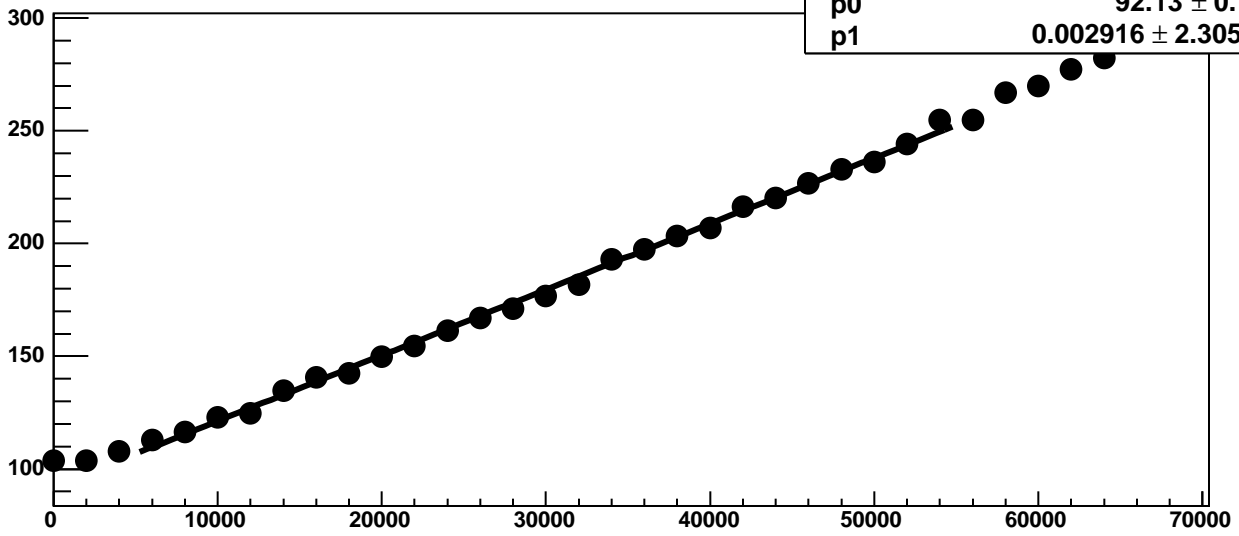
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

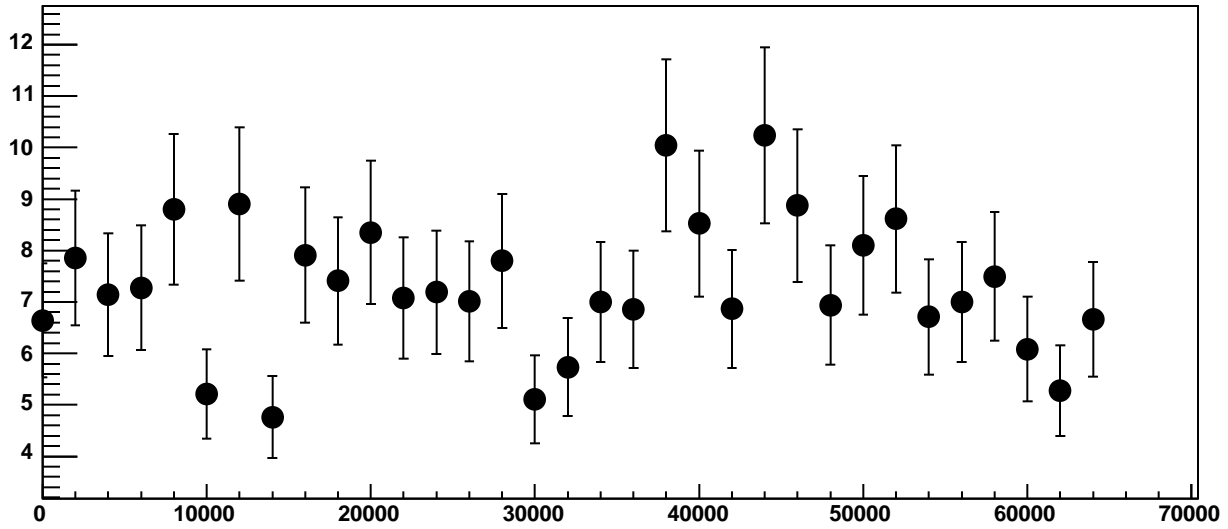


Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

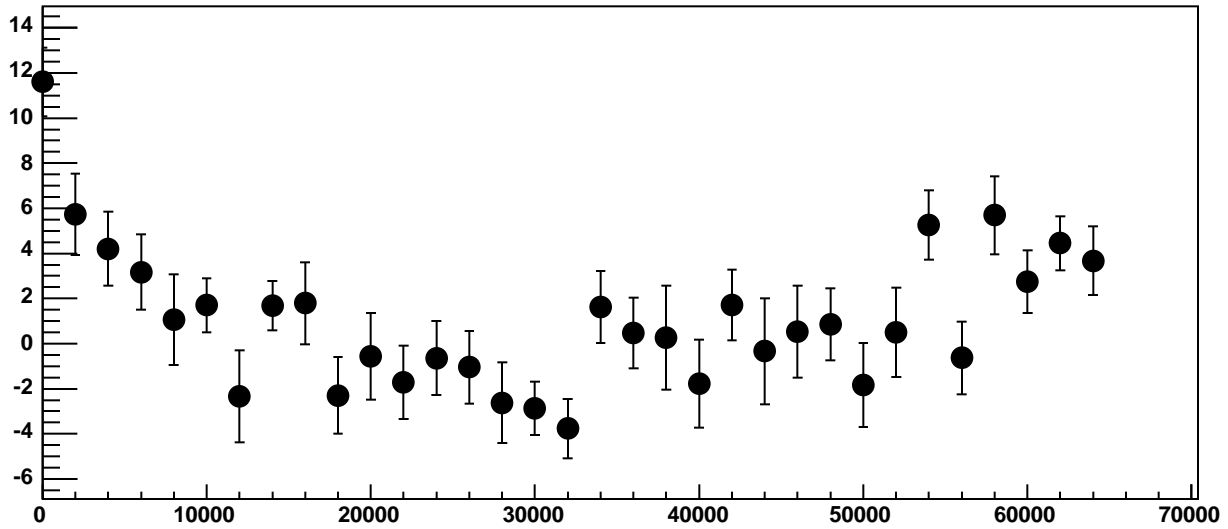


χ^2 / ndf 46.61 / 23
p0 92.13 ± 0.7308
p1 0.002916 ± 2.305e-05

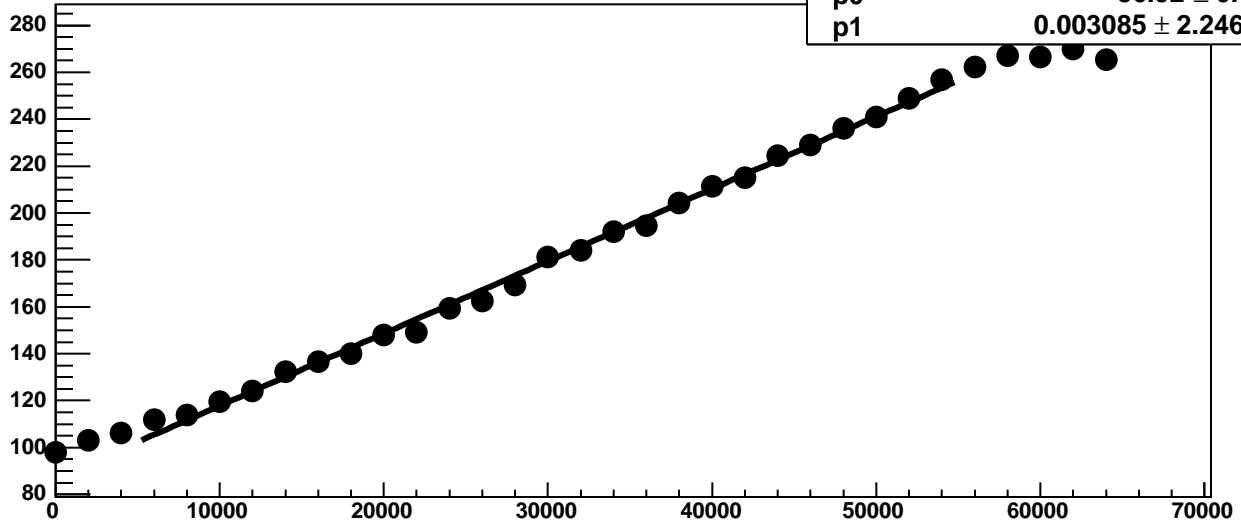
Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

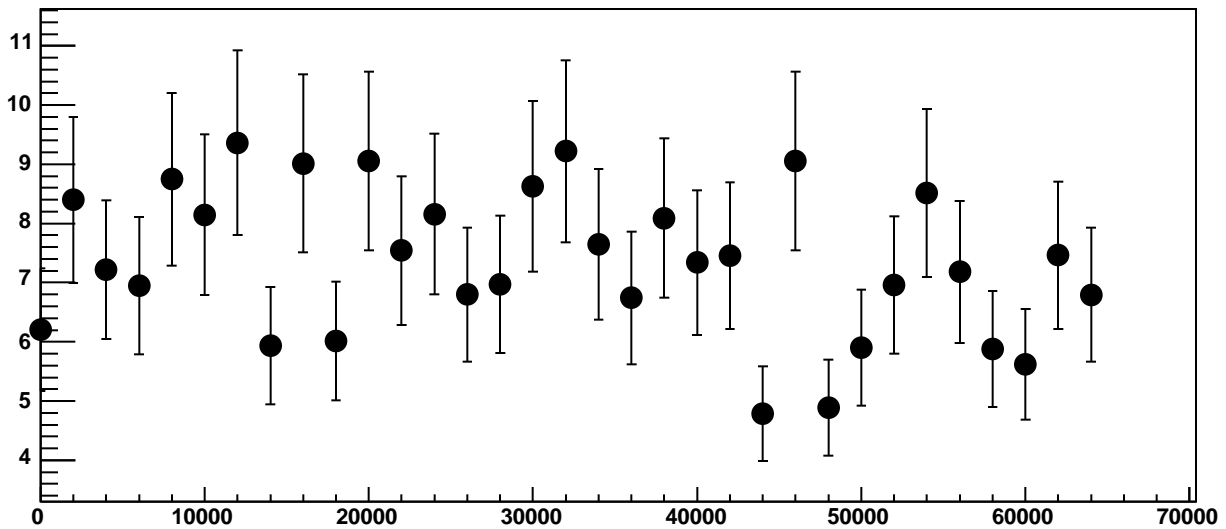


Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

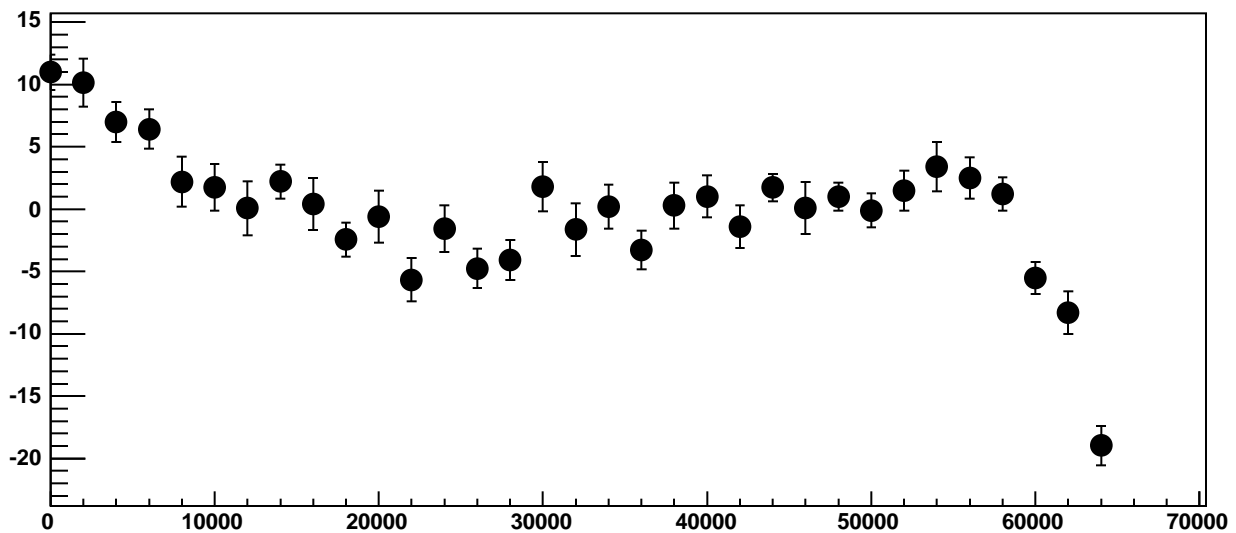


χ^2 / ndf 65.63 / 23
p0 86.92 ± 0.7875
p1 $0.003085 \pm 2.246e-05$

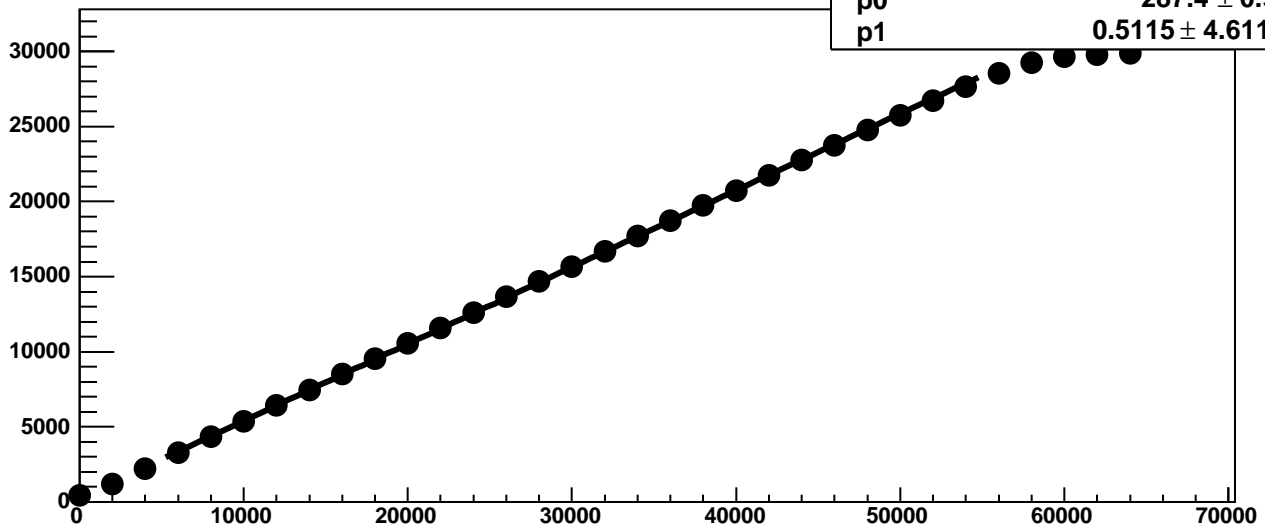
Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



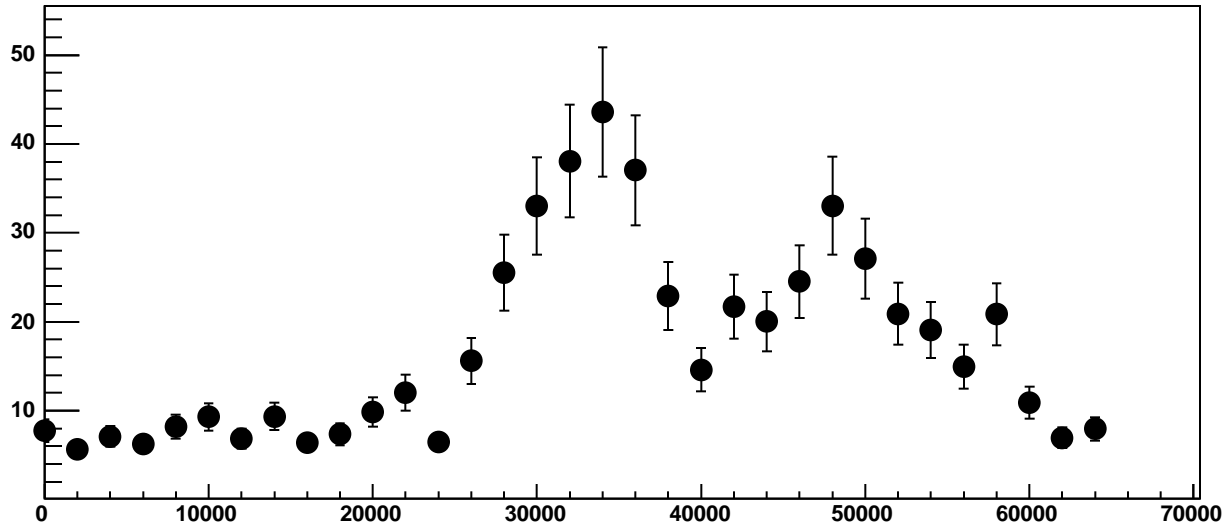
Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



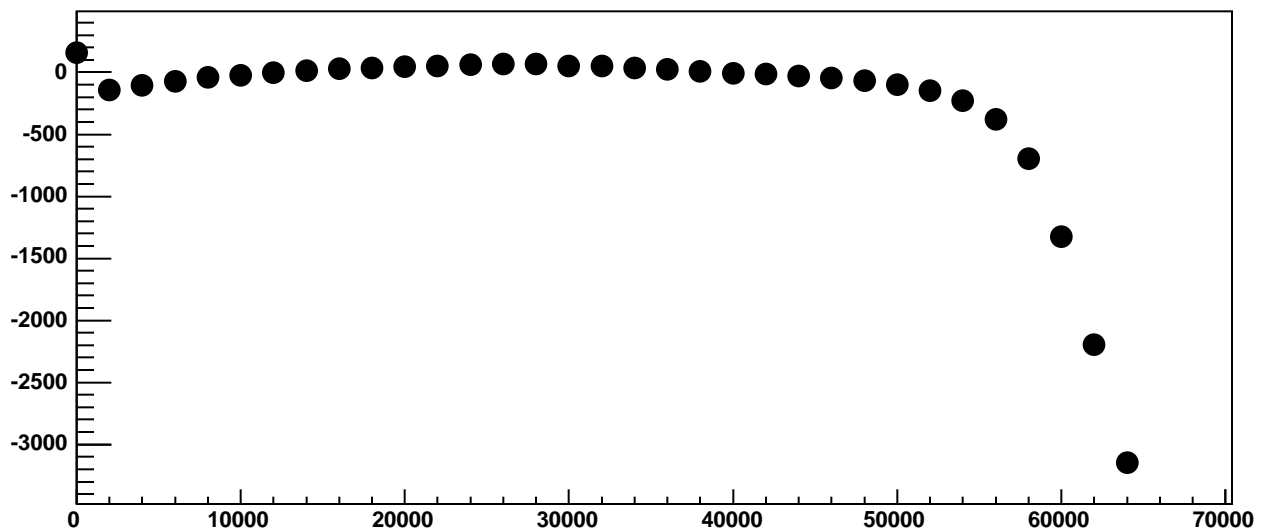
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



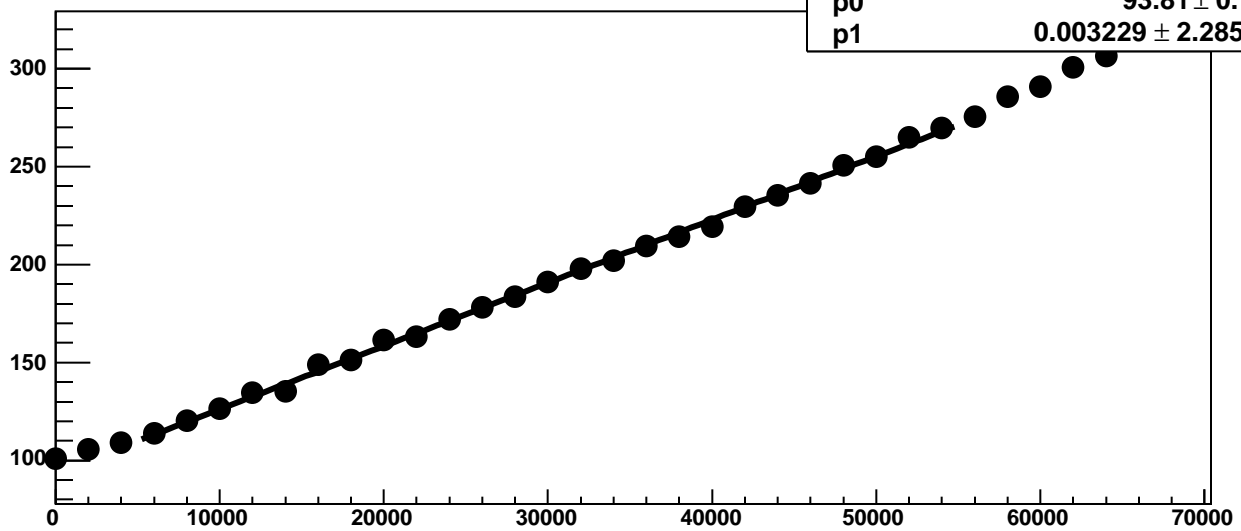
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

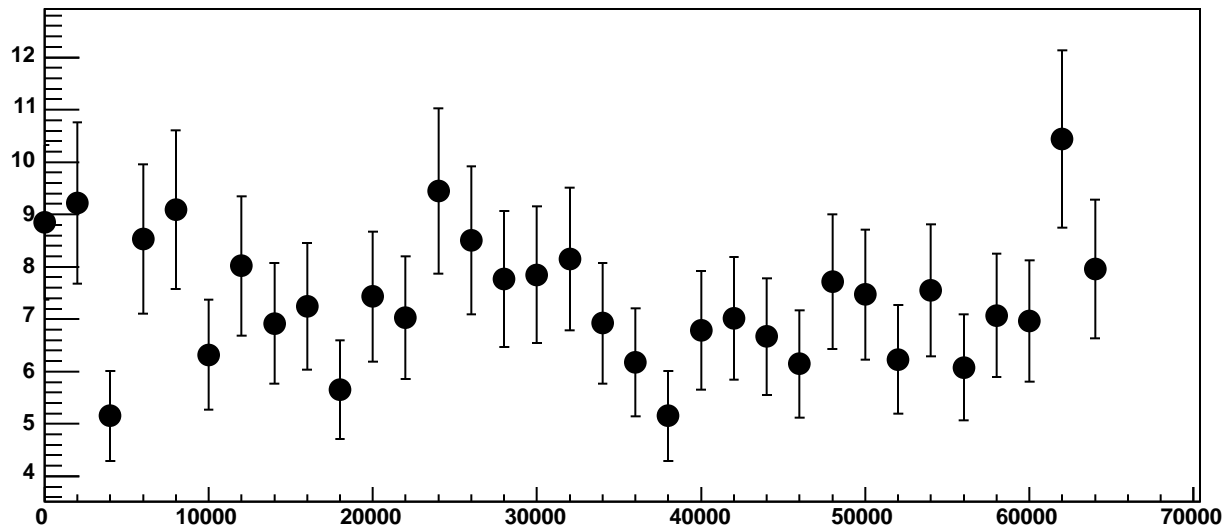


Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

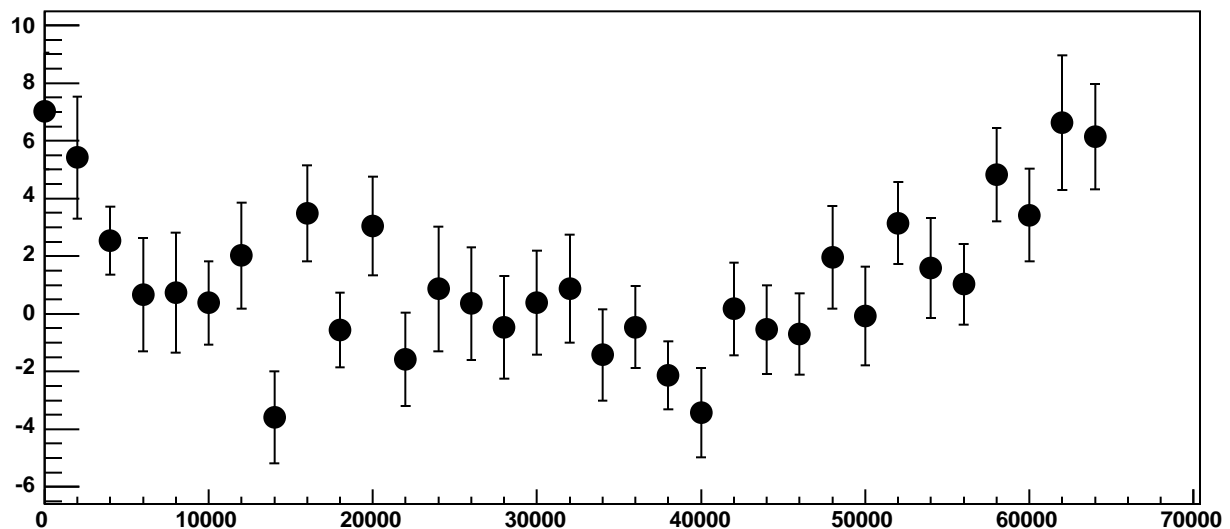


χ^2 / ndf 32.14 / 23
p0 93.81 ± 0.7815
p1 0.003229 ± 2.285e-05

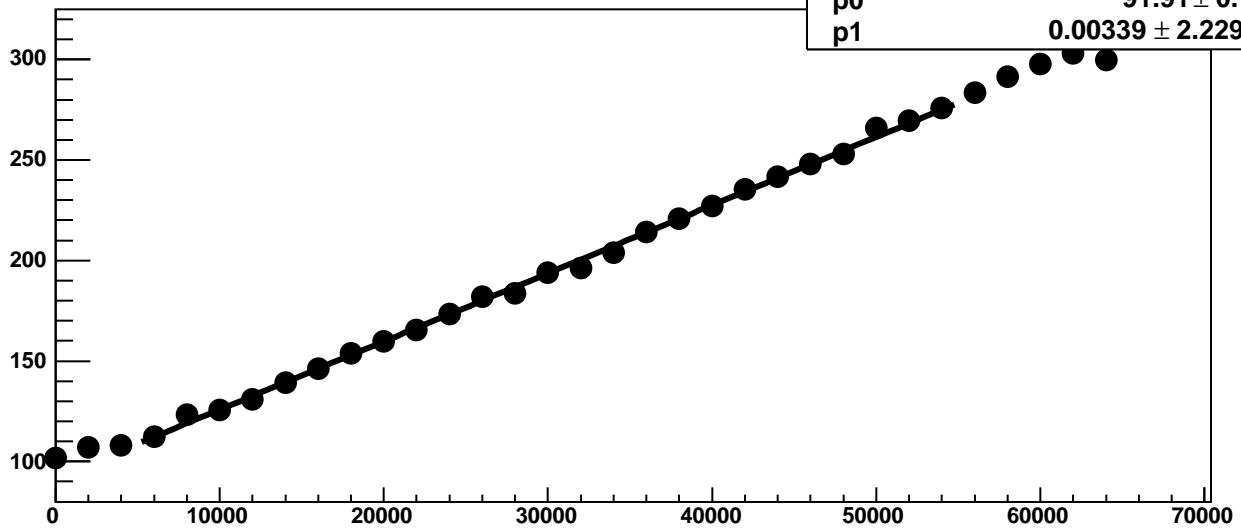
Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.1 / 23

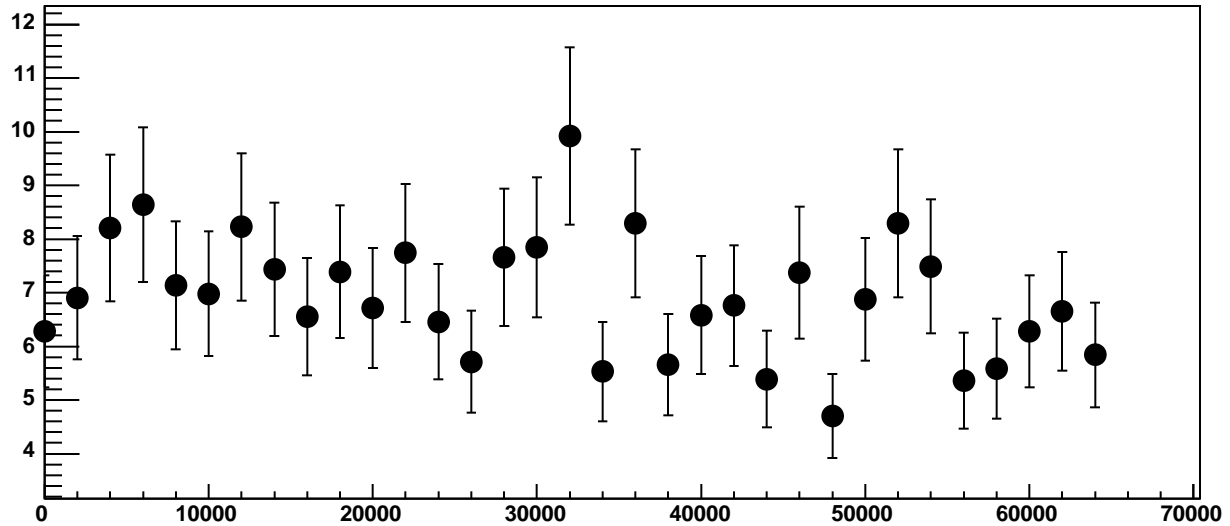
p0

91.91 ± 0.7688

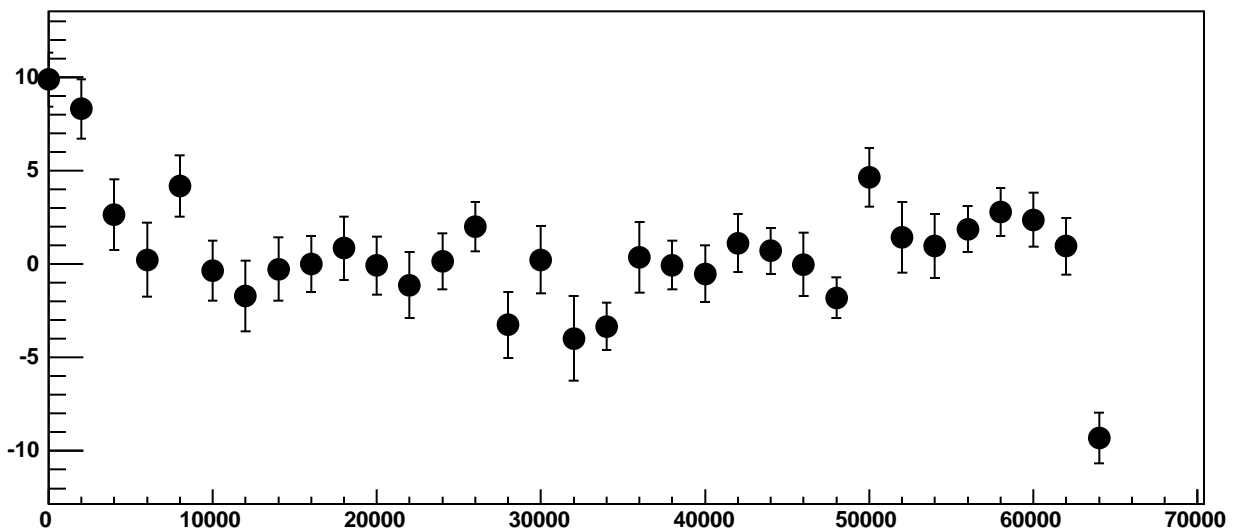
p1

$0.00339 \pm 2.229\text{e-}05$

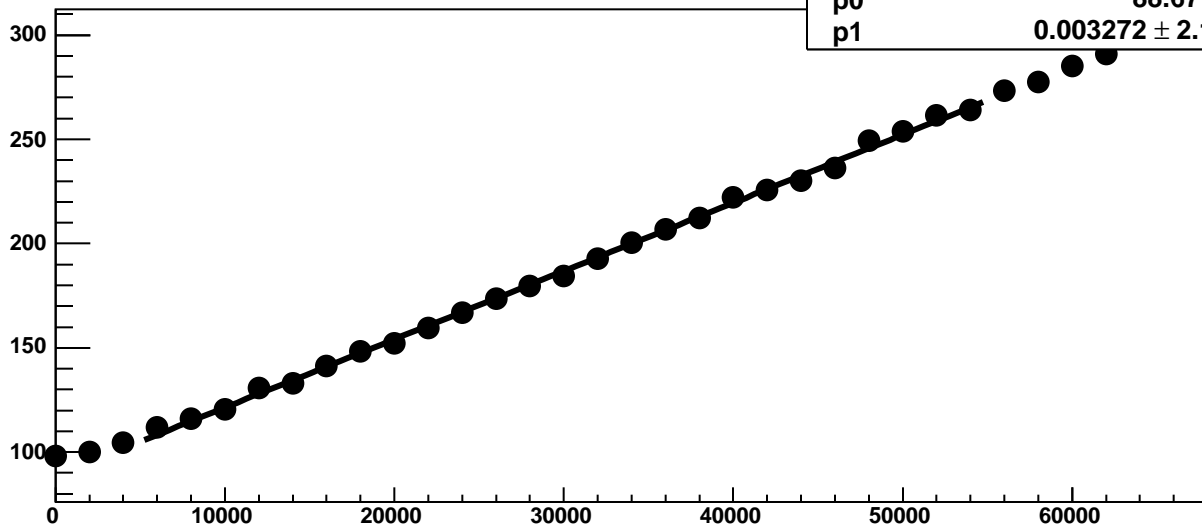
Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

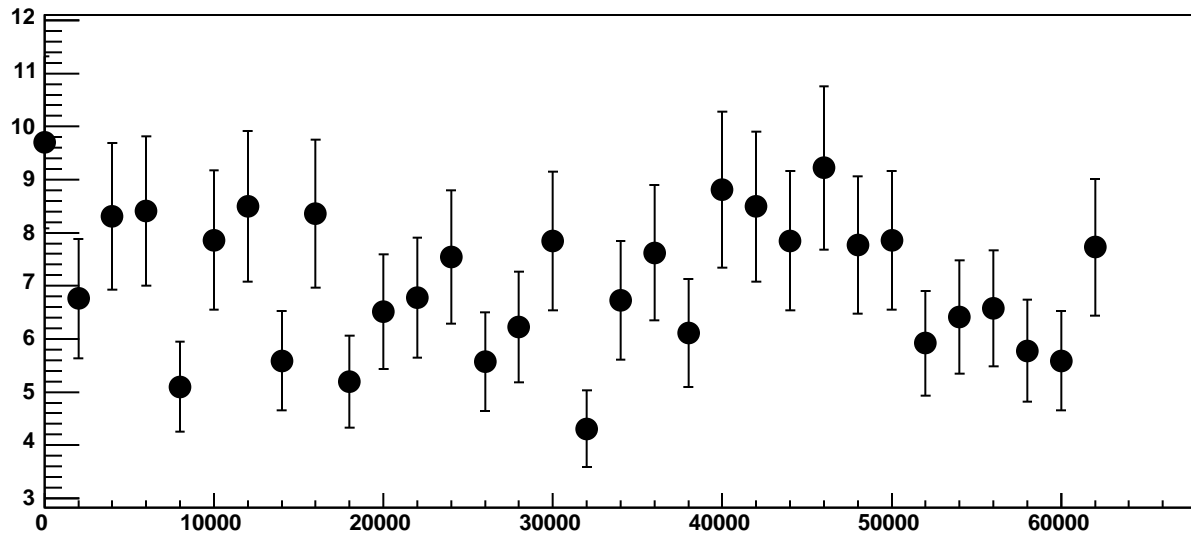


Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

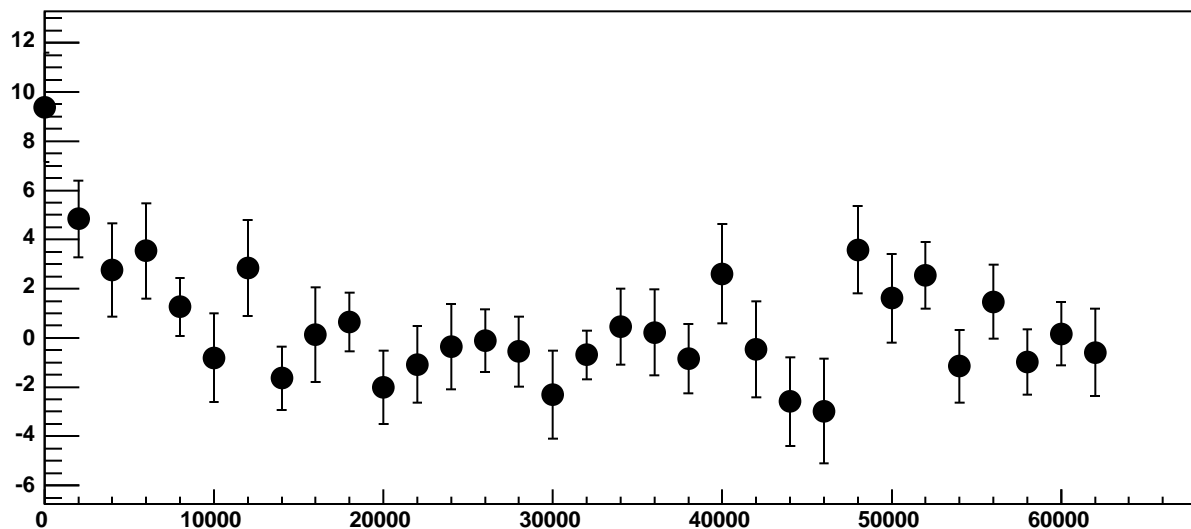


χ^2 / ndf 28.64 / 23
p0 88.67 ± 0.703
p1 0.003272 ± 2.182e-05

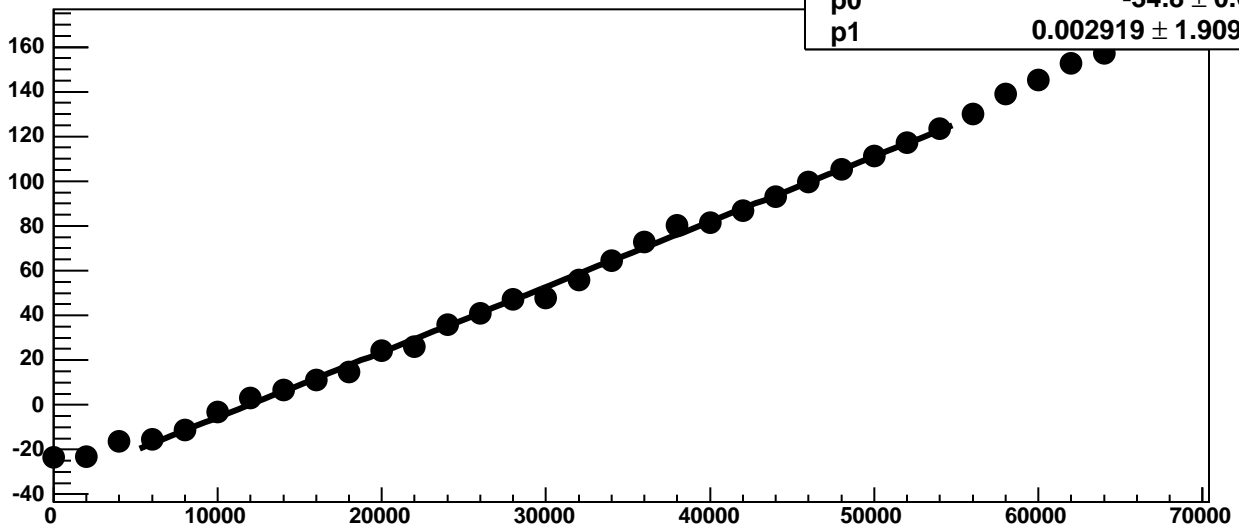
Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

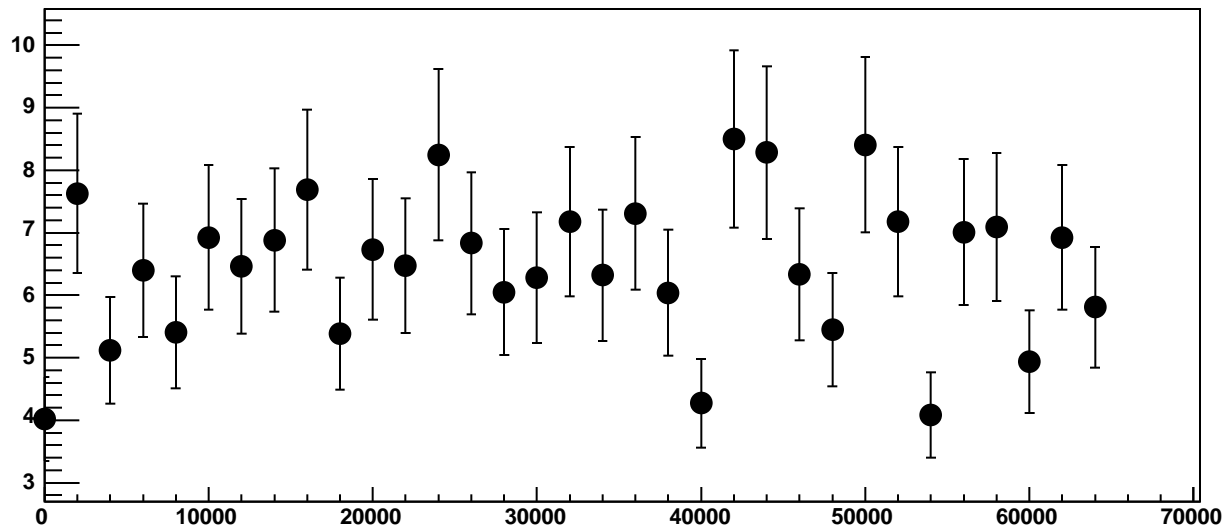


Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

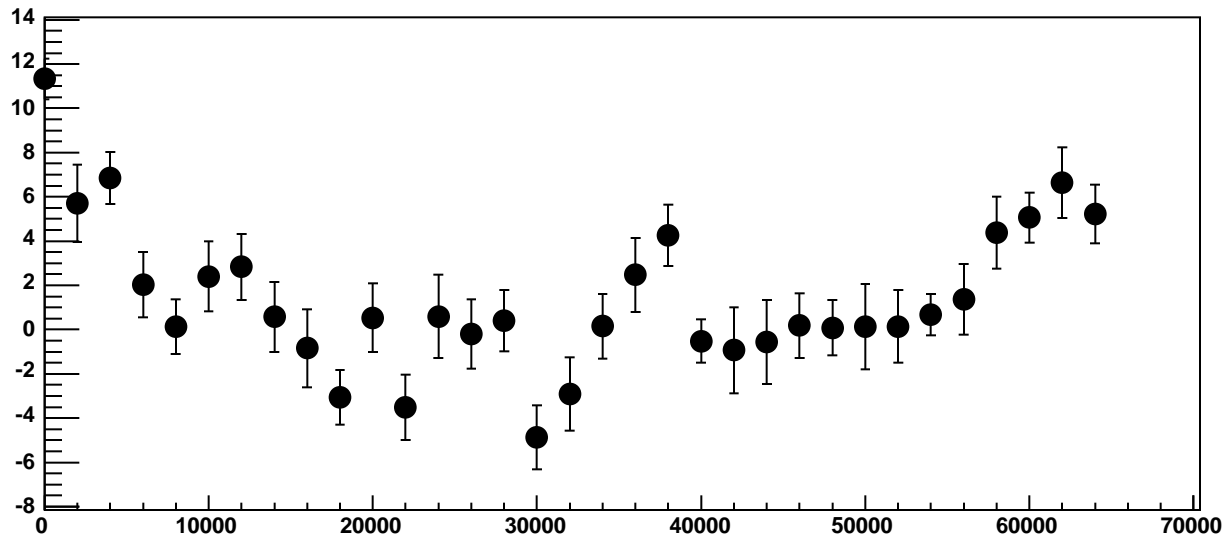


χ^2 / ndf 47.53 / 23
p0 -34.8 ± 0.6604
p1 $0.002919 \pm 1.909e-05$

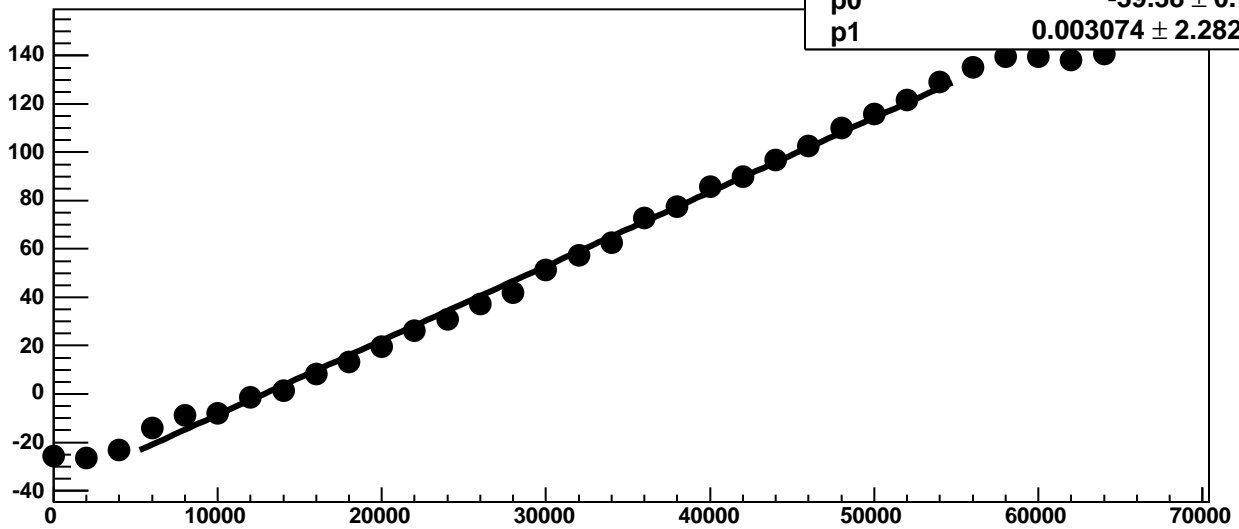
Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

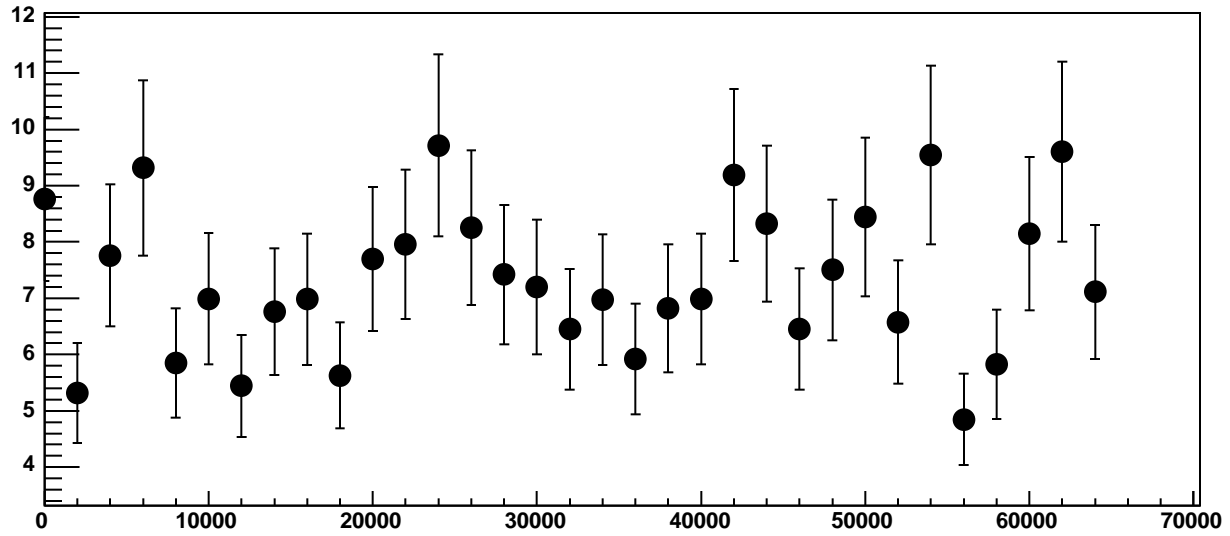


Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

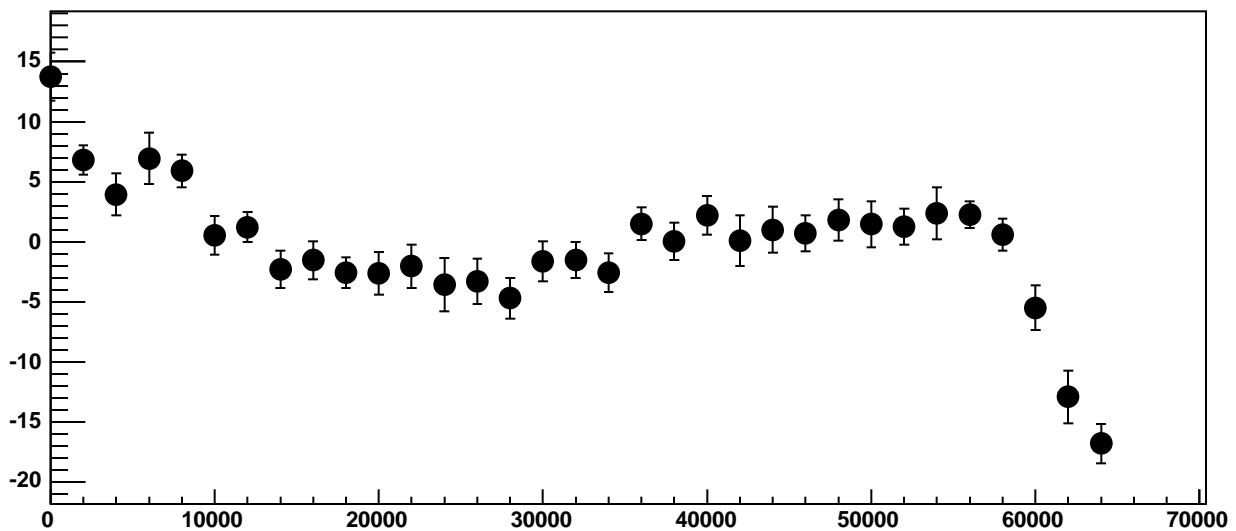


χ^2 / ndf 66.42 / 23
p0 -39.38 ± 0.7322
p1 $0.003074 \pm 2.282e-05$

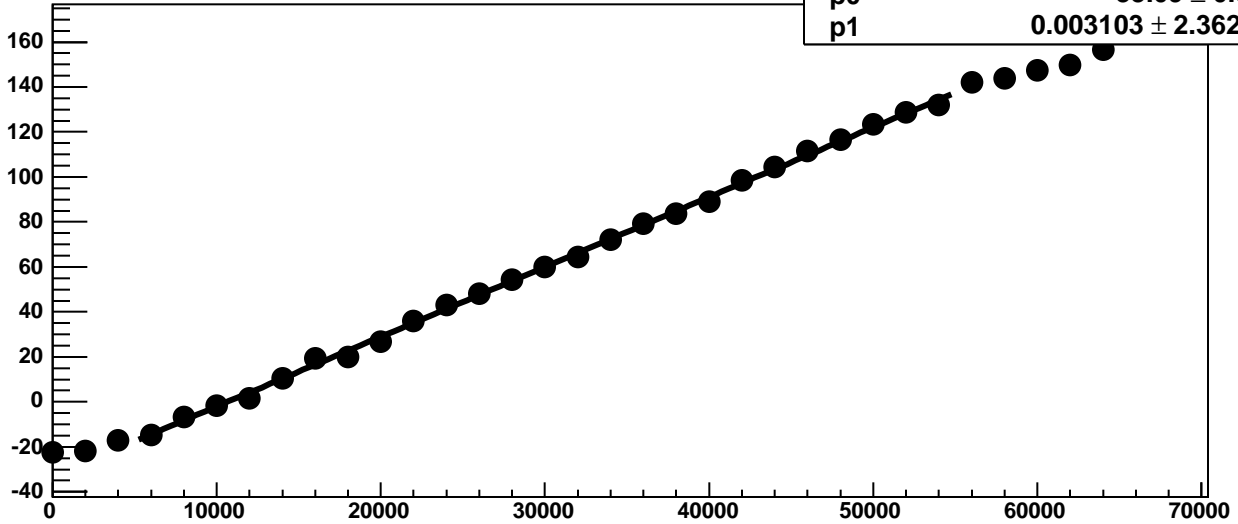
Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



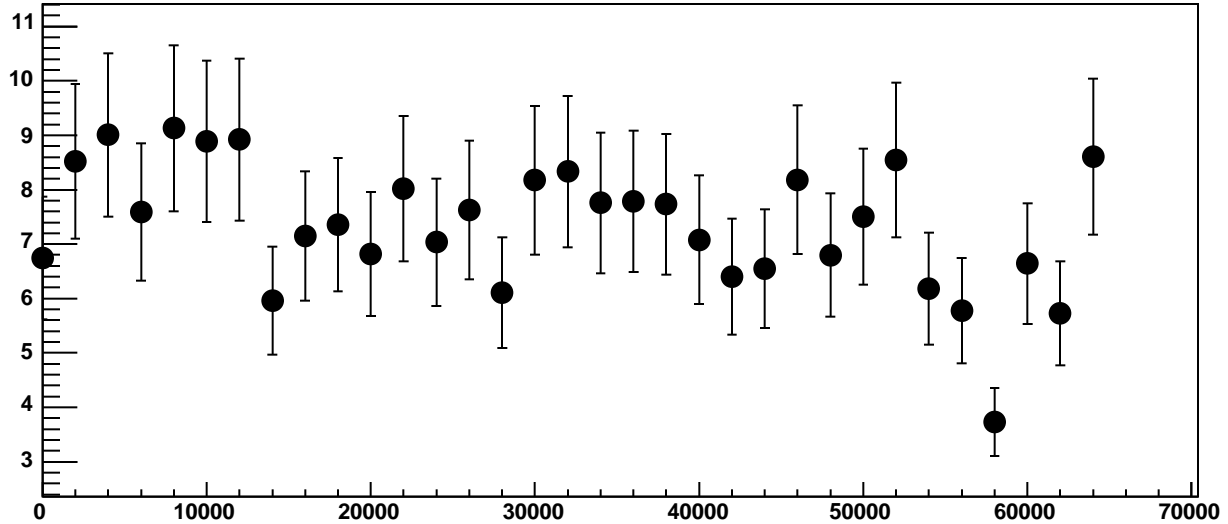
Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



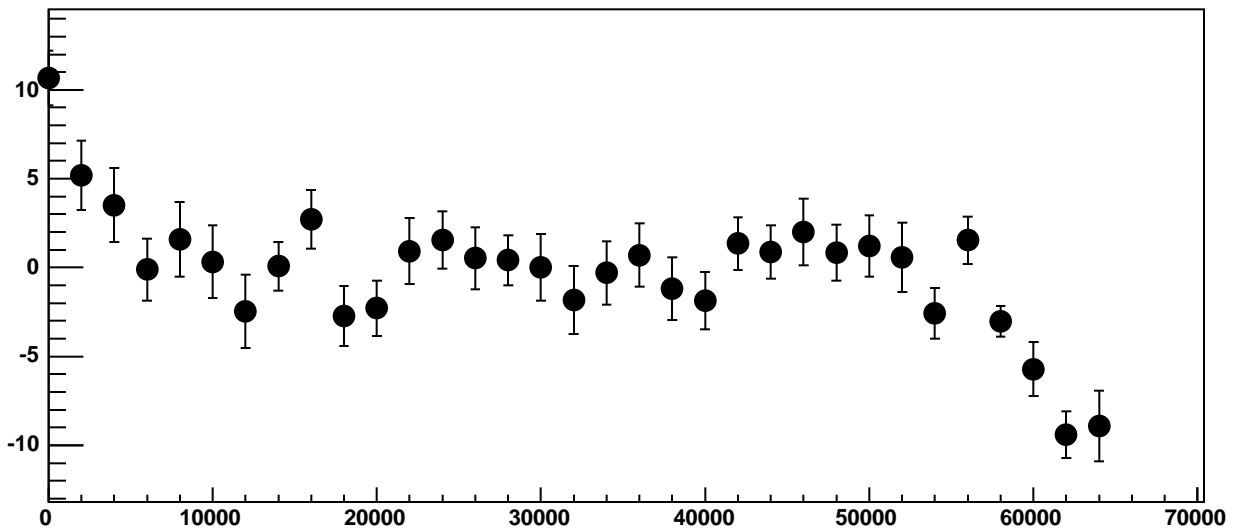
Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



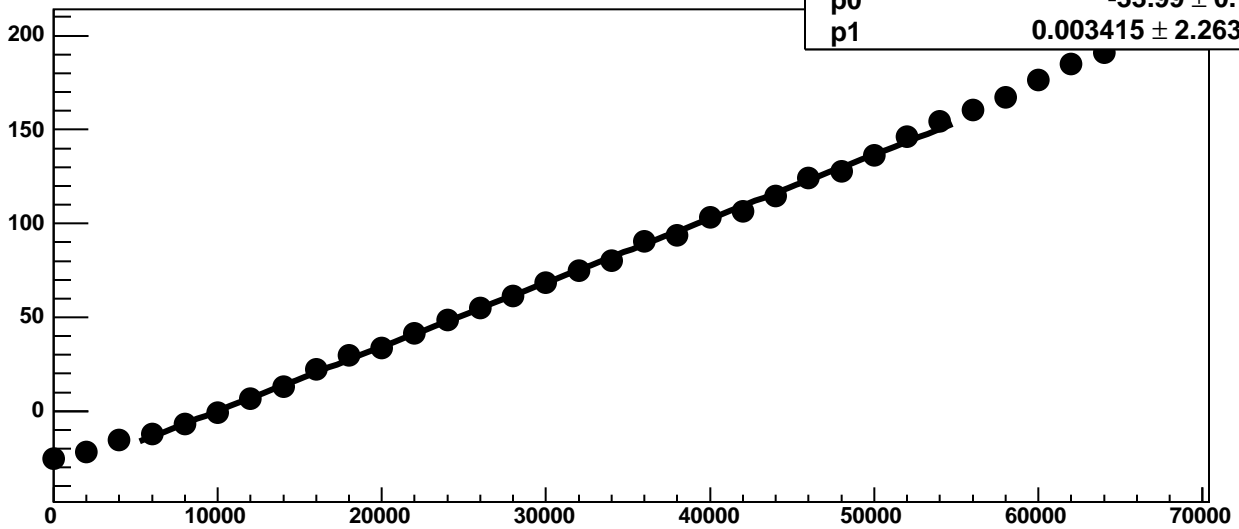
Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

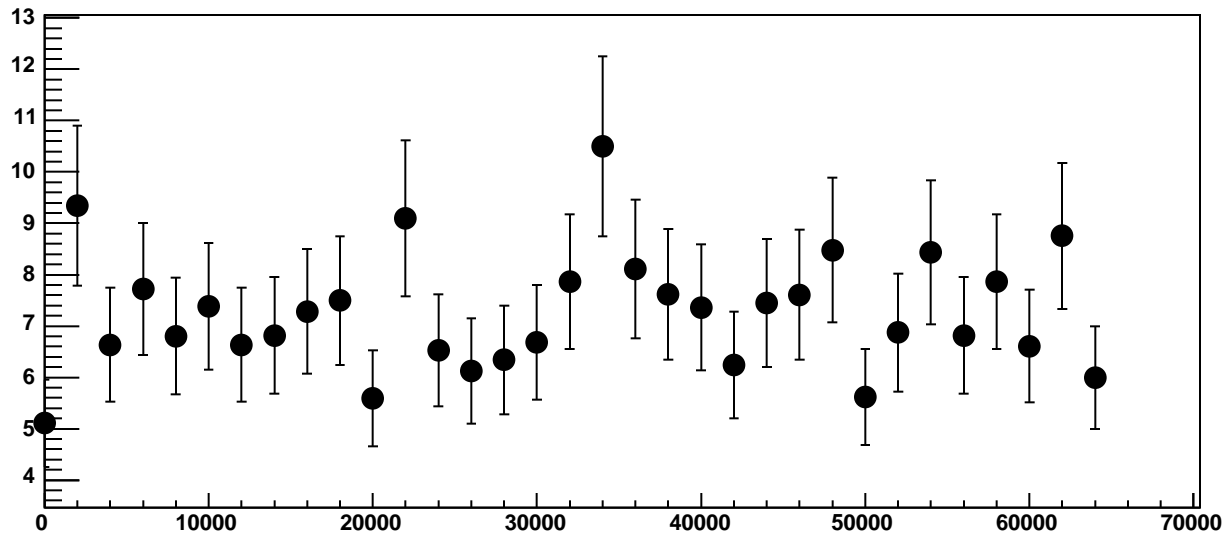


Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

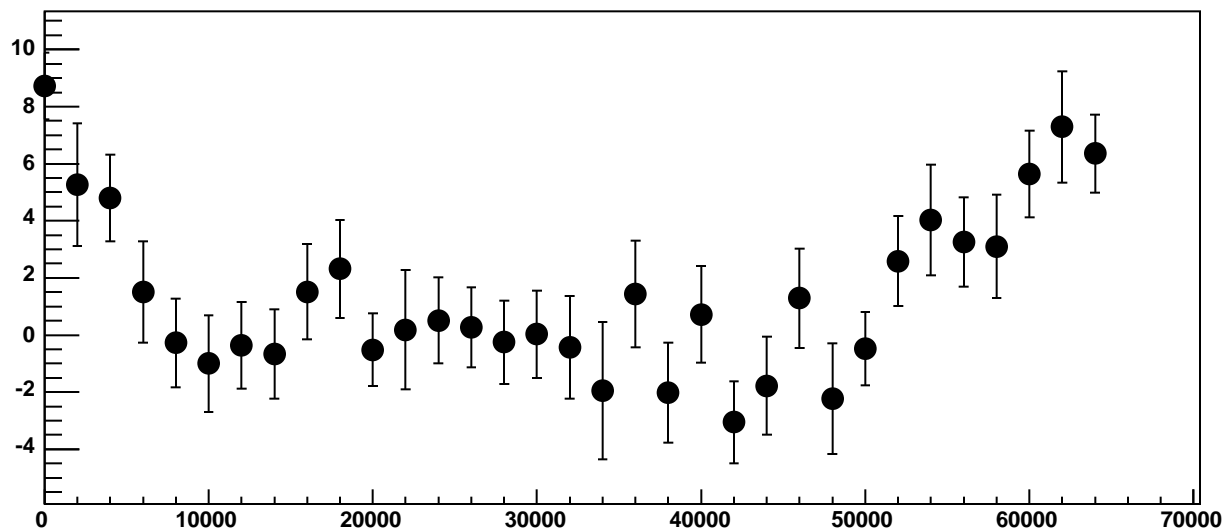


χ^2 / ndf 21.79 / 23
p0 -33.99 ± 0.7454
p1 $0.003415 \pm 2.263e-05$

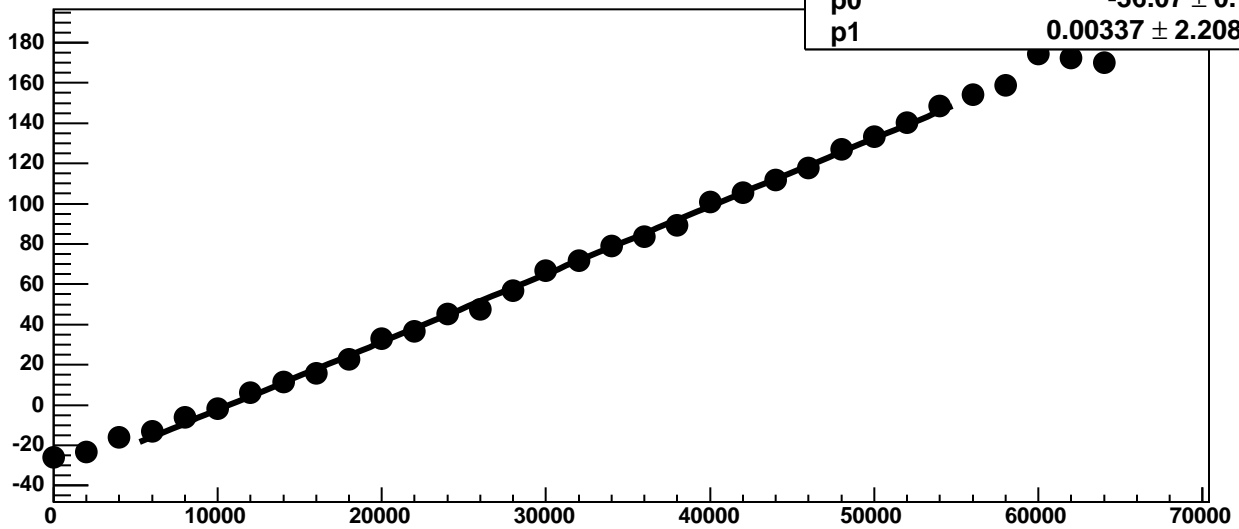
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

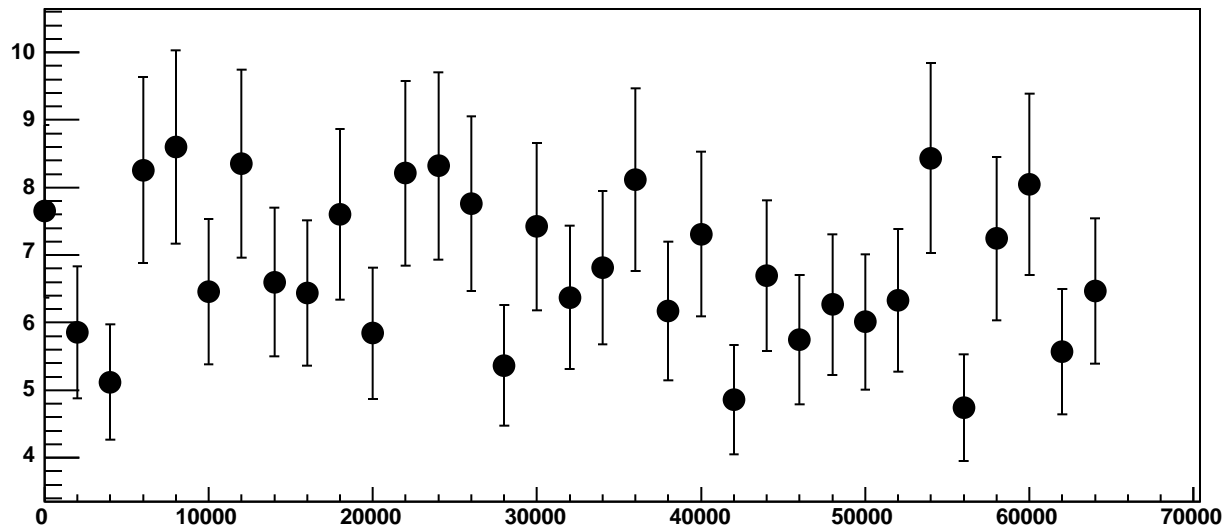


Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

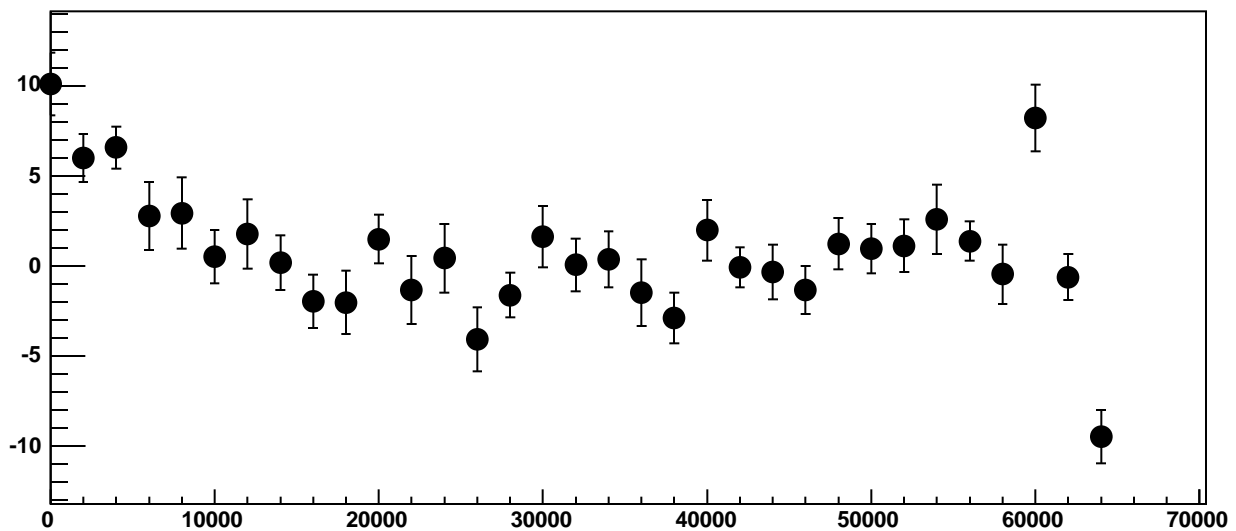


χ^2 / ndf 29.27 / 23
p0 -36.07 ± 0.7633
p1 $0.00337 \pm 2.208e-05$

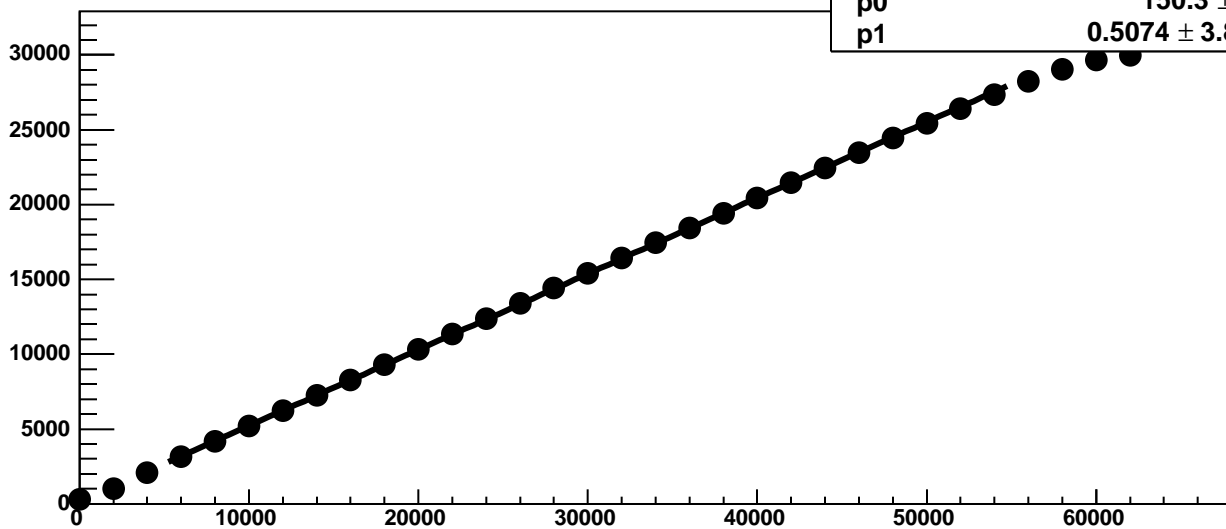
Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



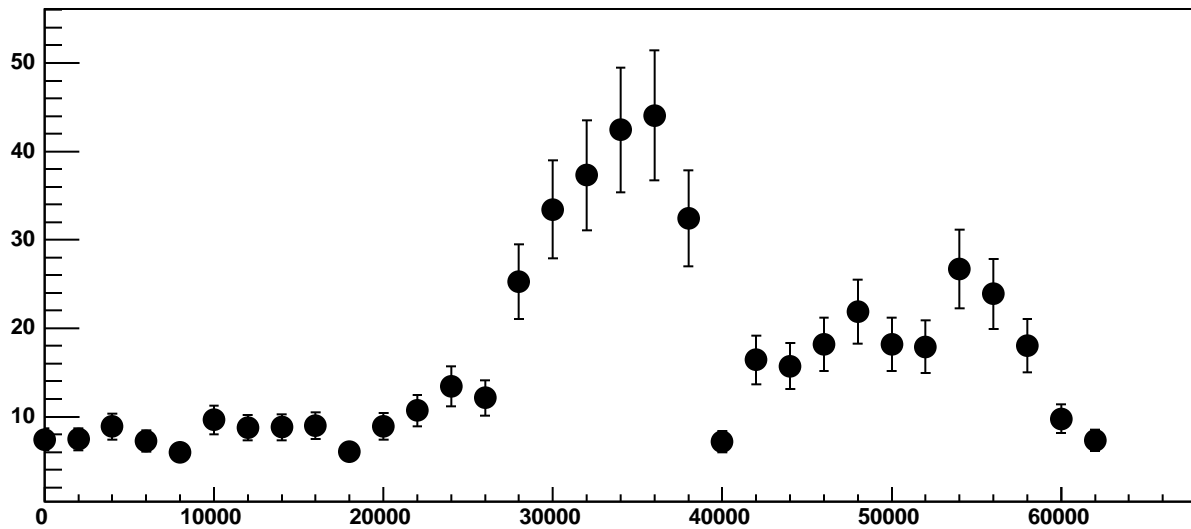
Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



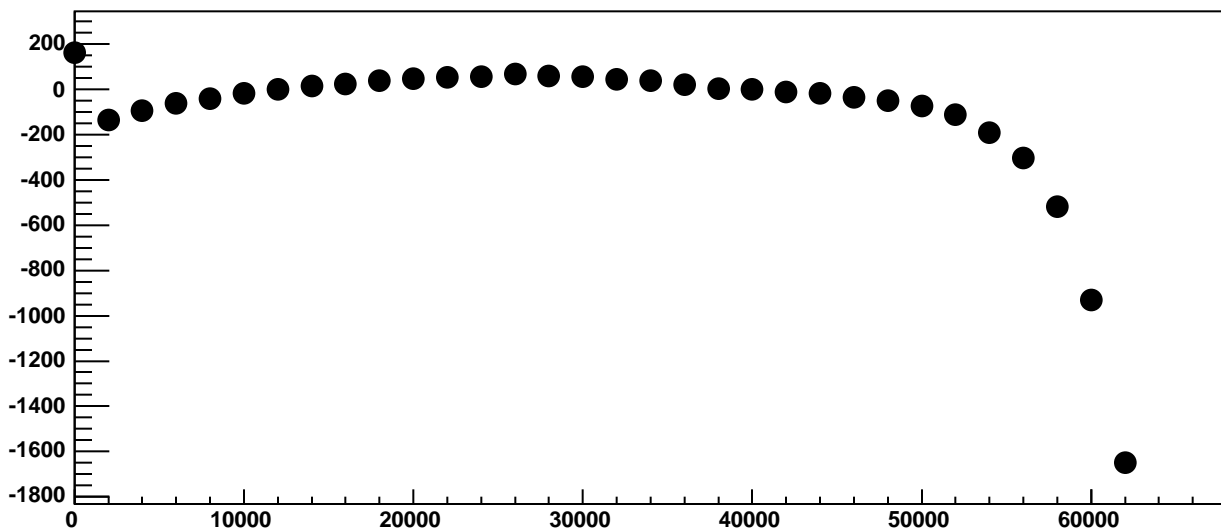
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



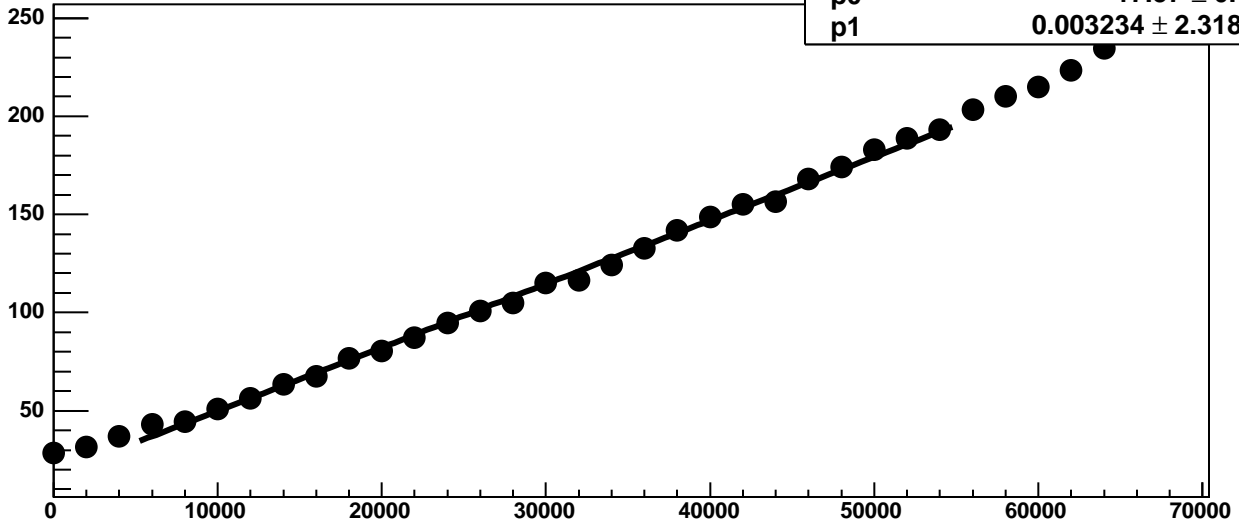
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

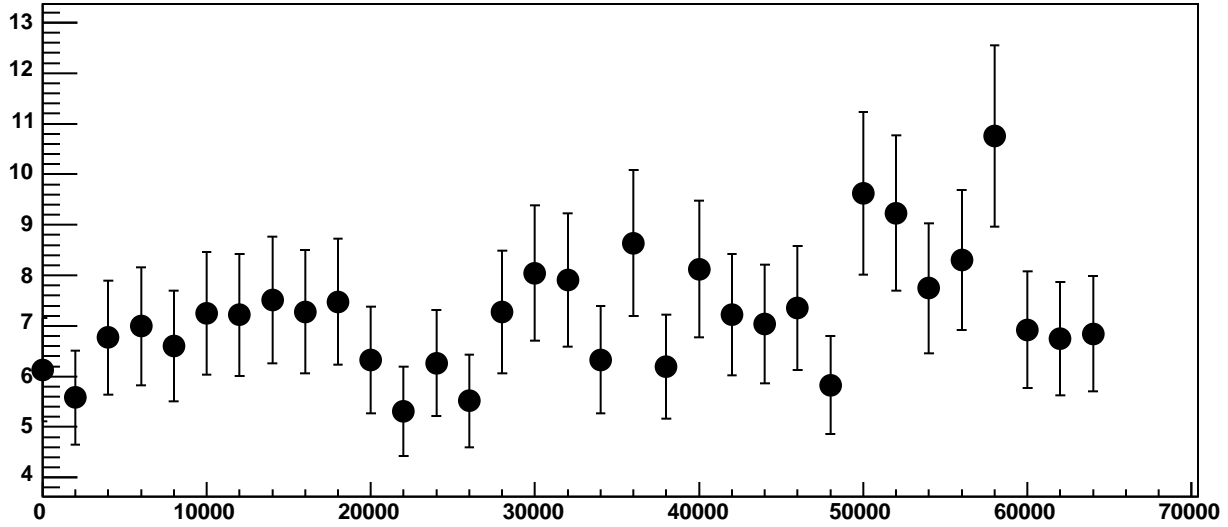


Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

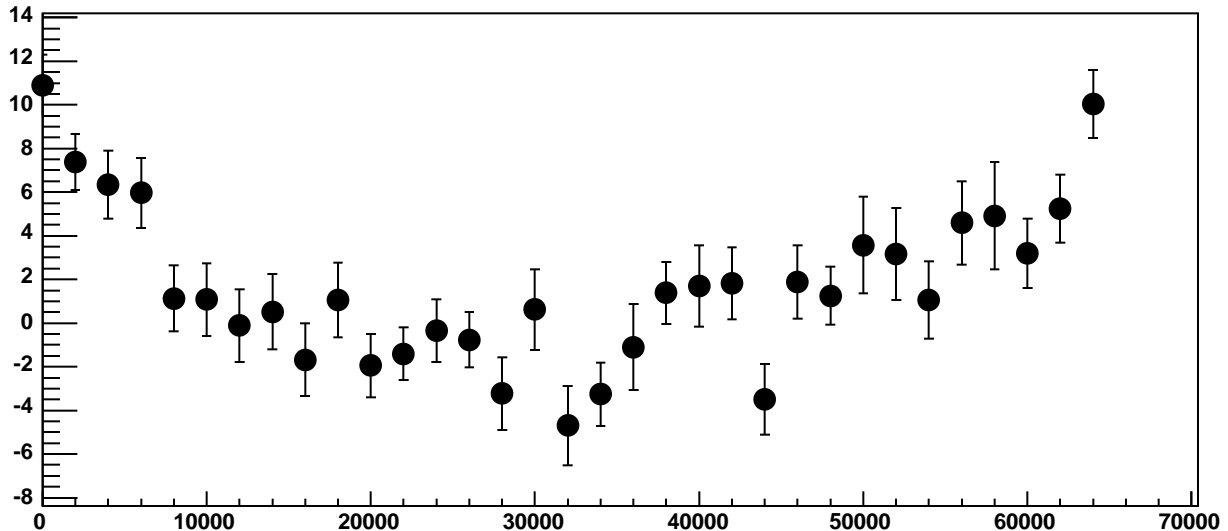


χ^2 / ndf 50.72 / 23
p0 17.57 ± 0.7432
p1 $0.003234 \pm 2.318e-05$

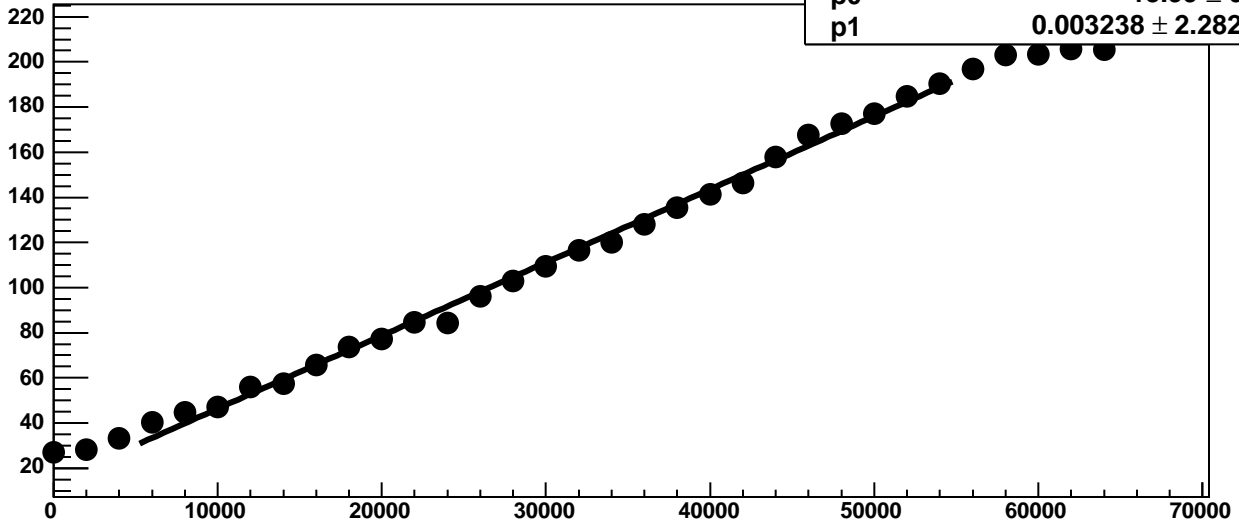
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

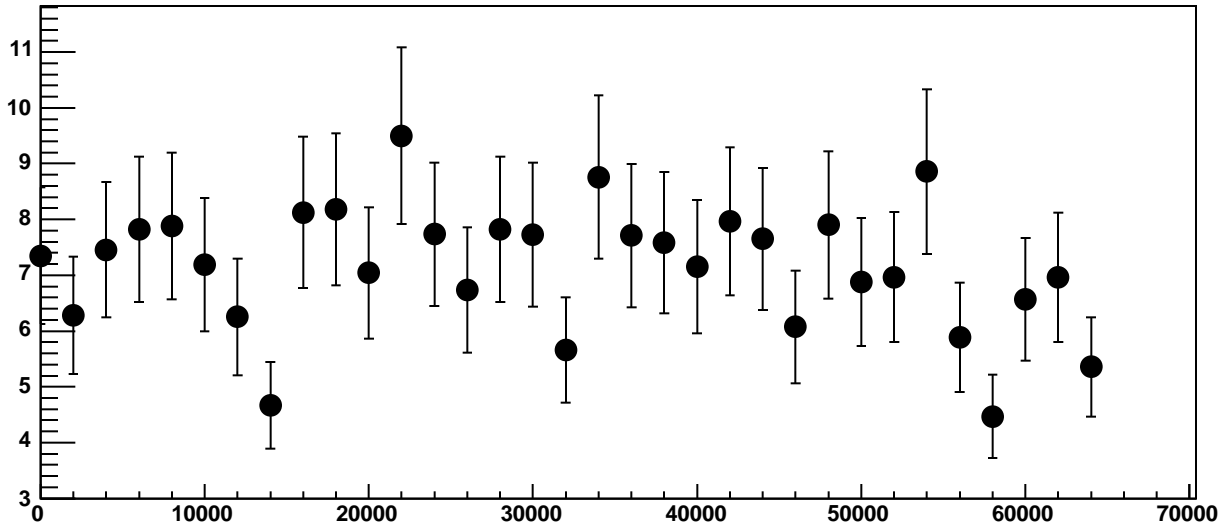


Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

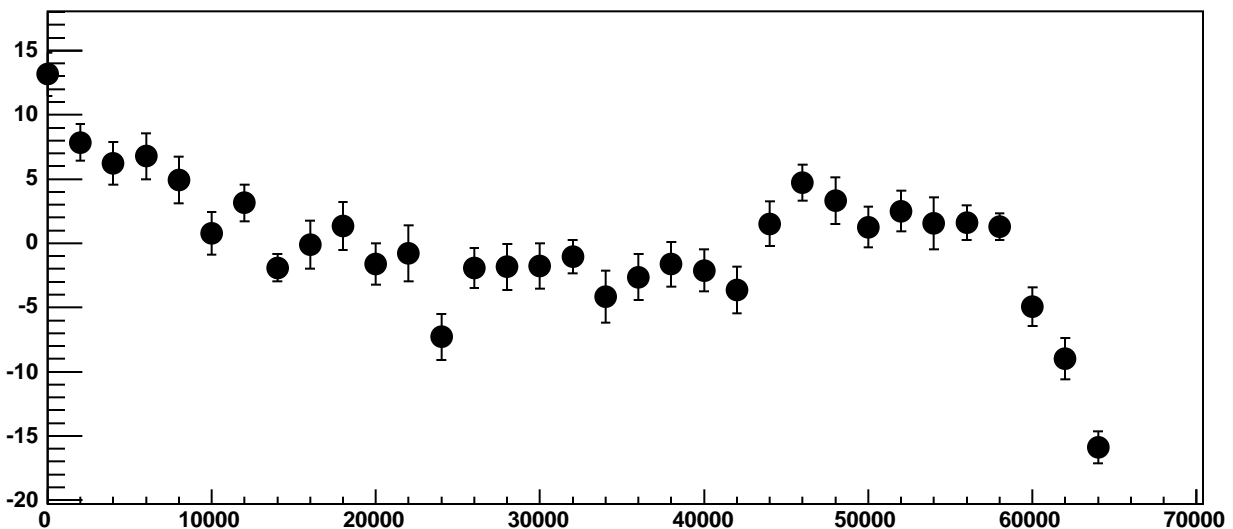


χ^2 / ndf 84.94 / 23
p0 13.99 ± 0.745
p1 $0.003238 \pm 2.282e-05$

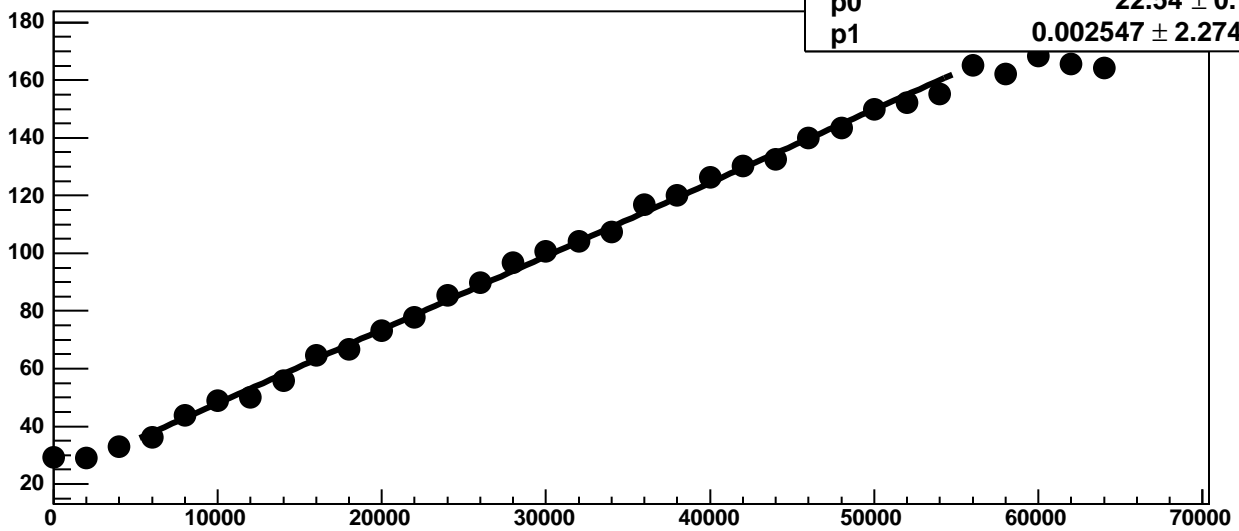
Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



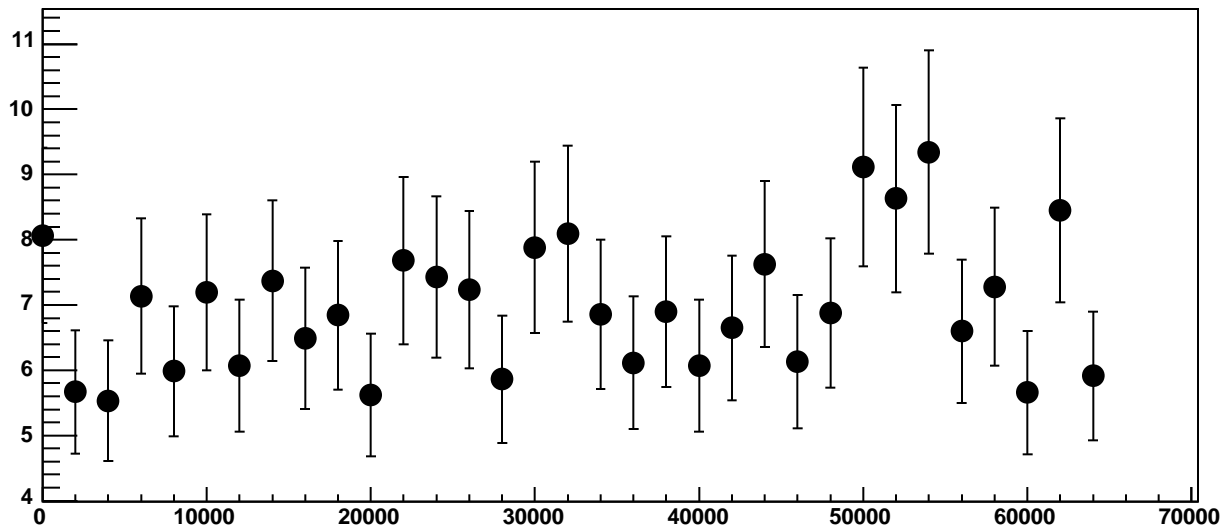
Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



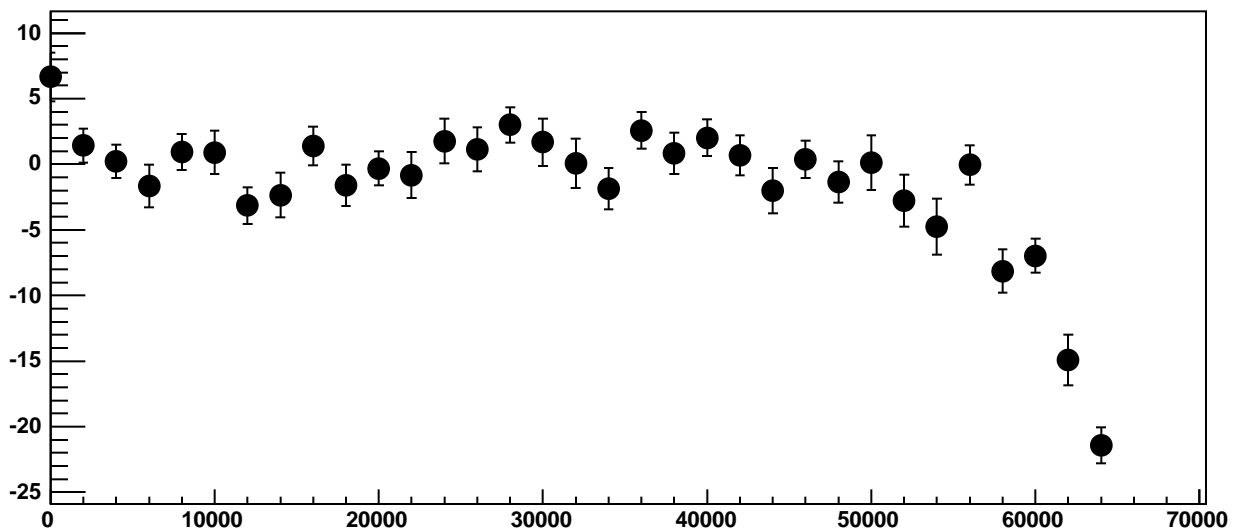
Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



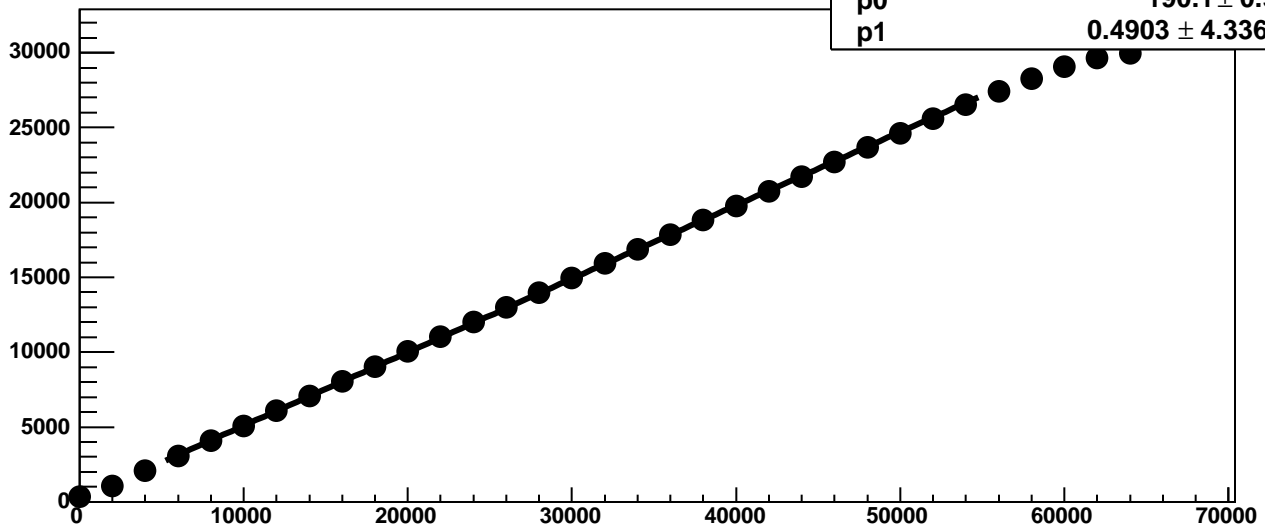
Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



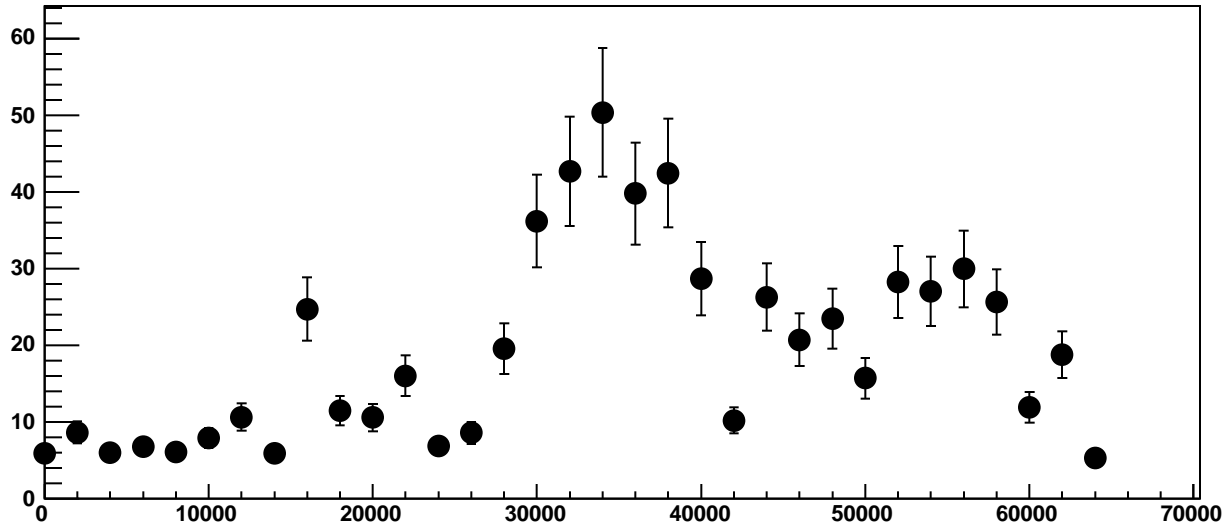
Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



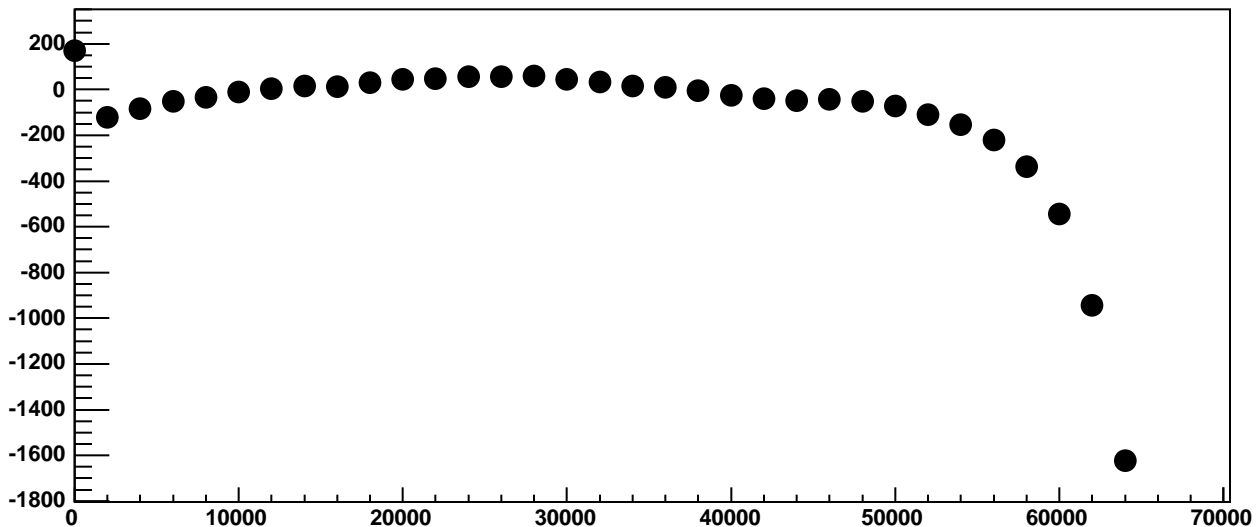
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



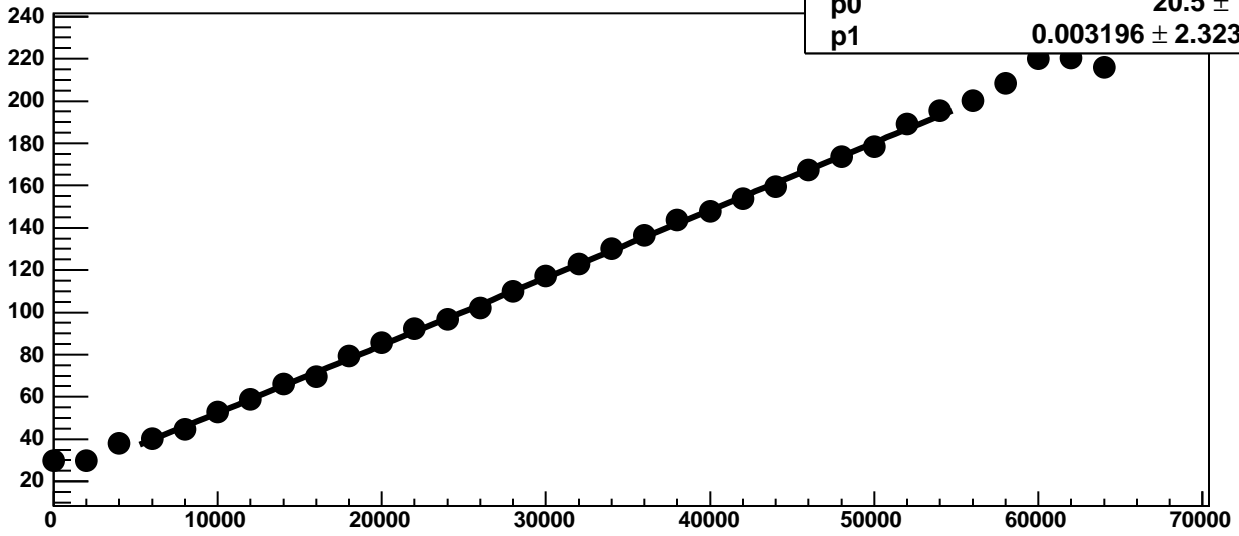
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

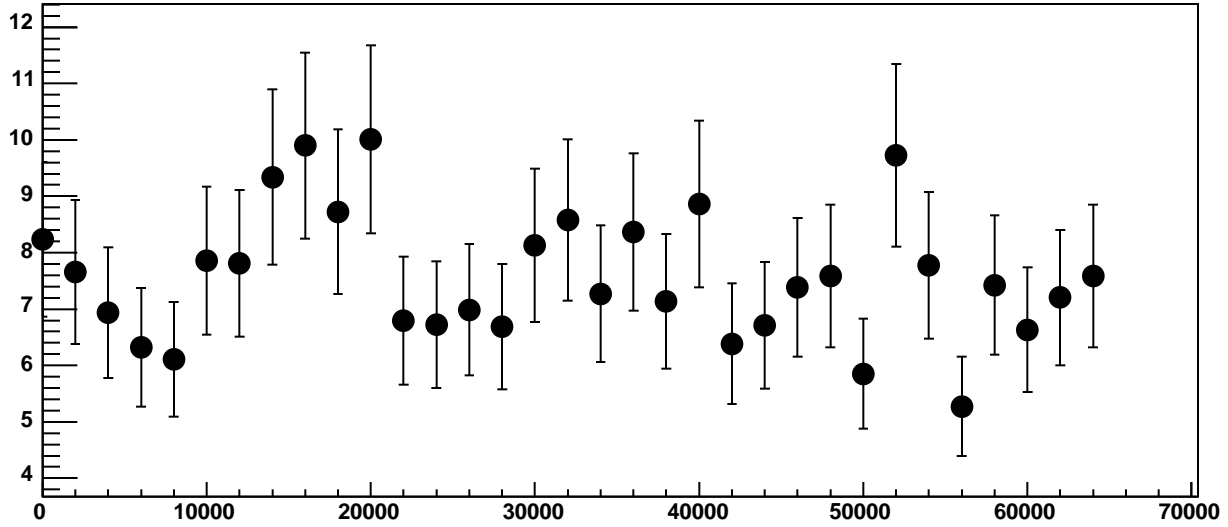


Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

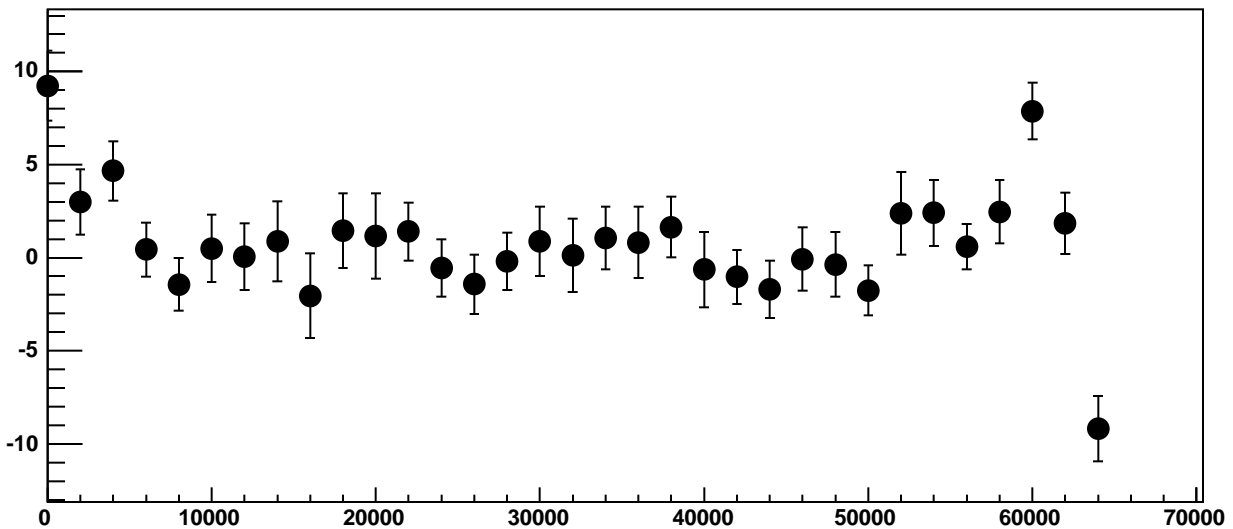


χ^2 / ndf 13.07 / 23
p0 20.5 ± 0.78
p1 $0.003196 \pm 2.323e-05$

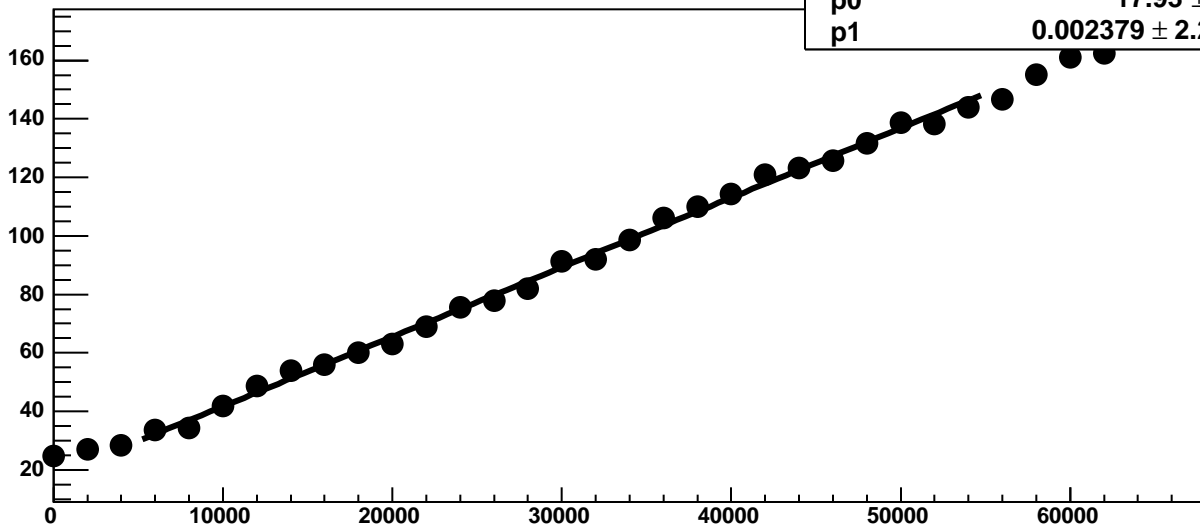
Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

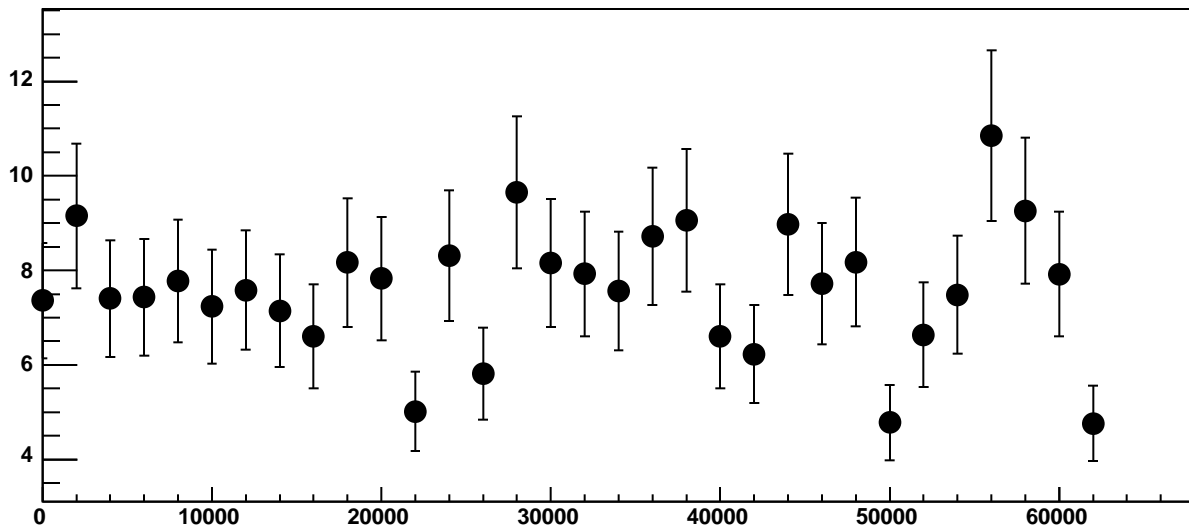


Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

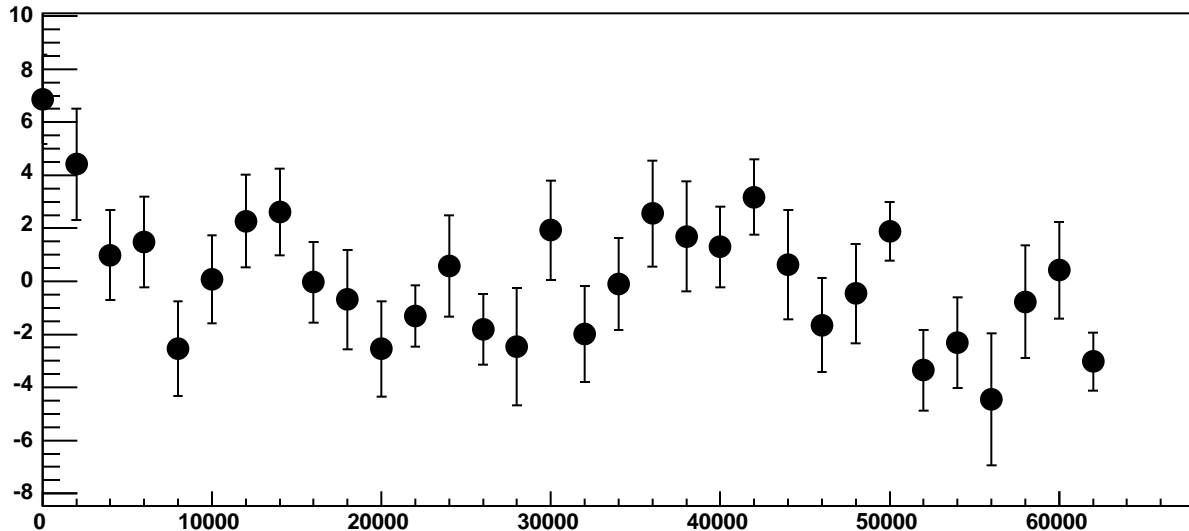


χ^2 / ndf 34.51 / 23
p0 17.93 ± 0.7565
p1 0.002379 ± 2.225e-05

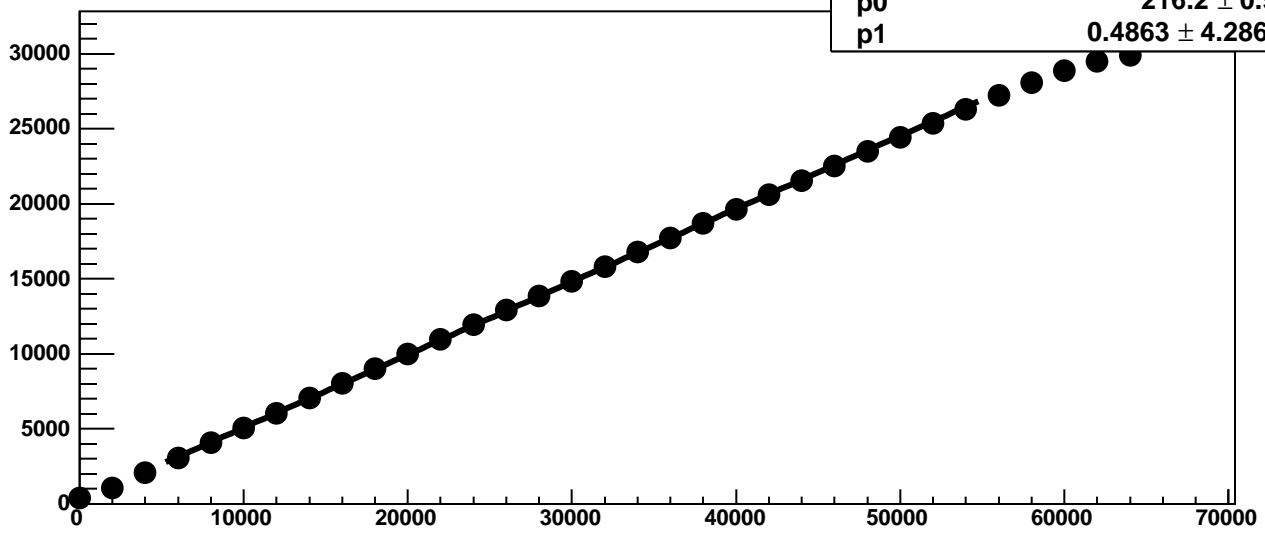
Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



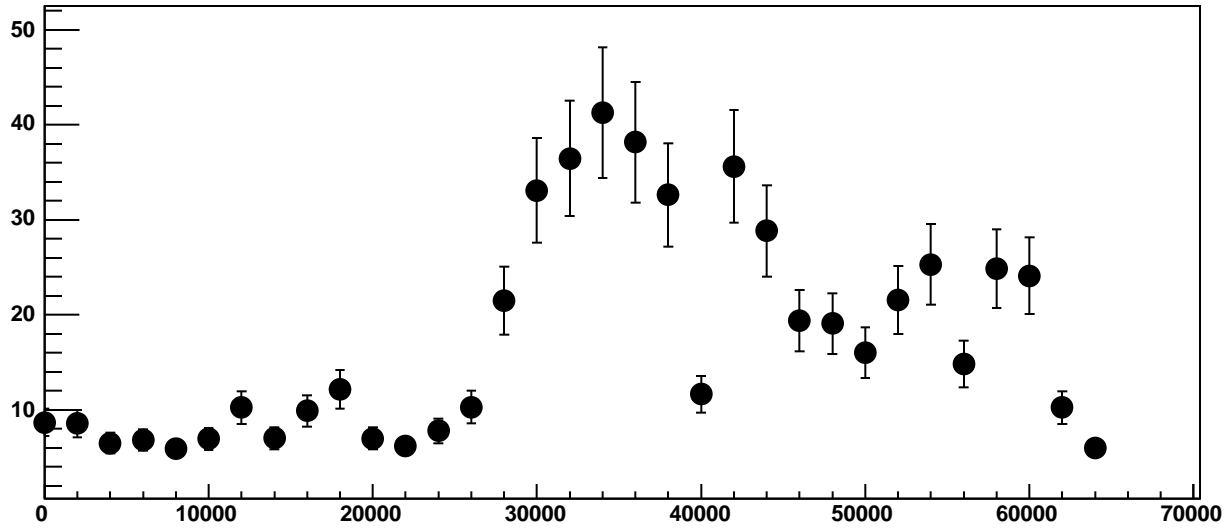
Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



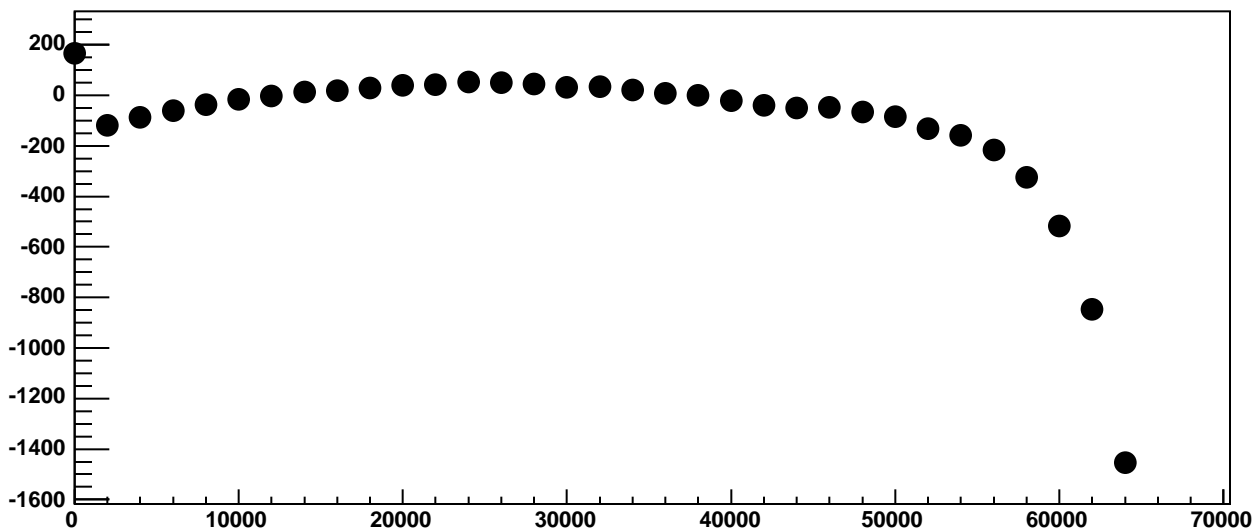
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



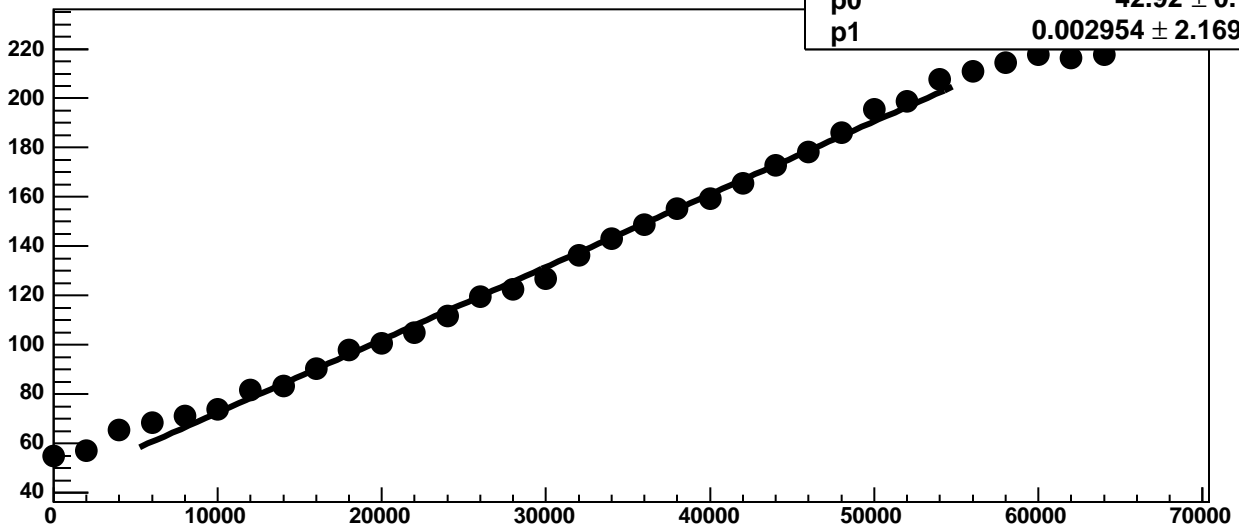
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

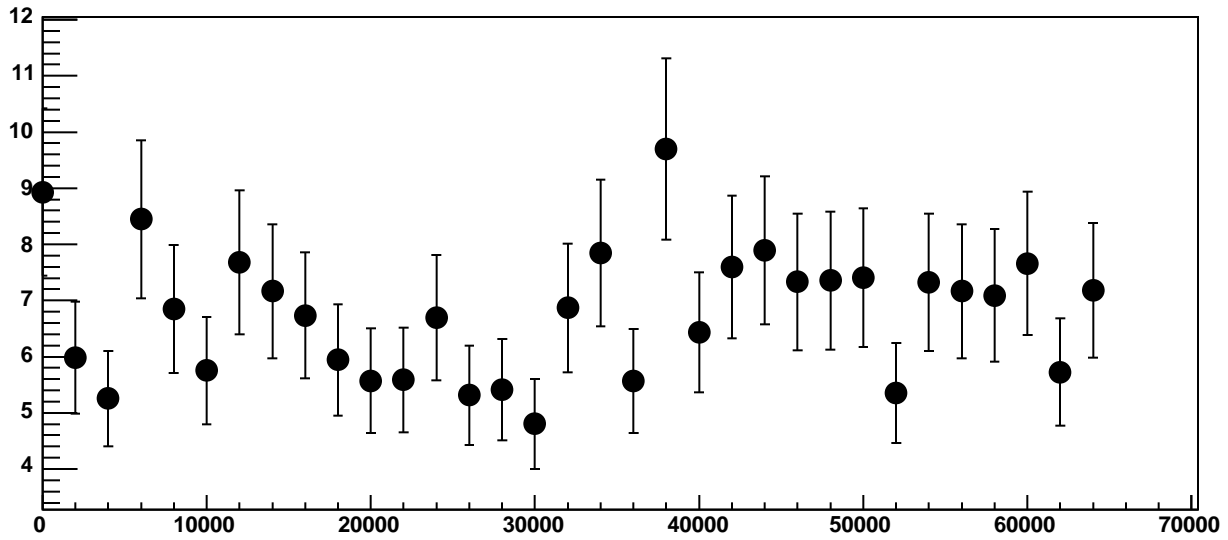


Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

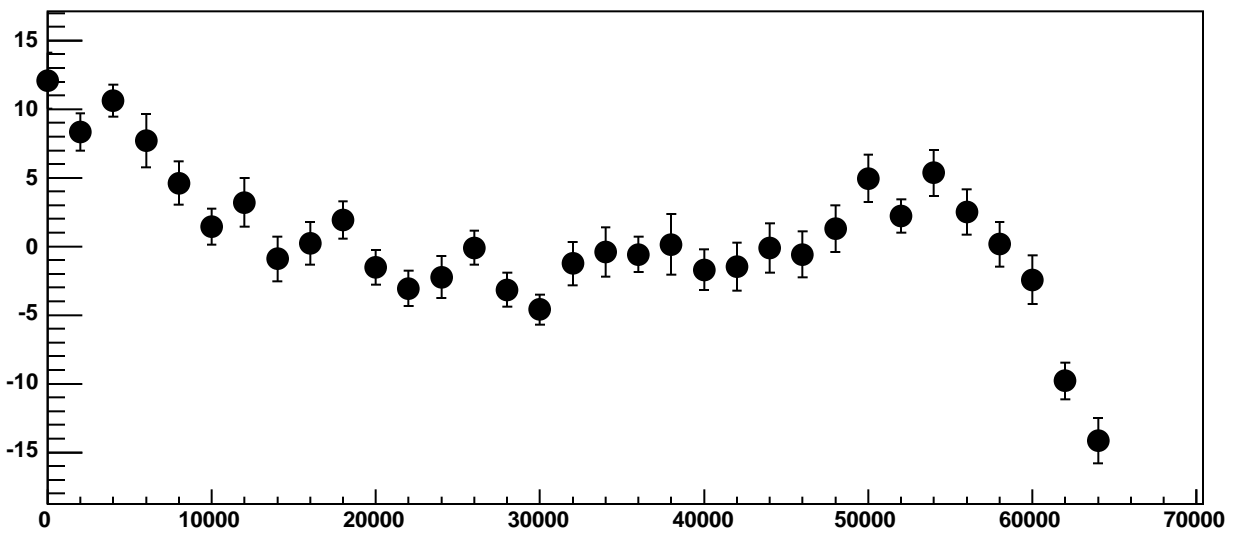


χ^2 / ndf 89.93 / 23
p0 42.92 ± 0.7044
p1 $0.002954 \pm 2.169\text{e-}05$

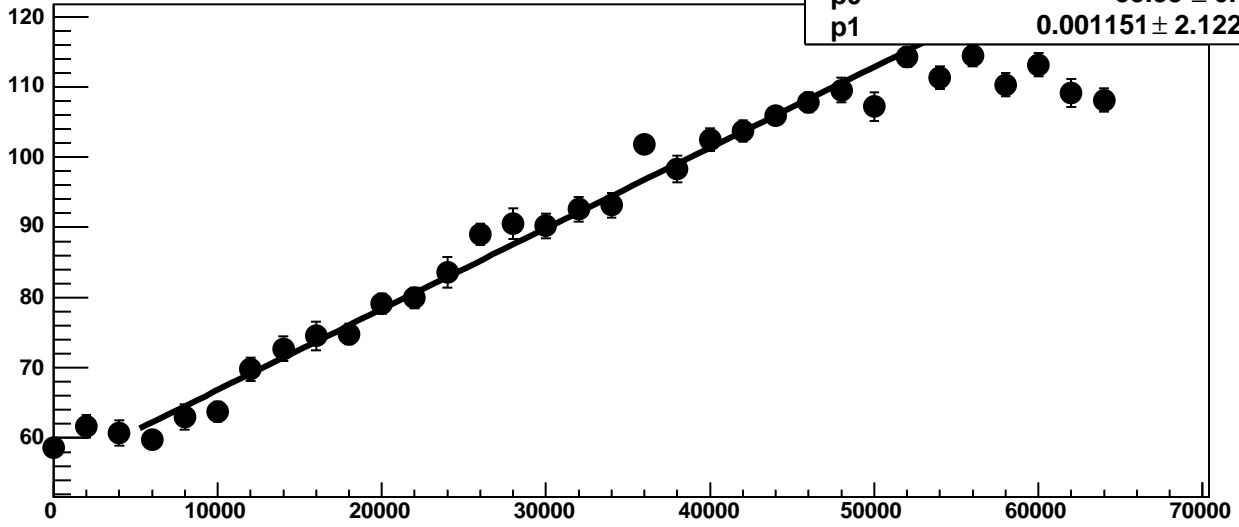
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



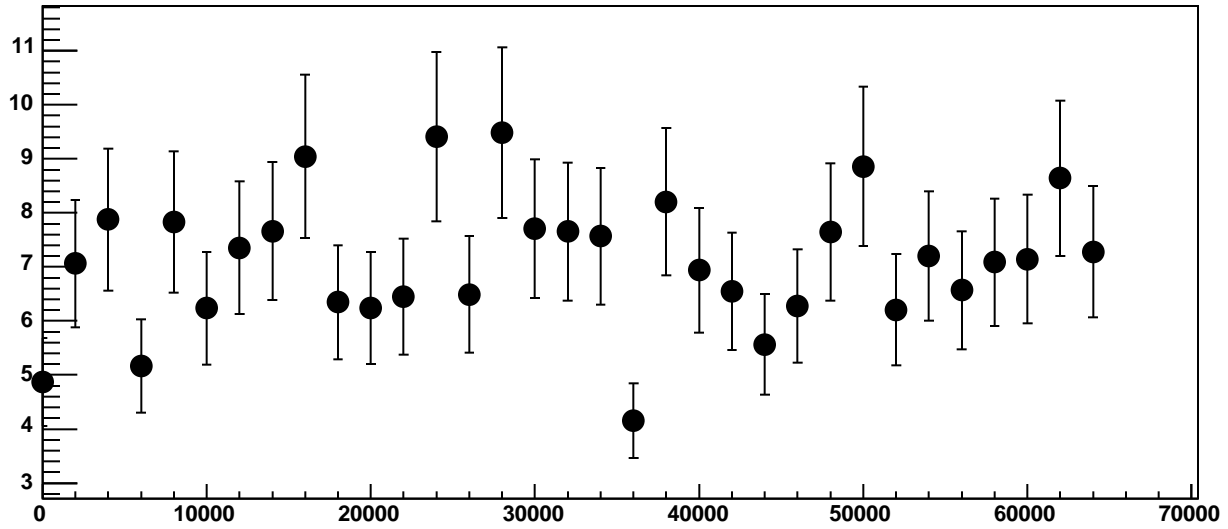
Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



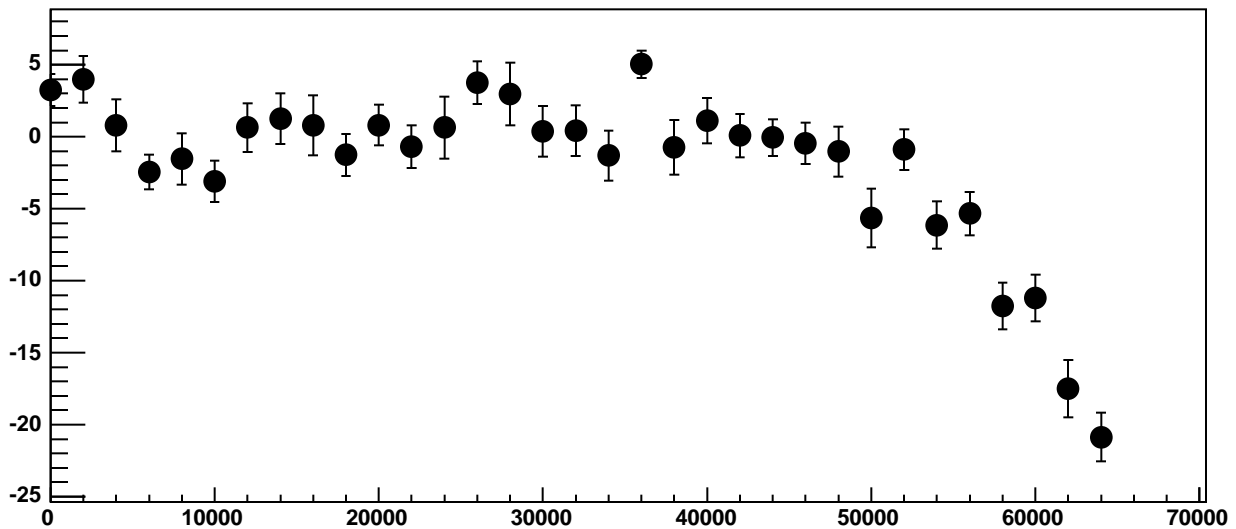
Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



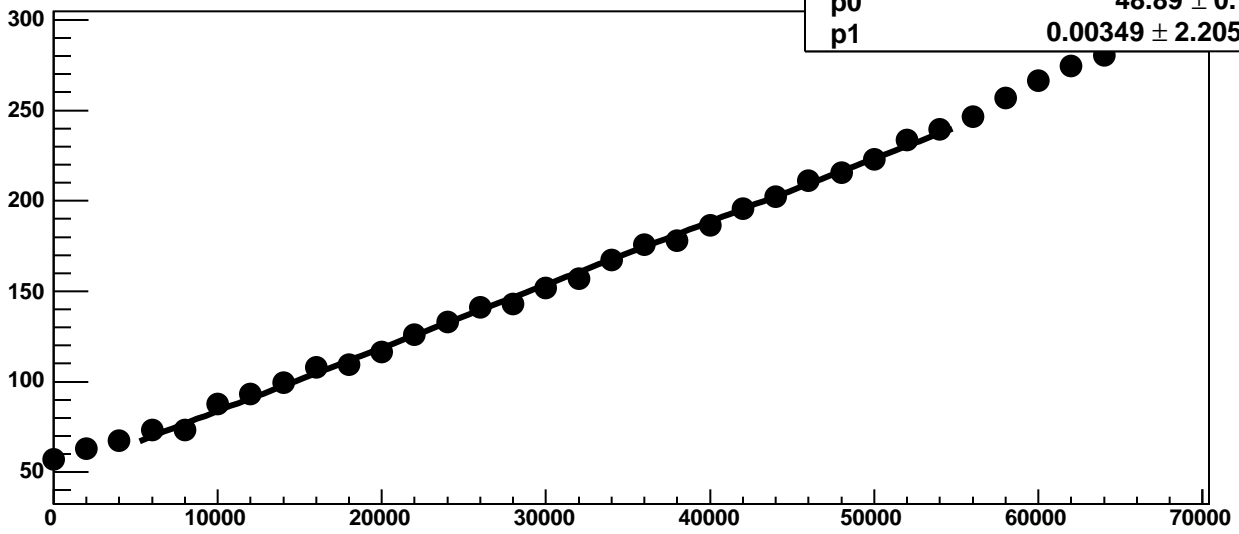
Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

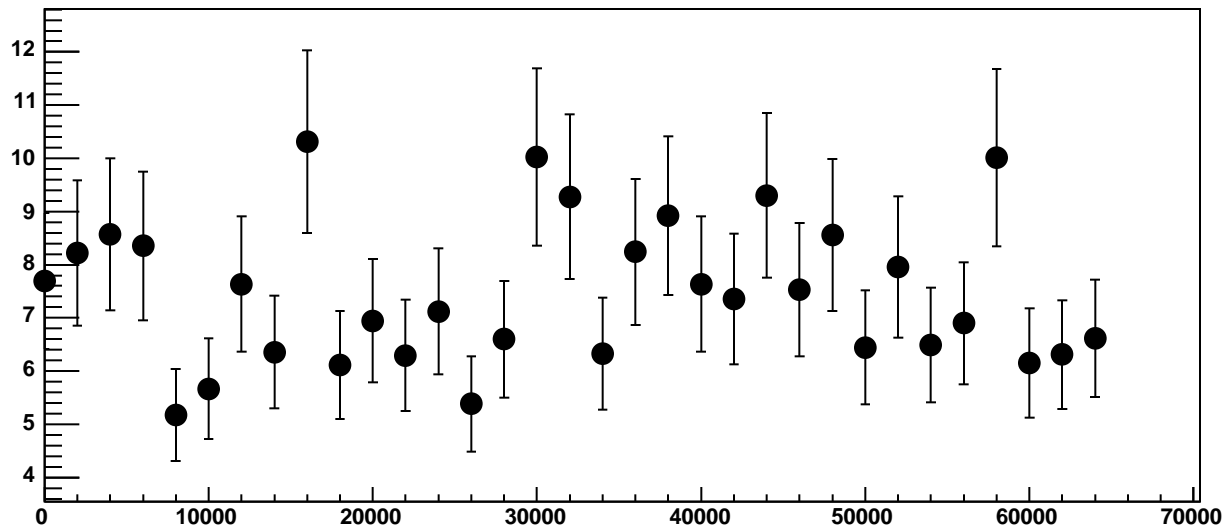


Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

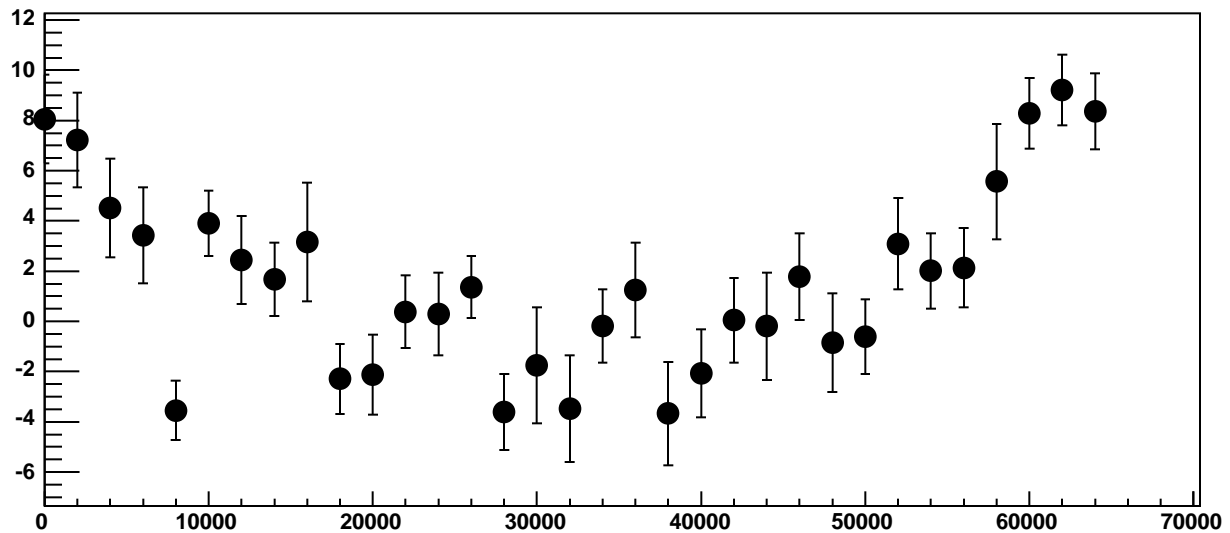


χ^2 / ndf 52.03 / 23
p0 48.89 ± 0.7028
p1 $0.00349 \pm 2.205e-05$

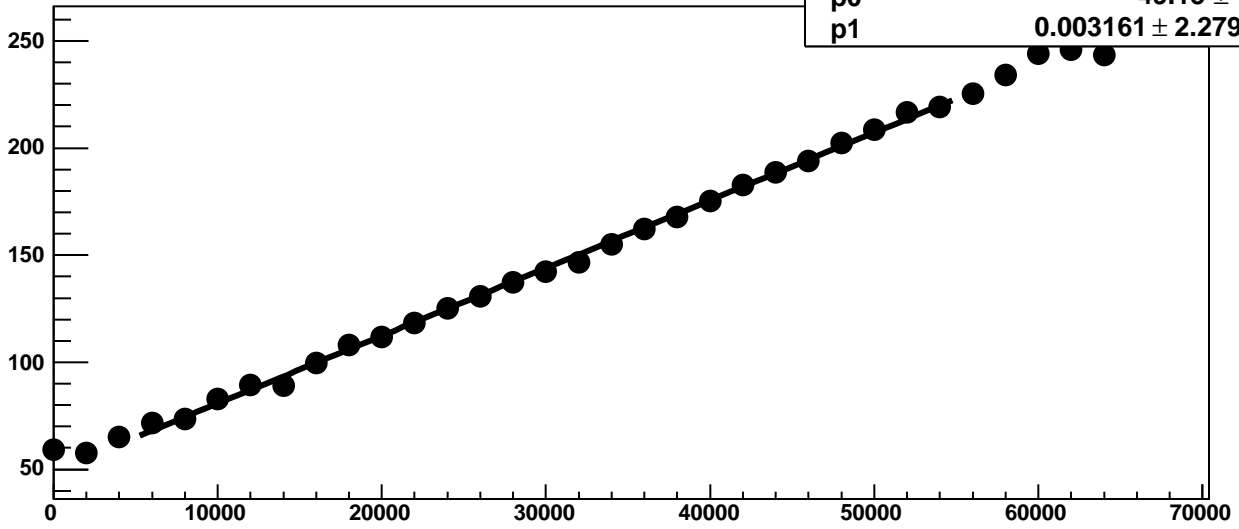
Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

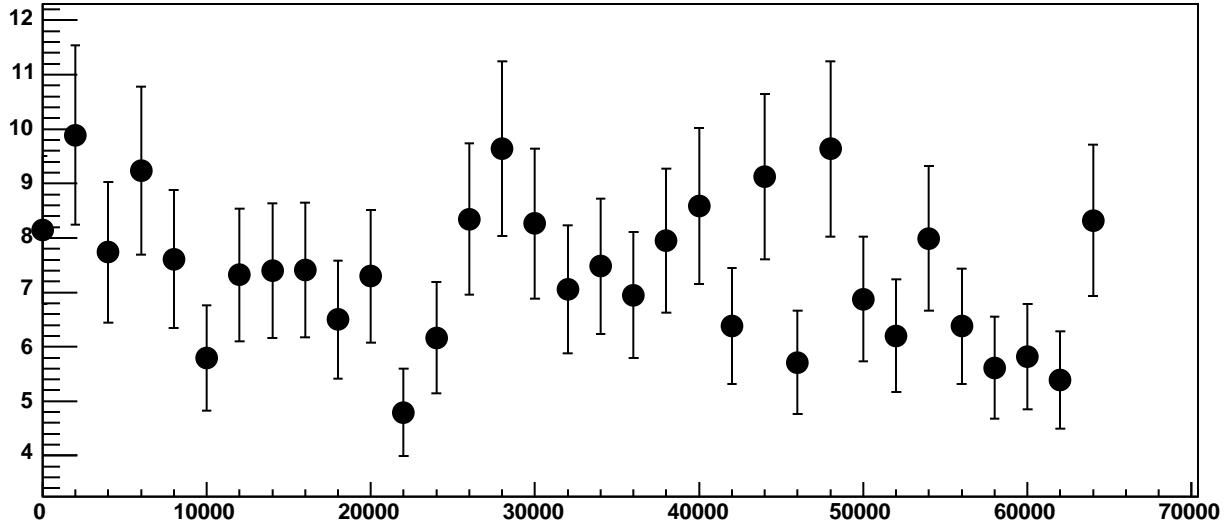


Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

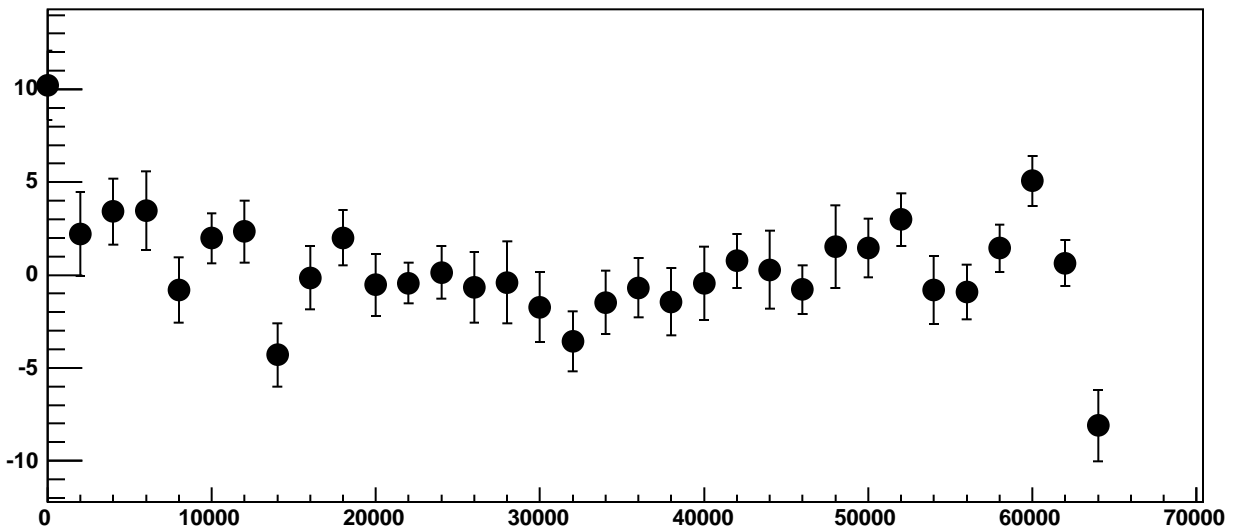


χ^2 / ndf 29.57 / 23
p0 49.15 ± 0.75
p1 0.003161 ± 2.279e-05

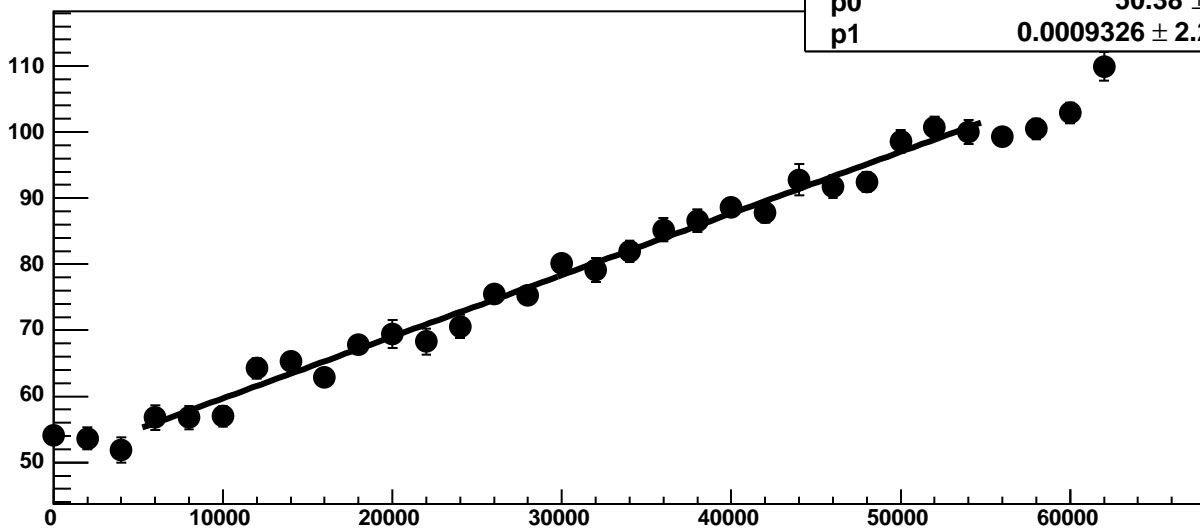
Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

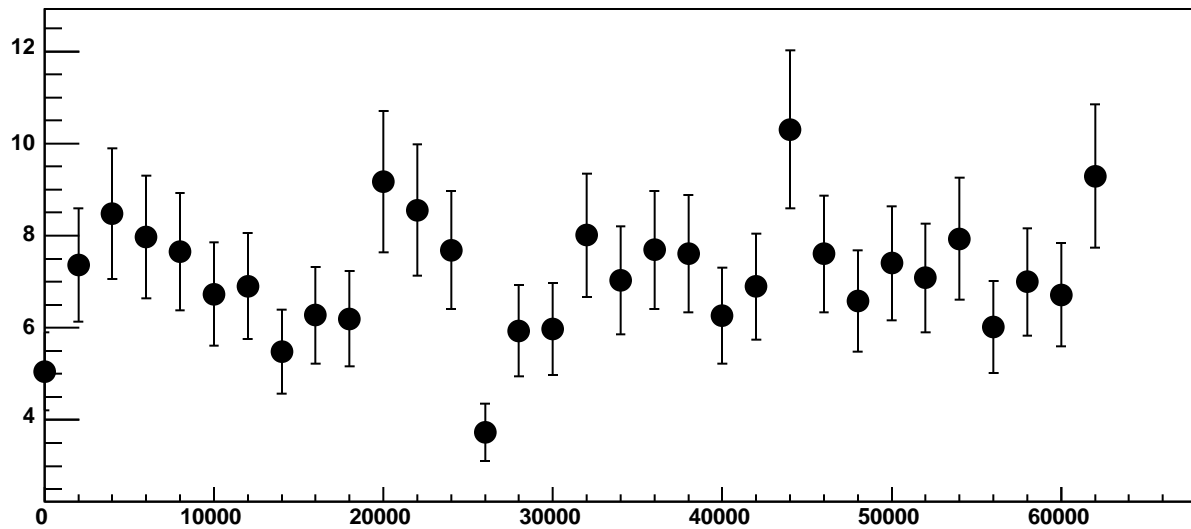


Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

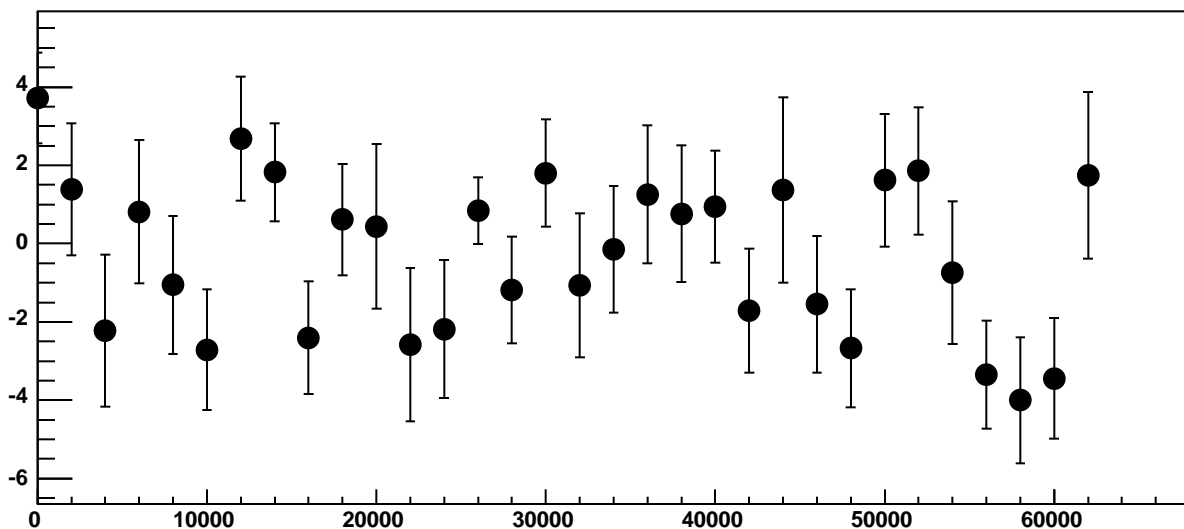


χ^2 / ndf 27.65 / 23
p0 50.38 ± 0.7292
p1 0.0009326 ± 2.282e-05

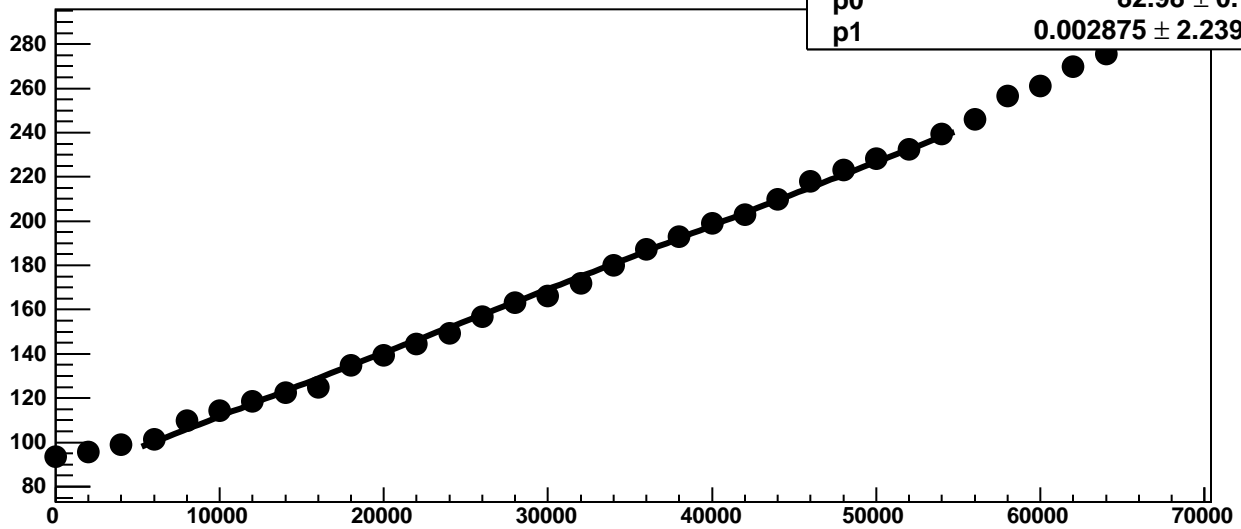
Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

37.89 / 23

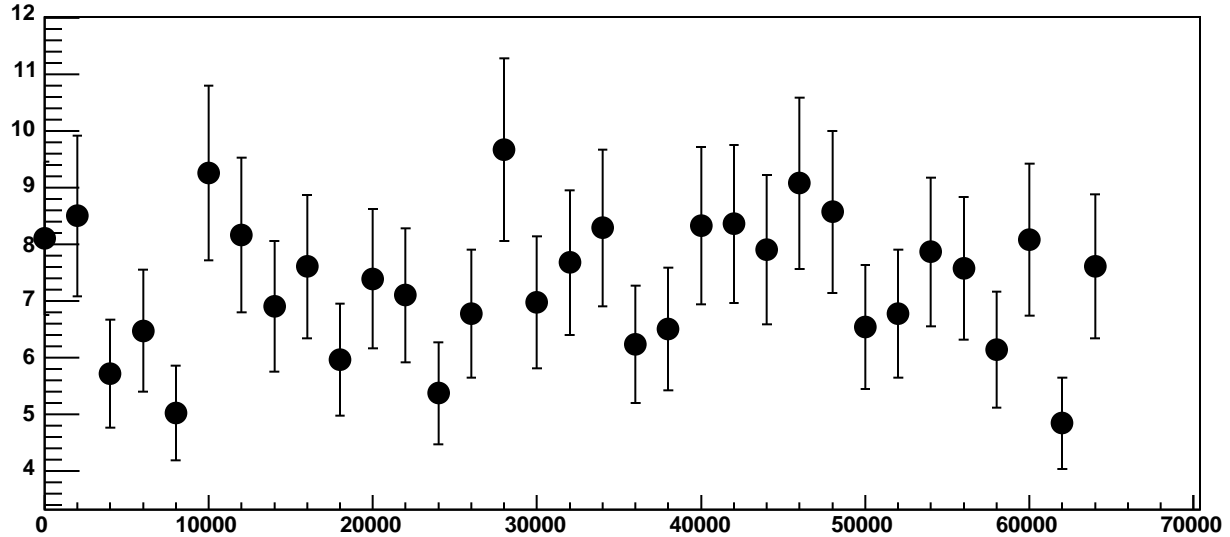
p0

82.98 ± 0.7154

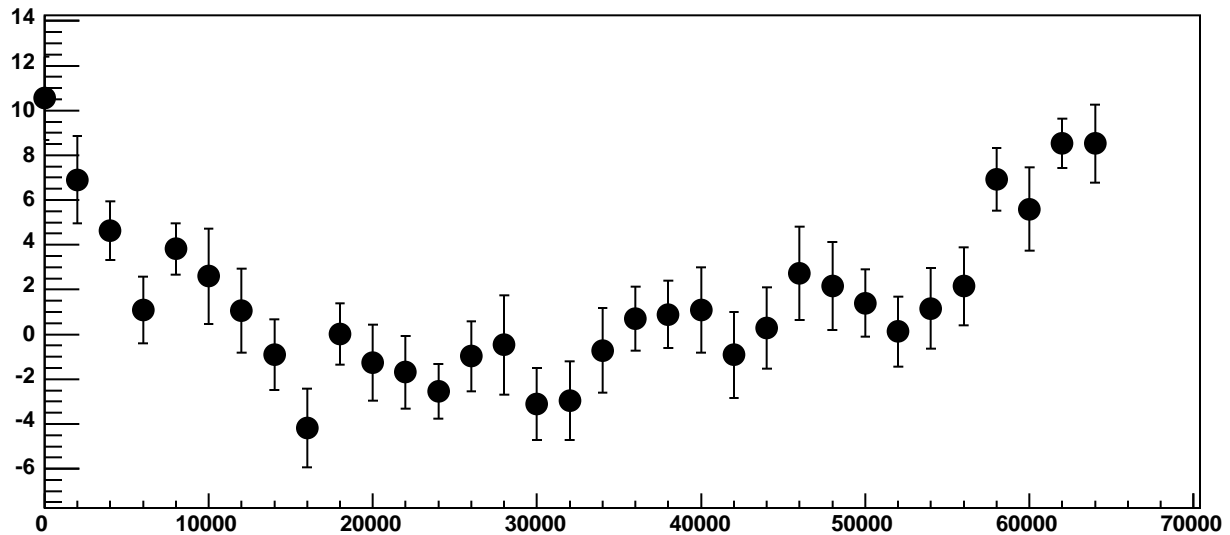
p1

$0.002875 \pm 2.239\text{e-}05$

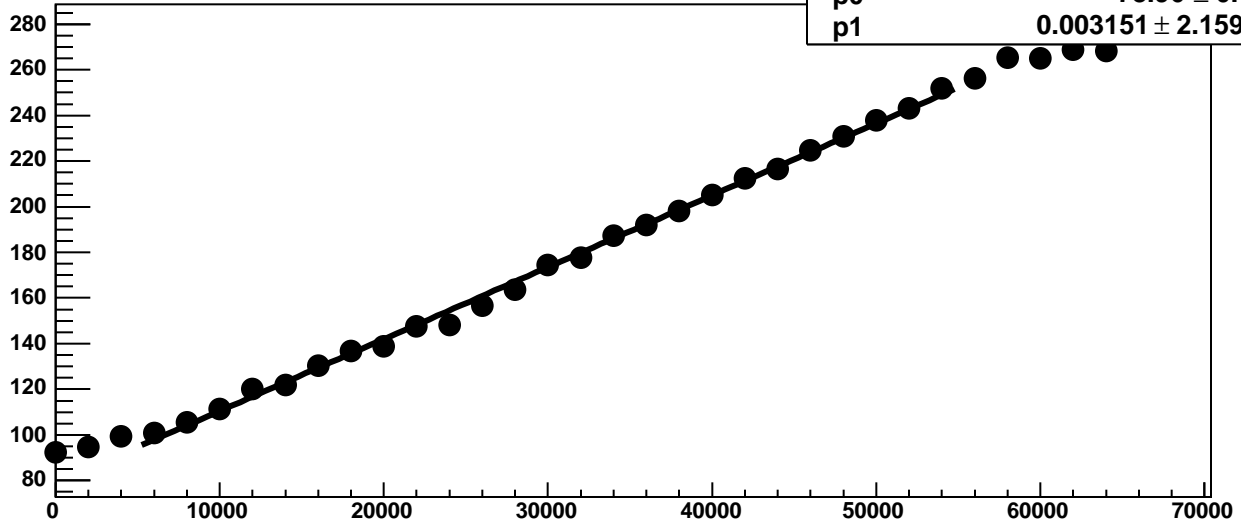
Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



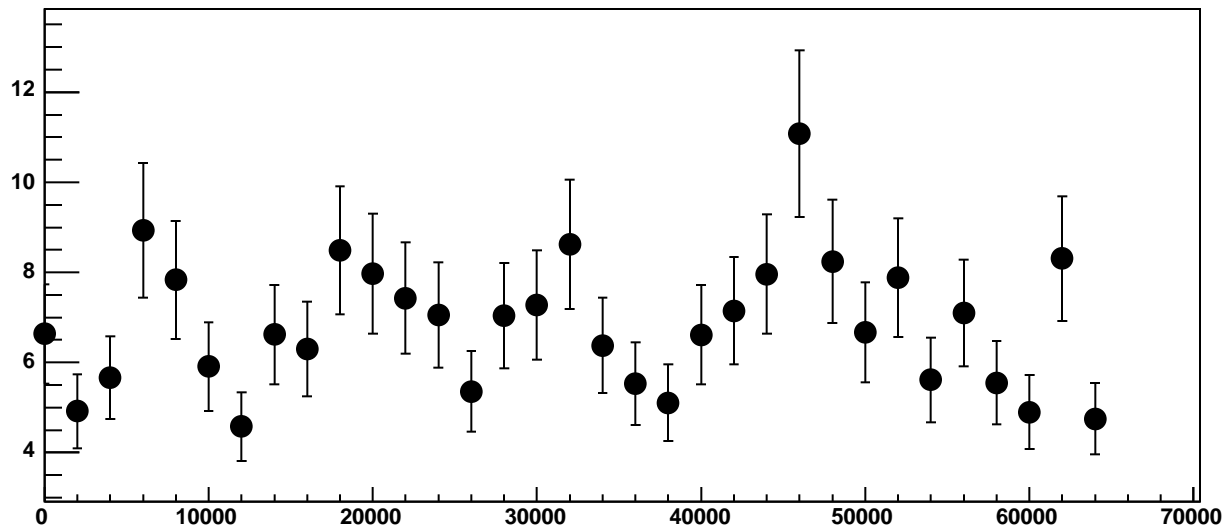
Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



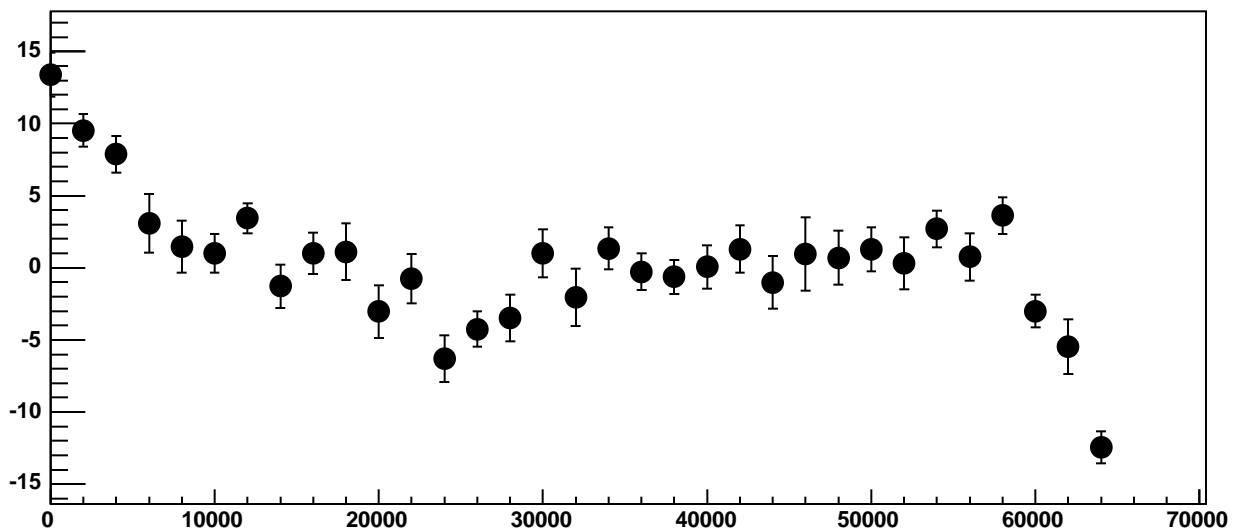
Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

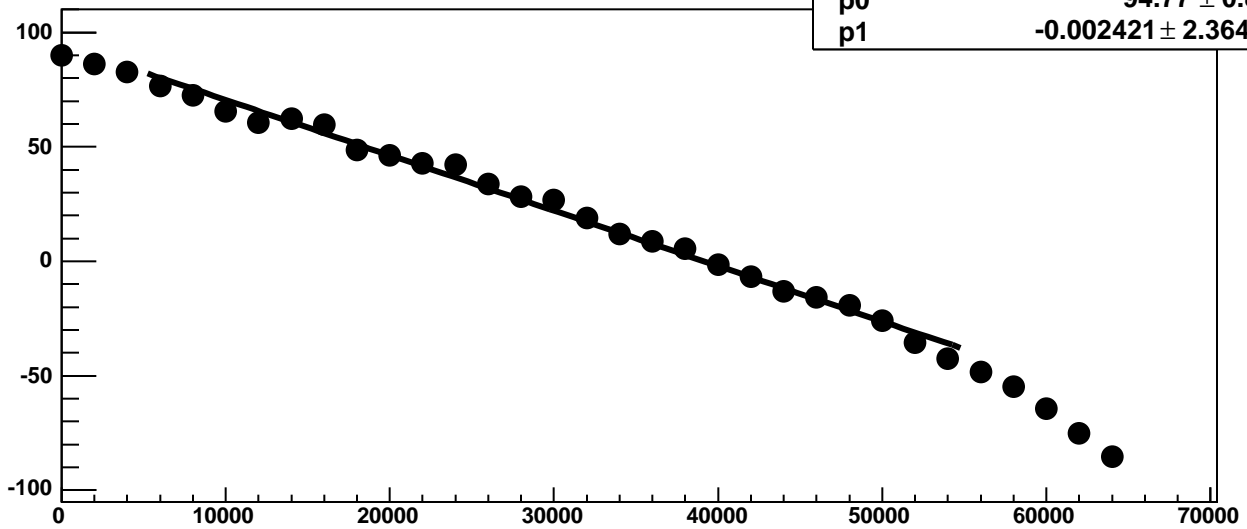


Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

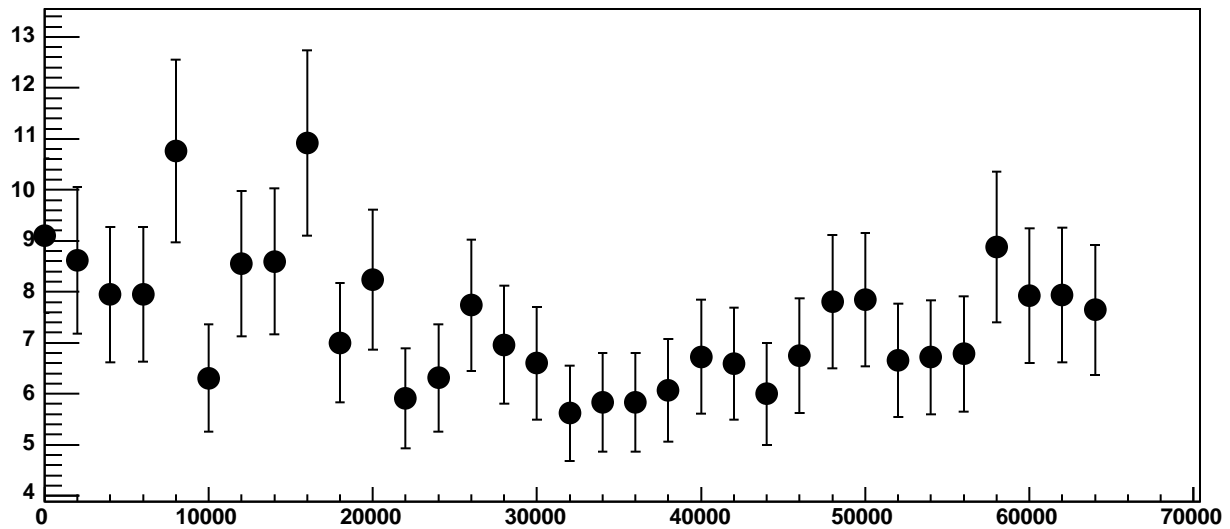


Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

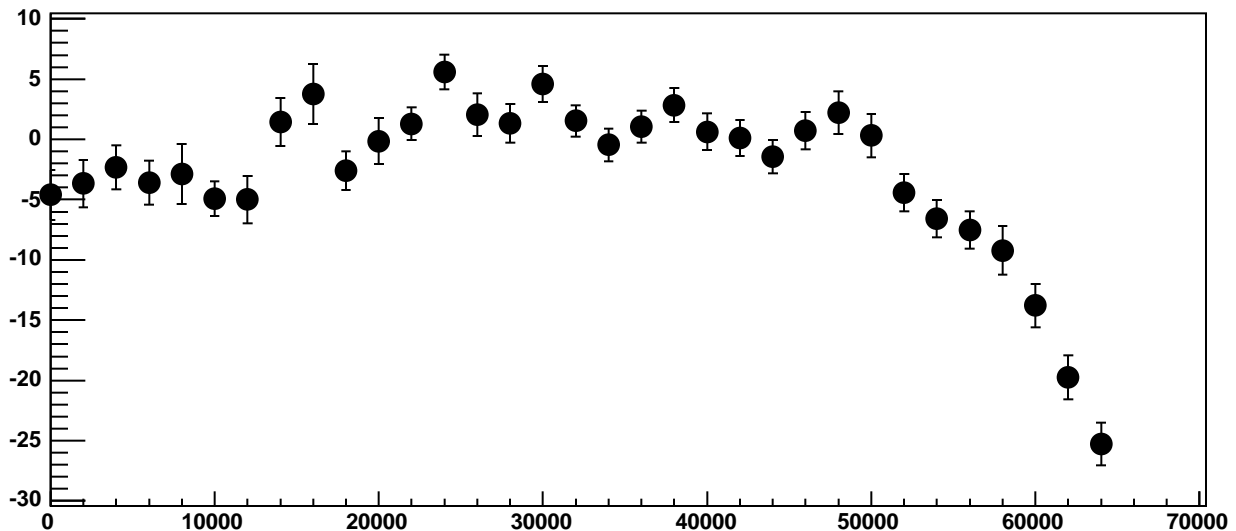
χ^2 / ndf 91.75 / 23
p0 94.77 ± 0.8152
p1 $-0.002421 \pm 2.364e-05$



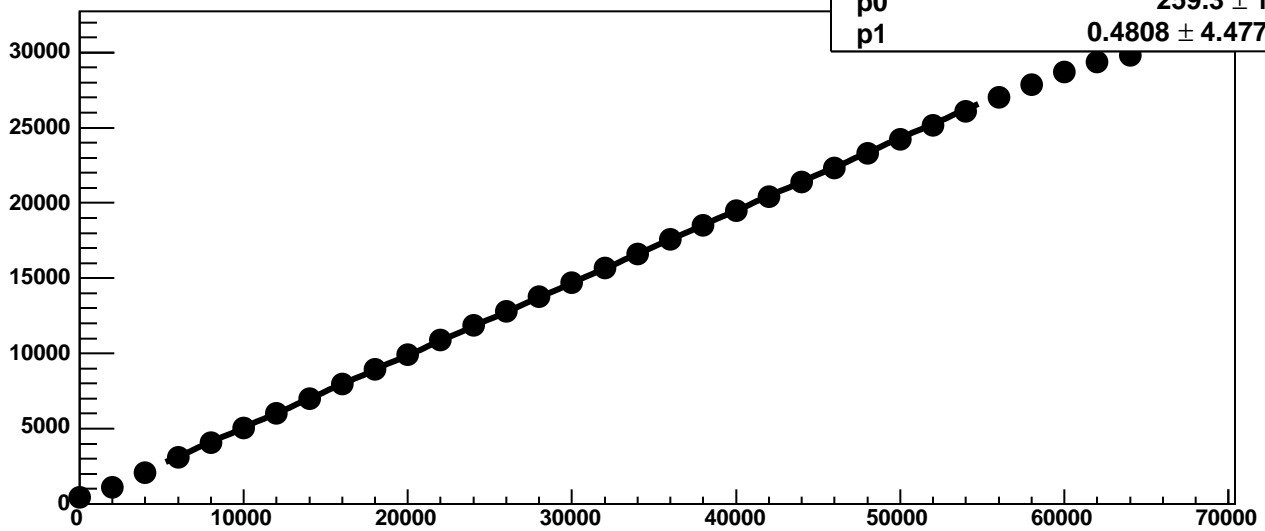
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



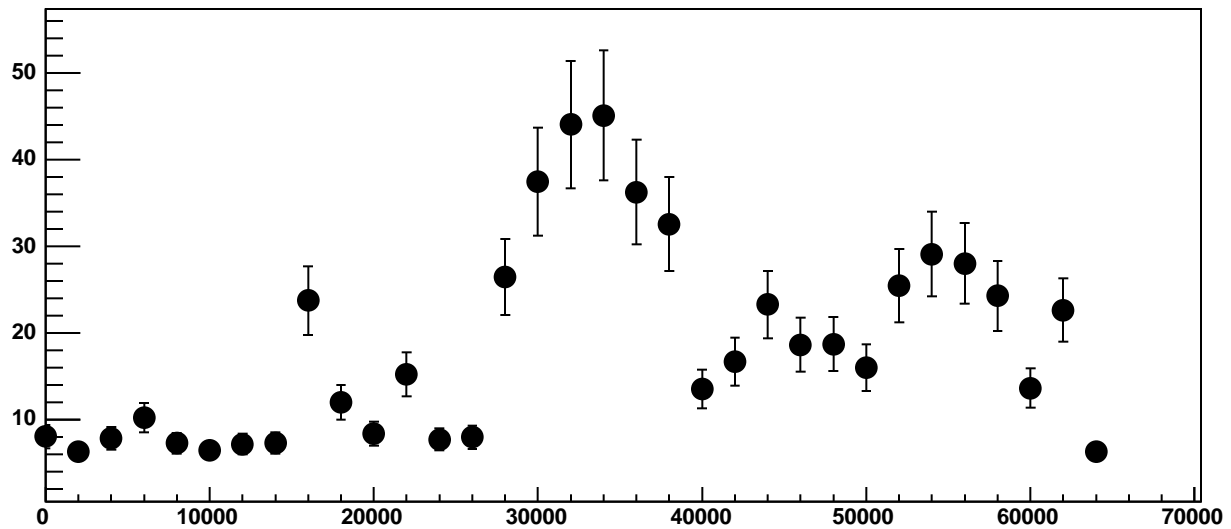
Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



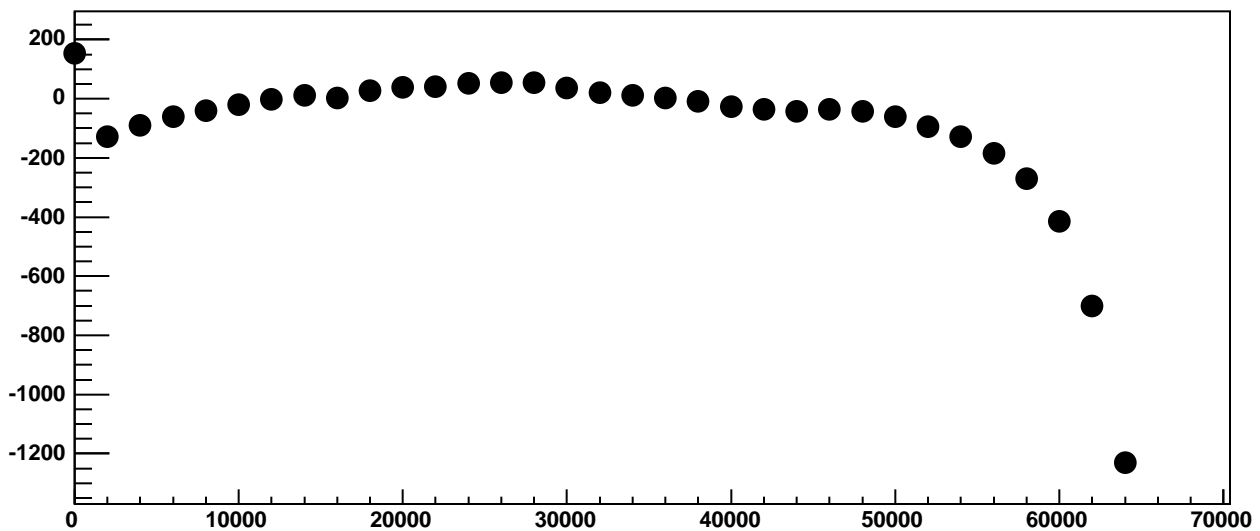
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



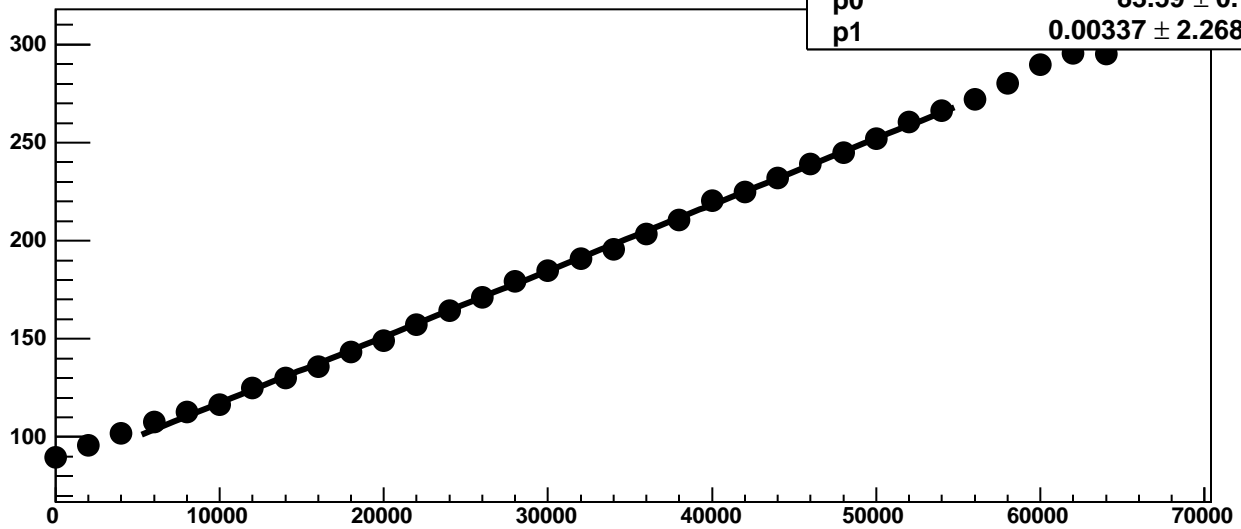
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.22 / 23

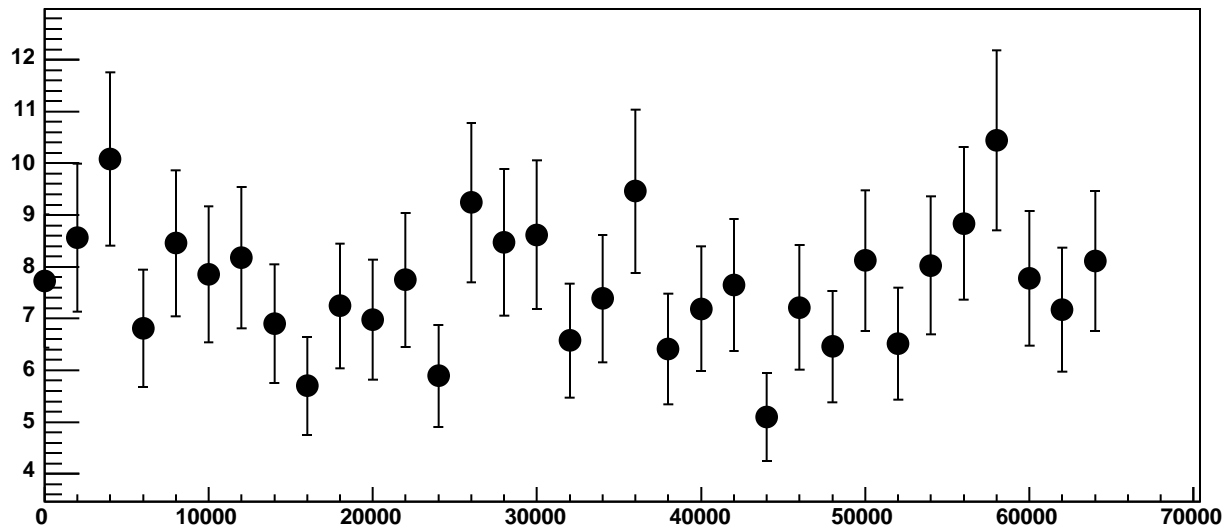
p0

83.59 ± 0.7655

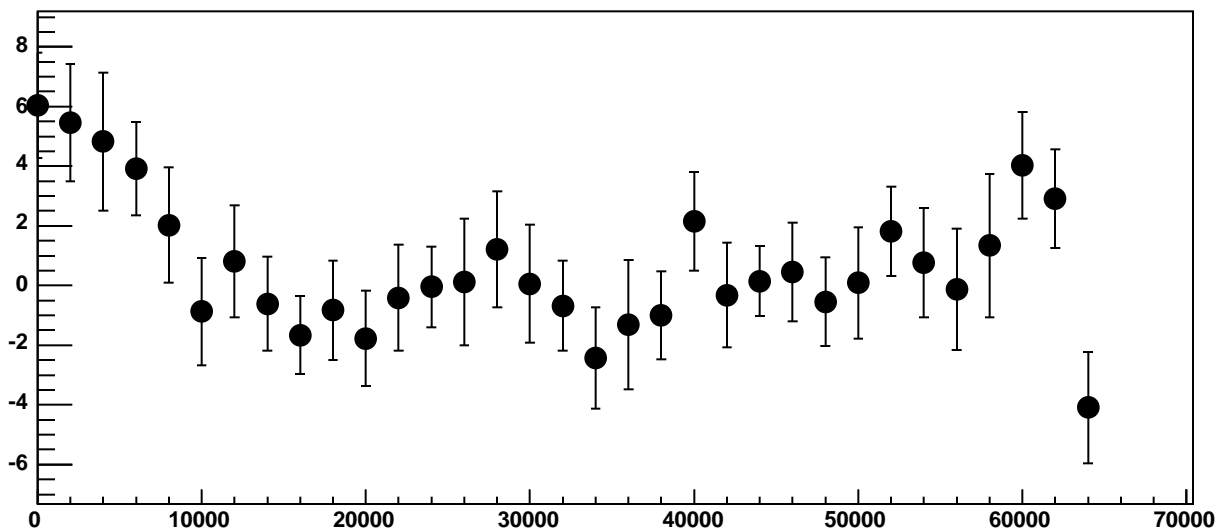
p1

$0.00337 \pm 2.268e-05$

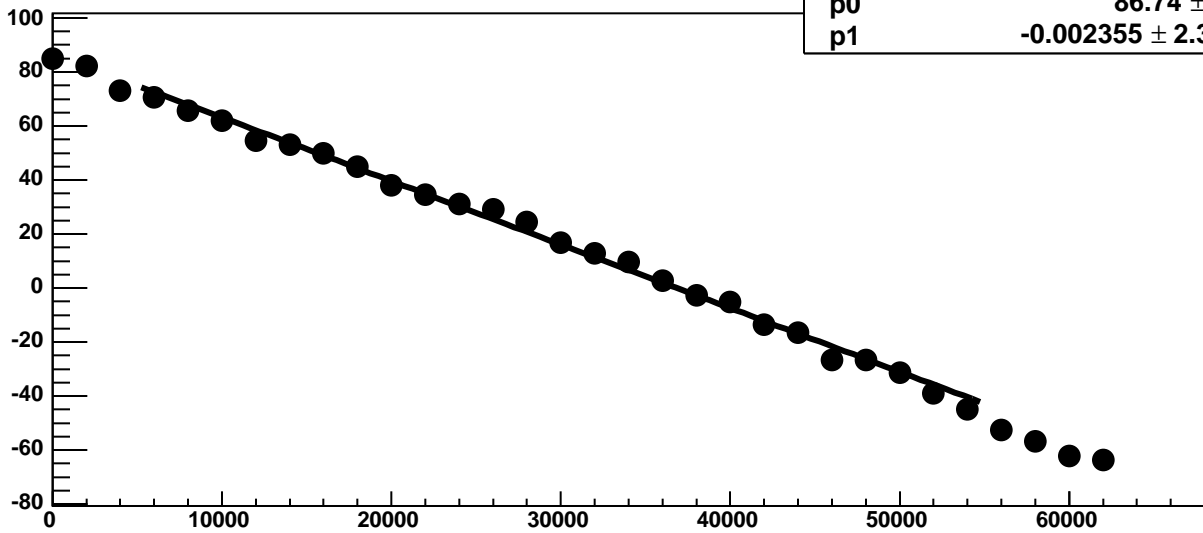
Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

54.2 / 23

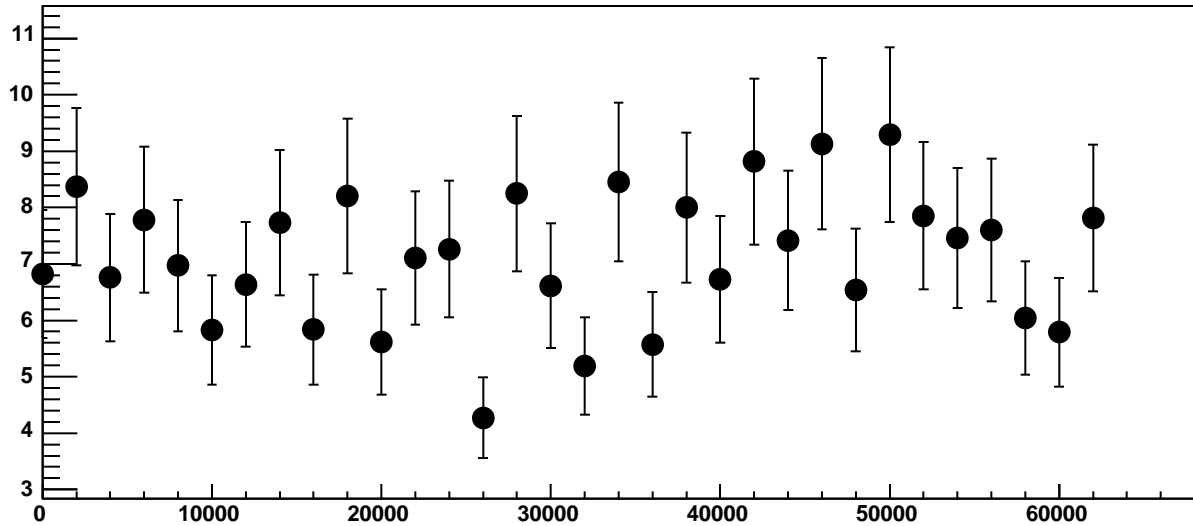
p0

86.74 ± 0.7258

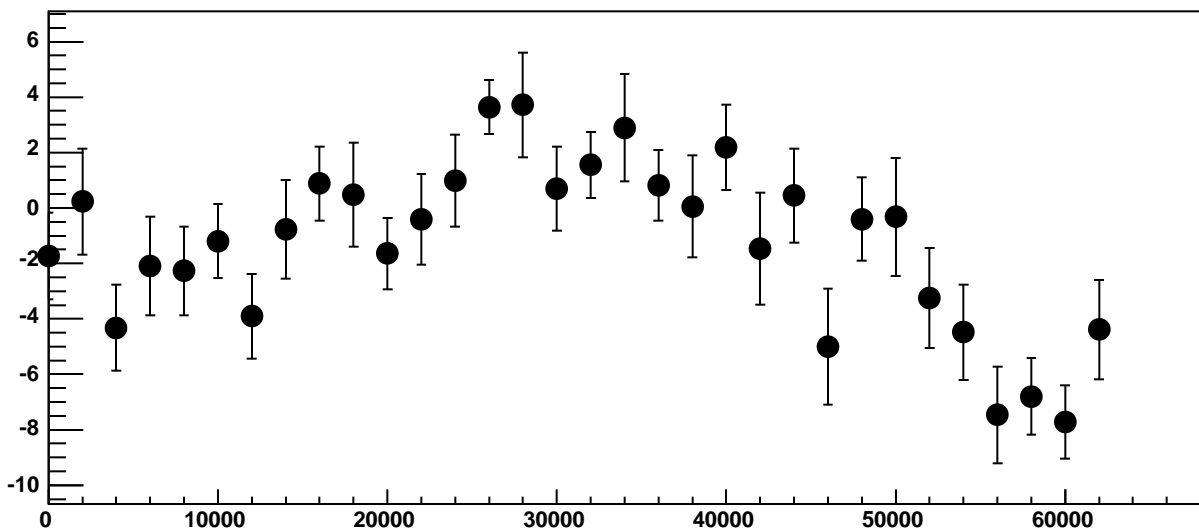
p1

$-0.002355 \pm 2.304e-05$

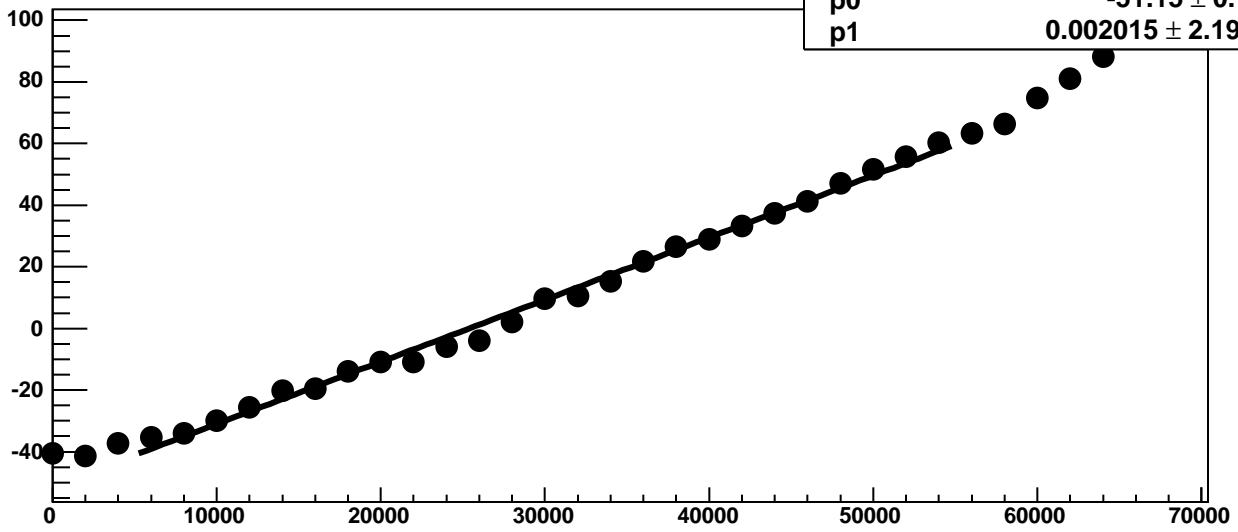
Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

51.04 / 23

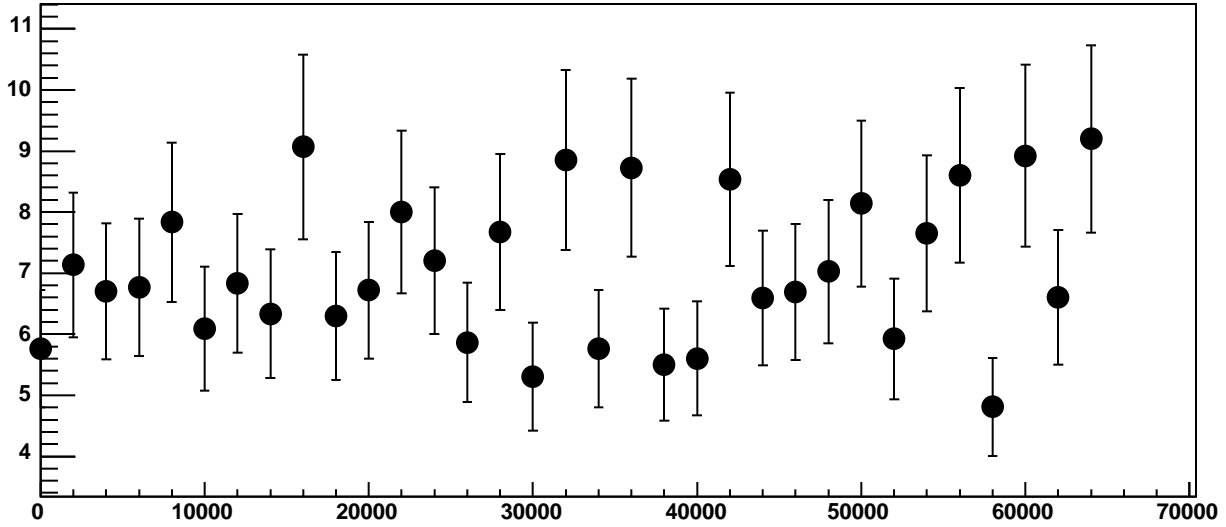
p0

-51.15 ± 0.7288

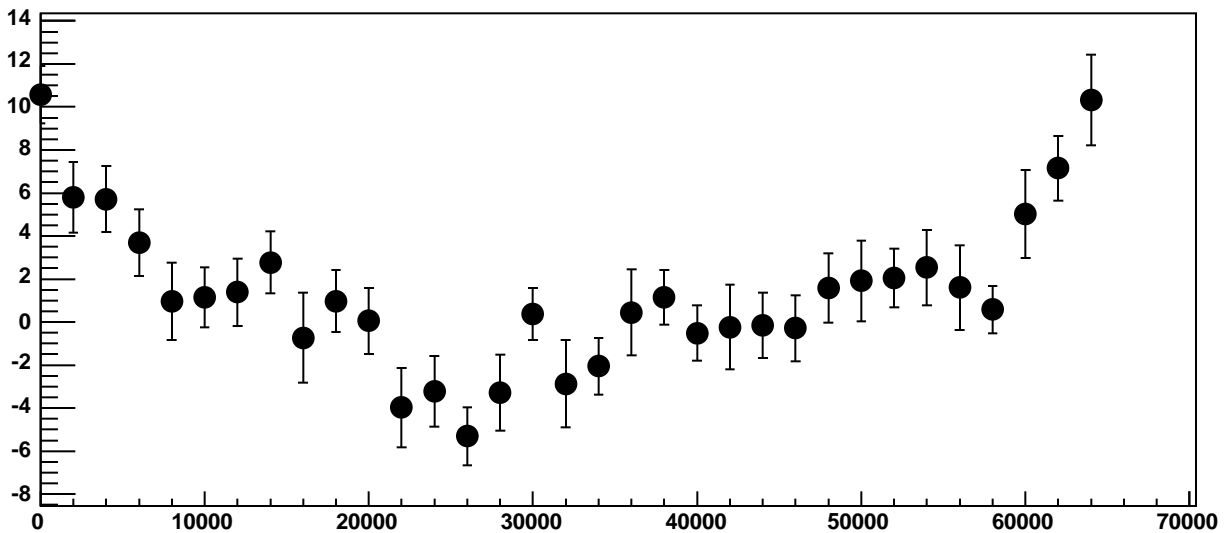
p1

$0.002015 \pm 2.19e-05$

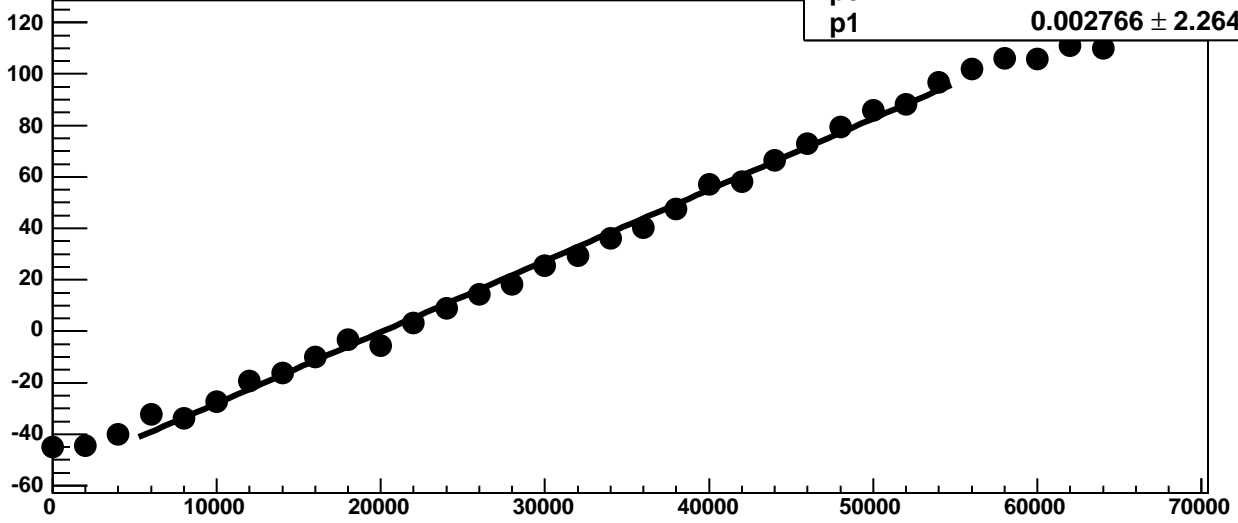
Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

66.5 / 23

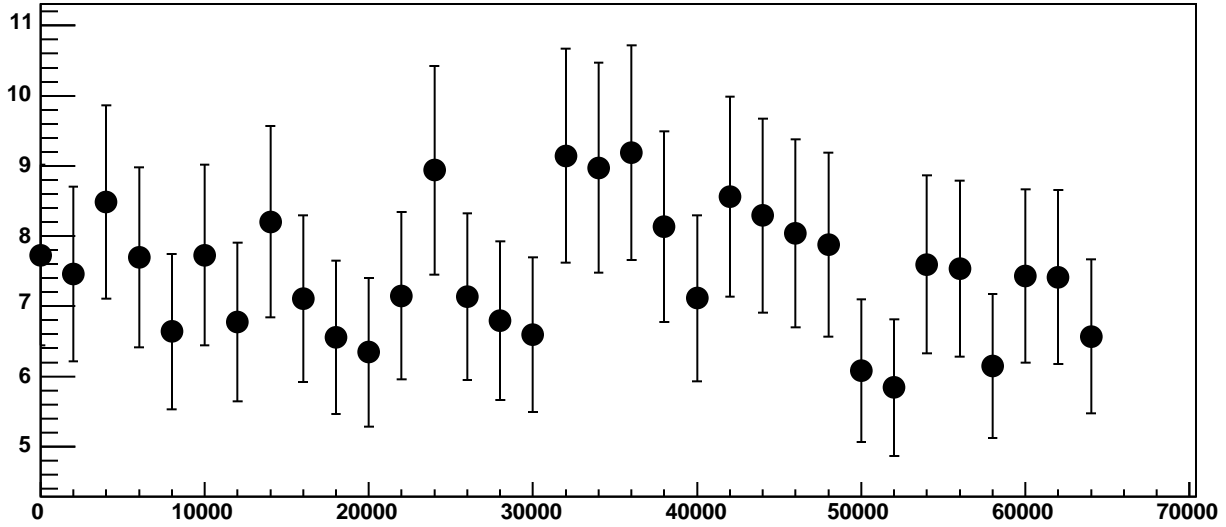
p0

-55.66 ± 0.756

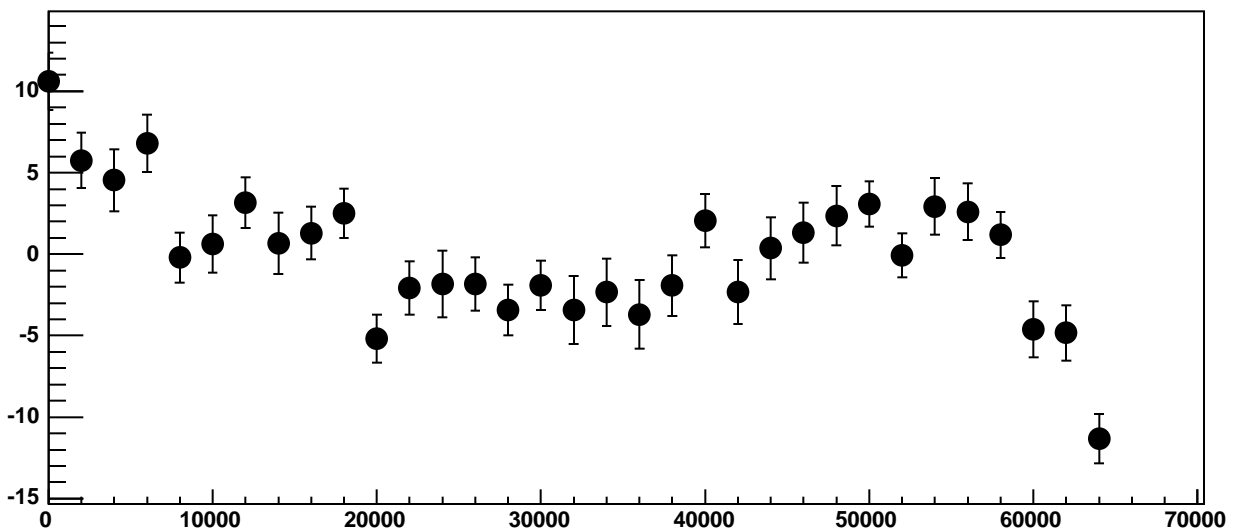
p1

$0.002766 \pm 2.264e-05$

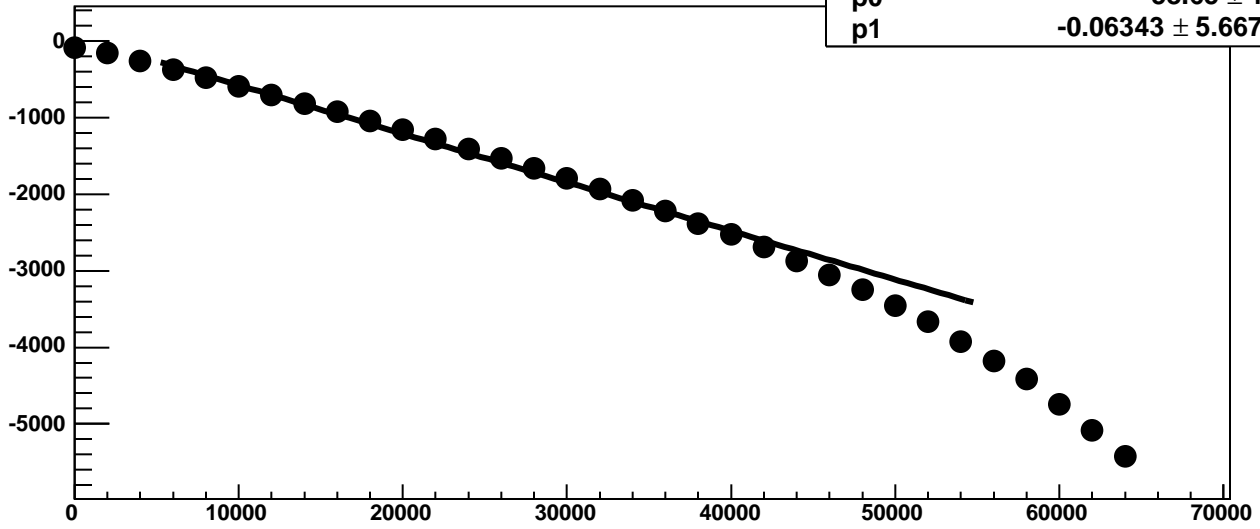
Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

9336 / 23

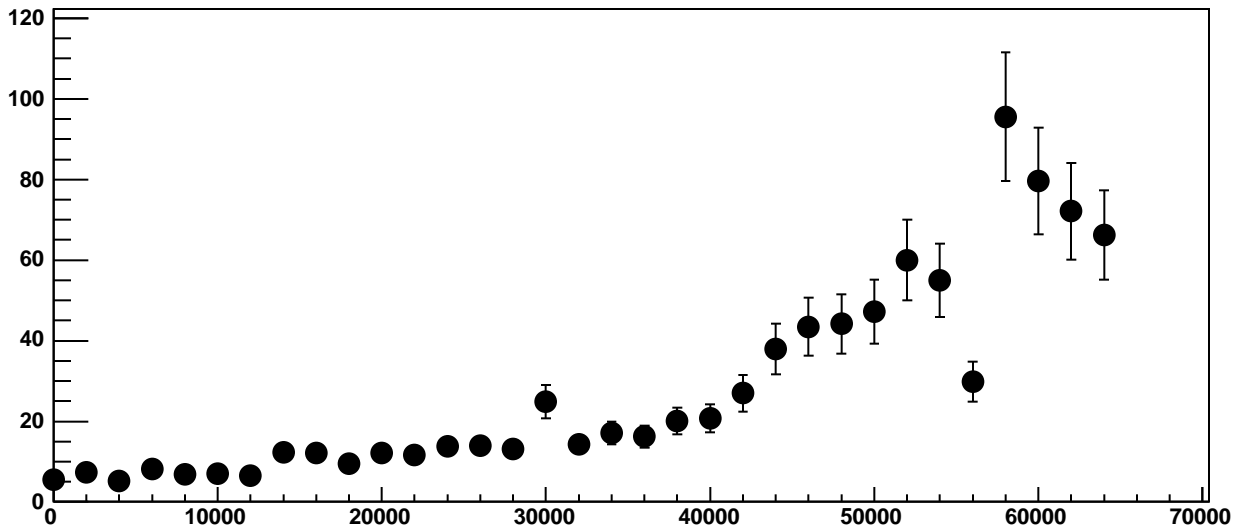
p0

58.65 ± 1.133

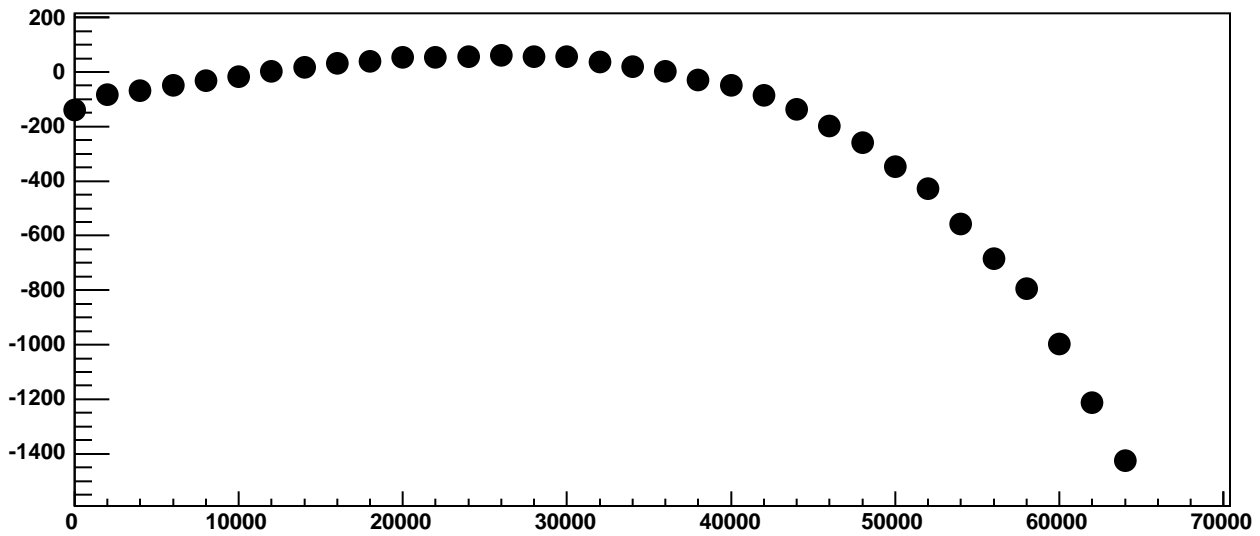
p1

$-0.06343 \pm 5.667\text{e-}05$

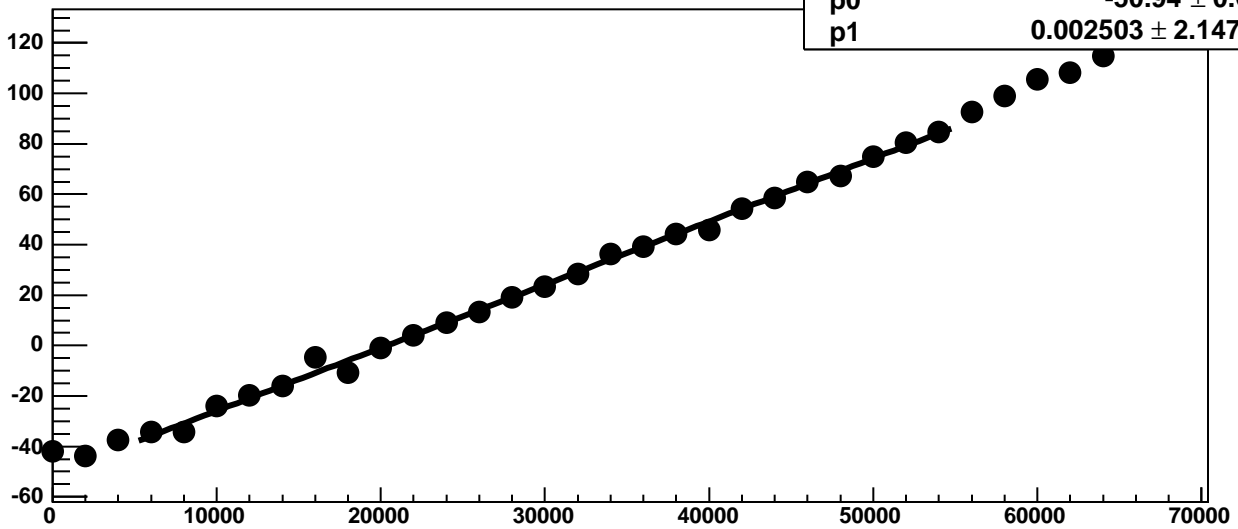
Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

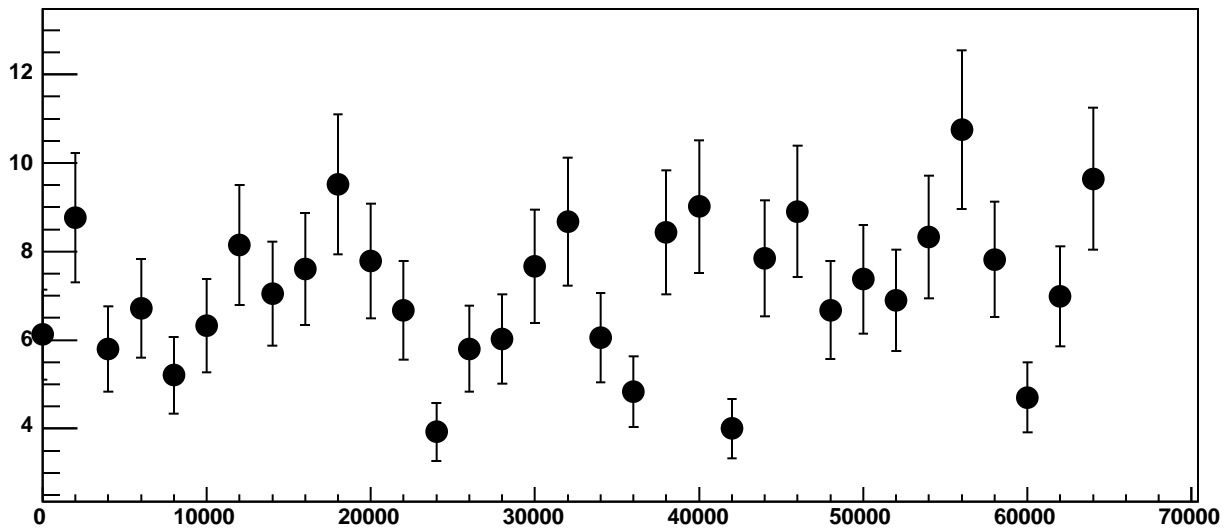


Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

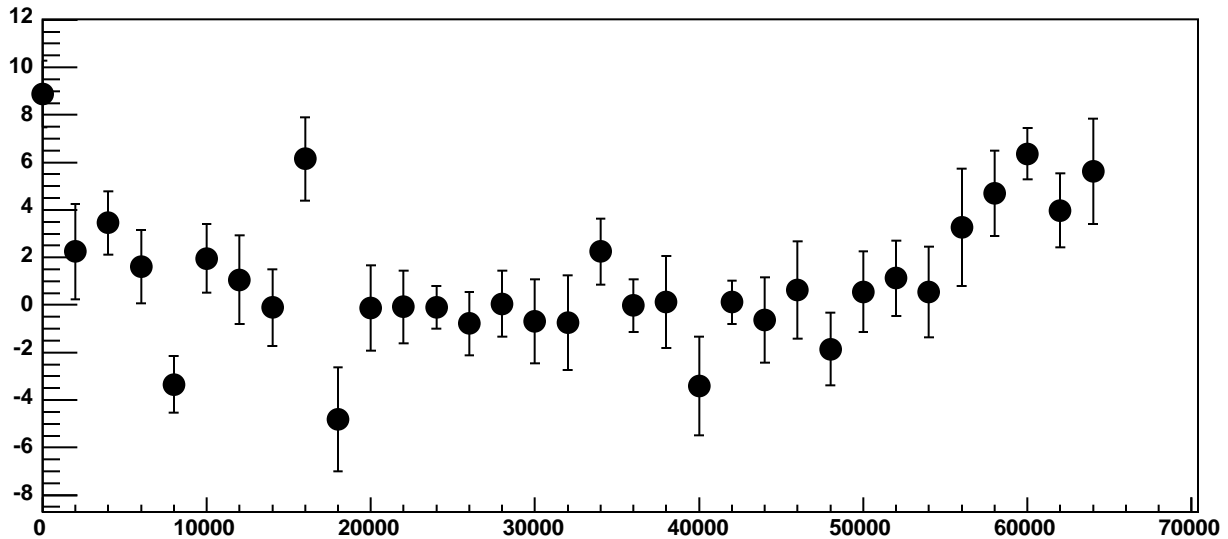


χ^2 / ndf 36.75 / 23
p0 -50.94 ± 0.6979
p1 $0.002503 \pm 2.147\text{e-}05$

Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

χ^2 / ndf

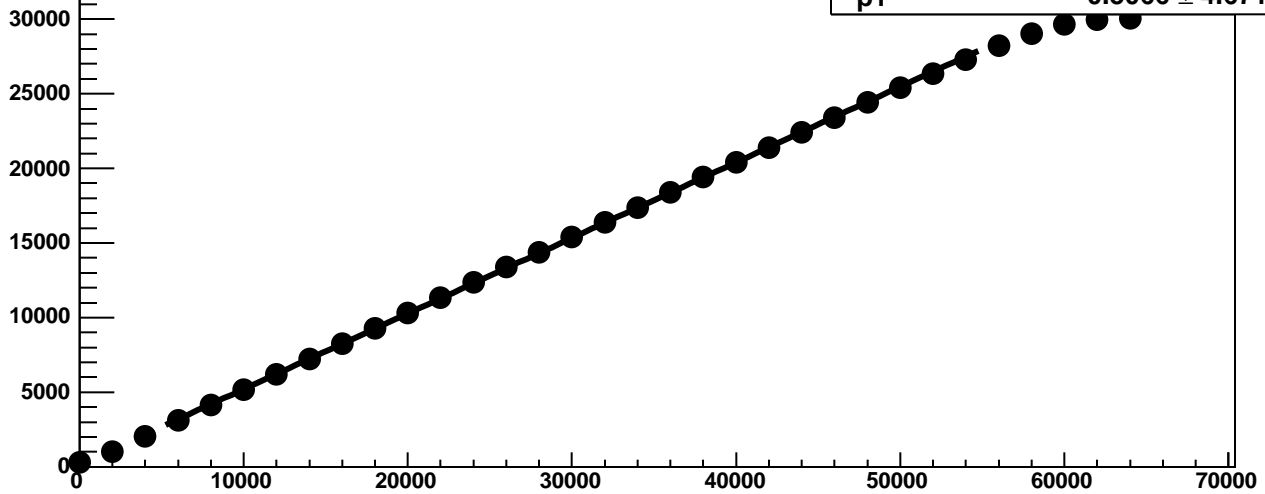
8080 / 23

p0

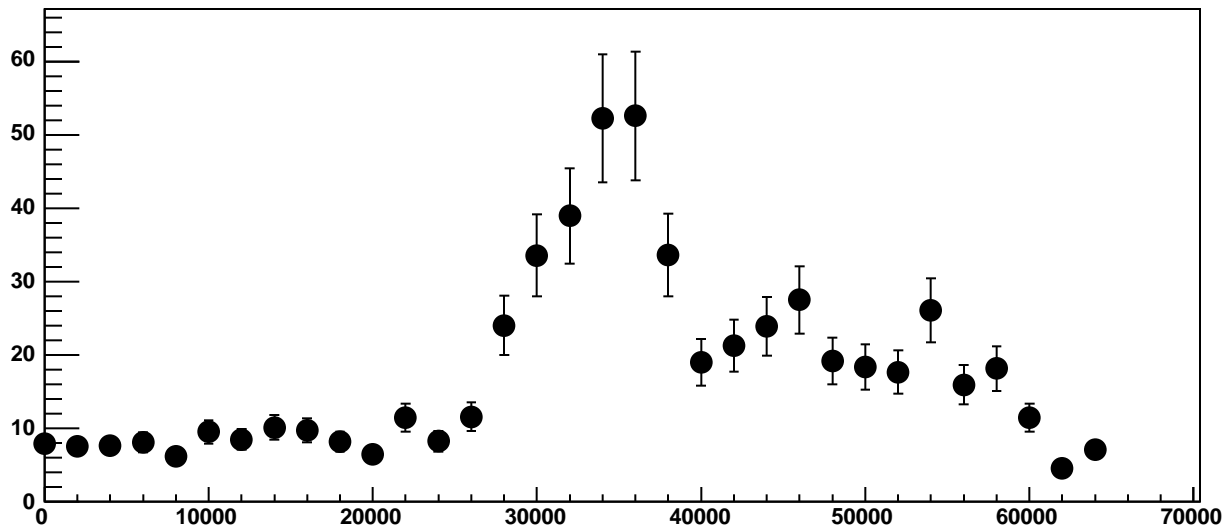
142.2 ± 1.037

p1

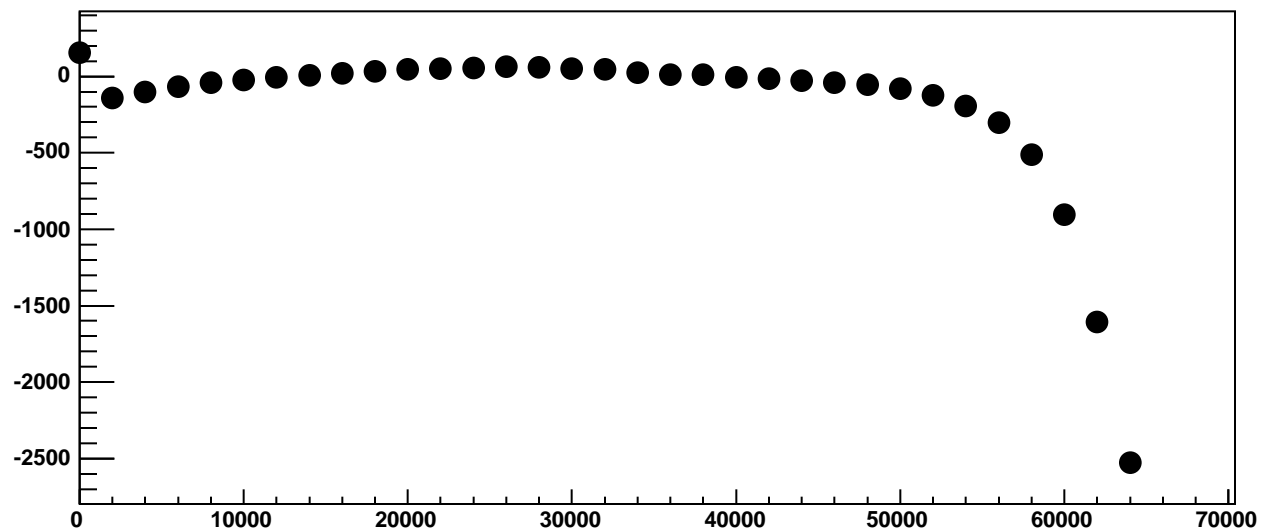
$0.5066 \pm 4.671\text{e-}05$



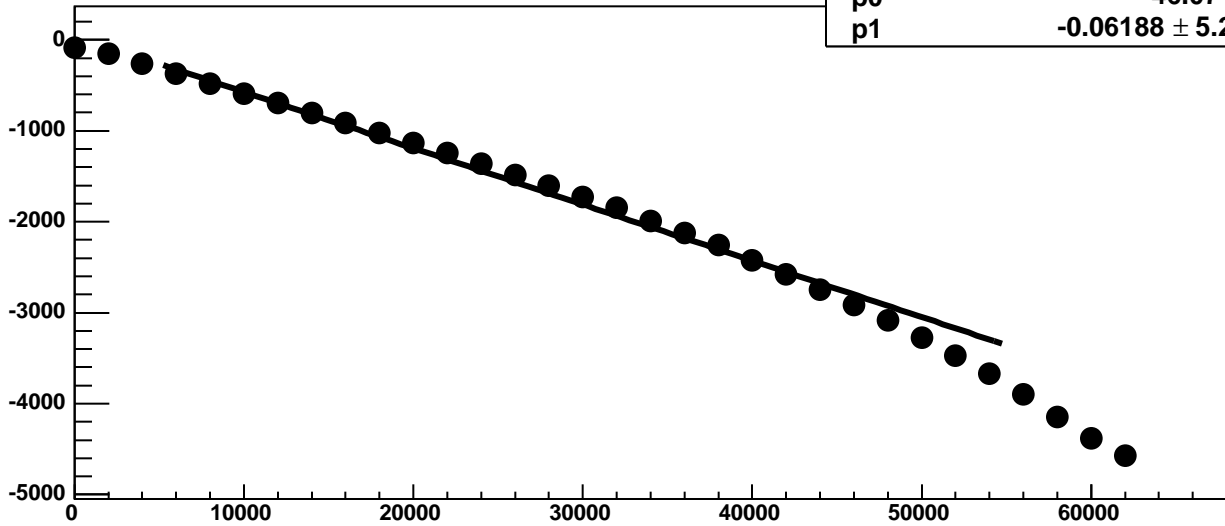
Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1.153e+04 / 23

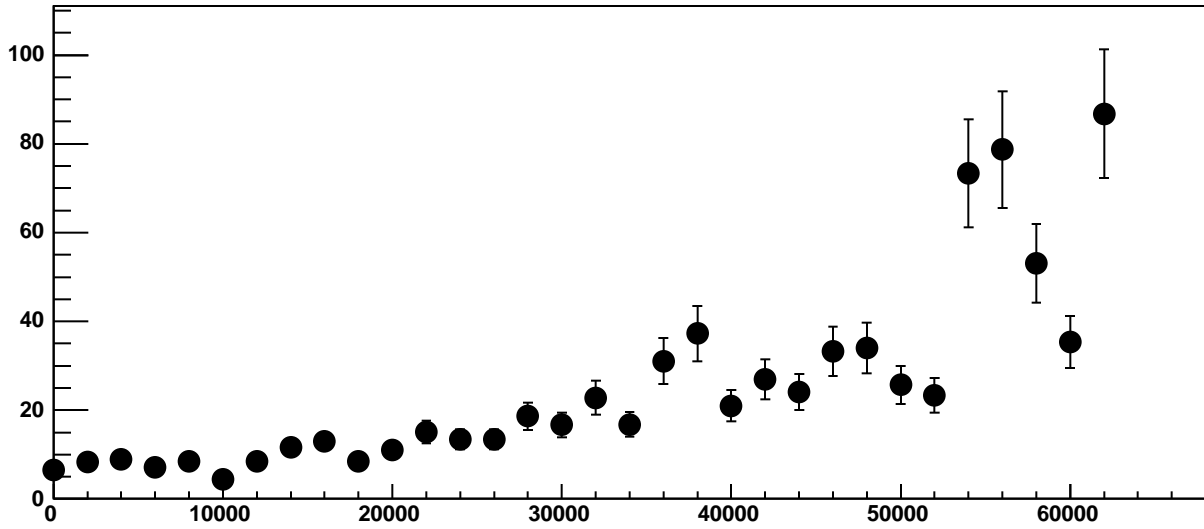
p0

46.07 ± 1.009

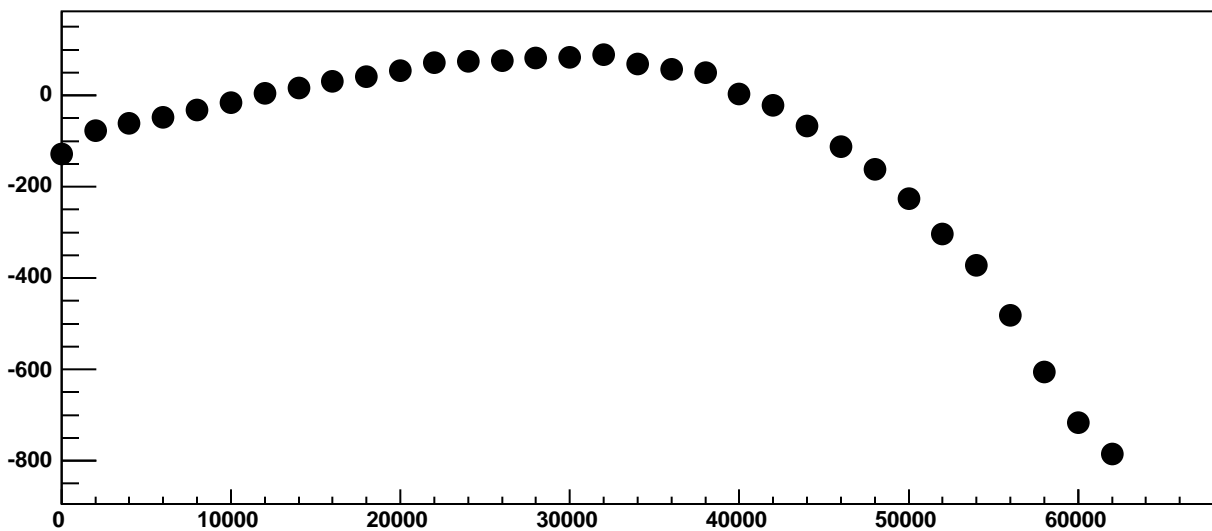
p1

-0.06188 ± 5.209e-05

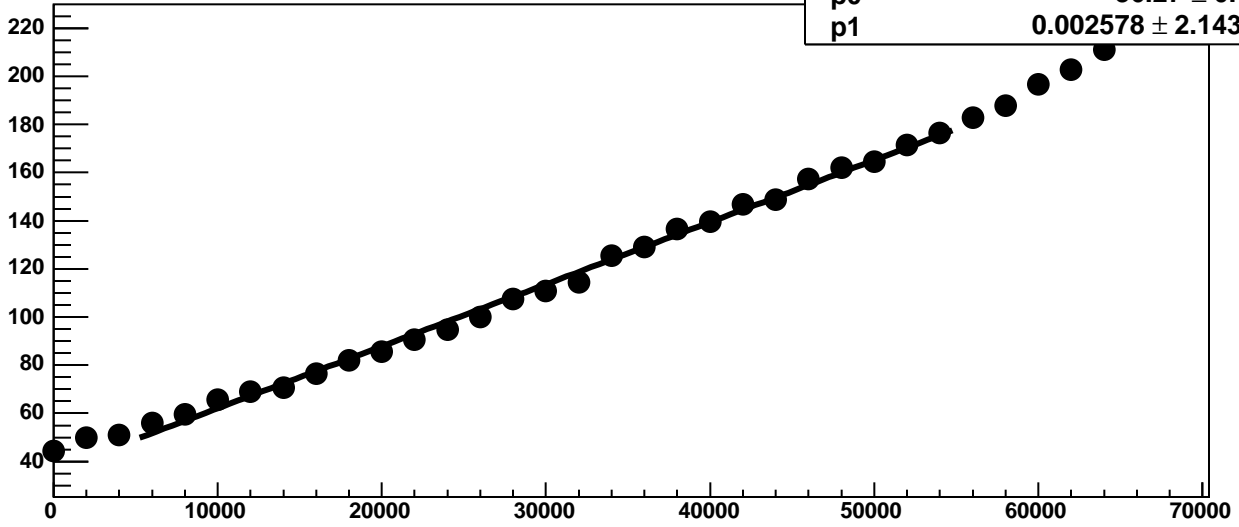
Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



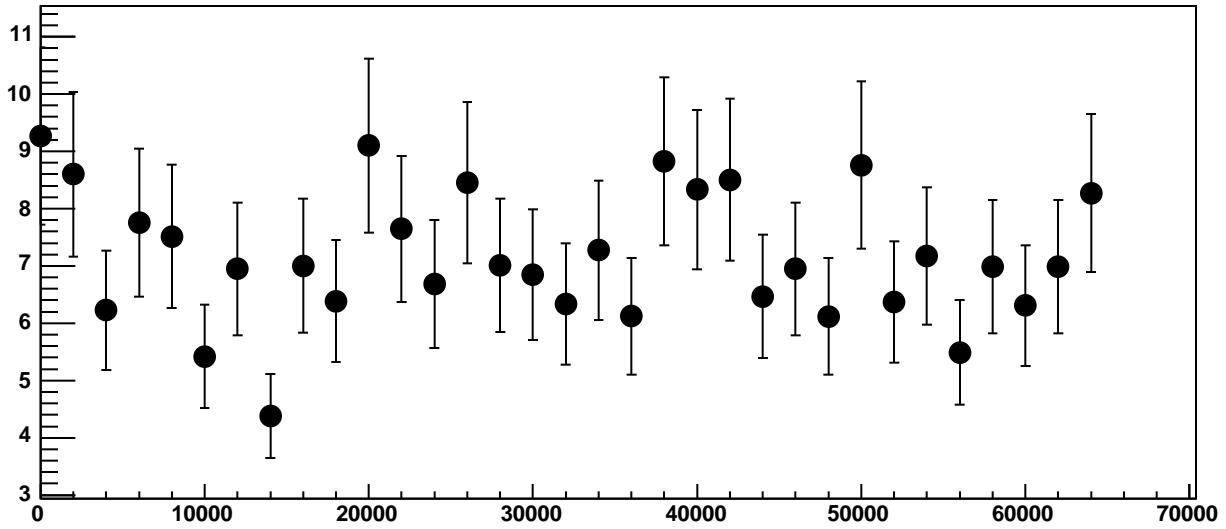
Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



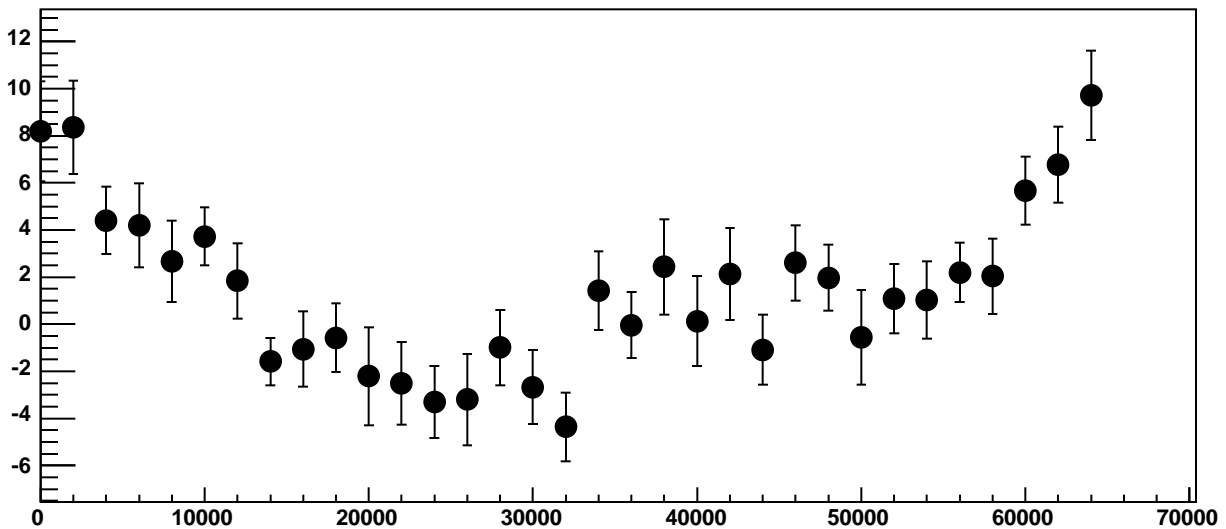
Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

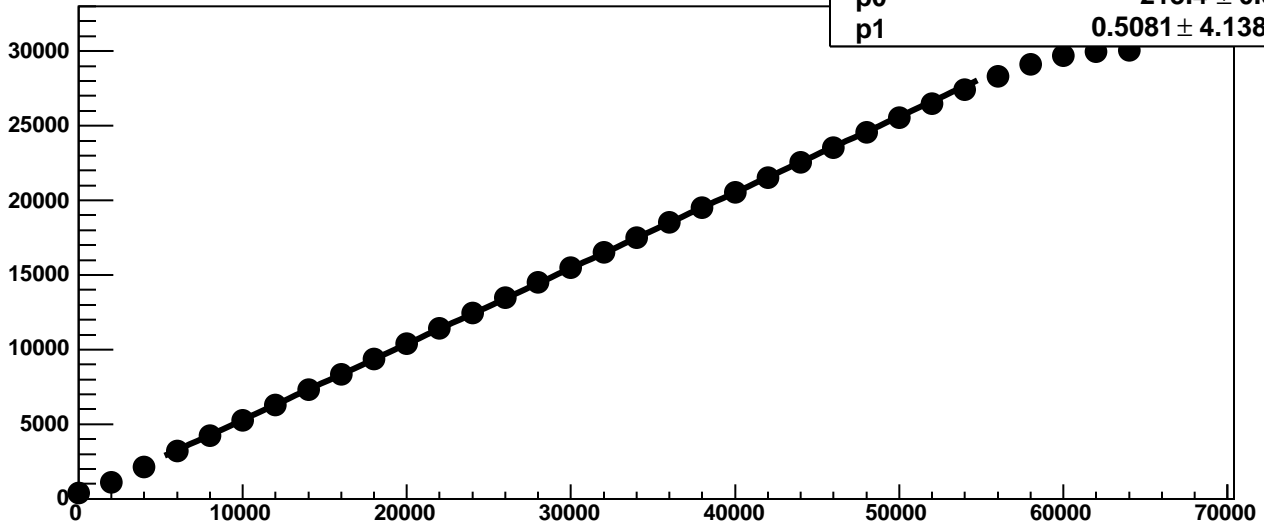


Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

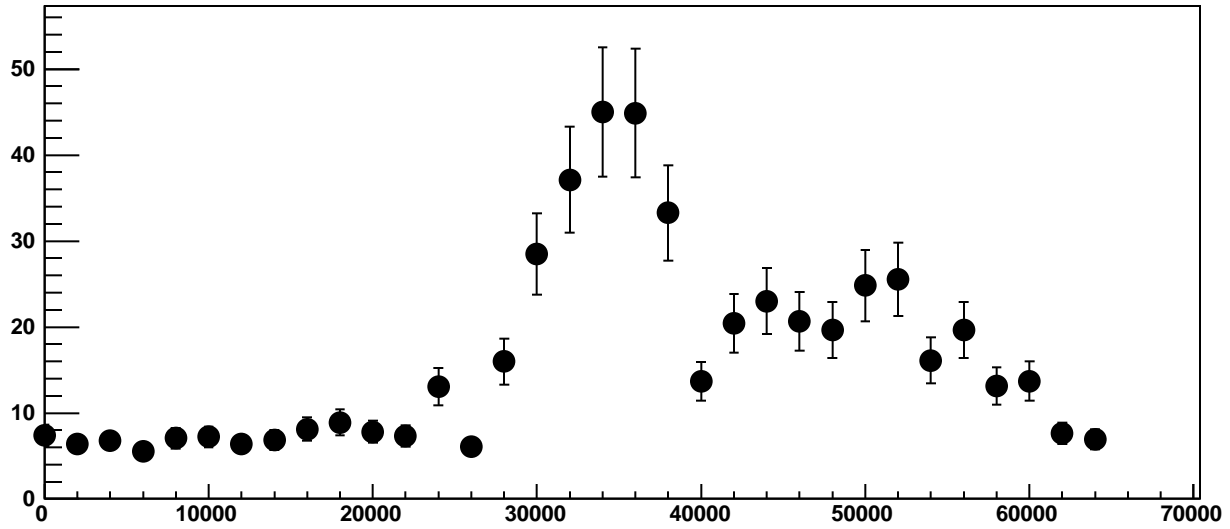


Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

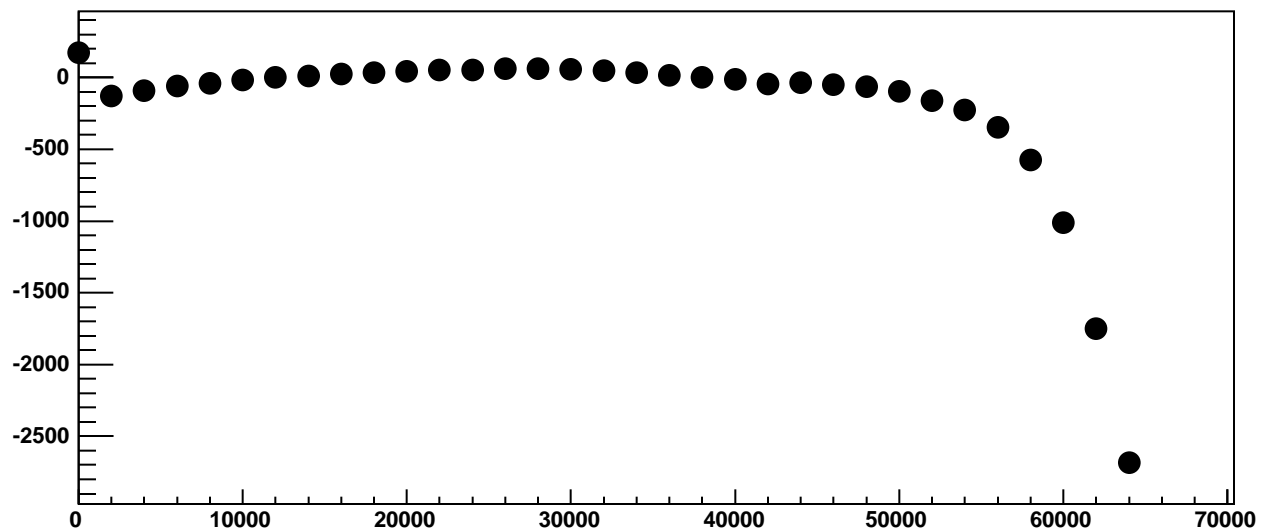
χ^2 / ndf 1.276e+04 / 23
p0 213.4 ± 0.8868
p1 0.5081 ± 4.138e-05



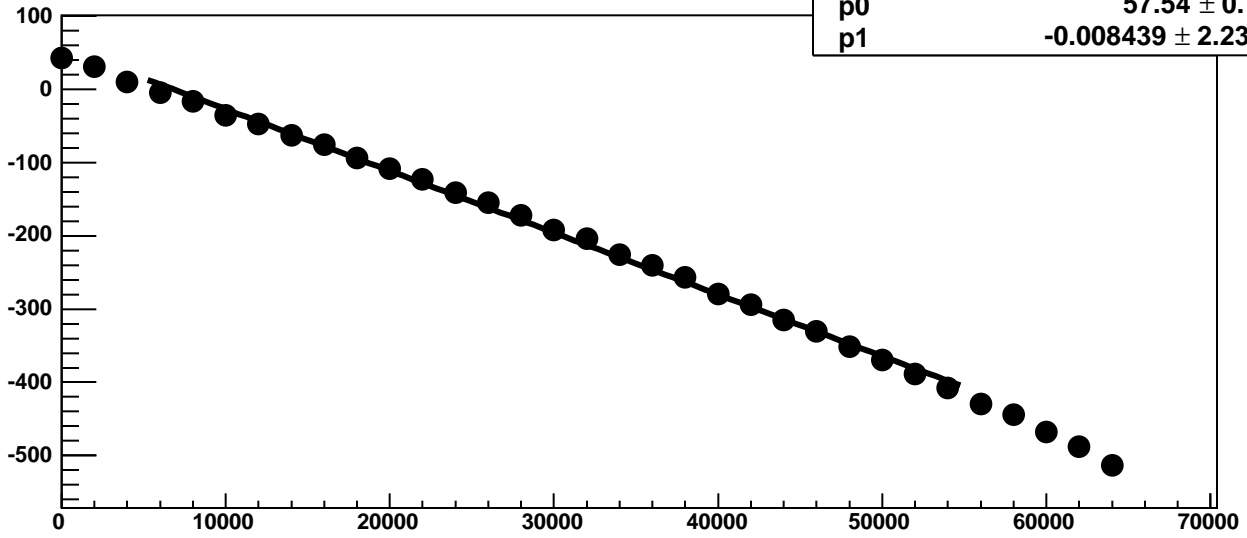
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

315.4 / 23

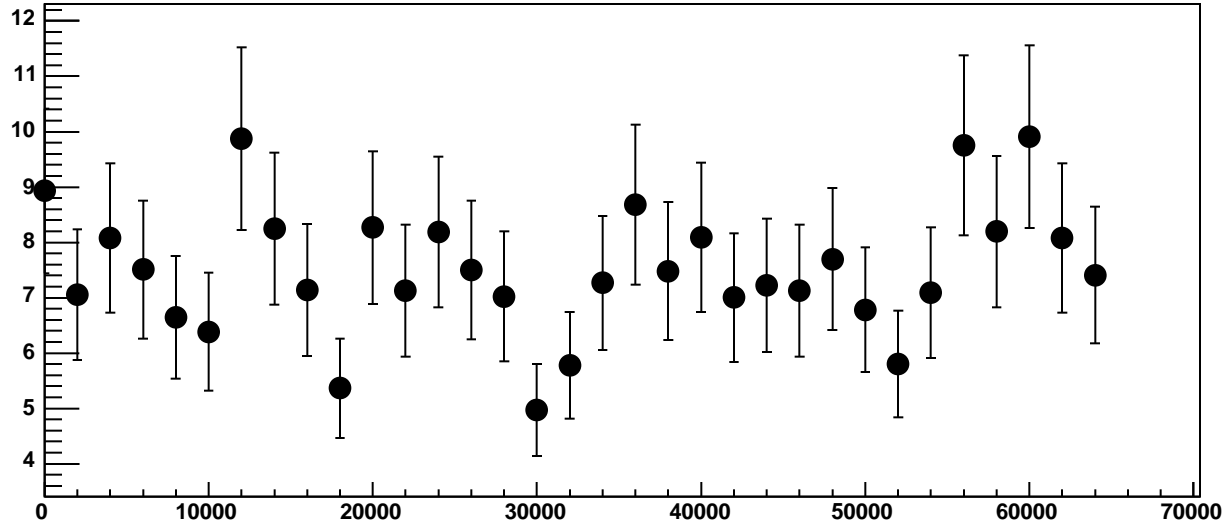
p0

57.54 ± 0.7486

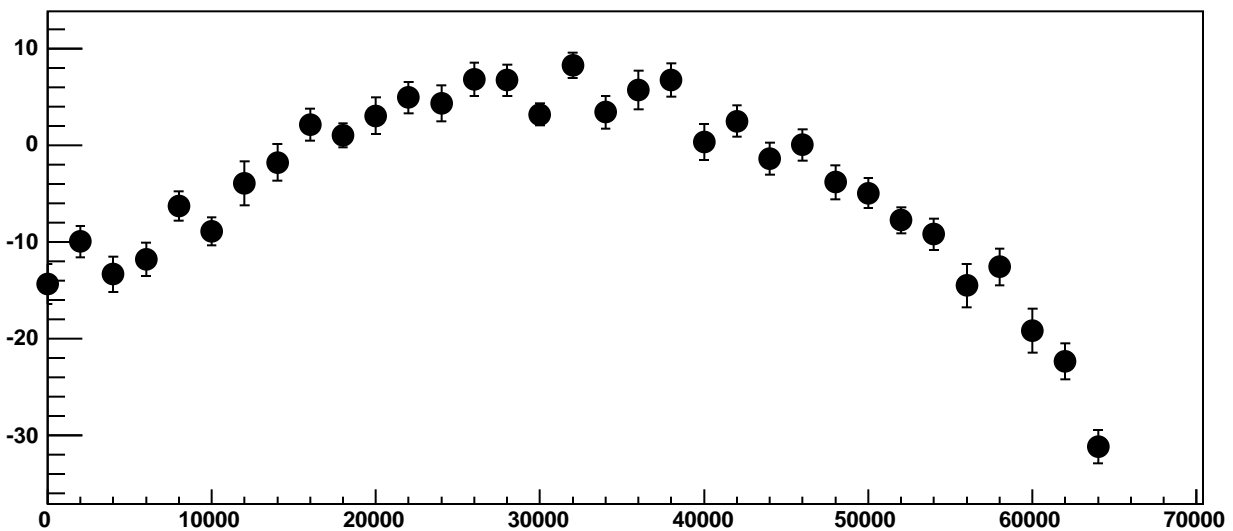
p1

$-0.008439 \pm 2.23e-05$

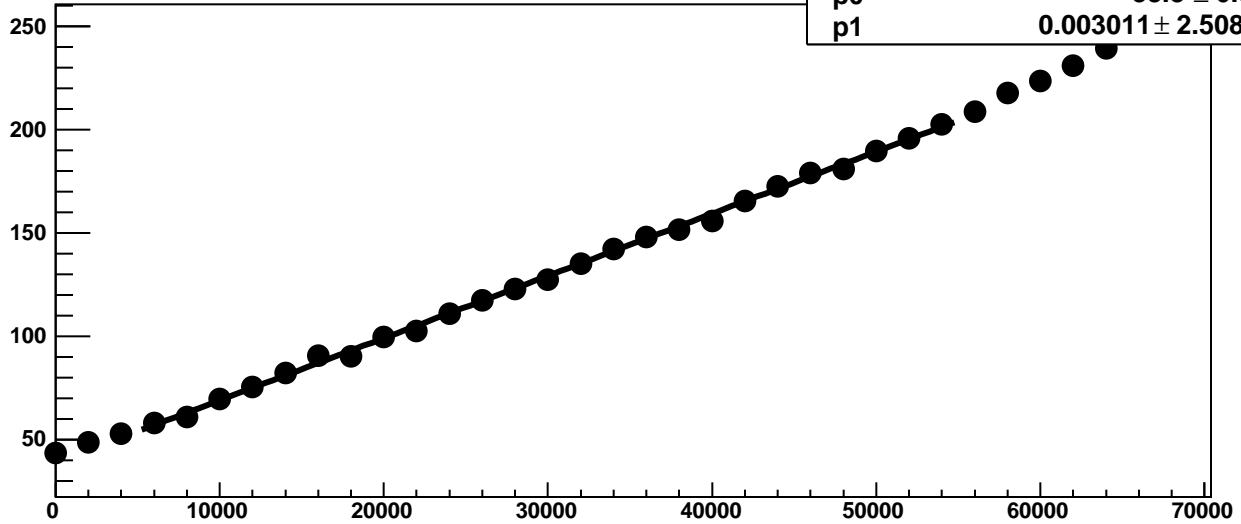
Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



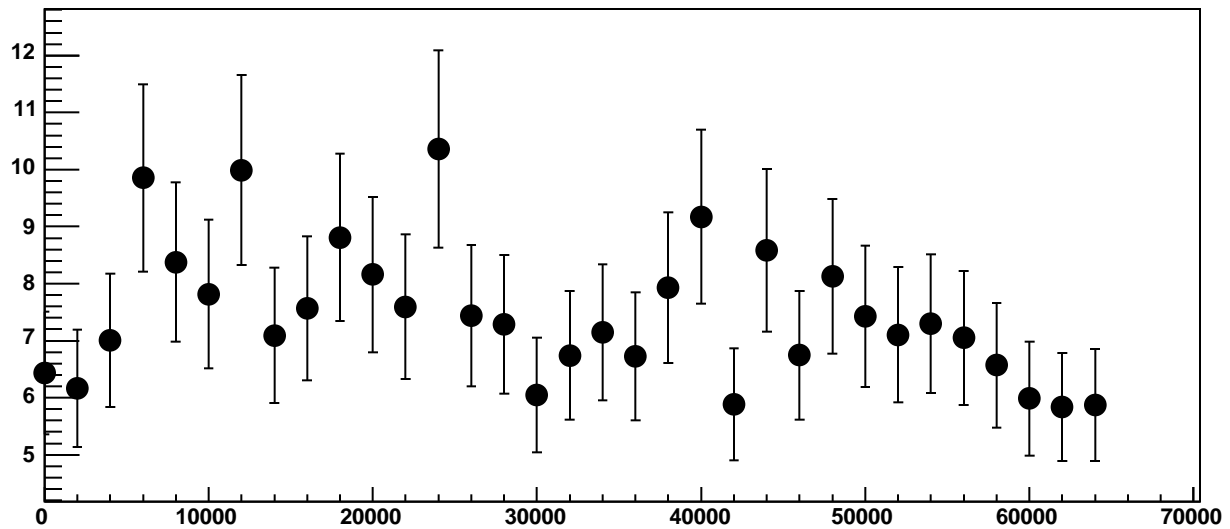
Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



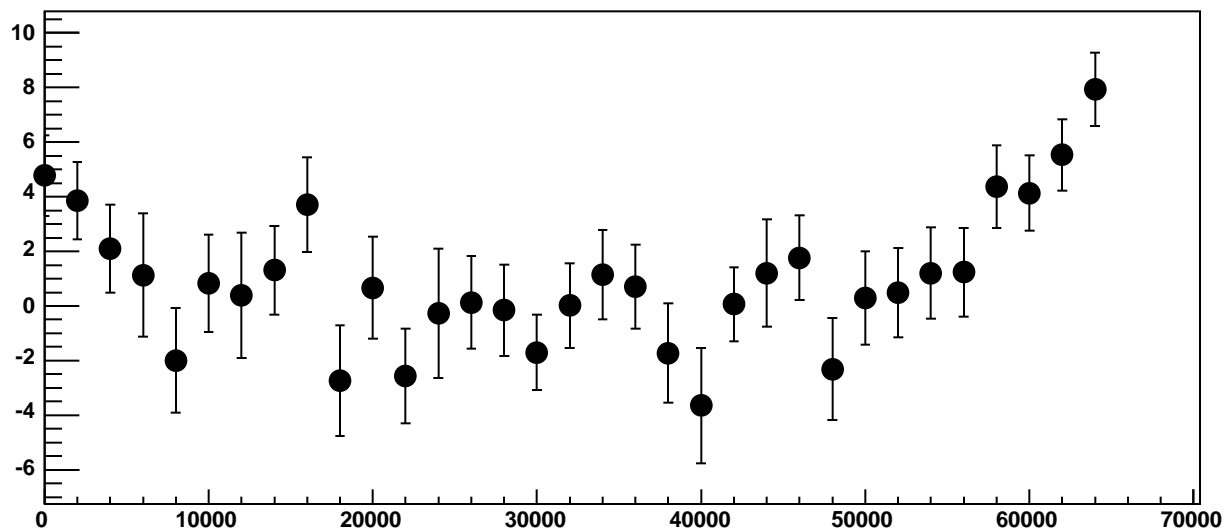
Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



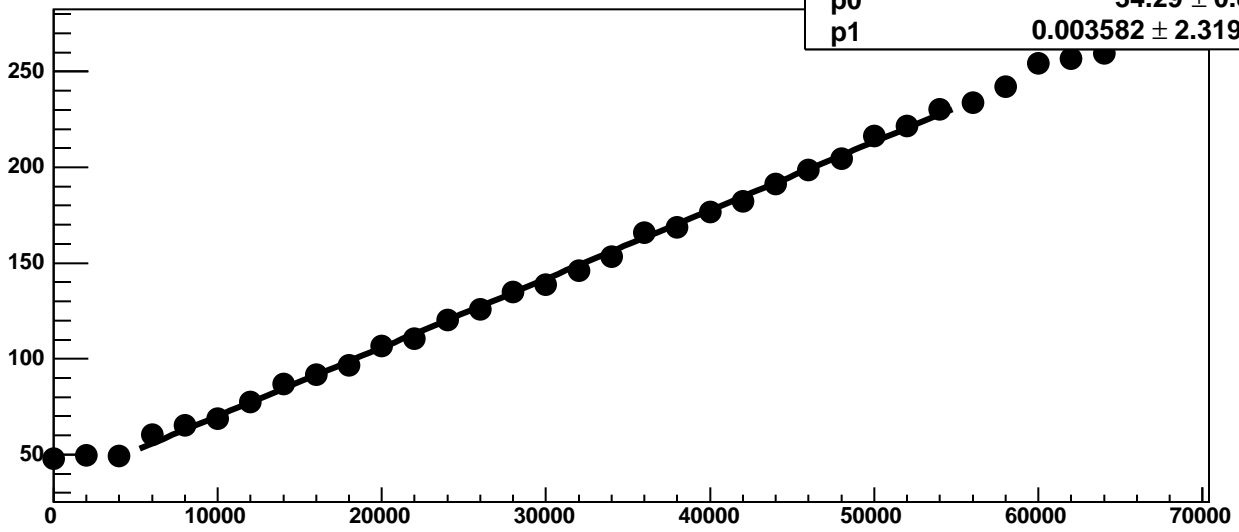
Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

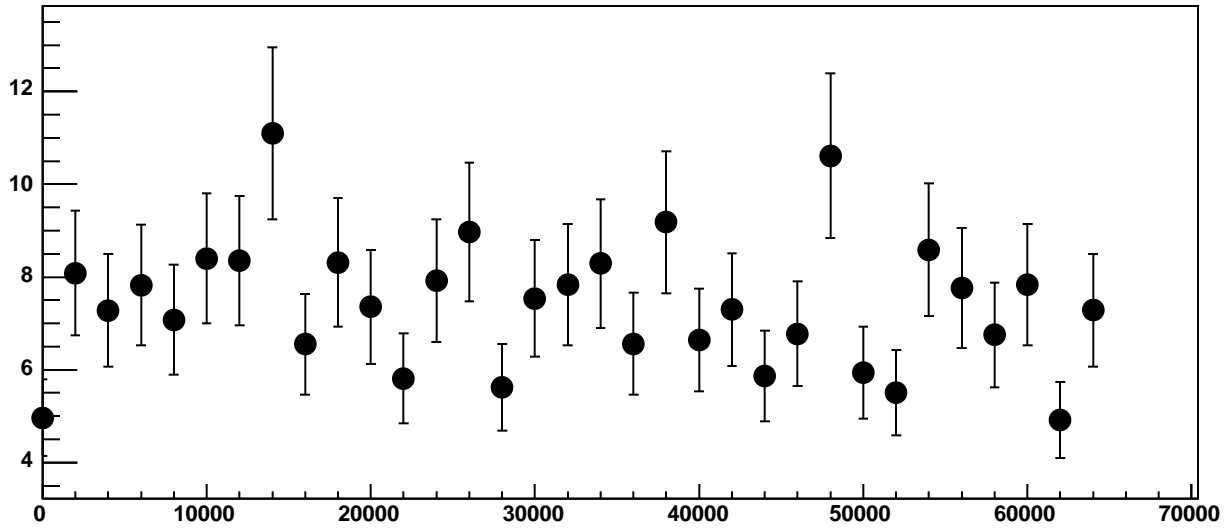


Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

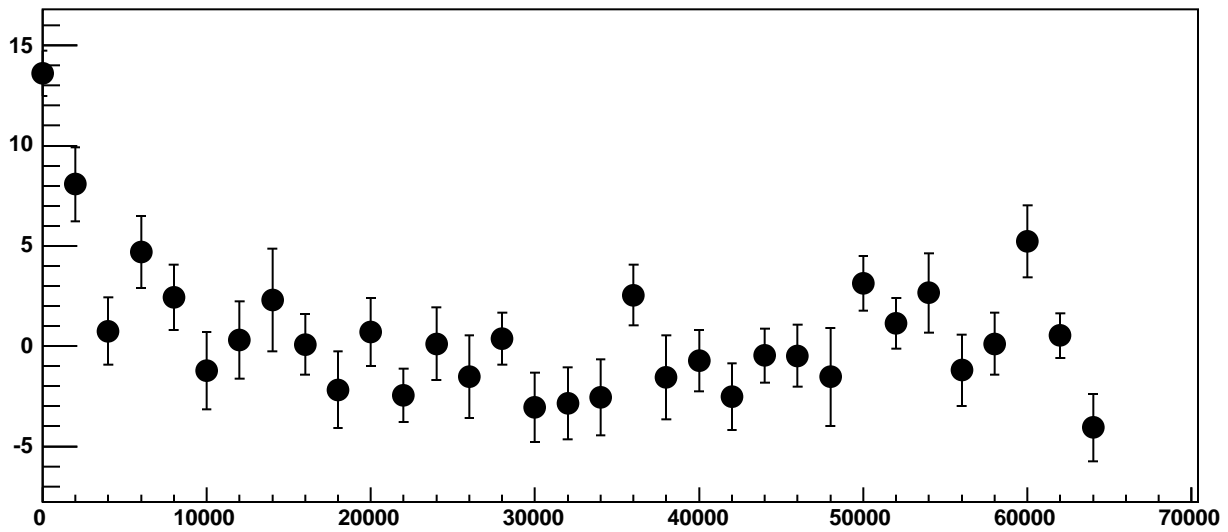


χ^2 / ndf 37.75 / 23
p0 34.29 ± 0.8004
p1 $0.003582 \pm 2.319e-05$

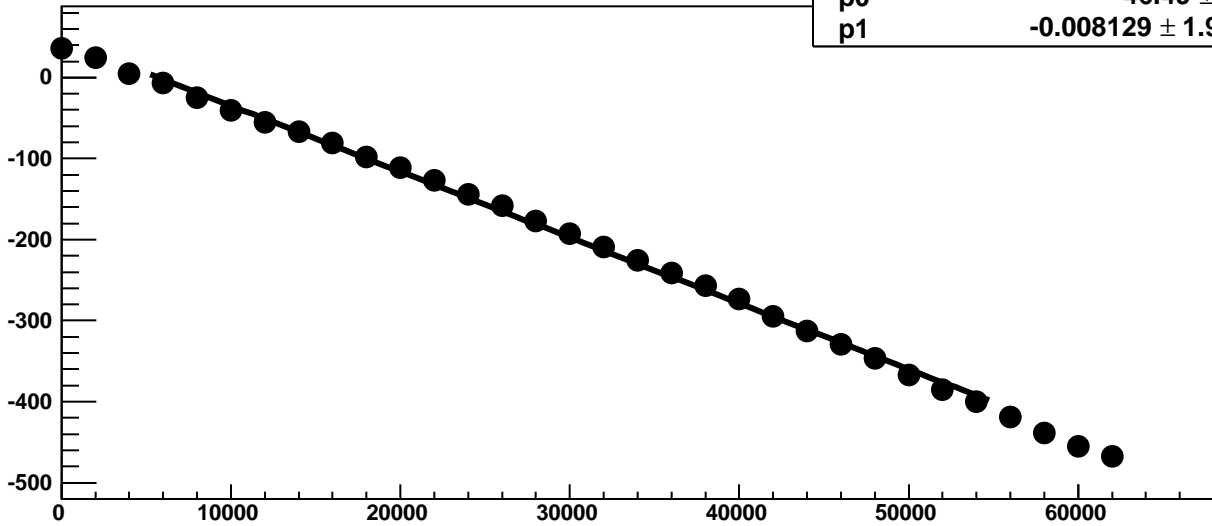
Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

271.9 / 23

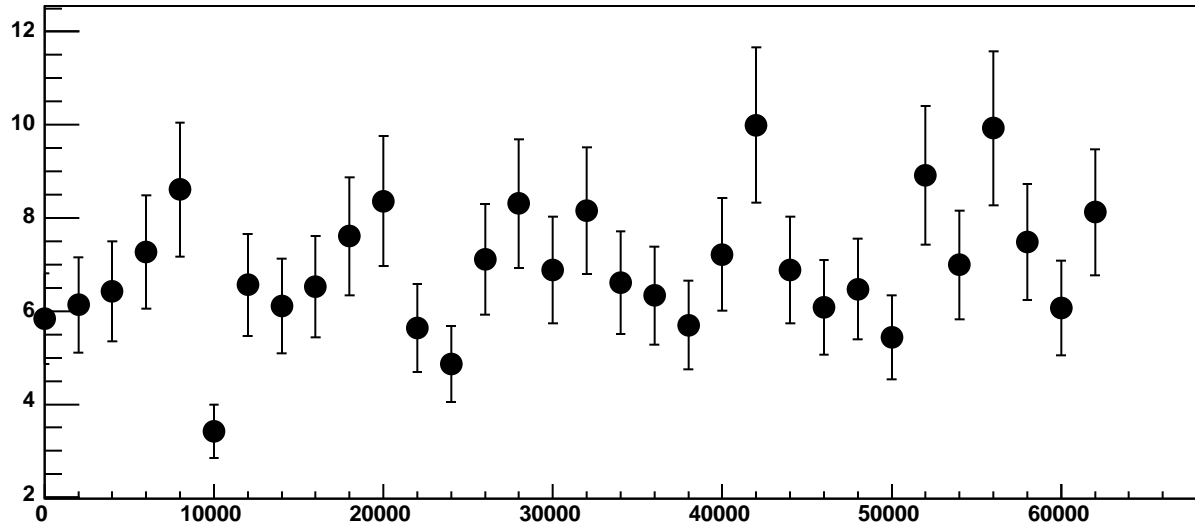
p0

46.49 ± 0.6309

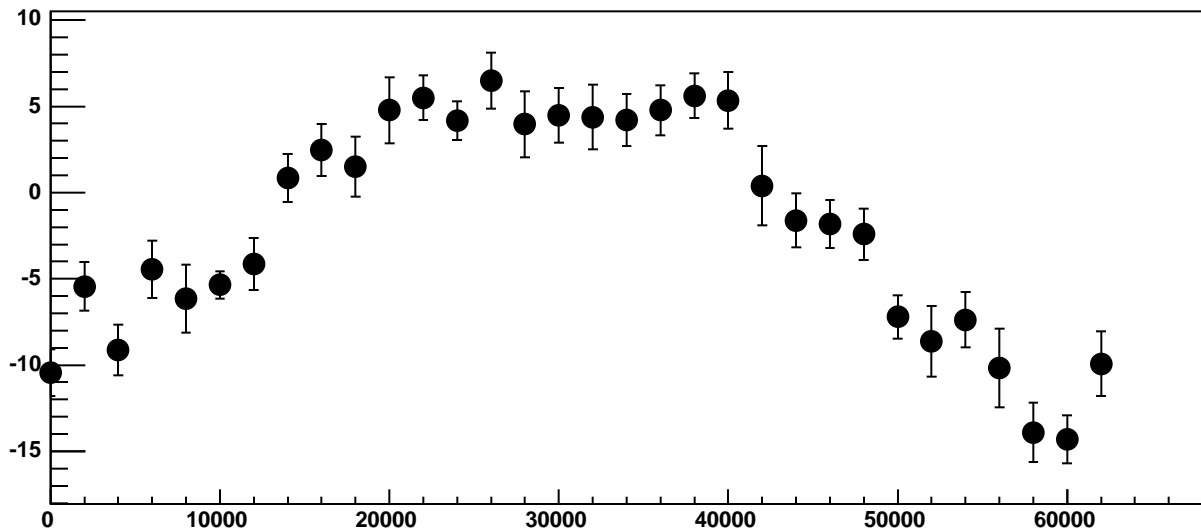
p1

$-0.008129 \pm 1.989e-05$

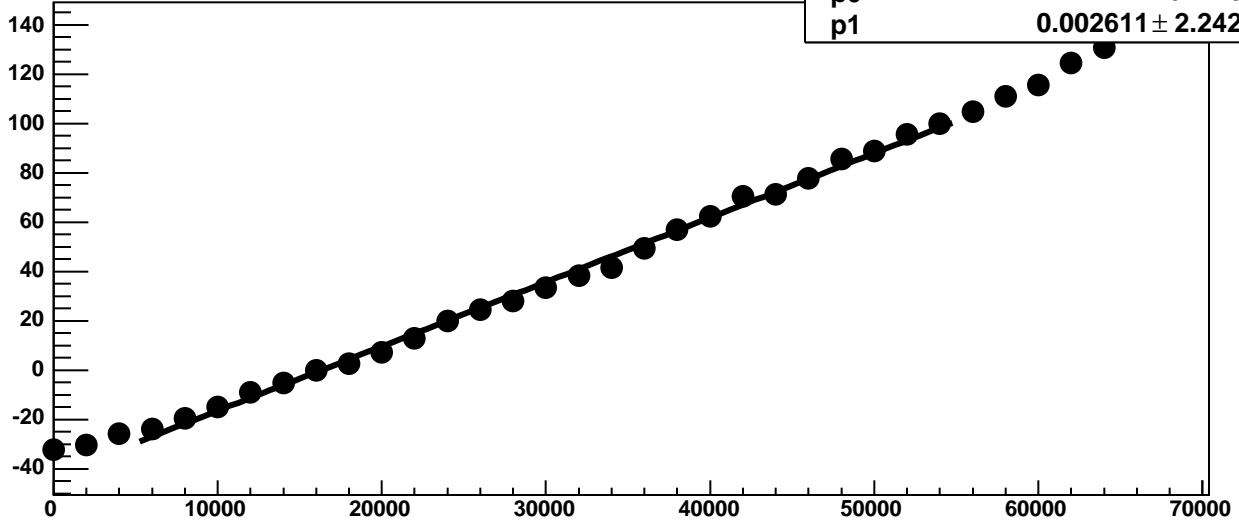
Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.54 / 23

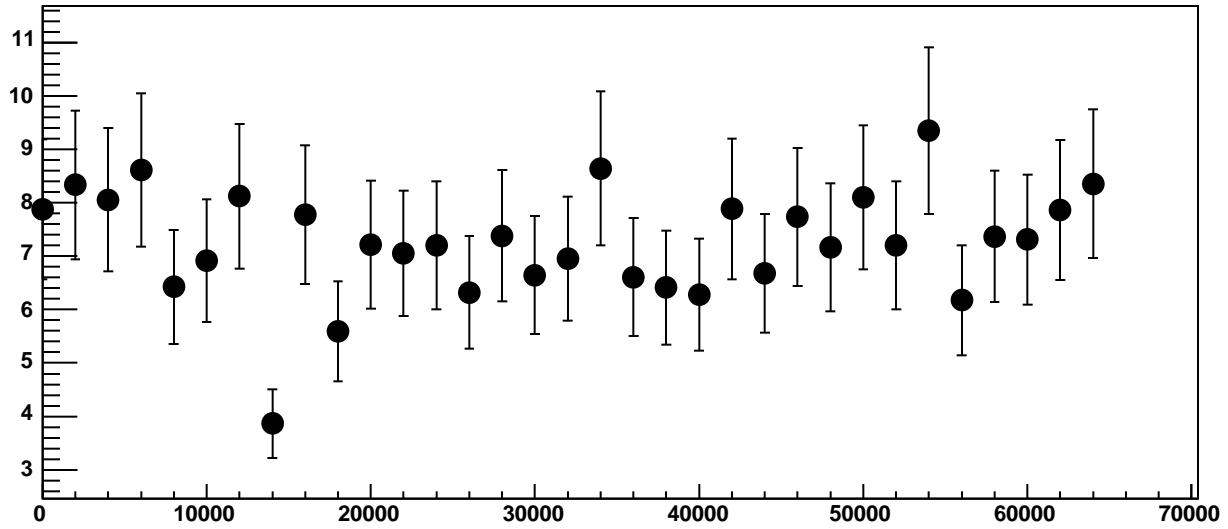
p0

-42.64 ± 0.703

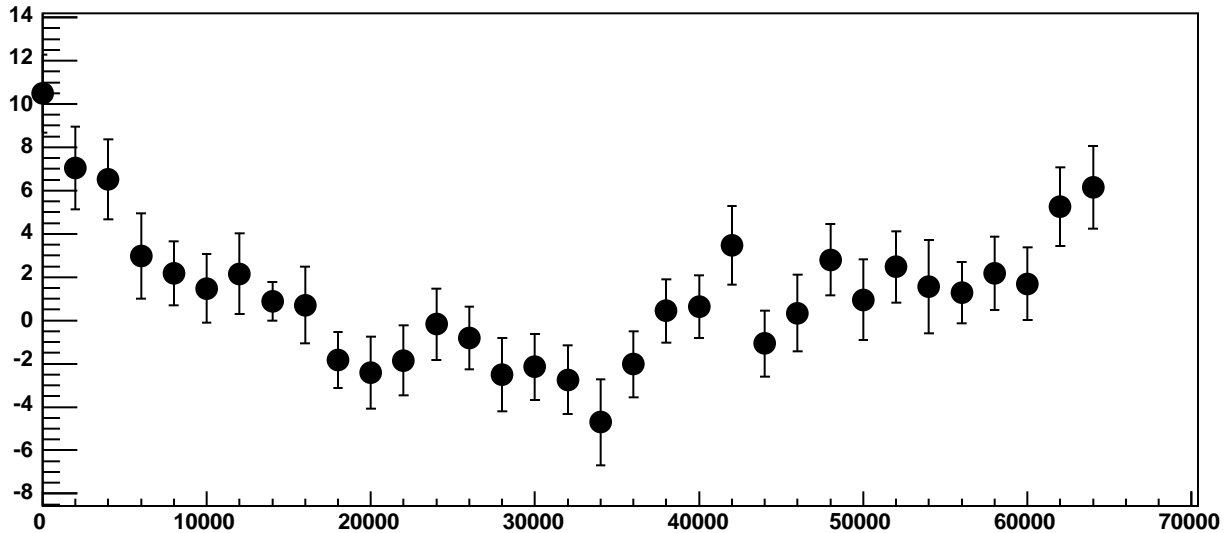
p1

$0.002611 \pm 2.242\text{e-}05$

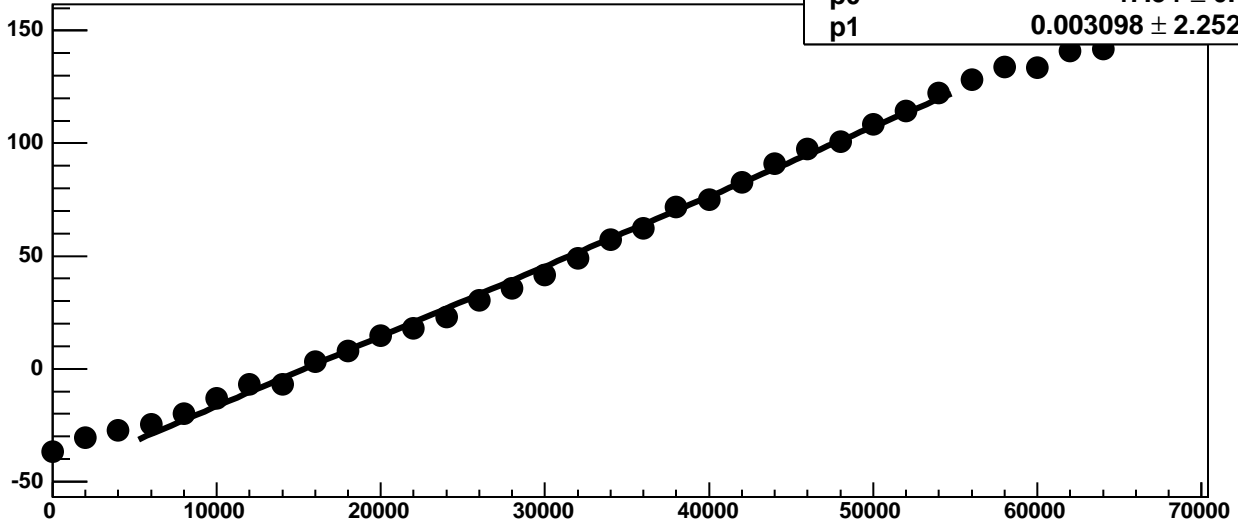
Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



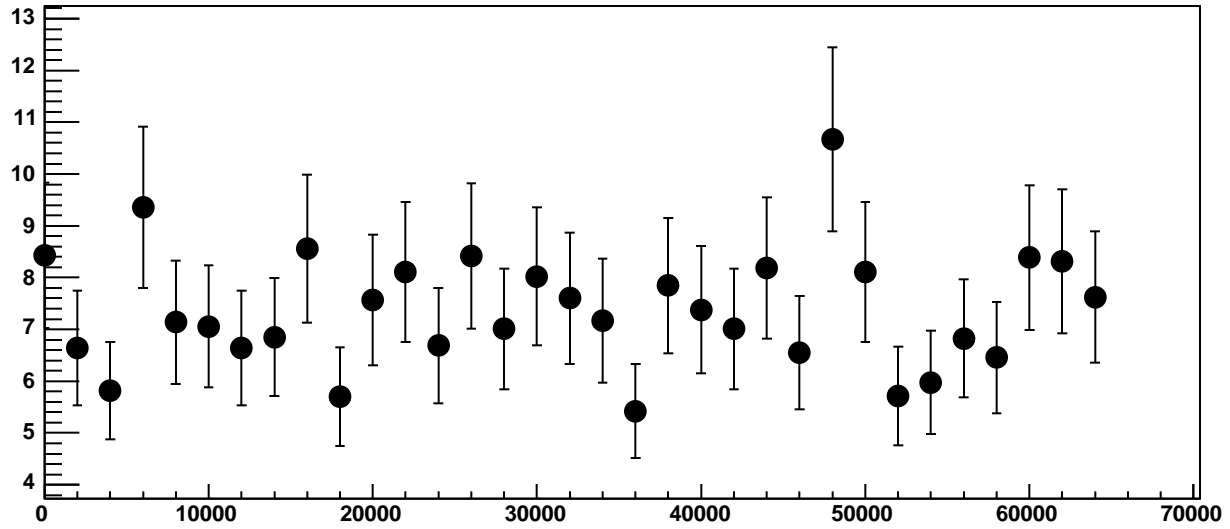
Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



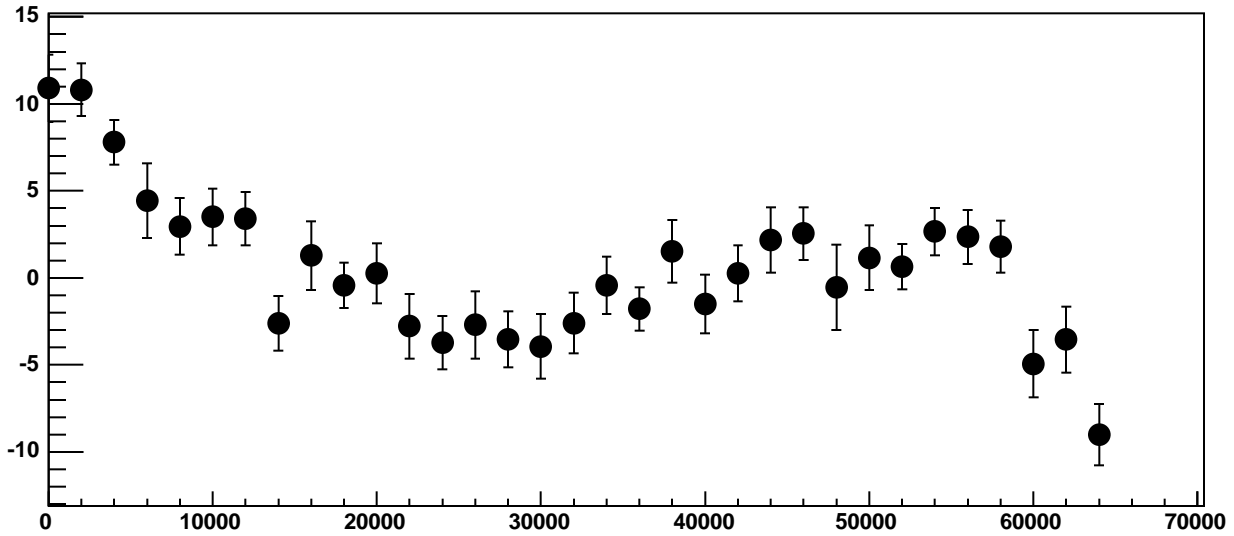
Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



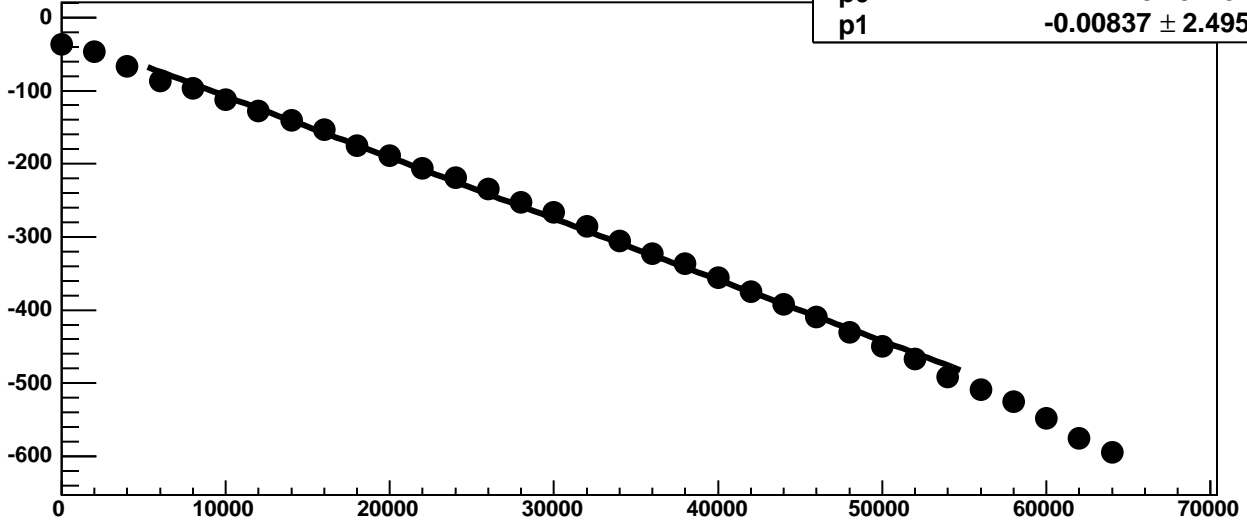
Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



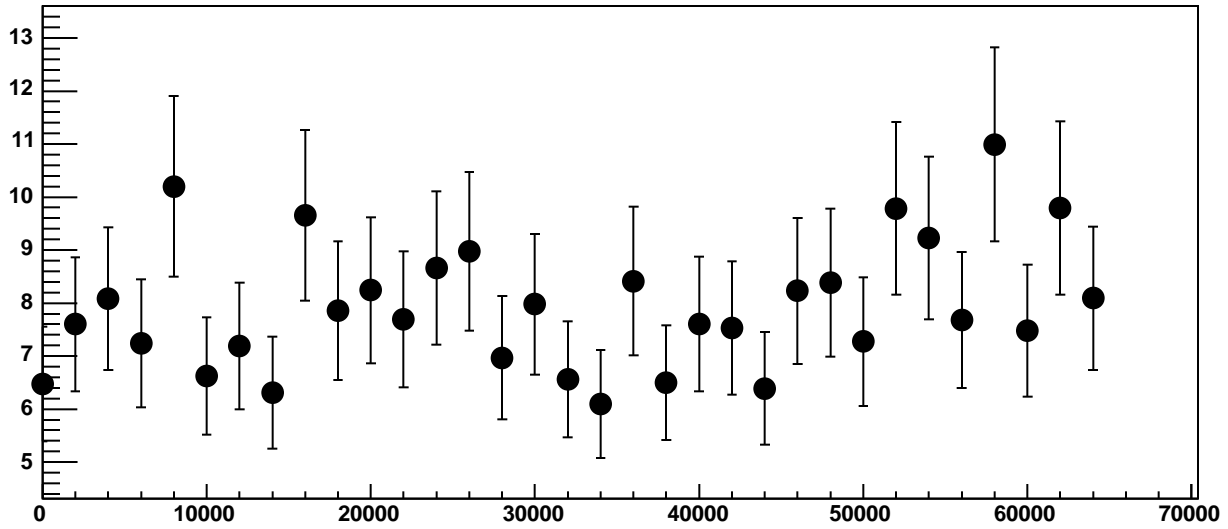
Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



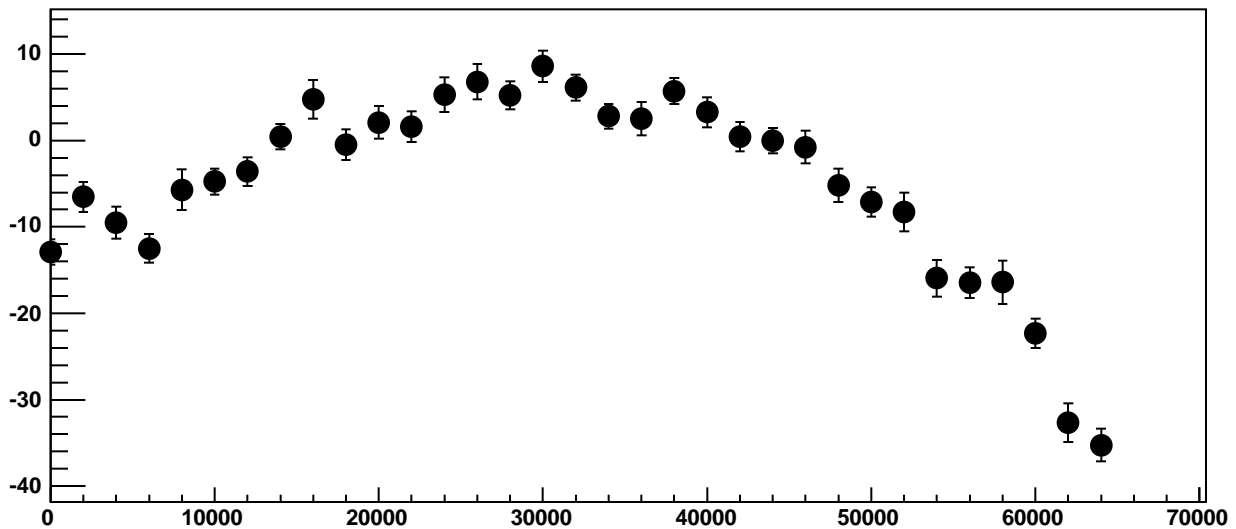
Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



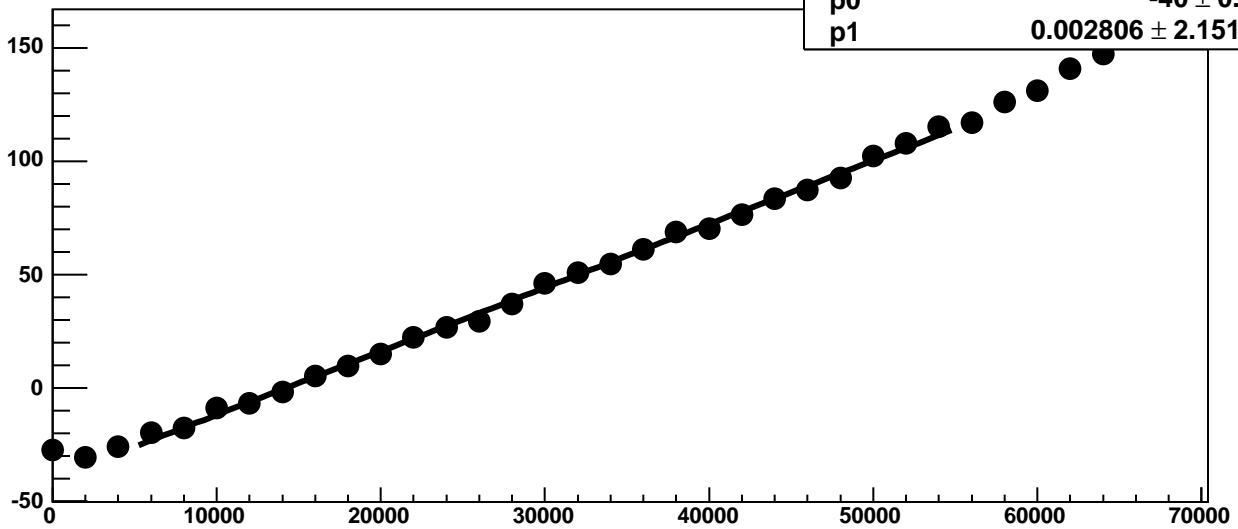
Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



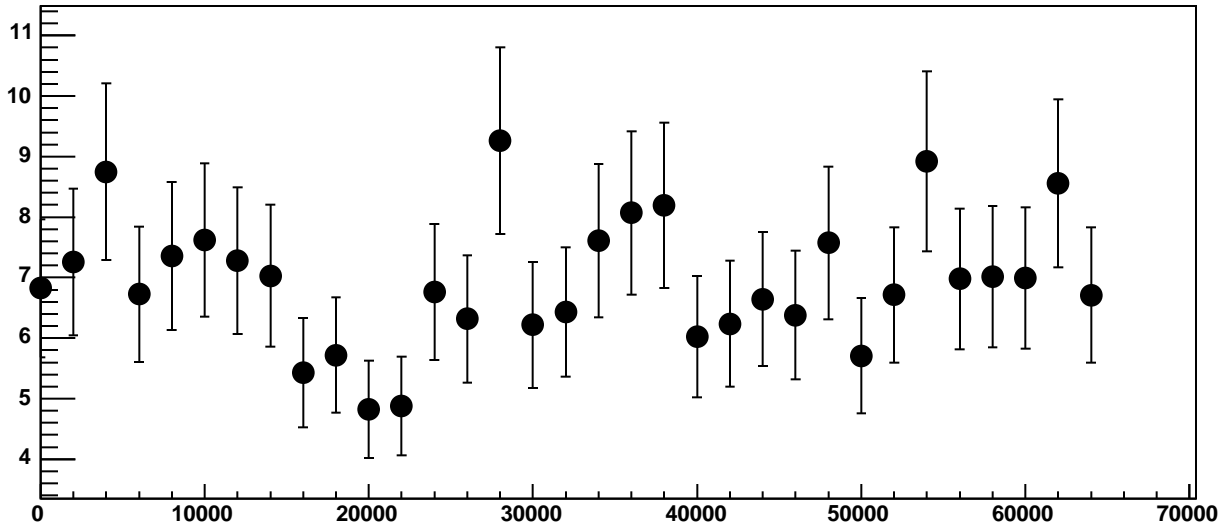
Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



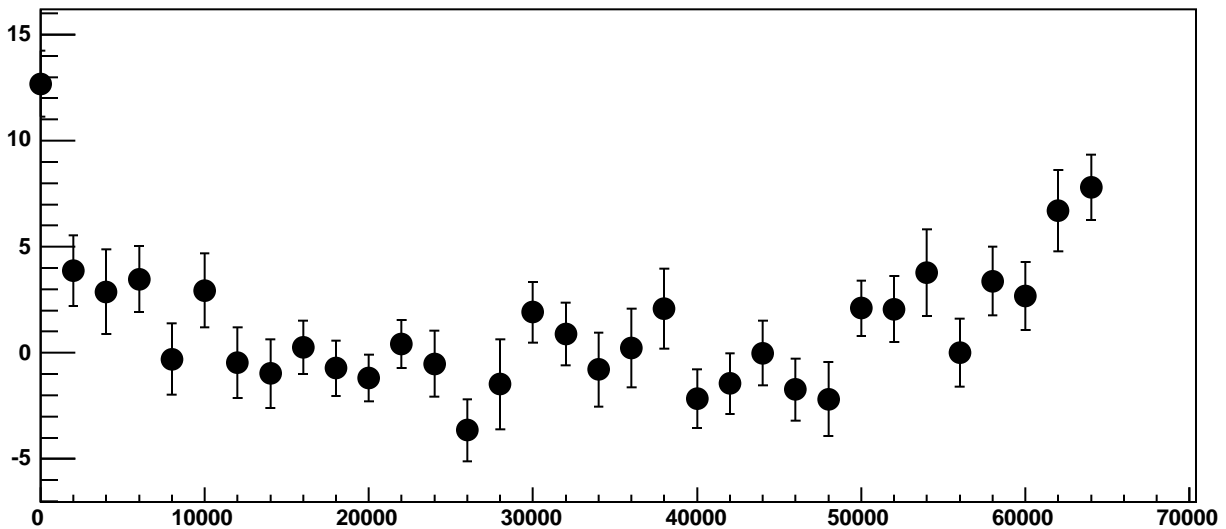
Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

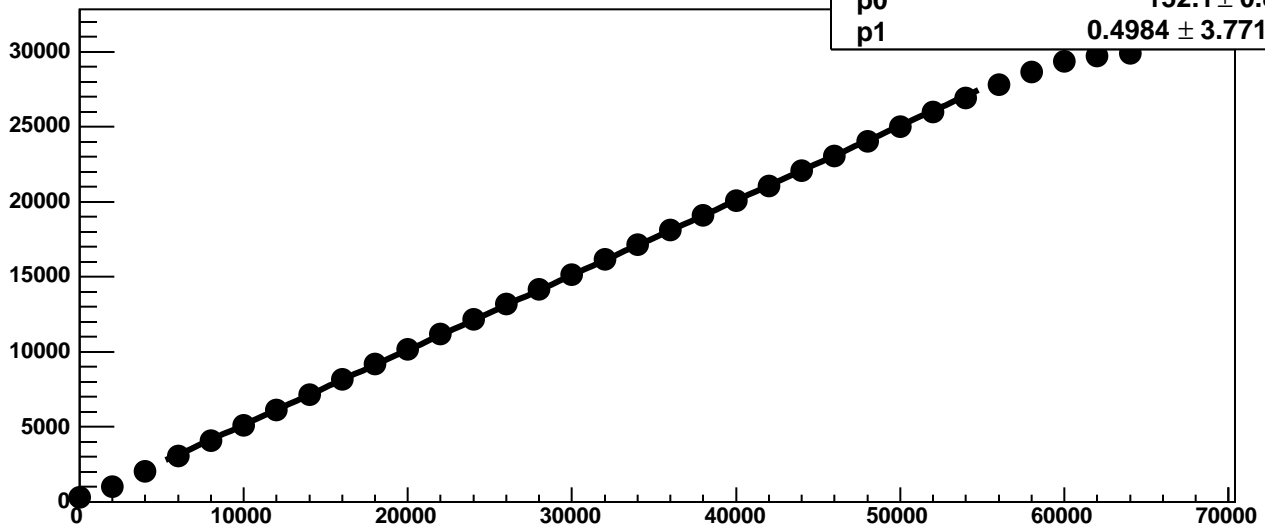


Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

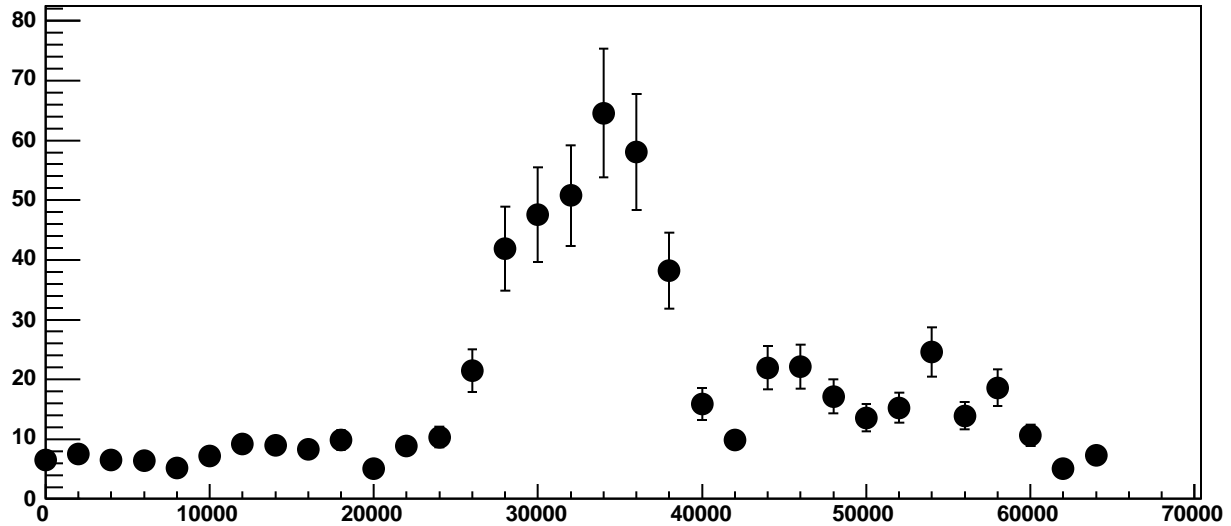


Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

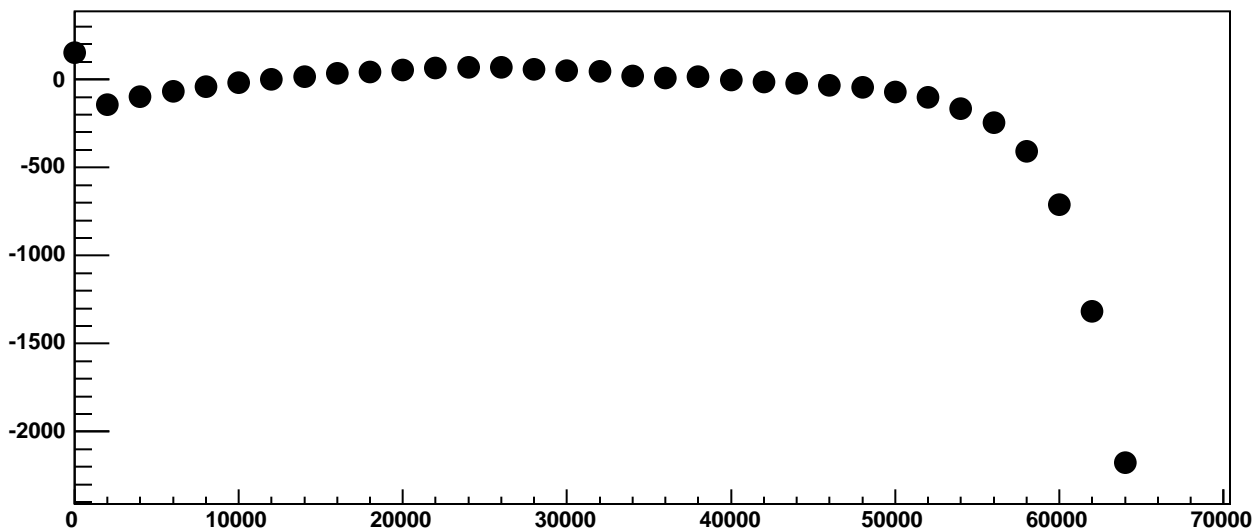
χ^2 / ndf 1.055e+04 / 23
p0 152.1 ± 0.8584
p1 0.4984 ± 3.771e-05



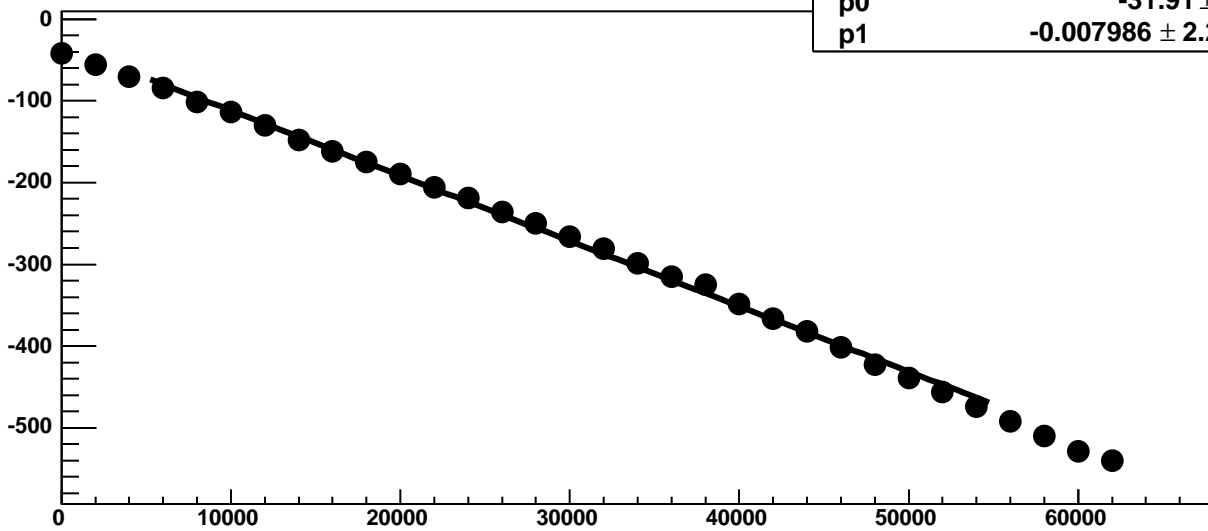
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

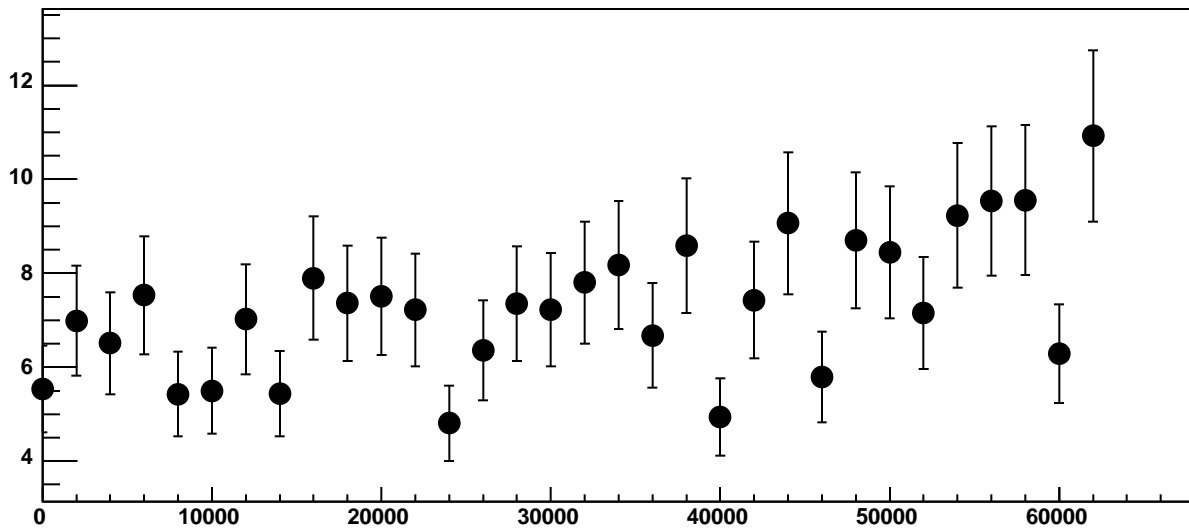


Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

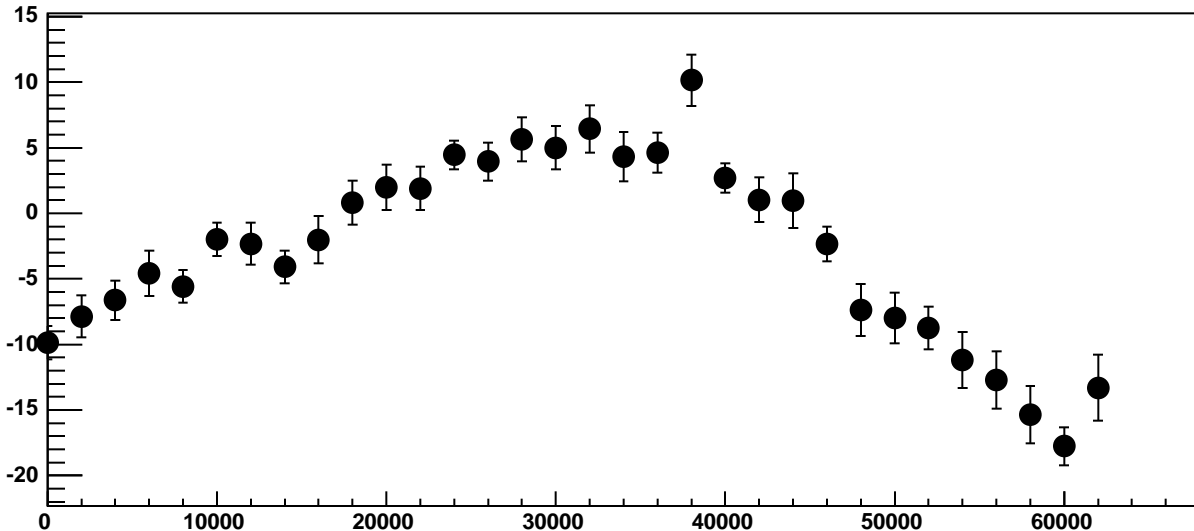


χ^2 / ndf	240.5 / 23
p0	-31.91 ± 0.6913
p1	$-0.007986 \pm 2.202e-05$

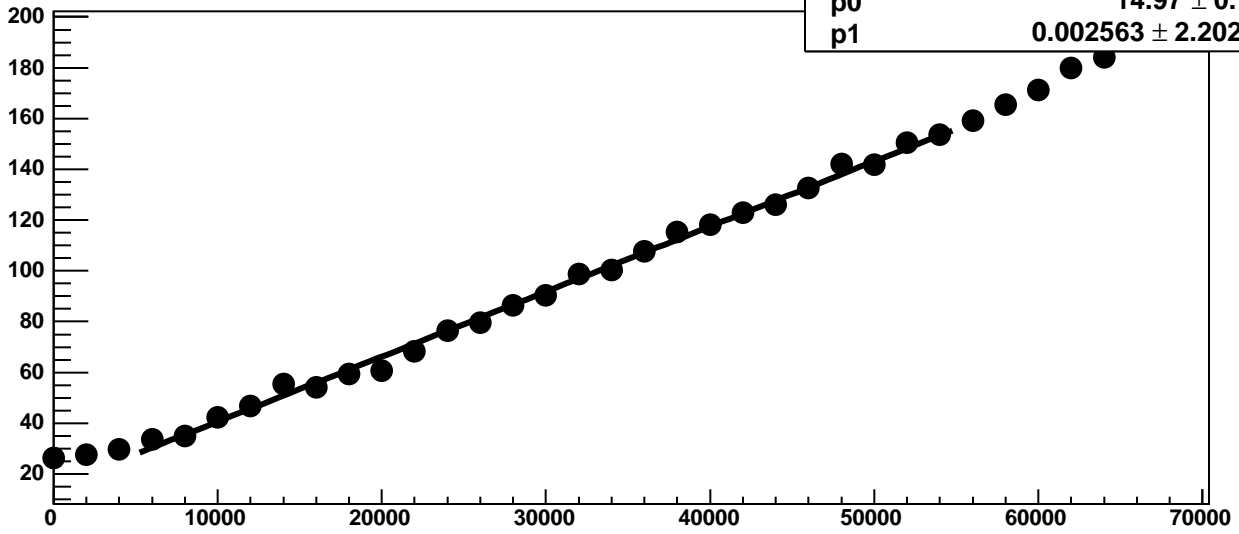
Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

47.73 / 23

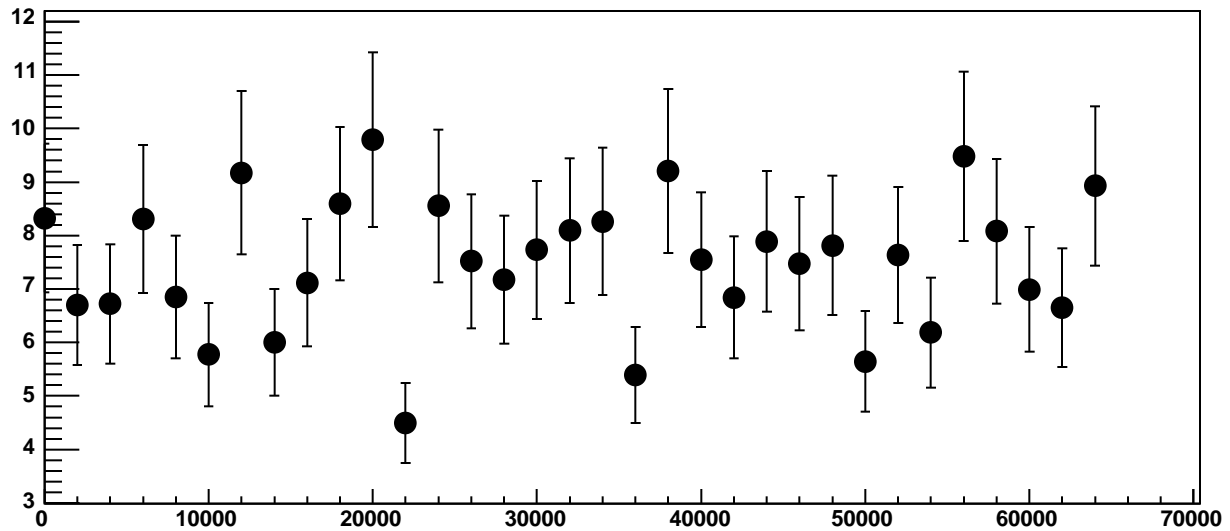
p0

14.97 ± 0.7373

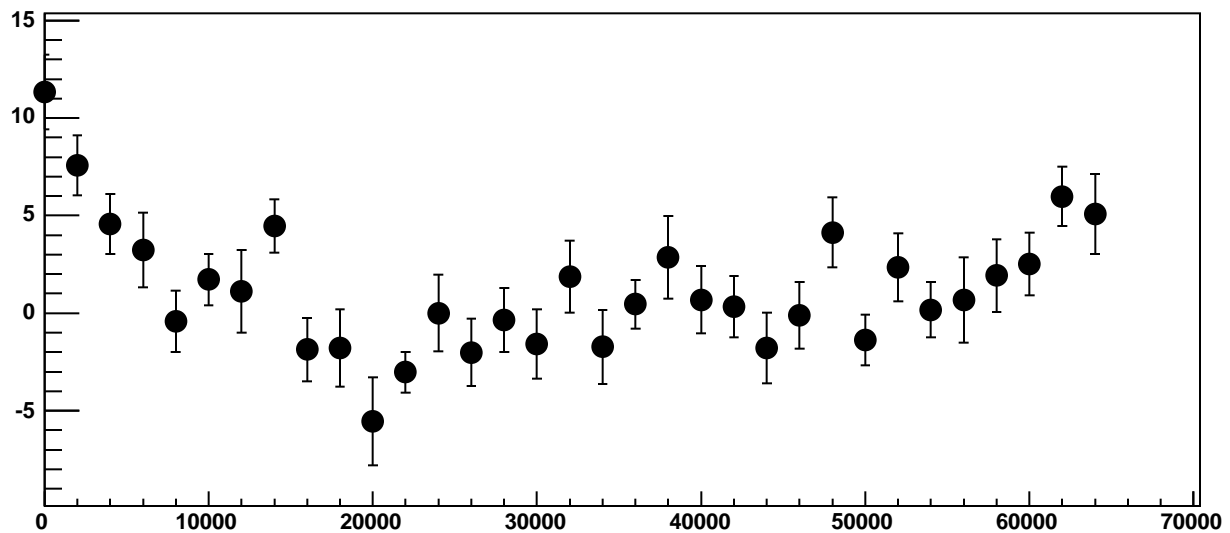
p1

$0.002563 \pm 2.202e-05$

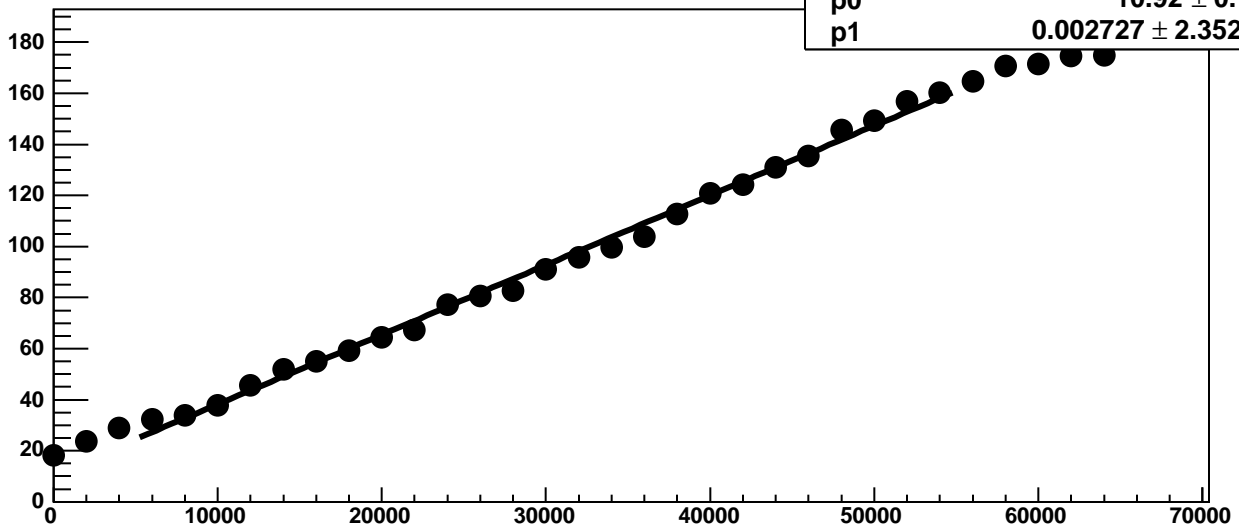
Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

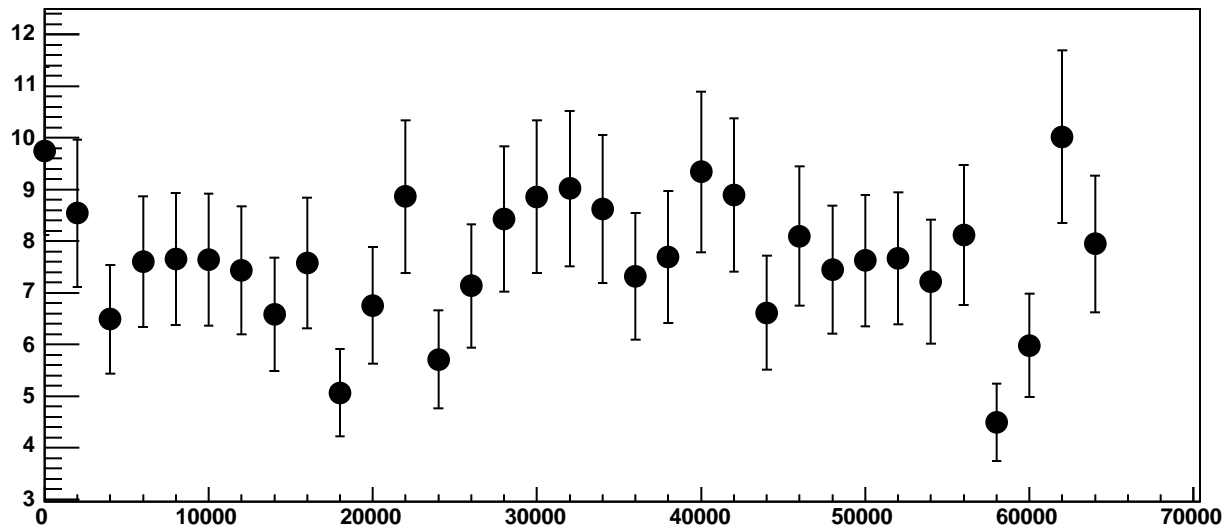


Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

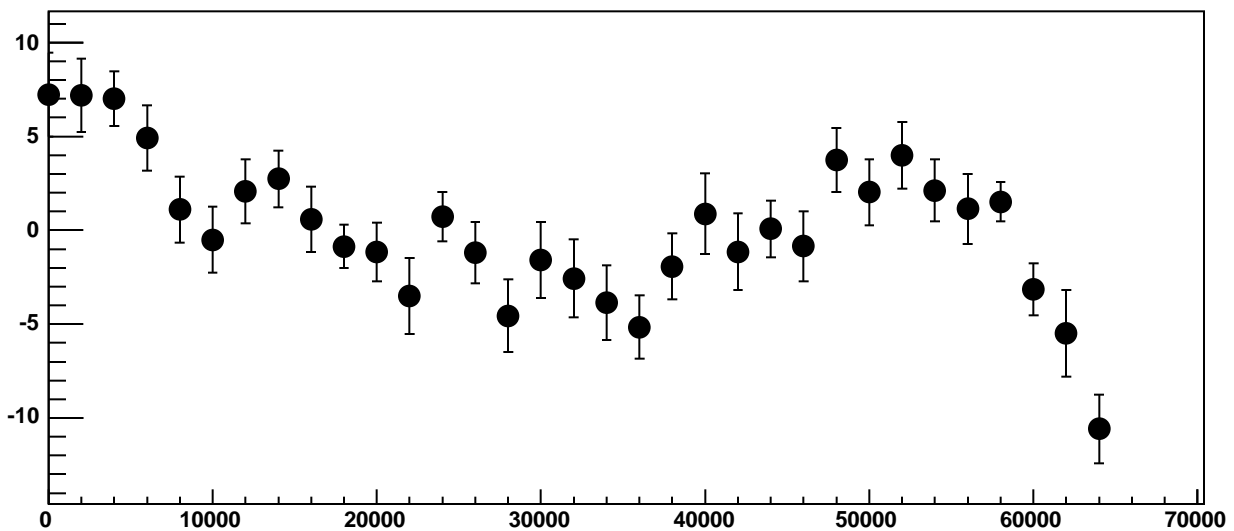


χ^2 / ndf 54 / 23
p0 10.92 ± 0.7605
p1 $0.002727 \pm 2.352e-05$

Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

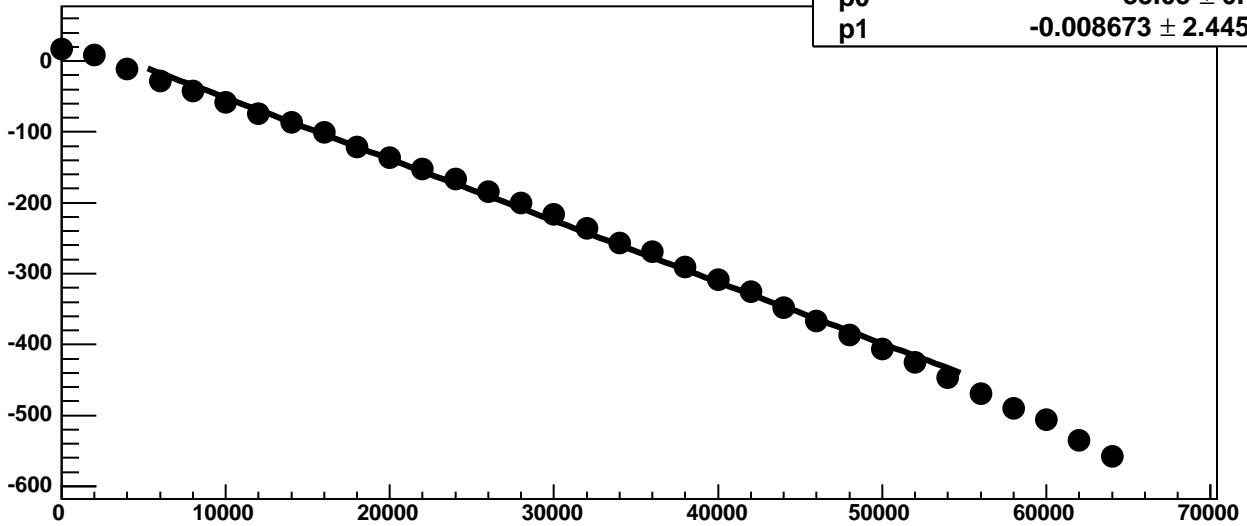


Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

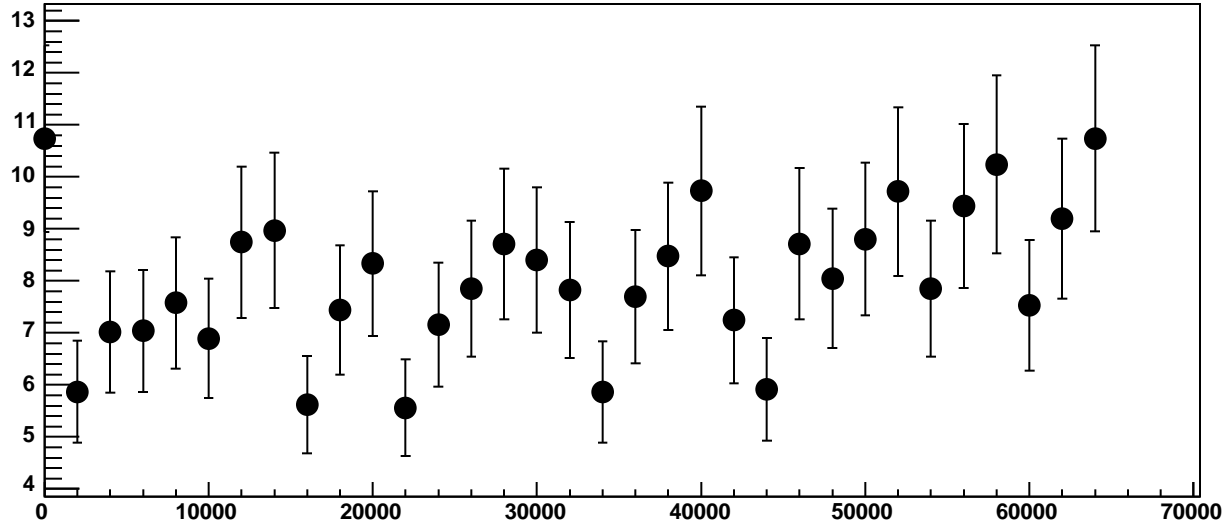


Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

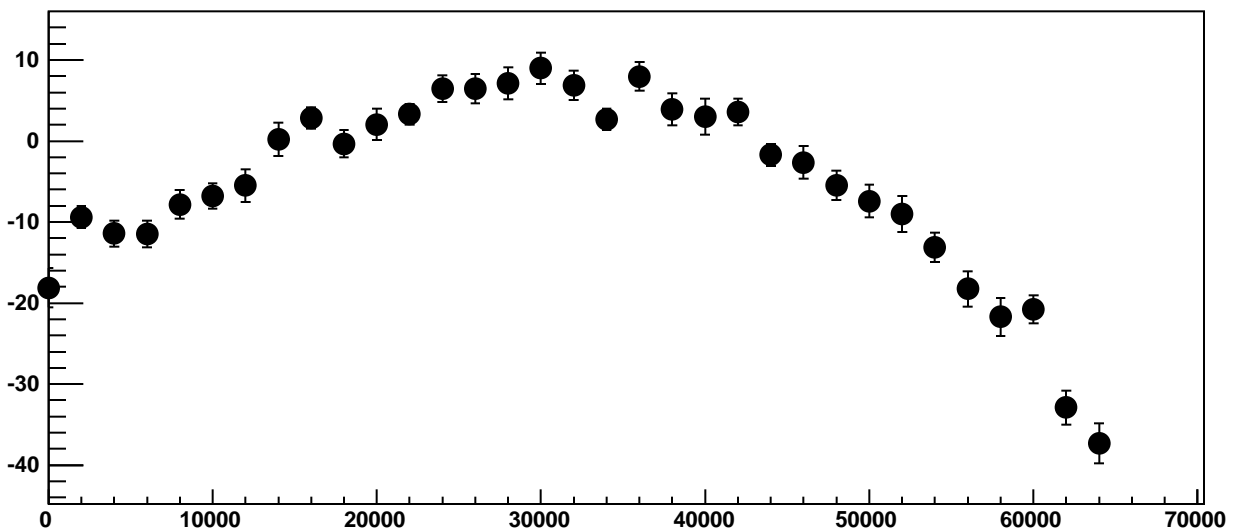
χ^2 / ndf 316.7 / 23
p0 35.05 ± 0.7839
p1 -0.008673 ± 2.445e-05



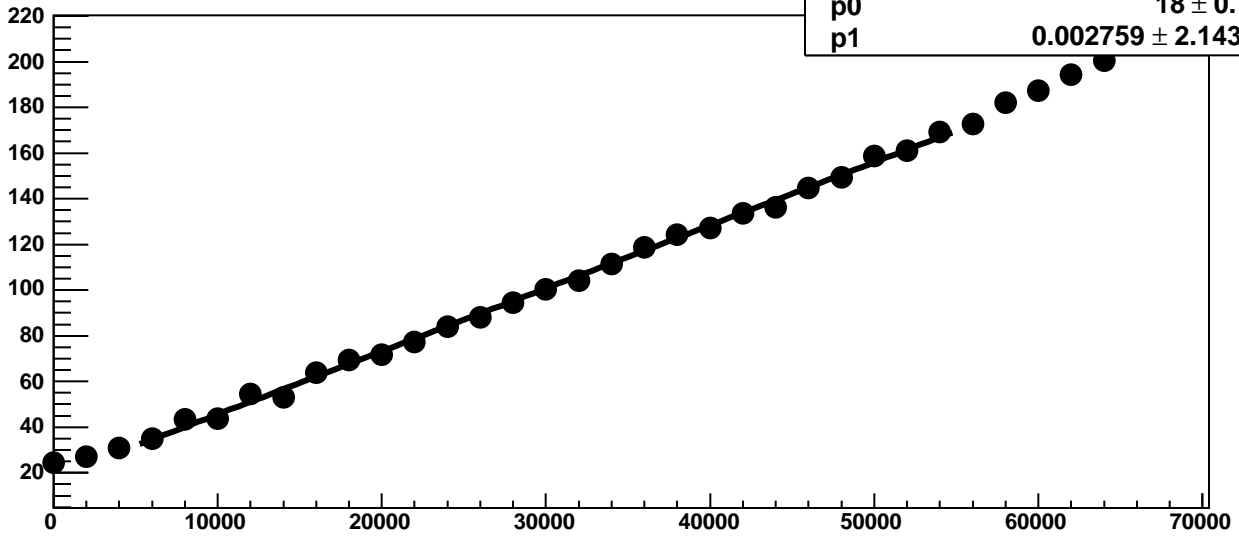
Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



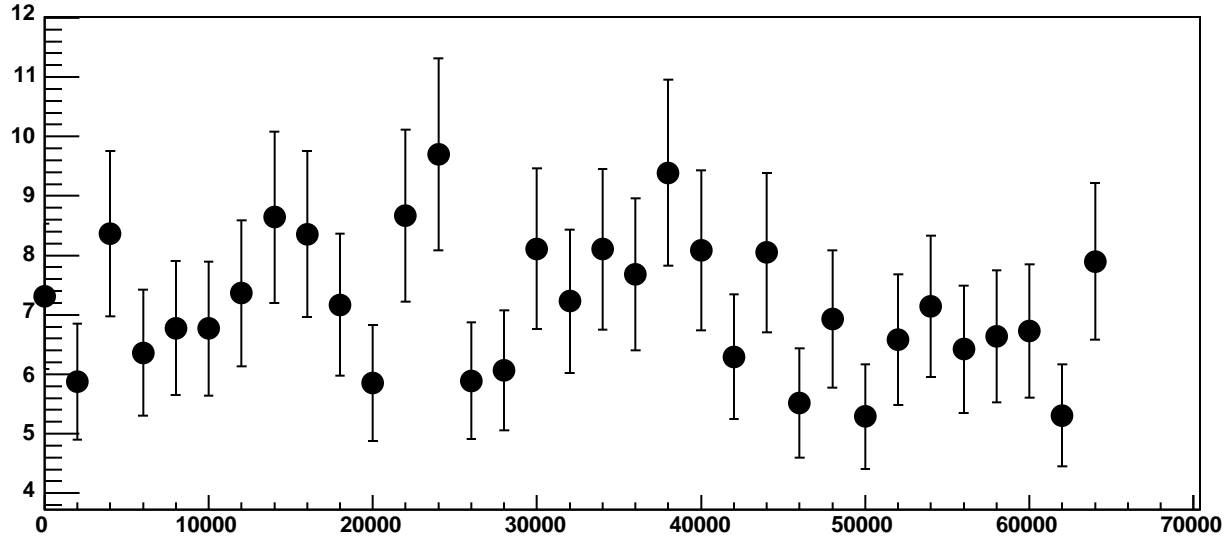
Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



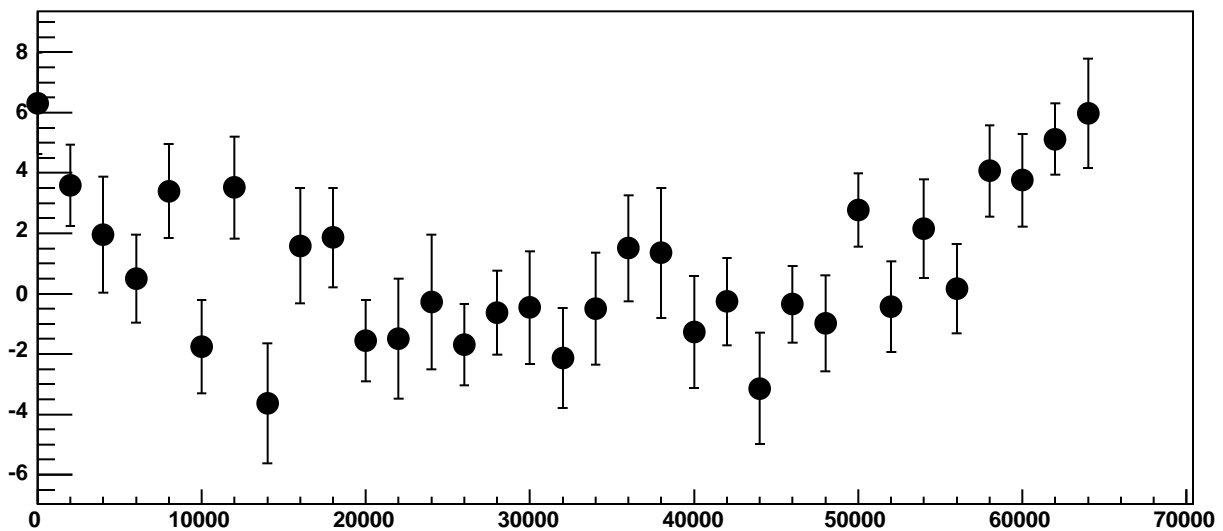
Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



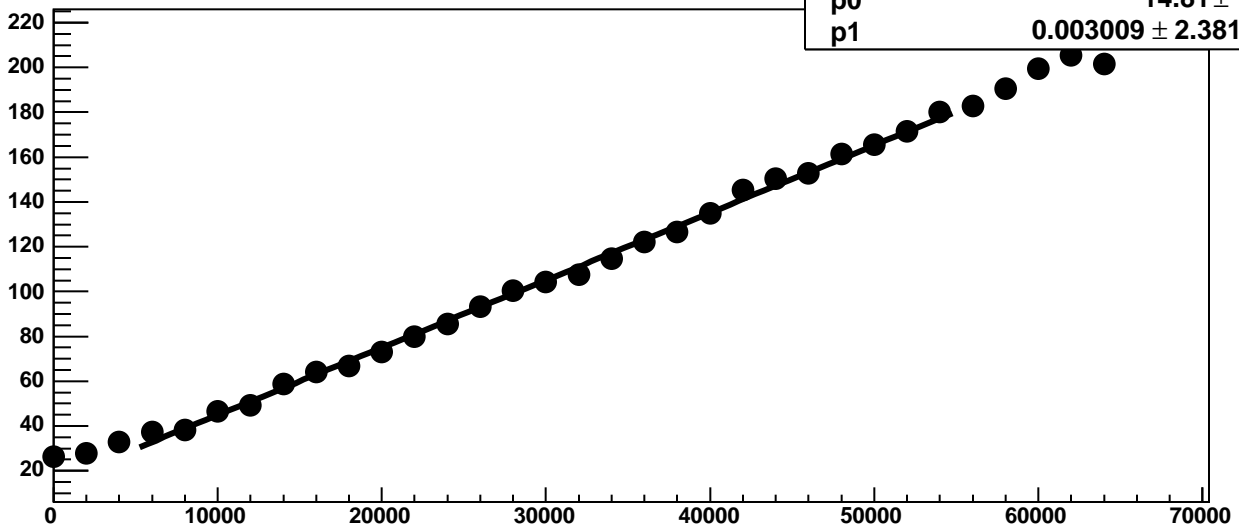
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

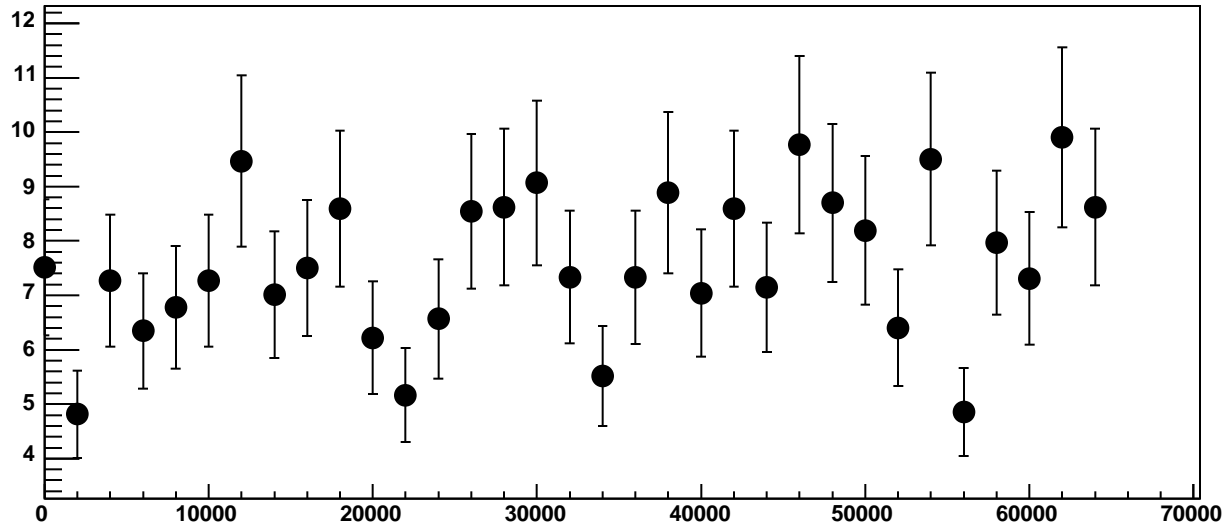


Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

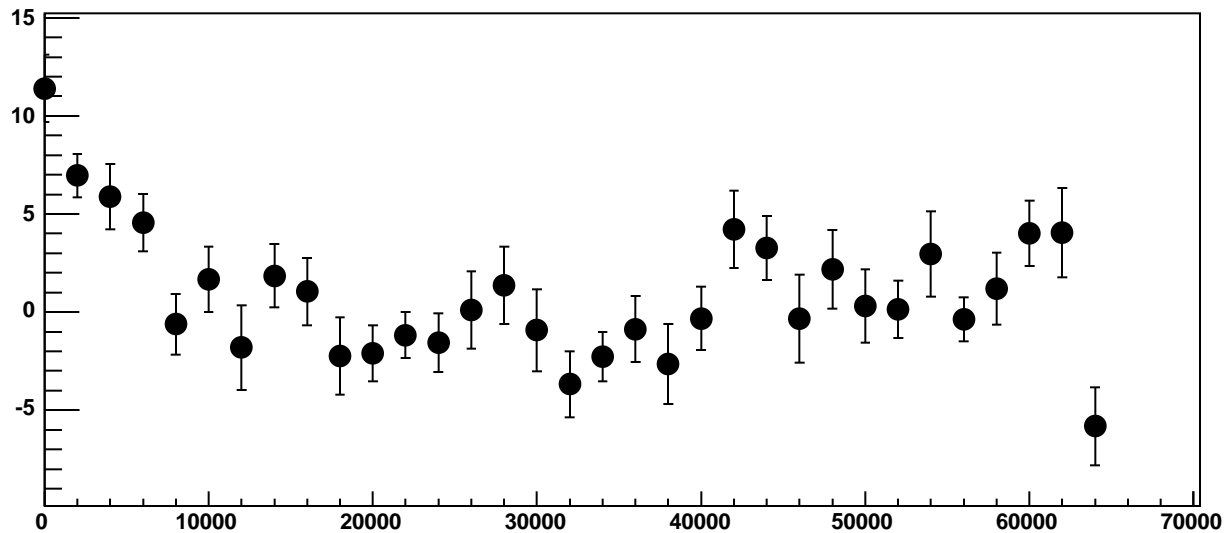


χ^2 / ndf 41.16 / 23
p0 14.81 ± 0.76
p1 $0.003009 \pm 2.381e-05$

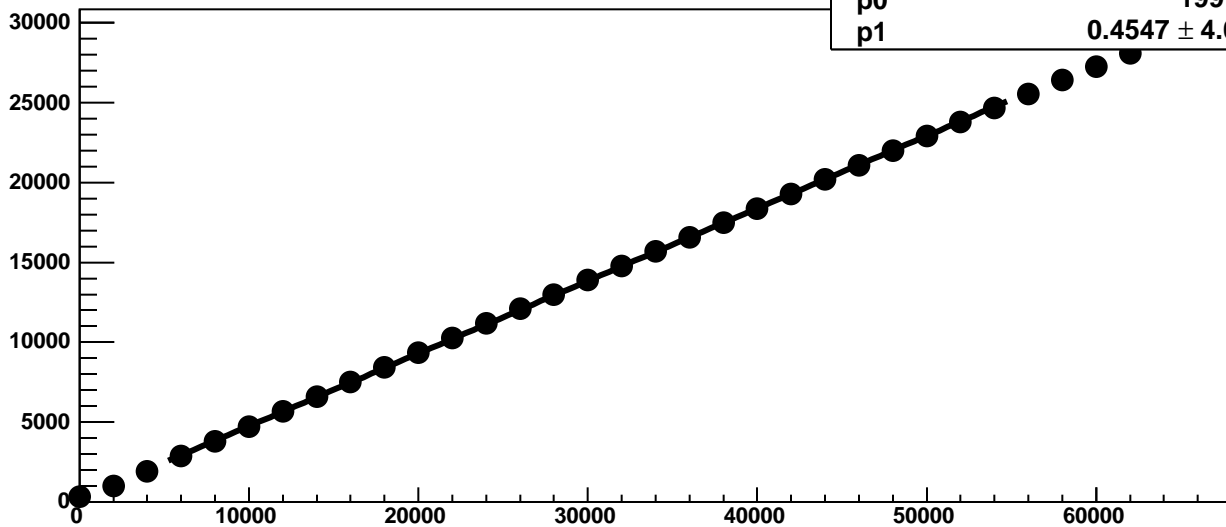
Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



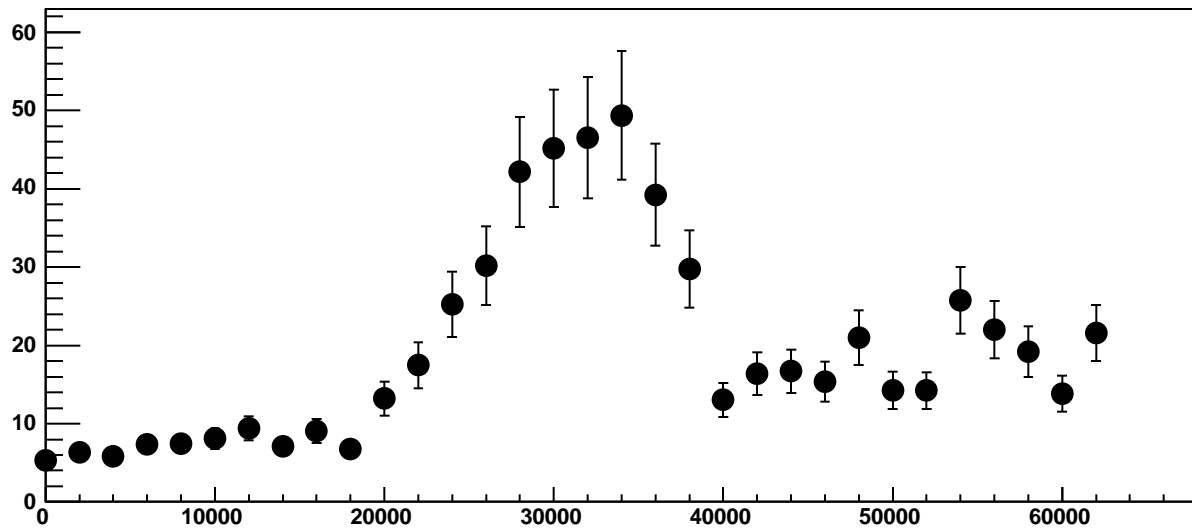
Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



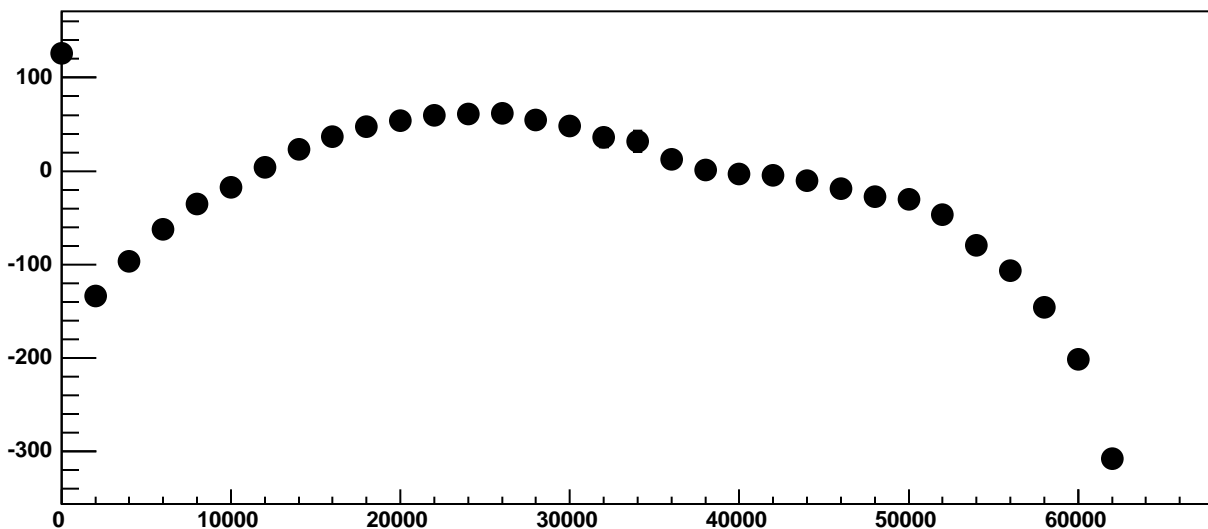
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



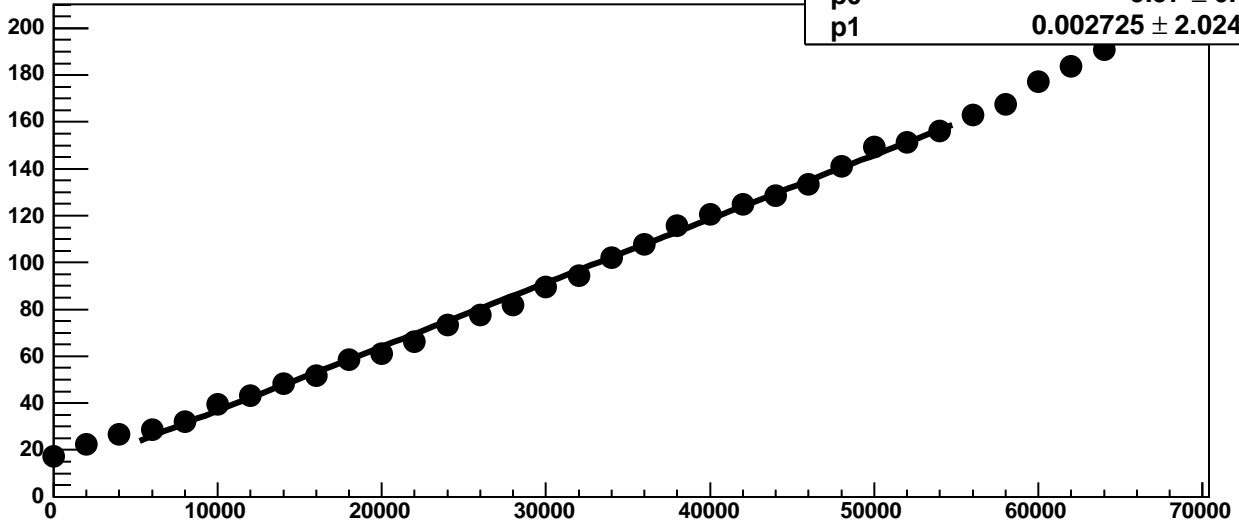
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

42.08 / 23

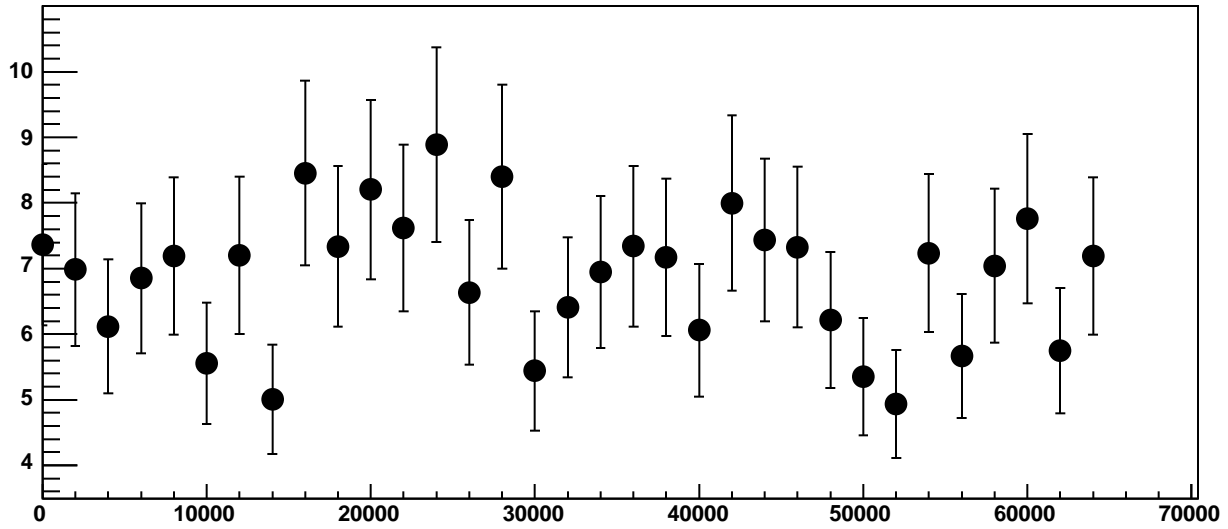
p0

9.57 ± 0.6948

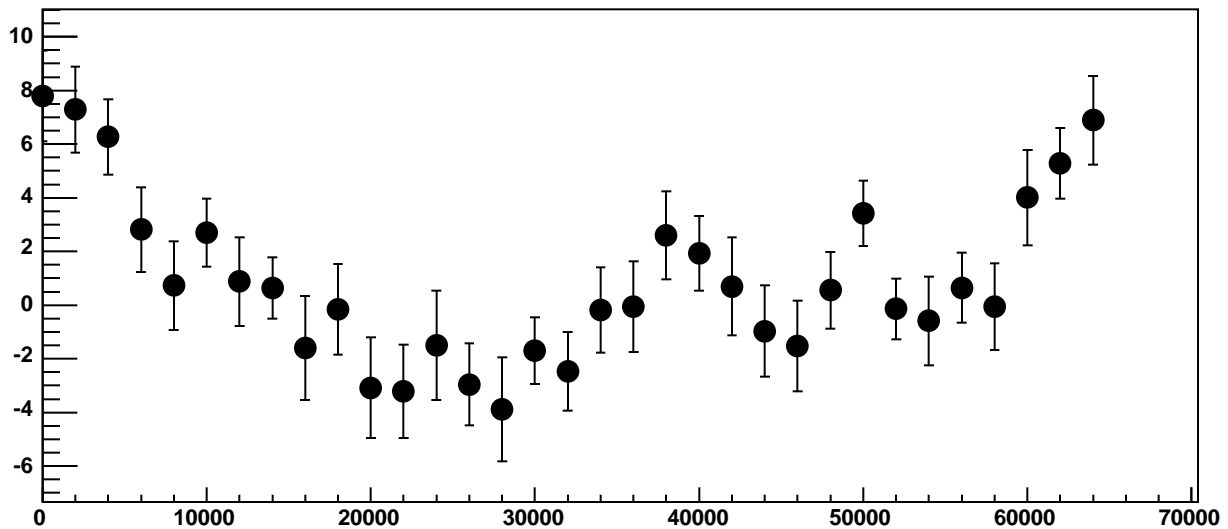
p1

$0.002725 \pm 2.024e-05$

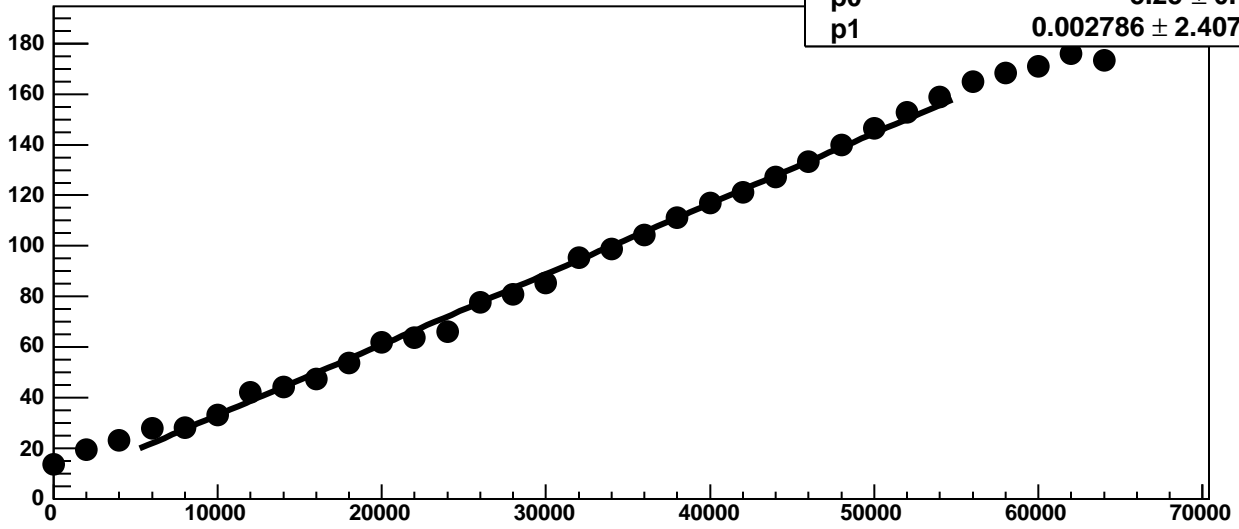
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



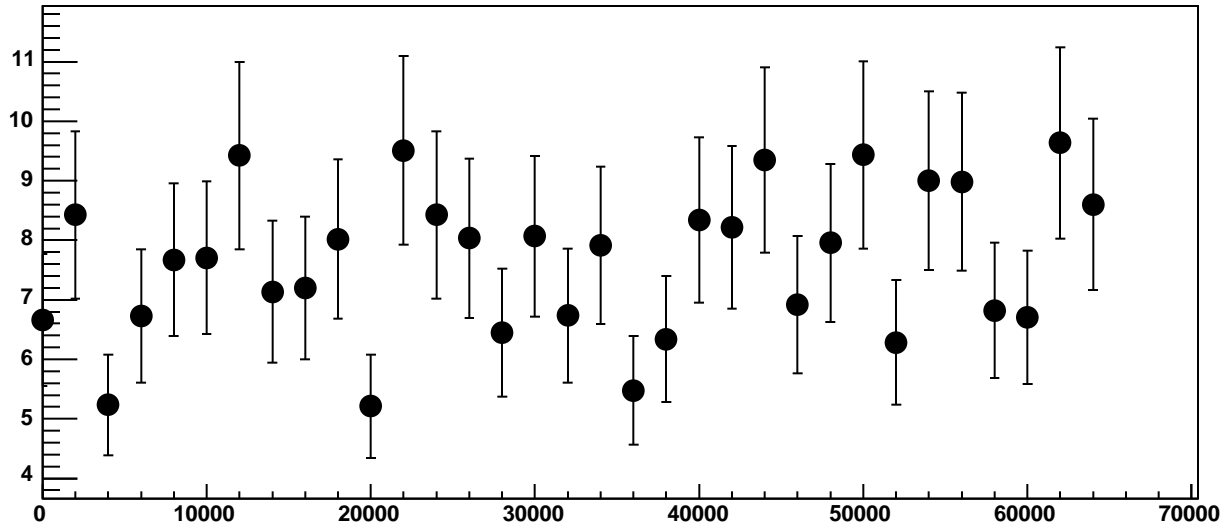
Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



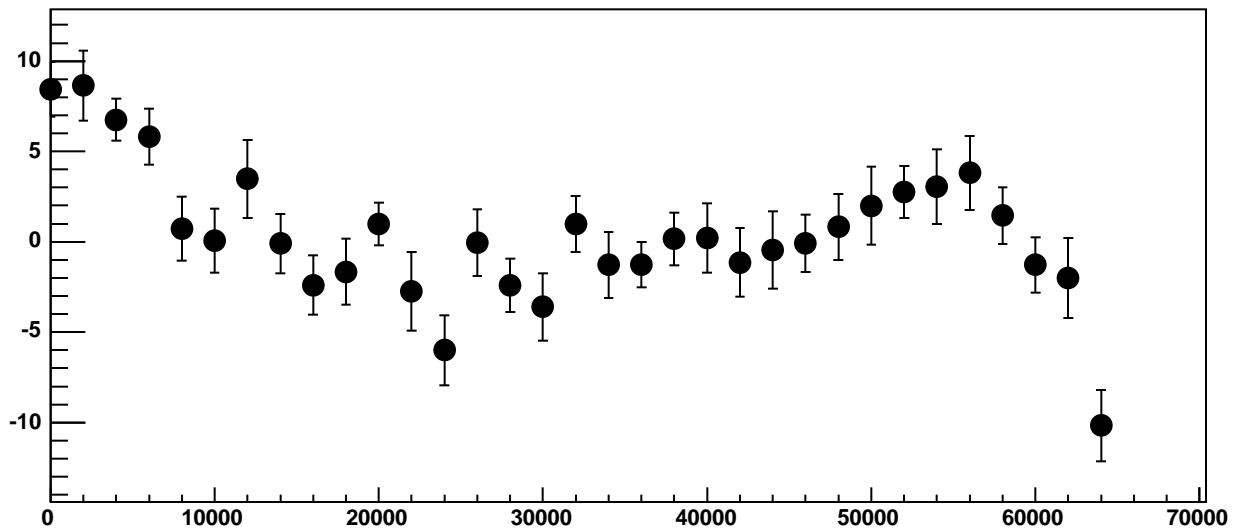
Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

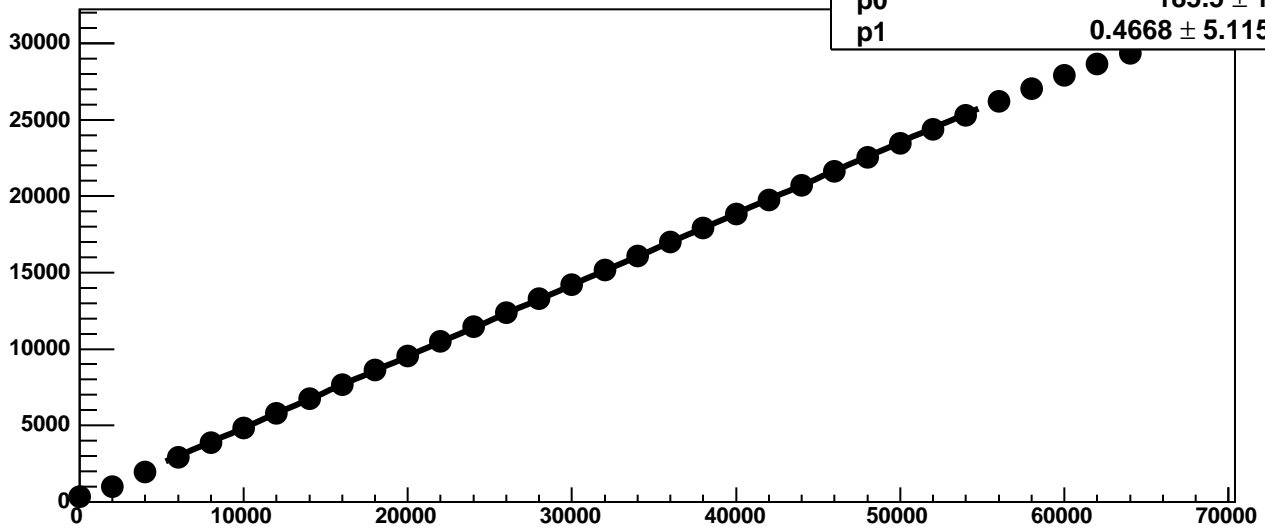


Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

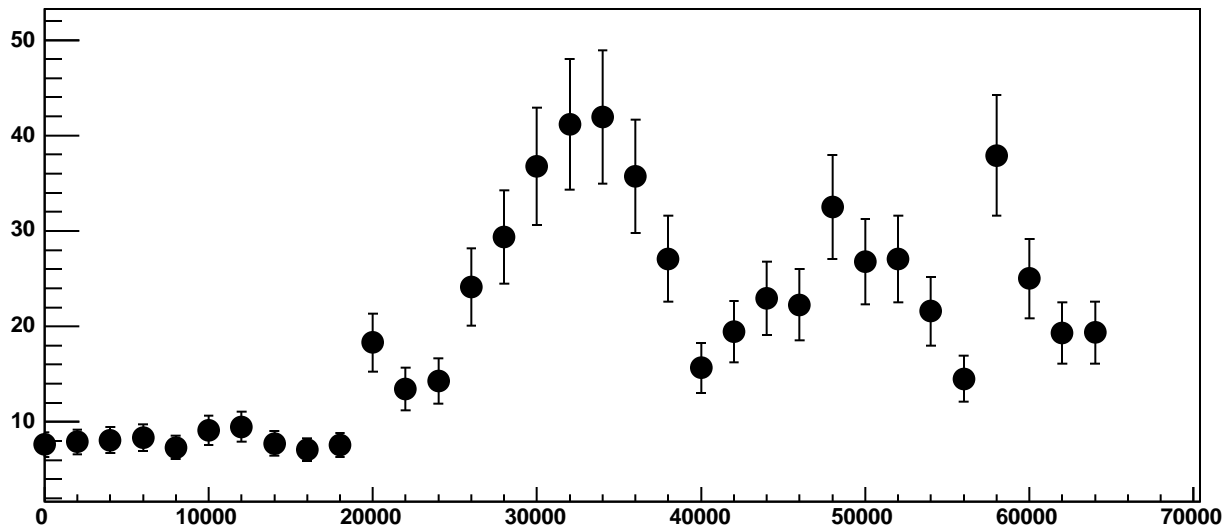


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

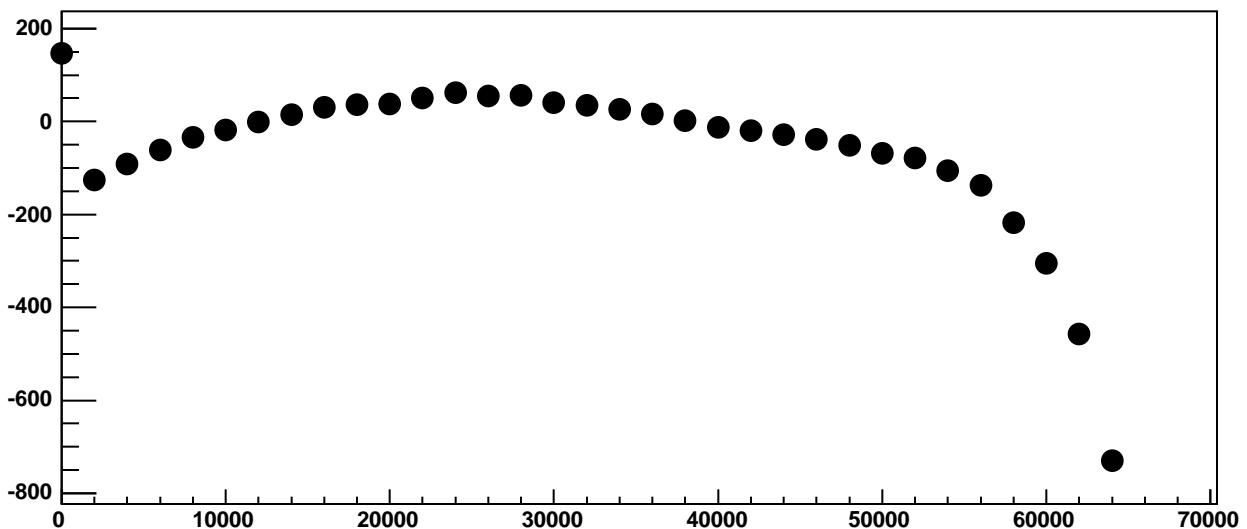
χ^2 / ndf 4169 / 23
p0 185.5 ± 1.092
p1 $0.4668 \pm 5.115e-05$



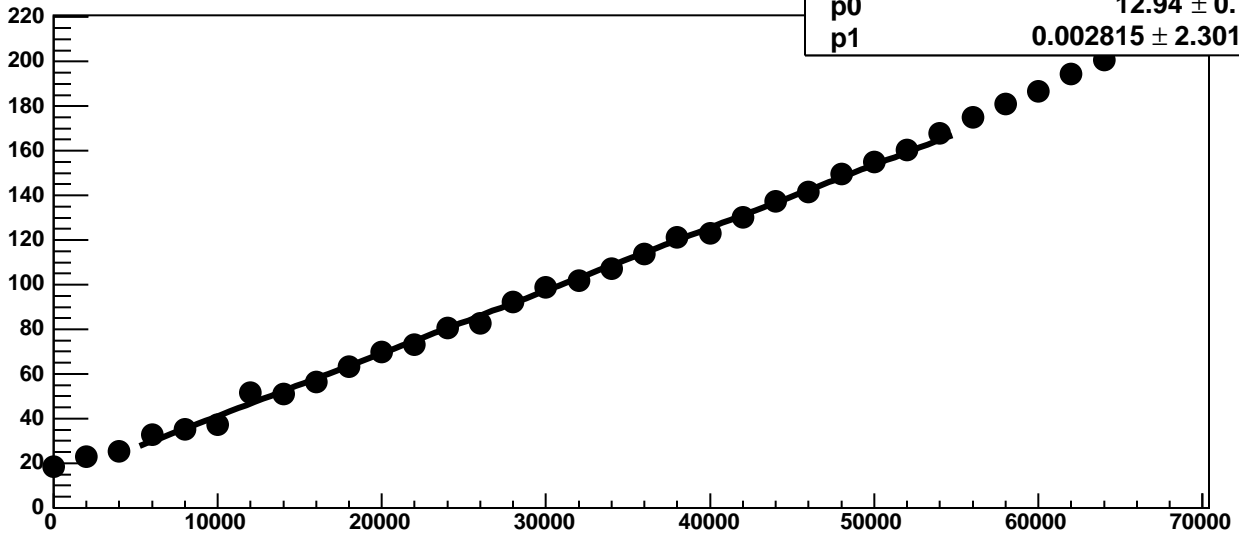
Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.03 / 23

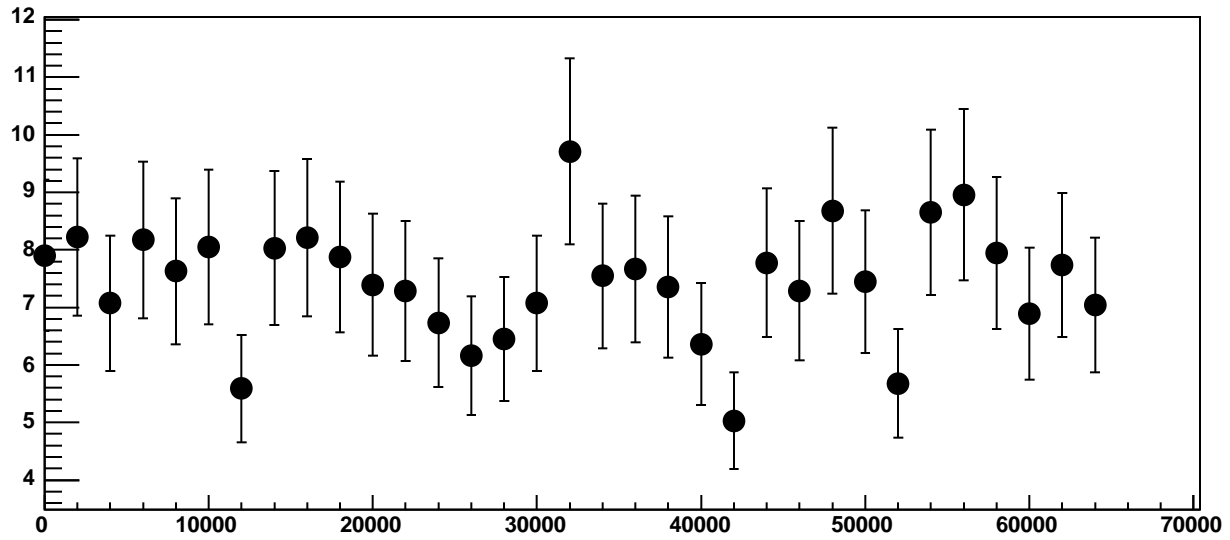
p0

12.94 ± 0.7765

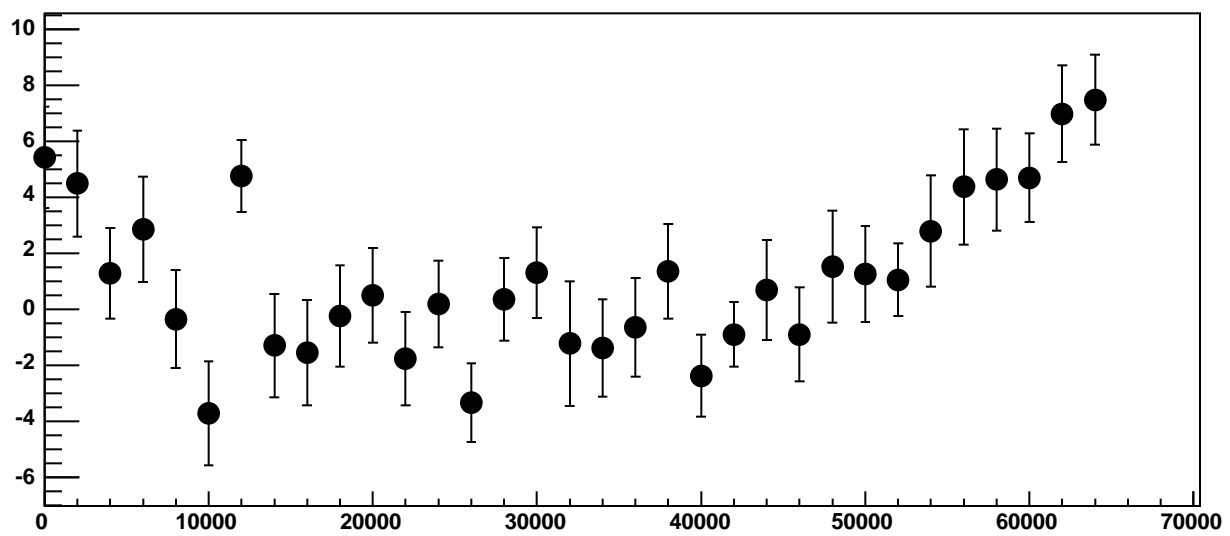
p1

$0.002815 \pm 2.301e-05$

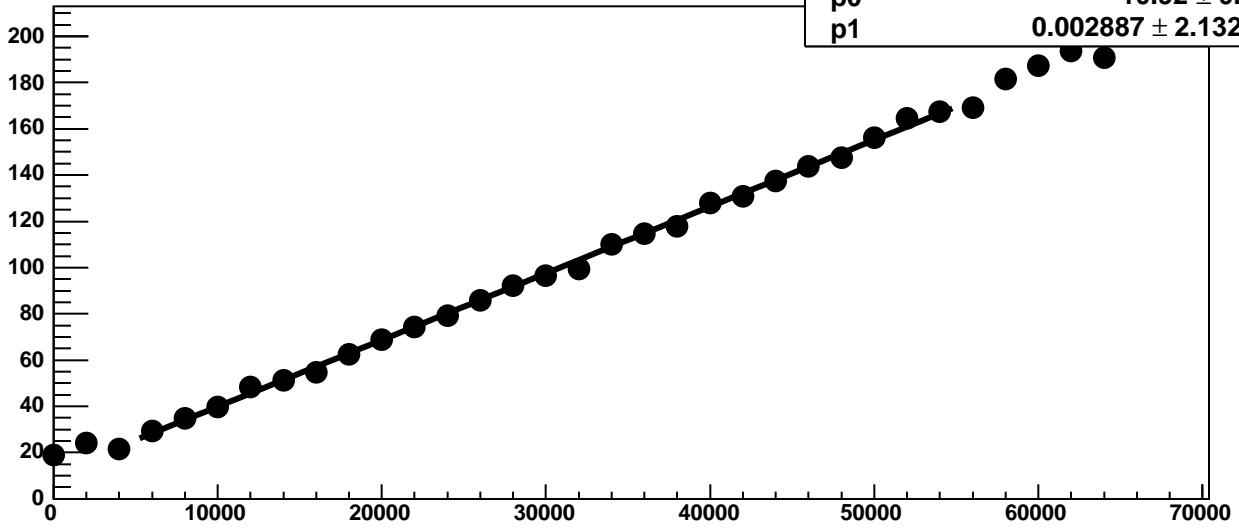
Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22.56 / 23

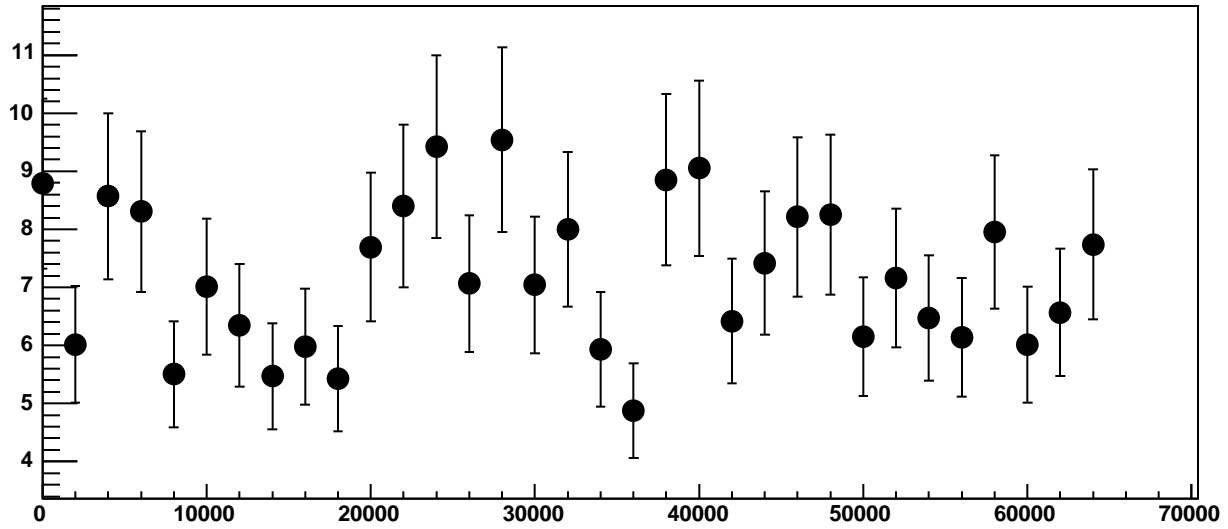
p0

10.92 ± 0.6941

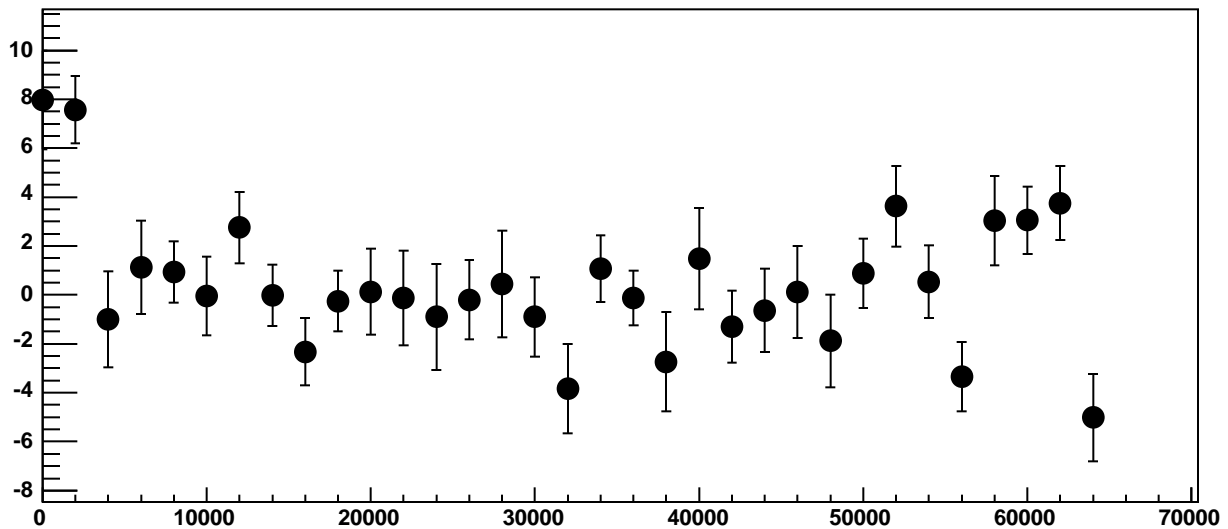
p1

$0.002887 \pm 2.132e-05$

Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

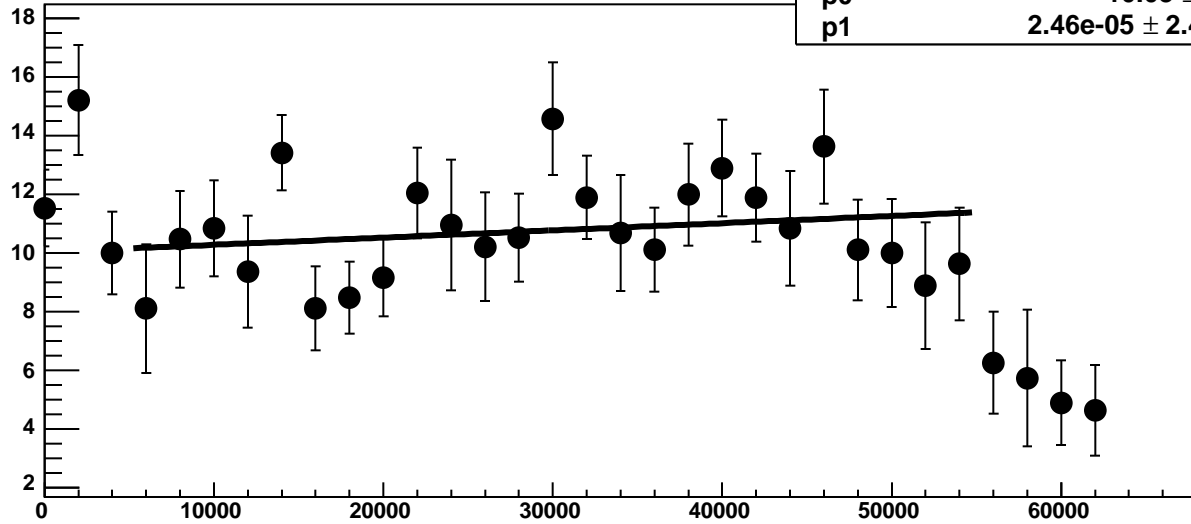


Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

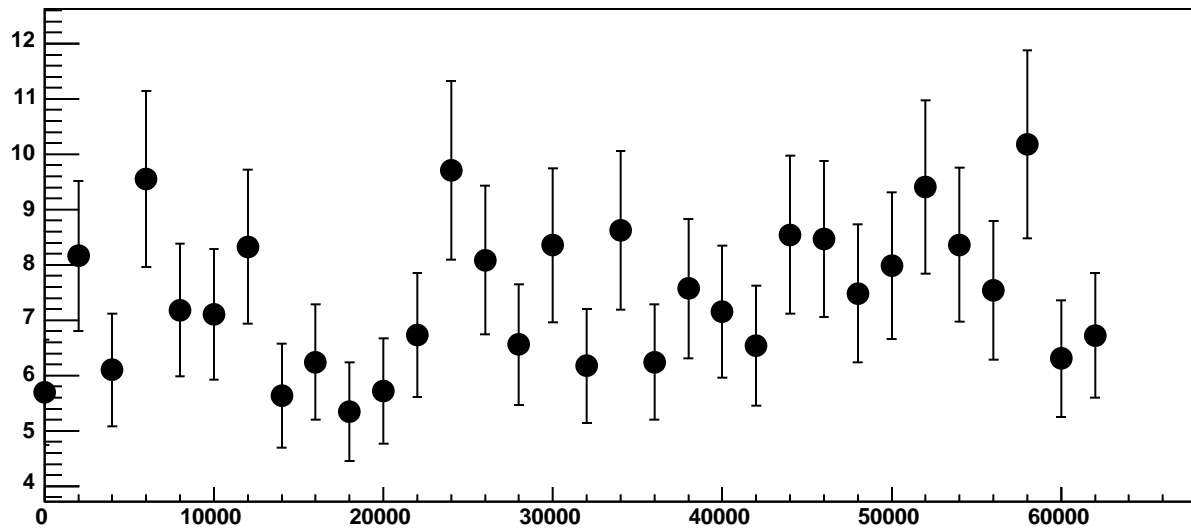


Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

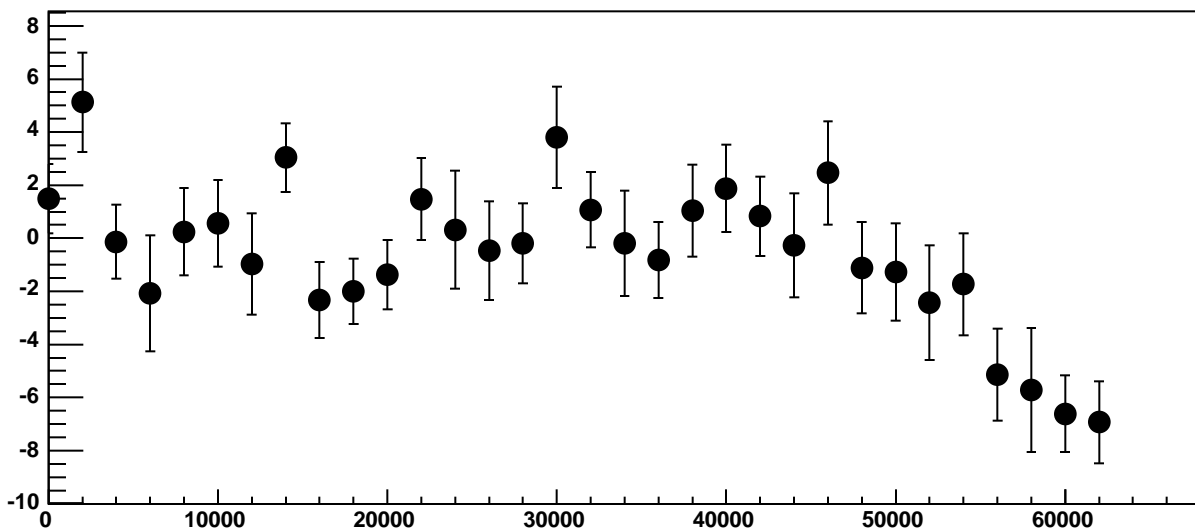
χ^2 / ndf 25.63 / 23
p0 10.03 ± 0.7619
p1 $2.46\text{e-}05 \pm 2.417\text{e-}05$



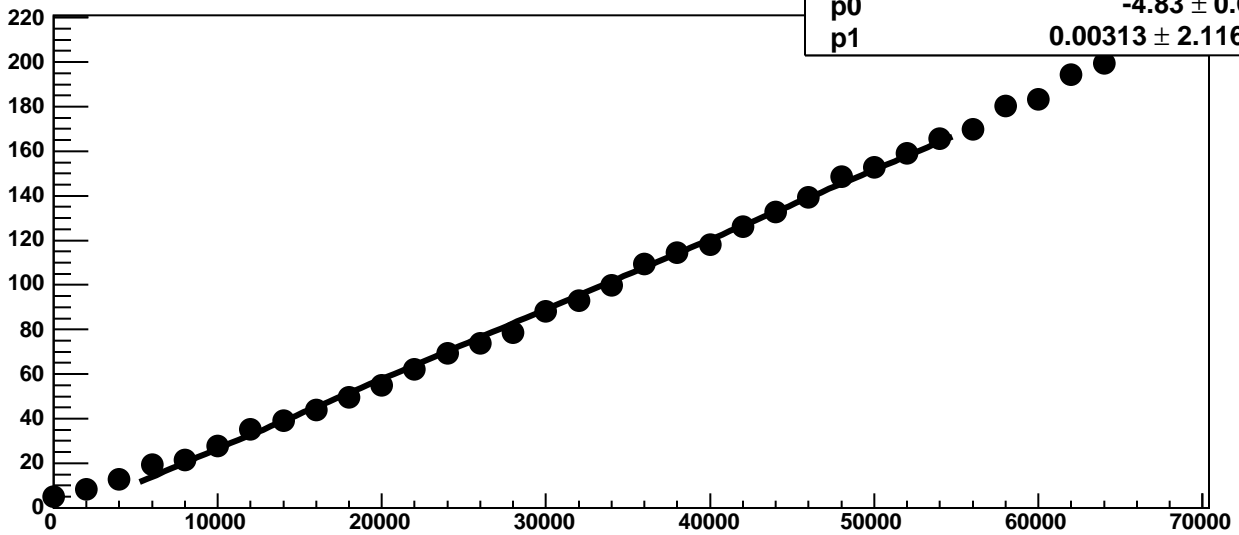
Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

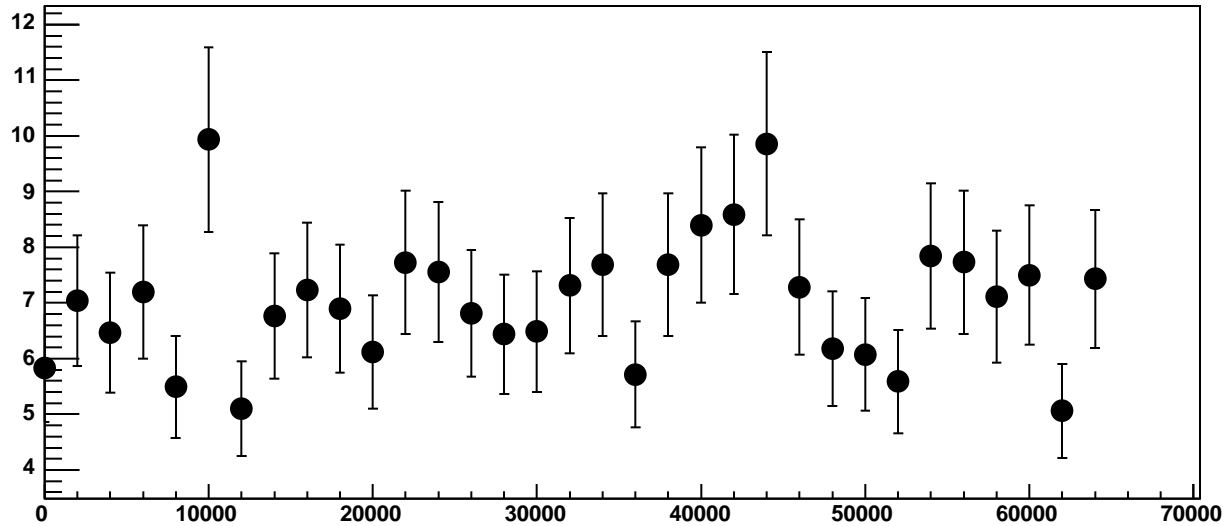


Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

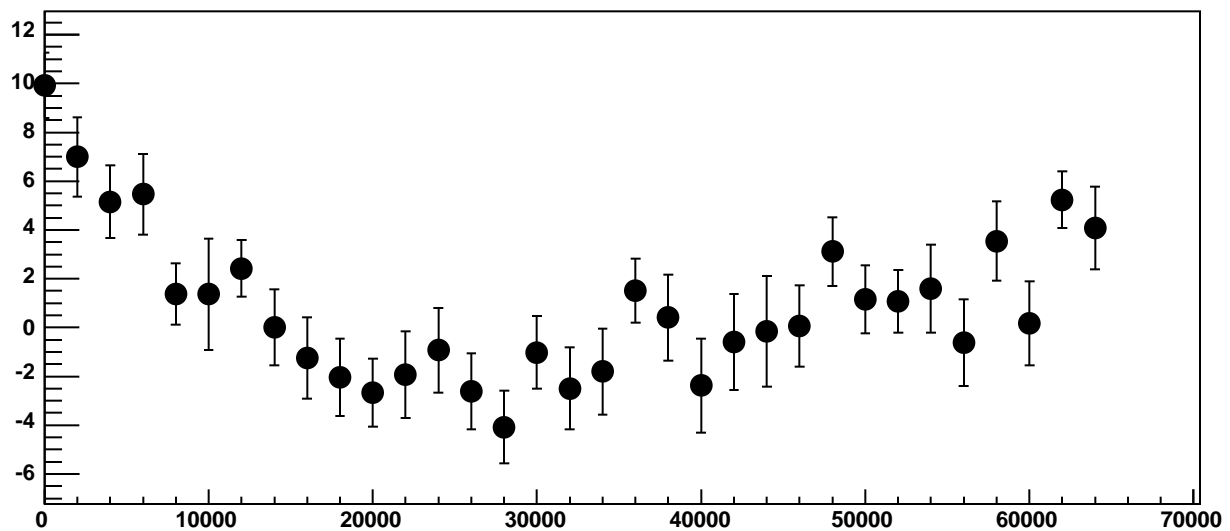


χ^2 / ndf 48.24 / 23
p0 -4.83 ± 0.6968
p1 $0.00313 \pm 2.116e-05$

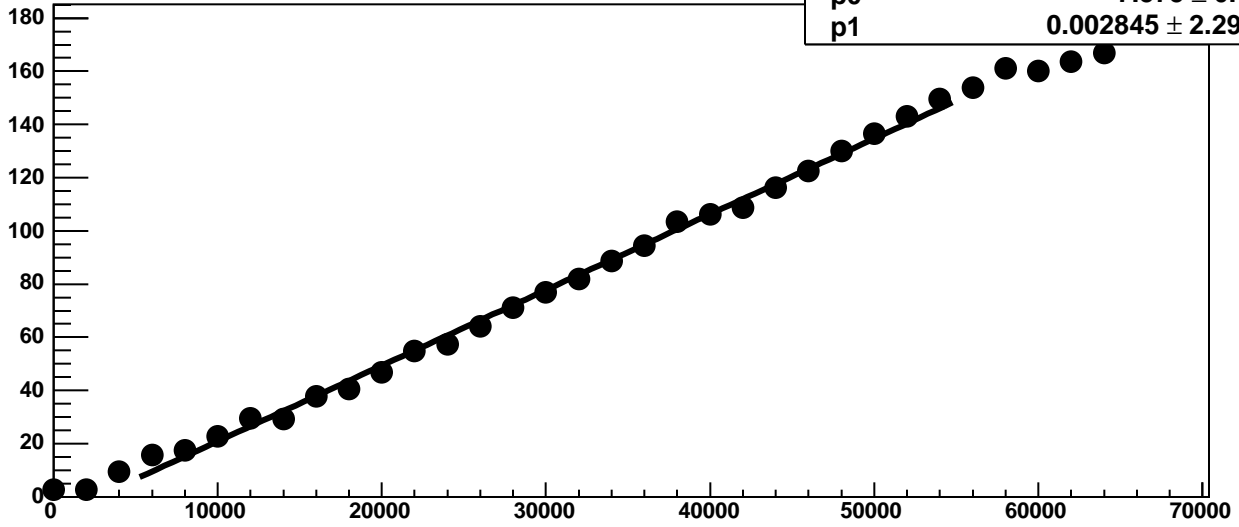
Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



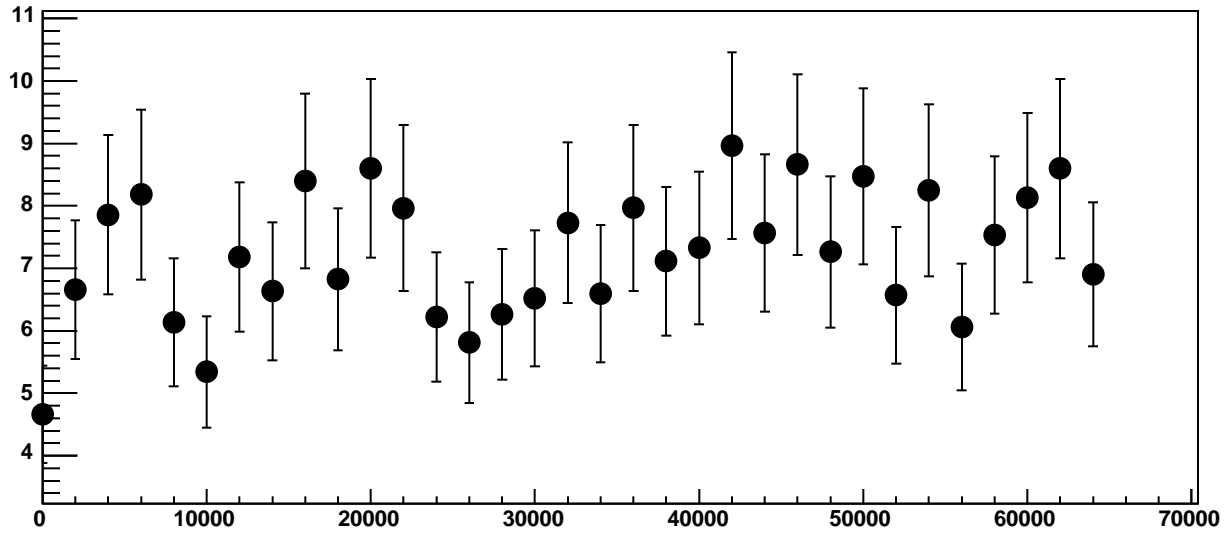
Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



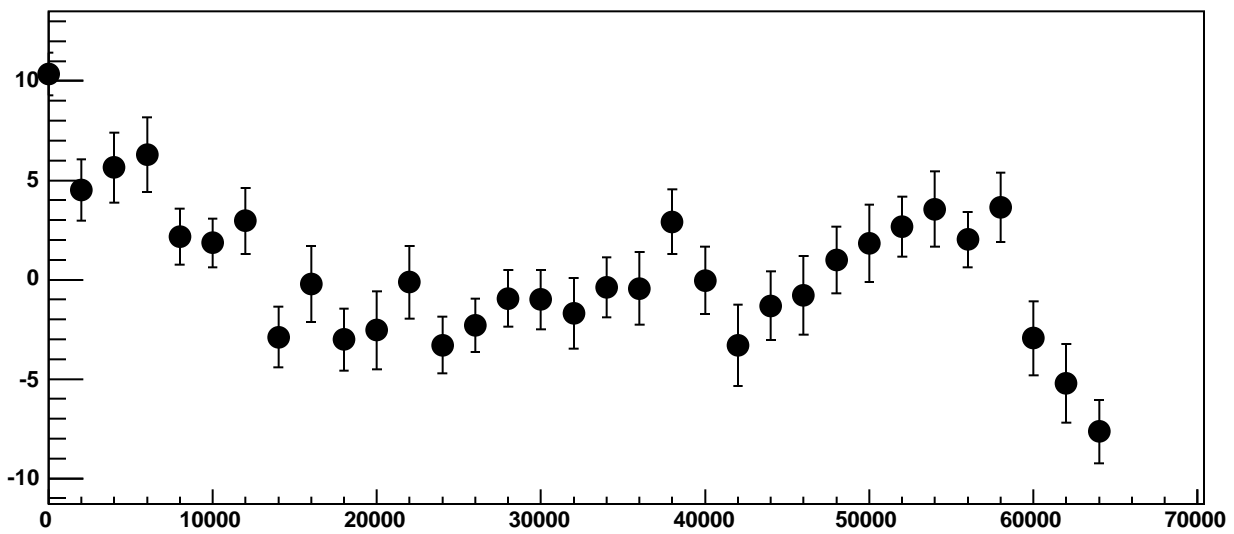
Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



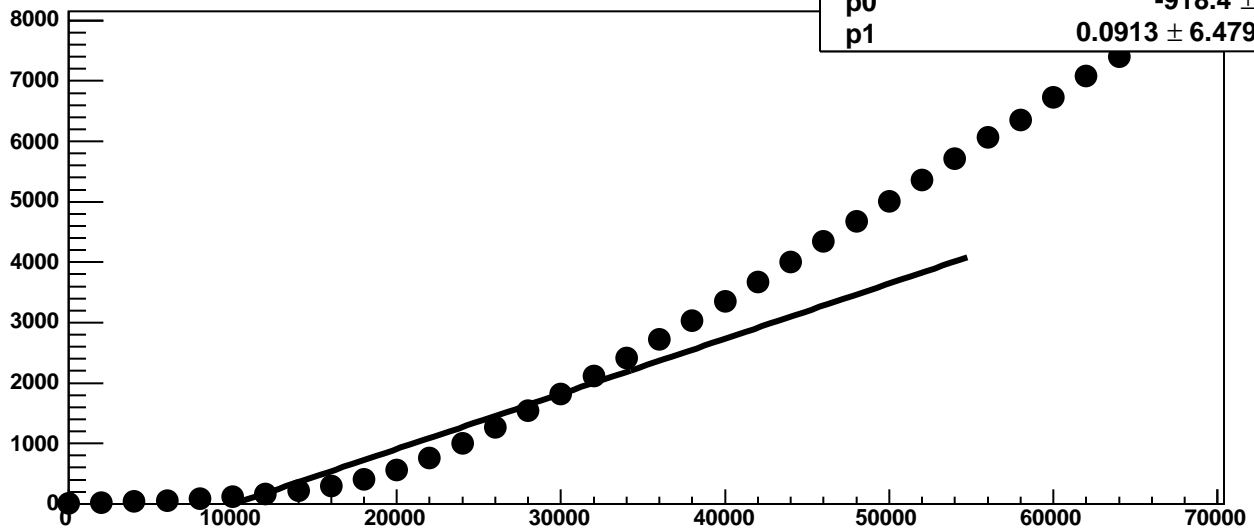
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



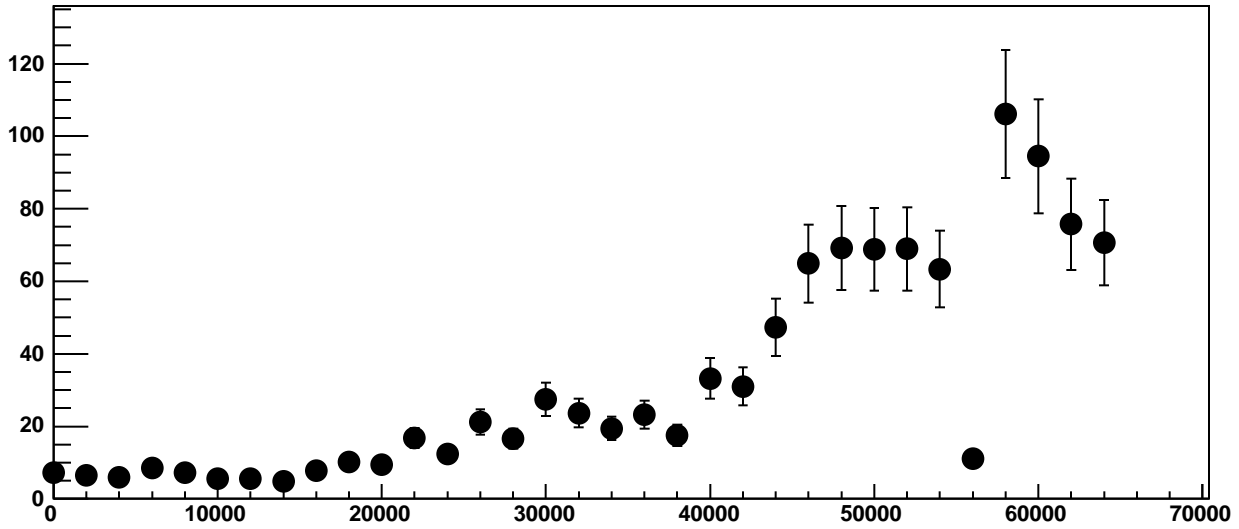
Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



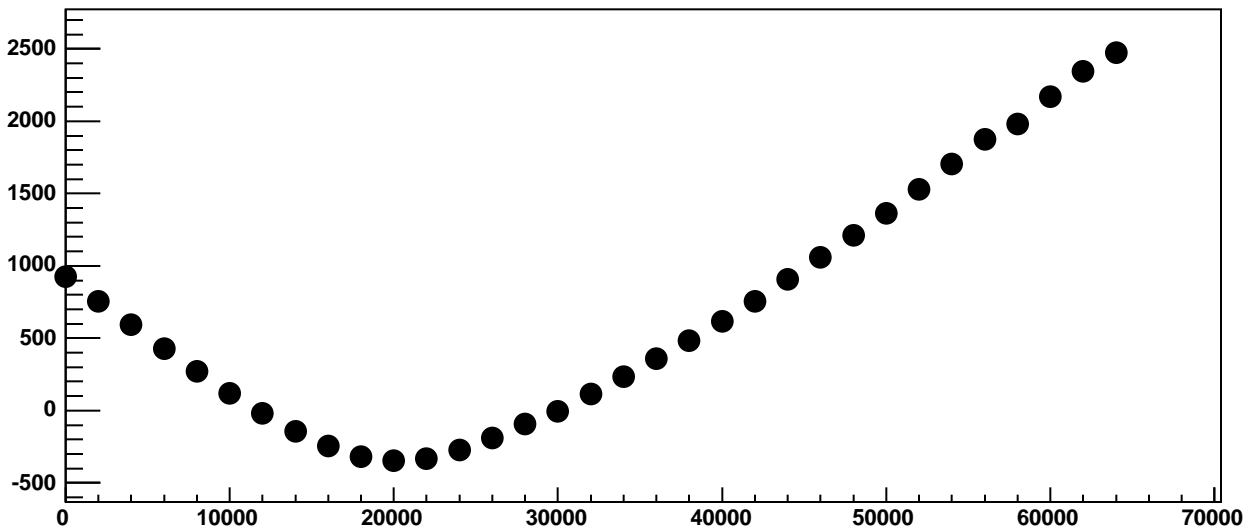
Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



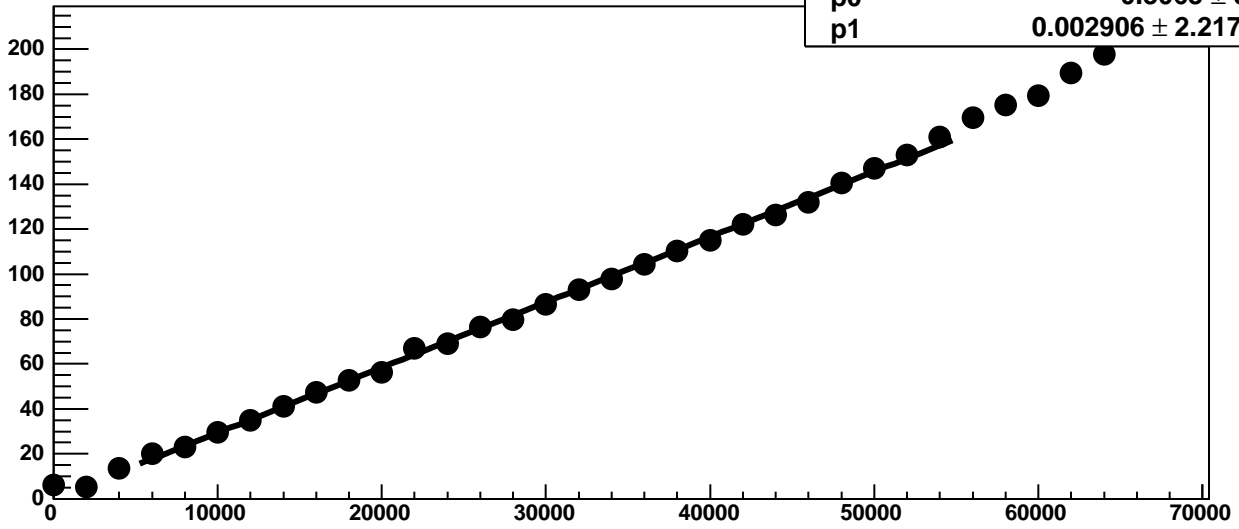
Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

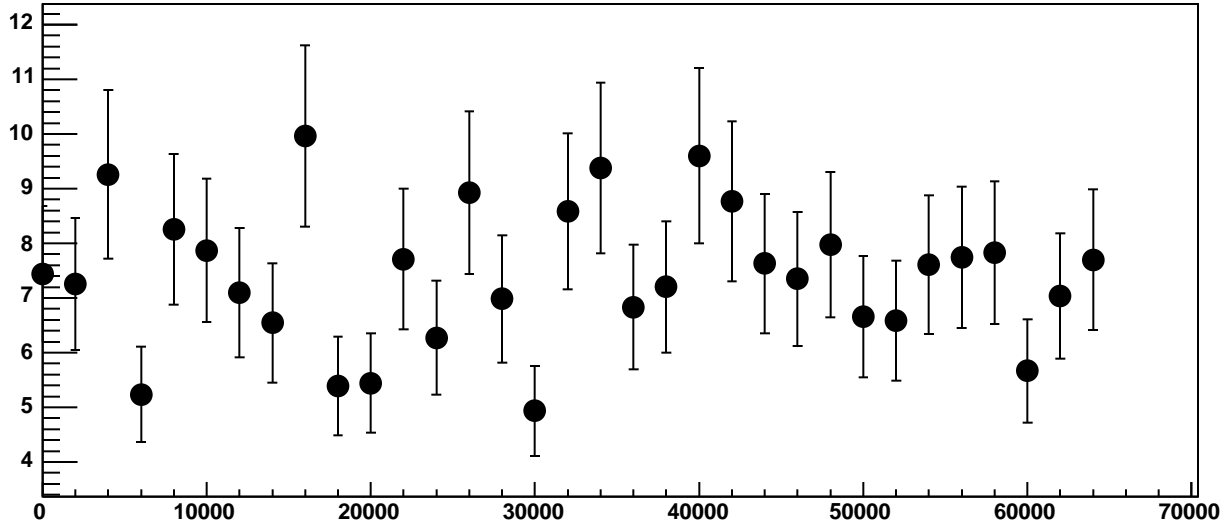


Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

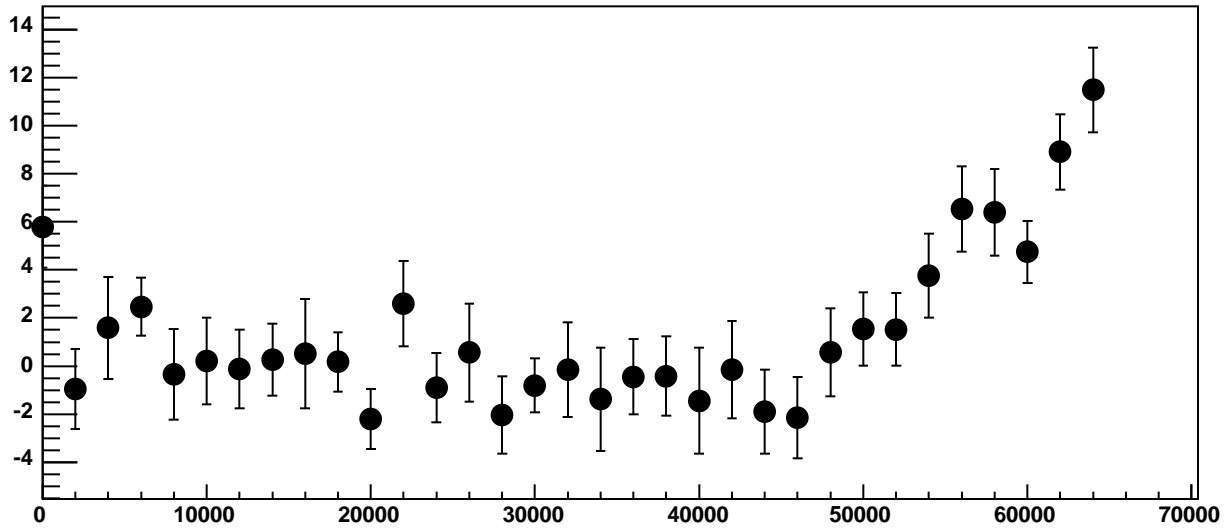


χ^2 / ndf 22.89 / 23
p0 0.3063 ± 0.711
p1 $0.002906 \pm 2.217\text{e-}05$

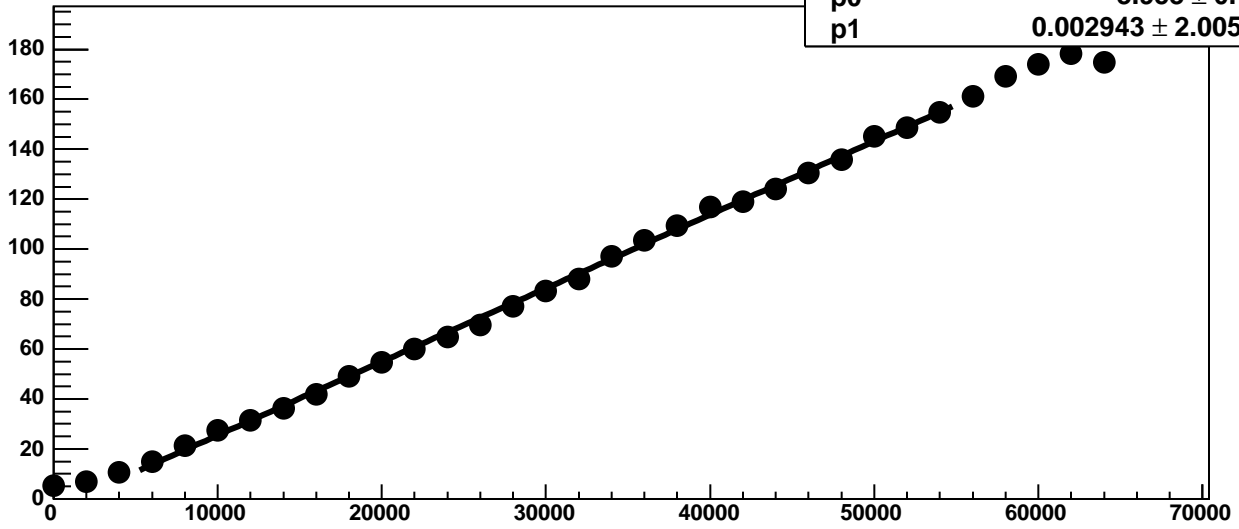
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

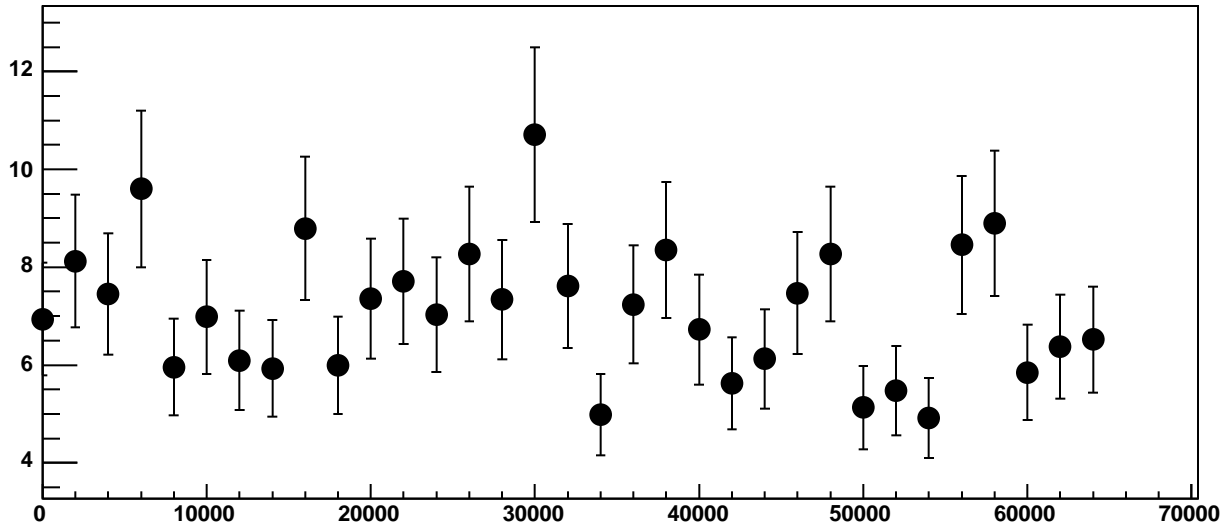


Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

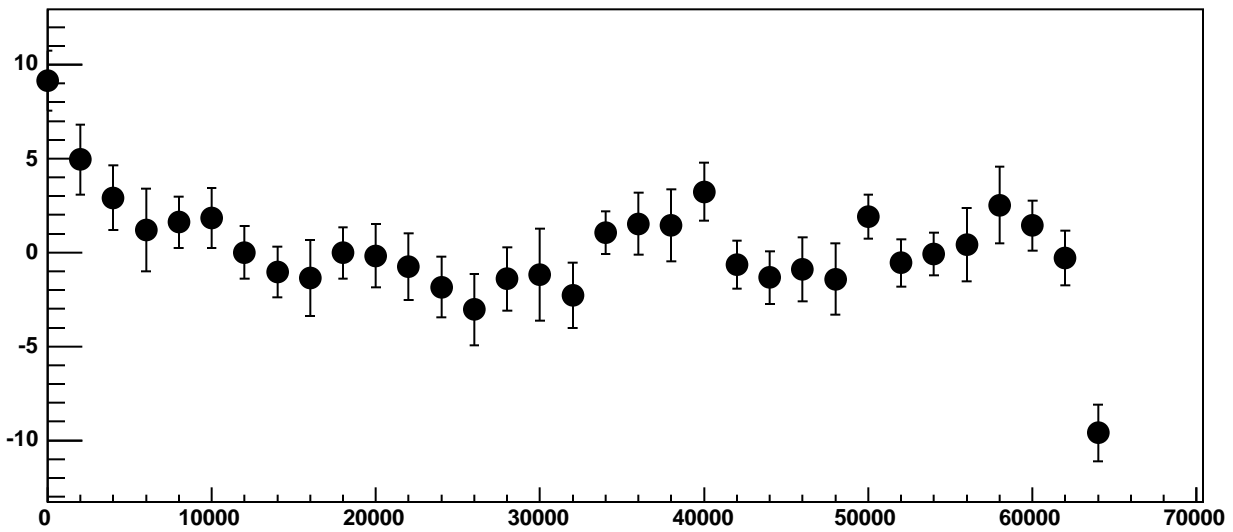


χ^2 / ndf 22.16 / 23
p0 -3.953 ± 0.7102
p1 $0.002943 \pm 2.005e-05$

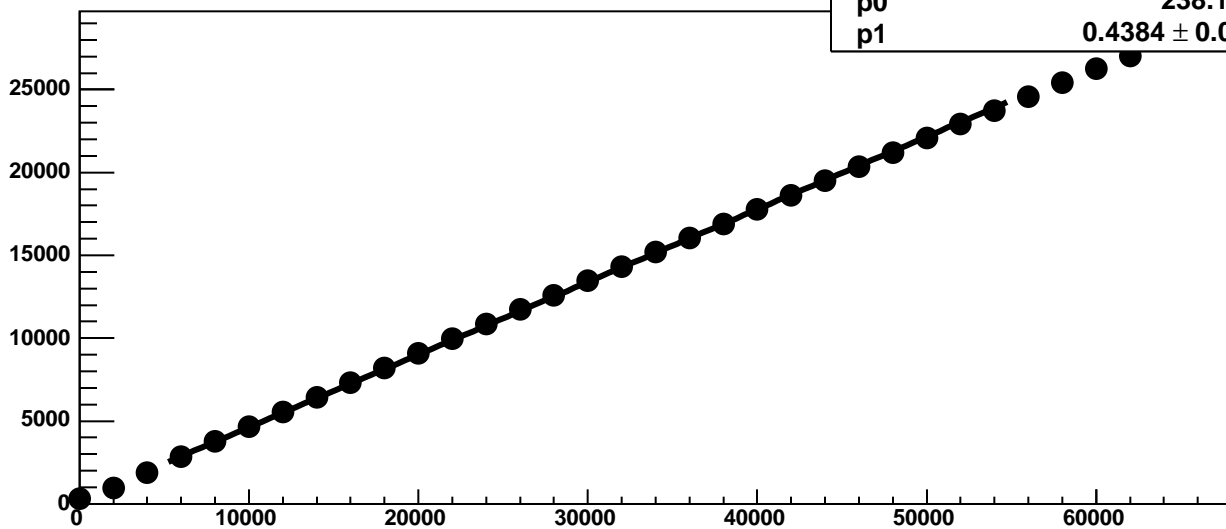
Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

1052 / 23

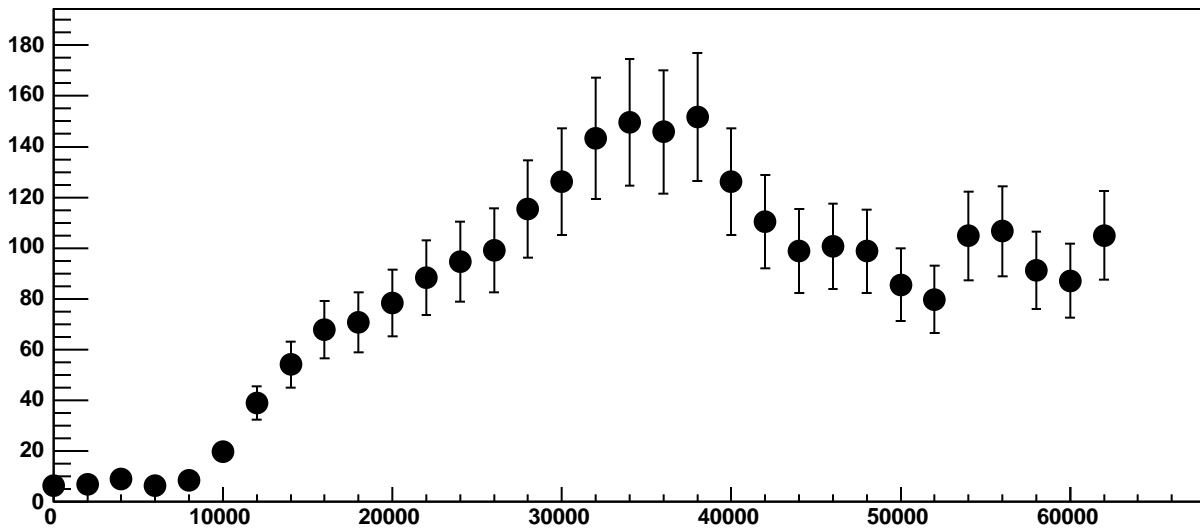
p0

238.1 ± 1.784

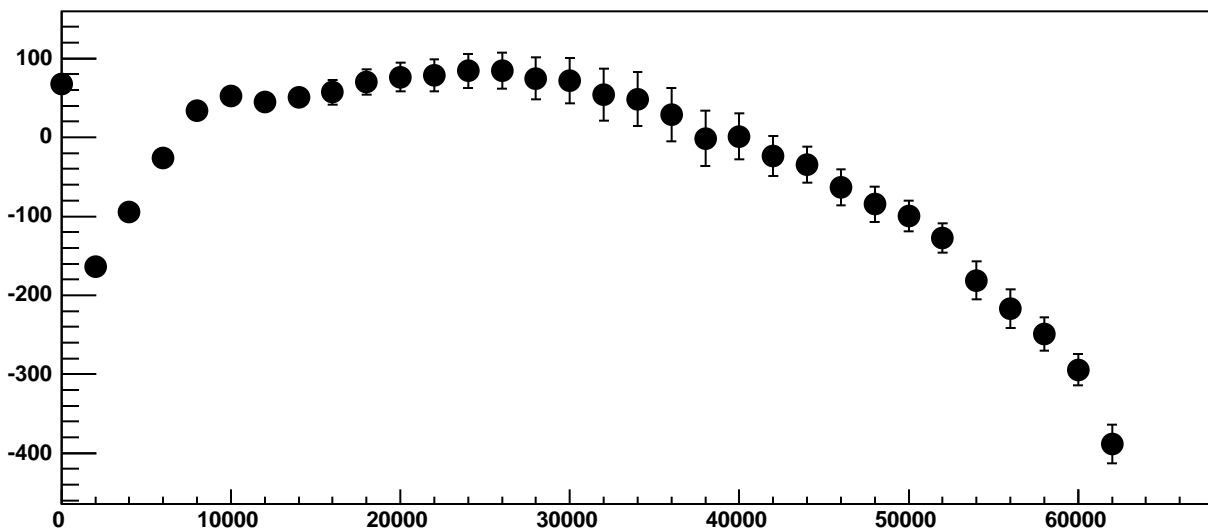
p1

0.4384 ± 0.0001708

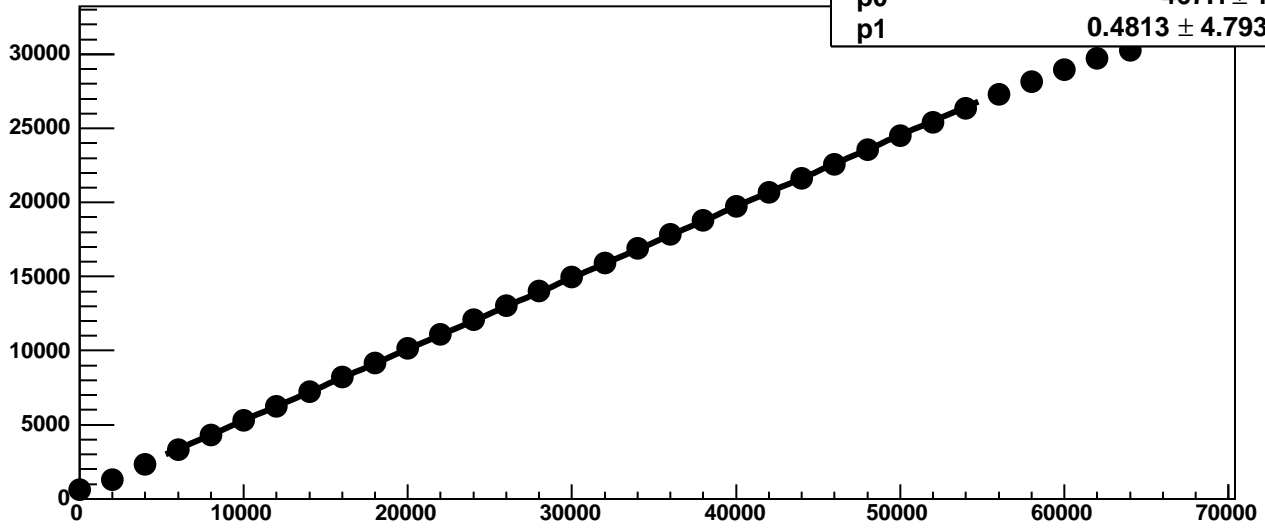
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



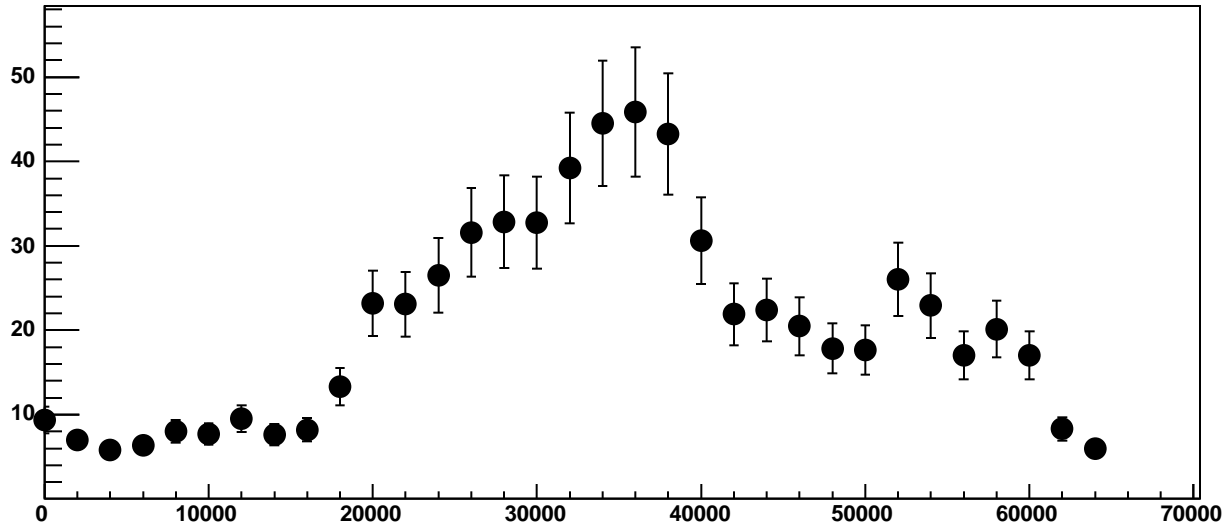
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



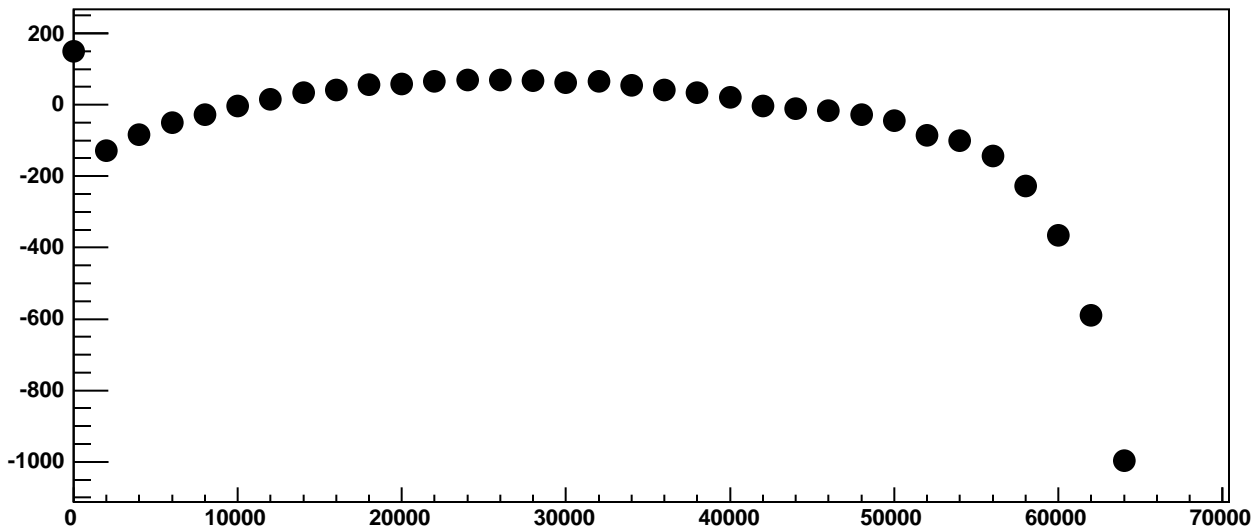
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



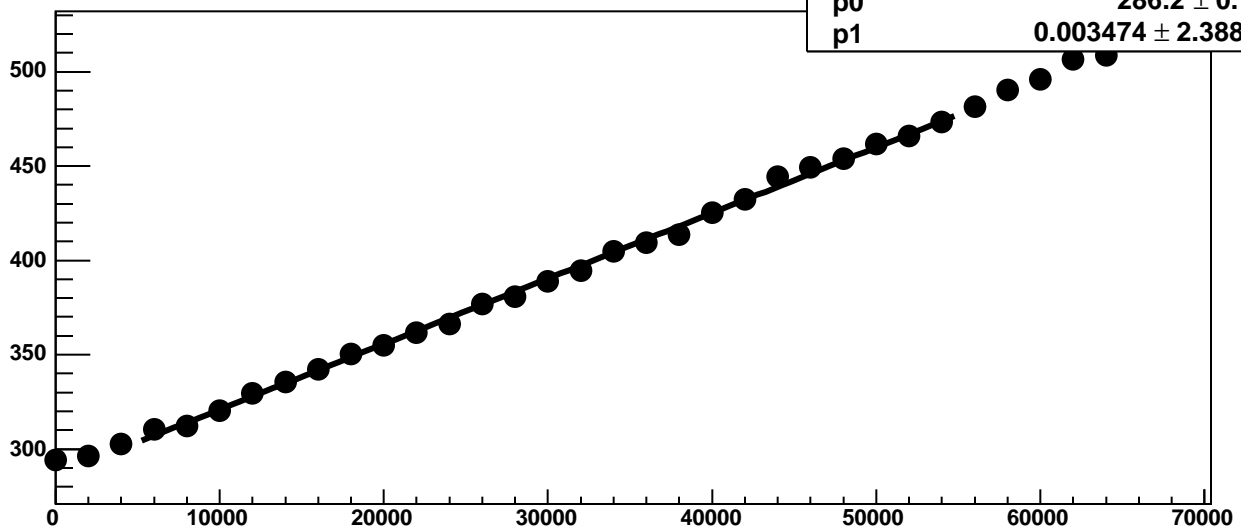
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

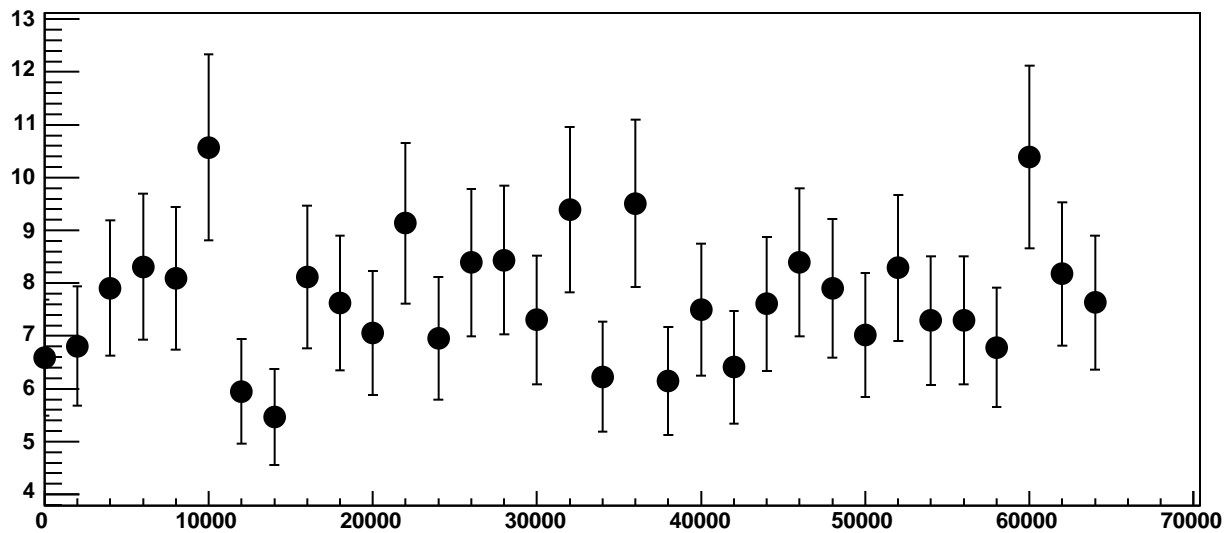


Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

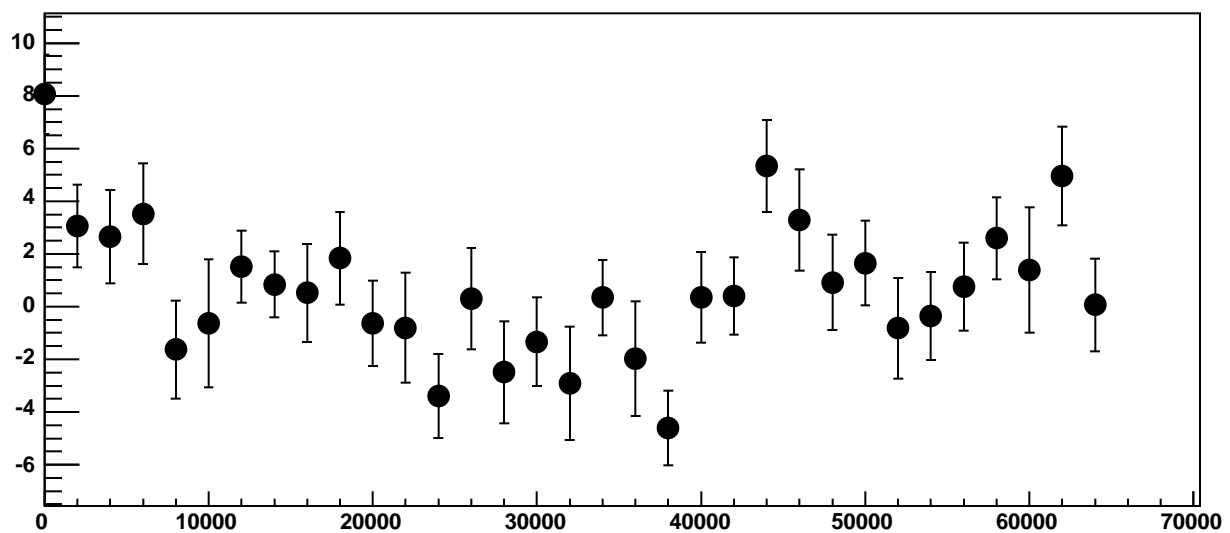


χ^2 / ndf 41.51 / 23
p0 286.2 ± 0.7926
p1 $0.003474 \pm 2.388e-05$

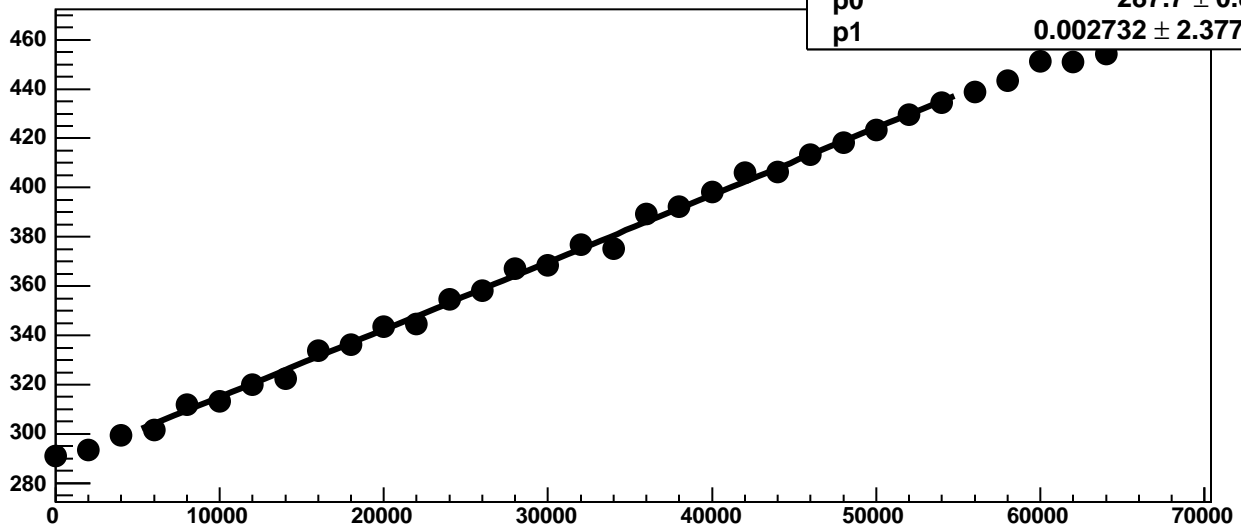
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

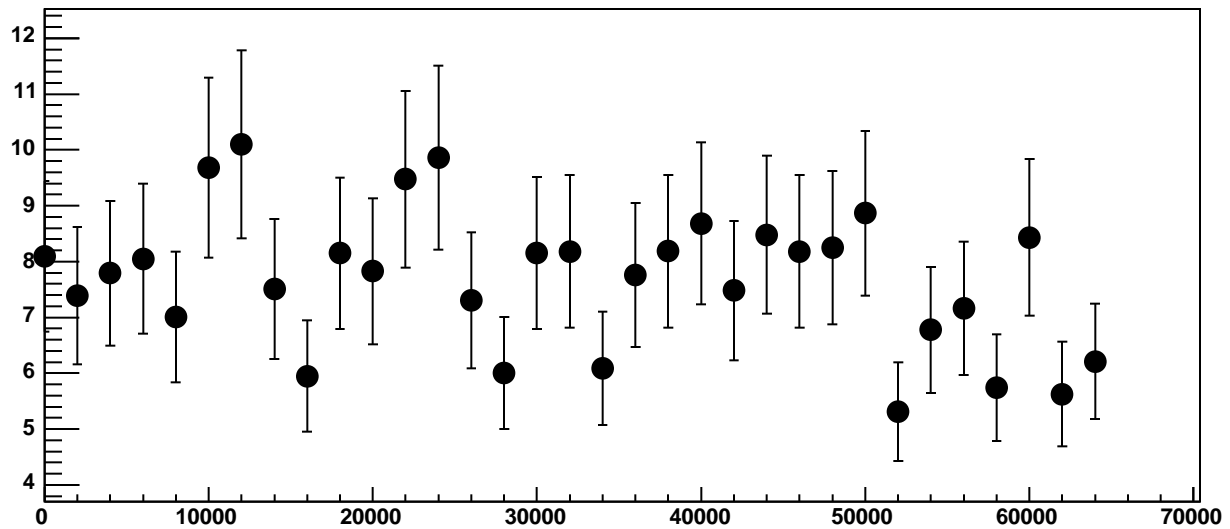


Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

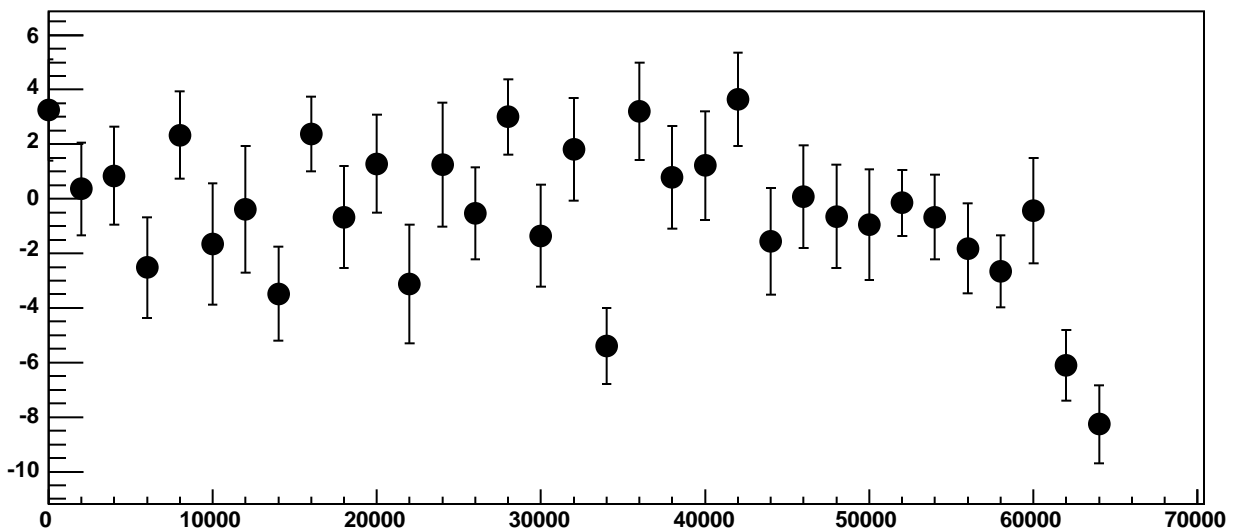


χ^2 / ndf 45.33 / 23
p0 287.7 ± 0.8155
p1 $0.002732 \pm 2.377\text{e-}05$

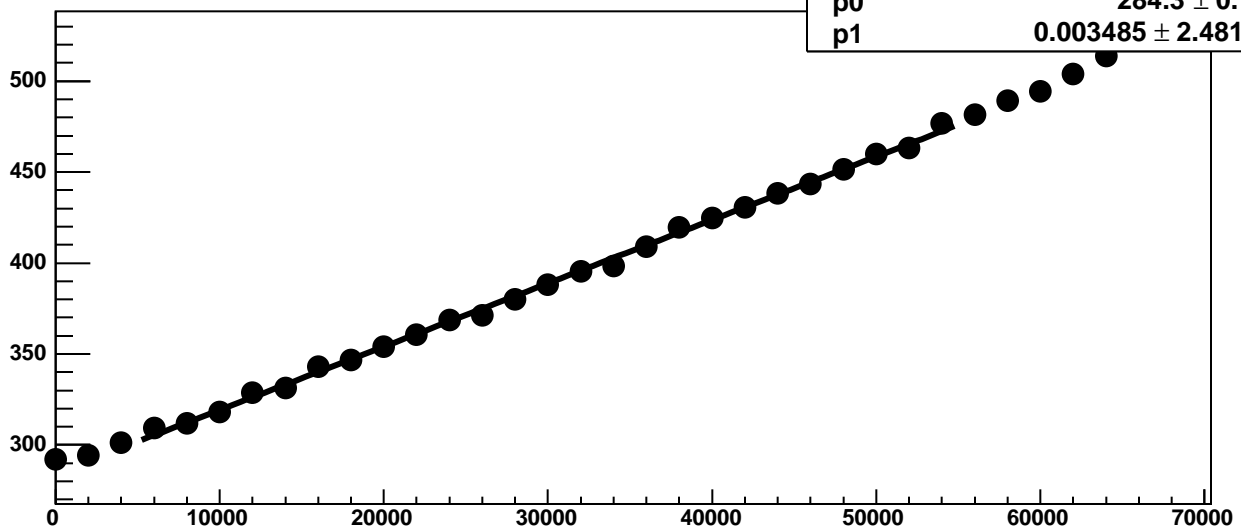
Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

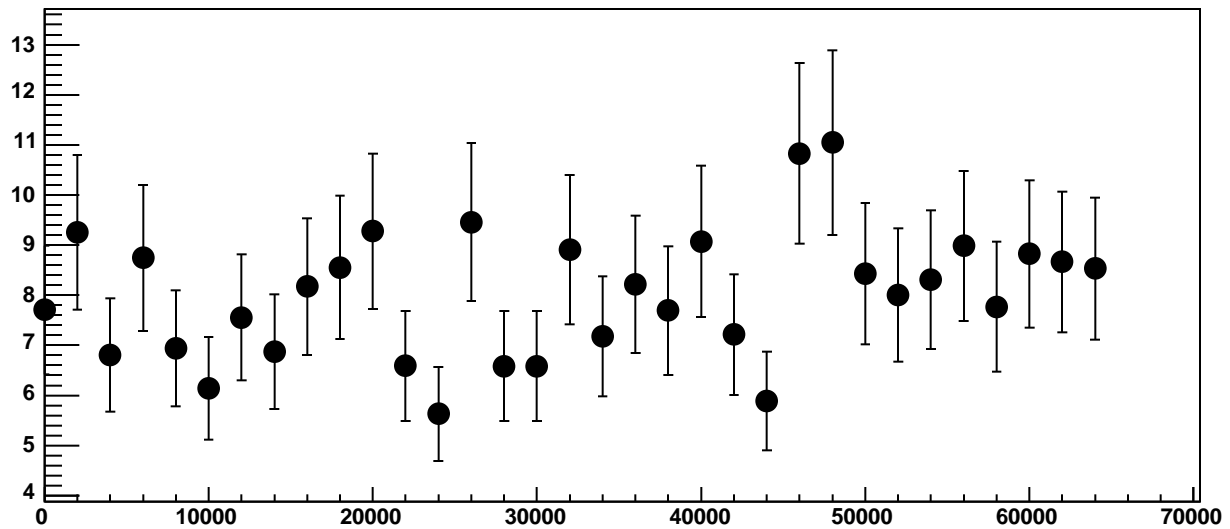


Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

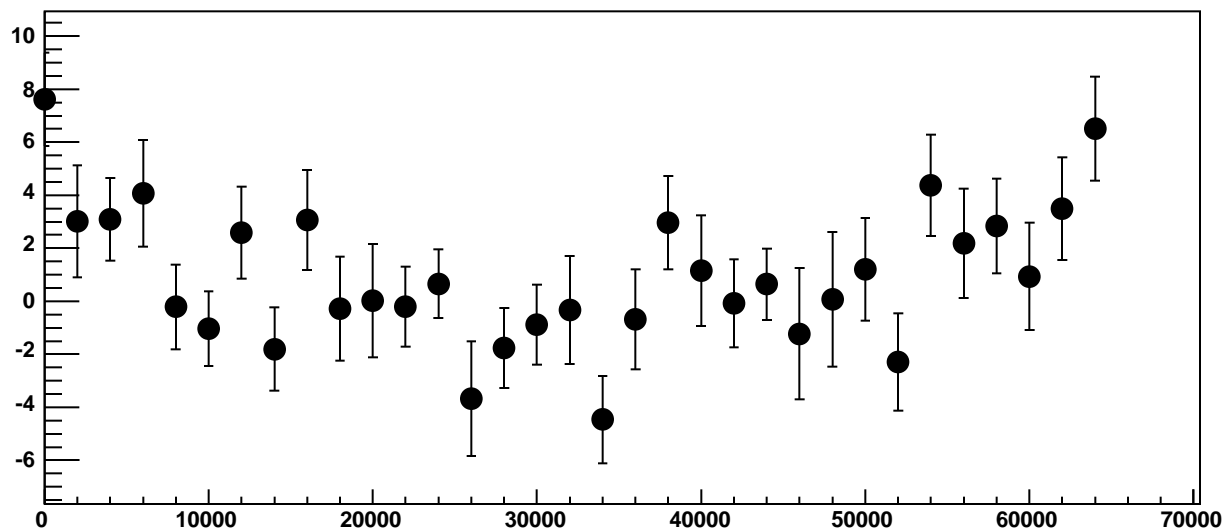


χ^2 / ndf 34.04 / 23
p0 284.3 ± 0.7922
p1 $0.003485 \pm 2.481e-05$

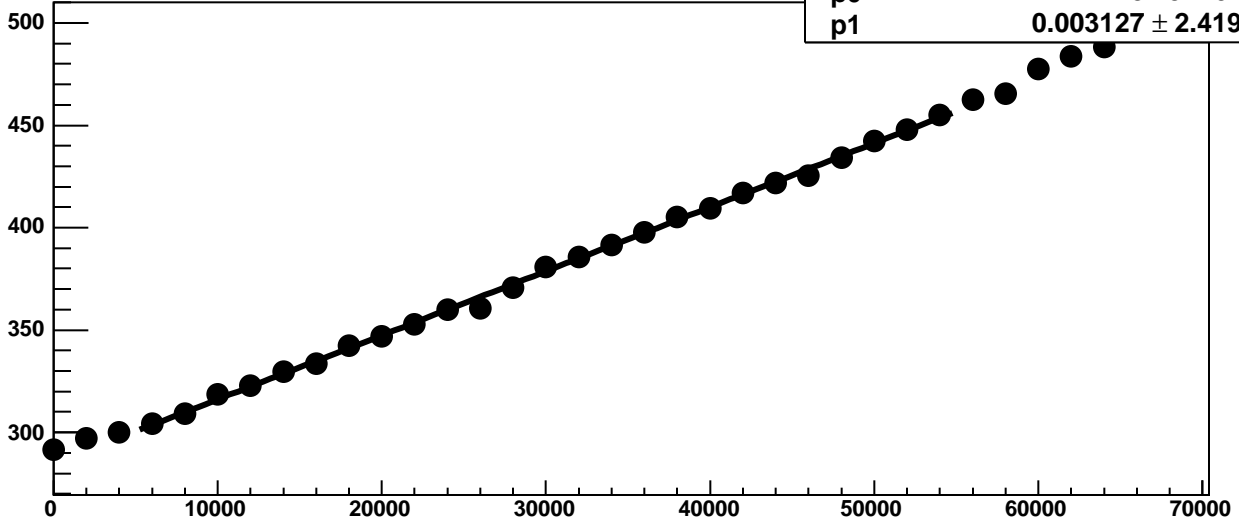
Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

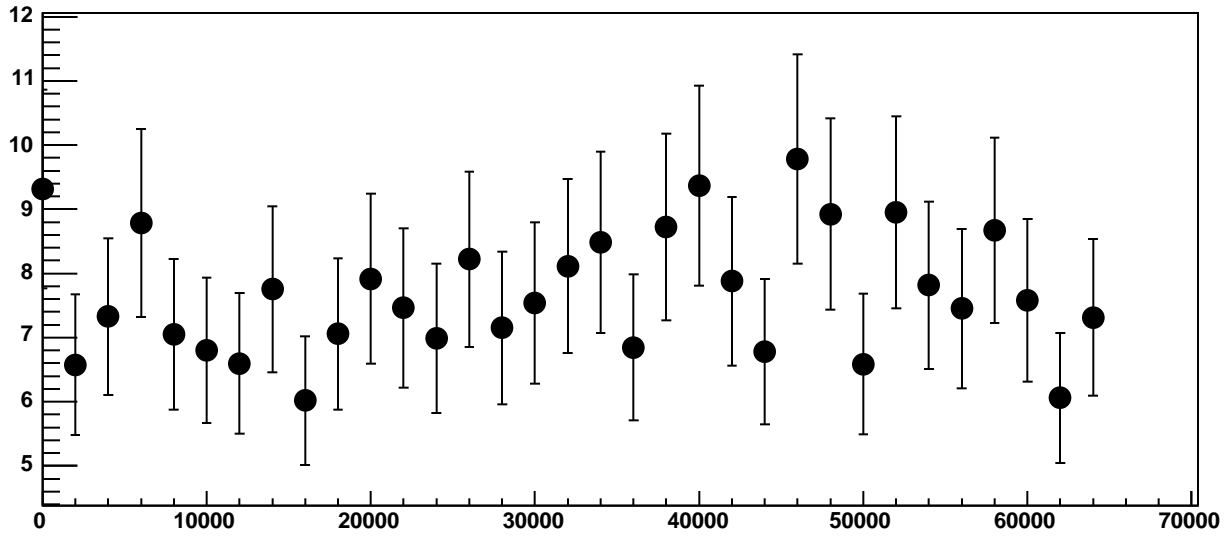


Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

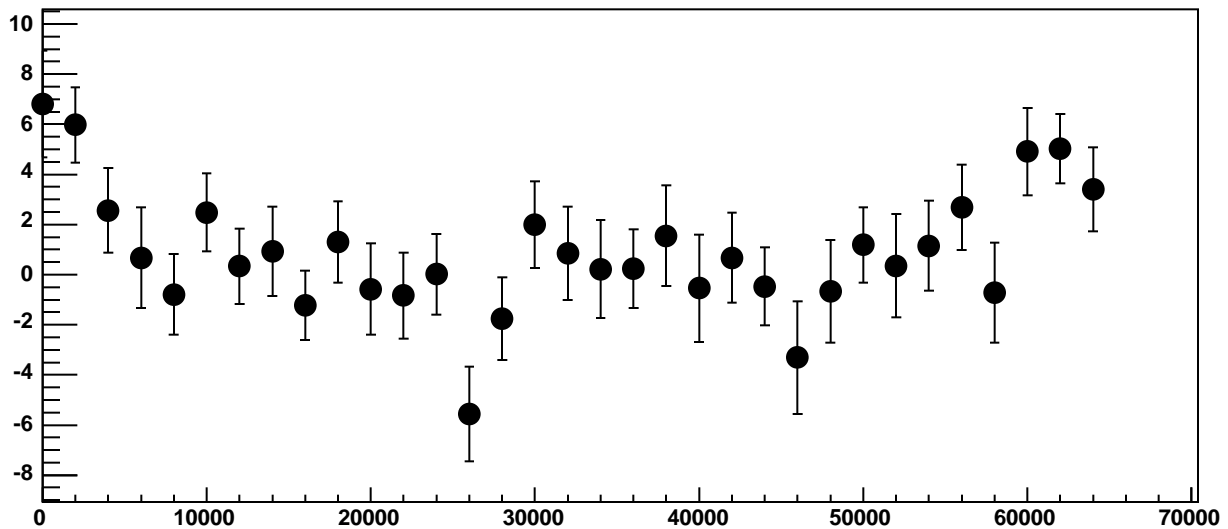


χ^2 / ndf 20.59 / 23
p0 284.9 ± 0.7764
p1 $0.003127 \pm 2.419\text{e-}05$

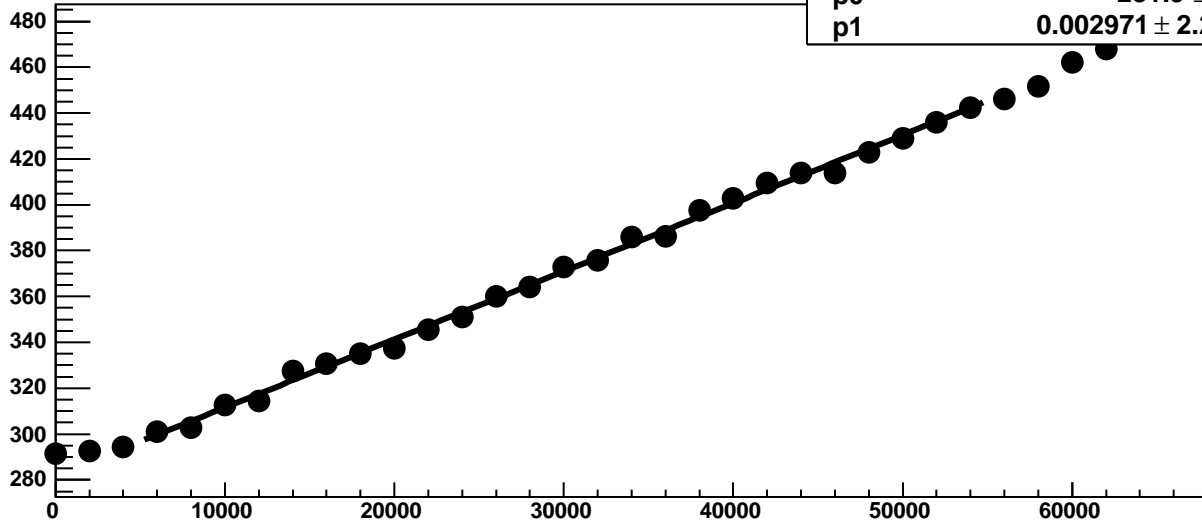
Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

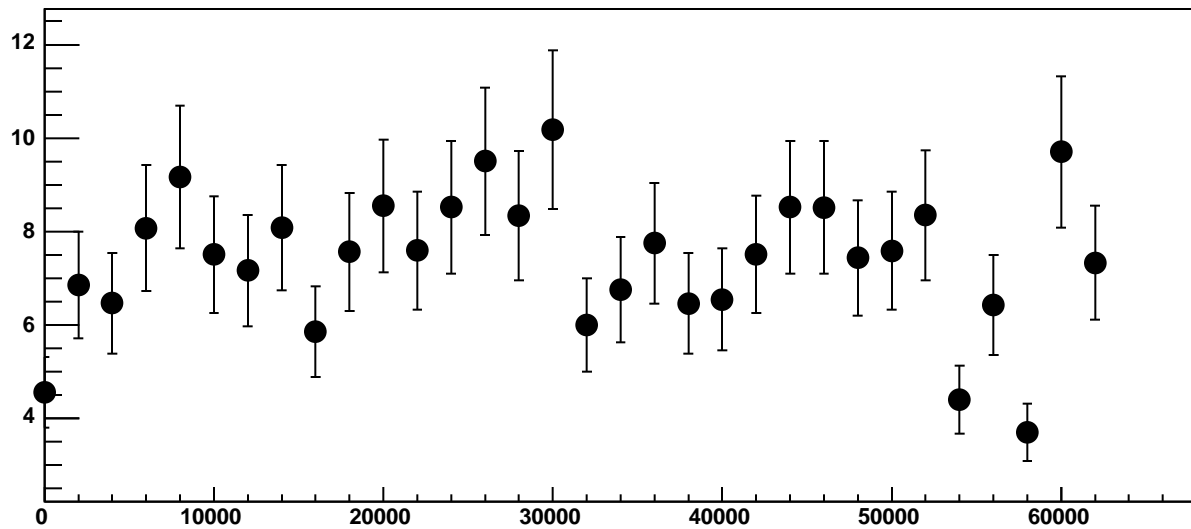


Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

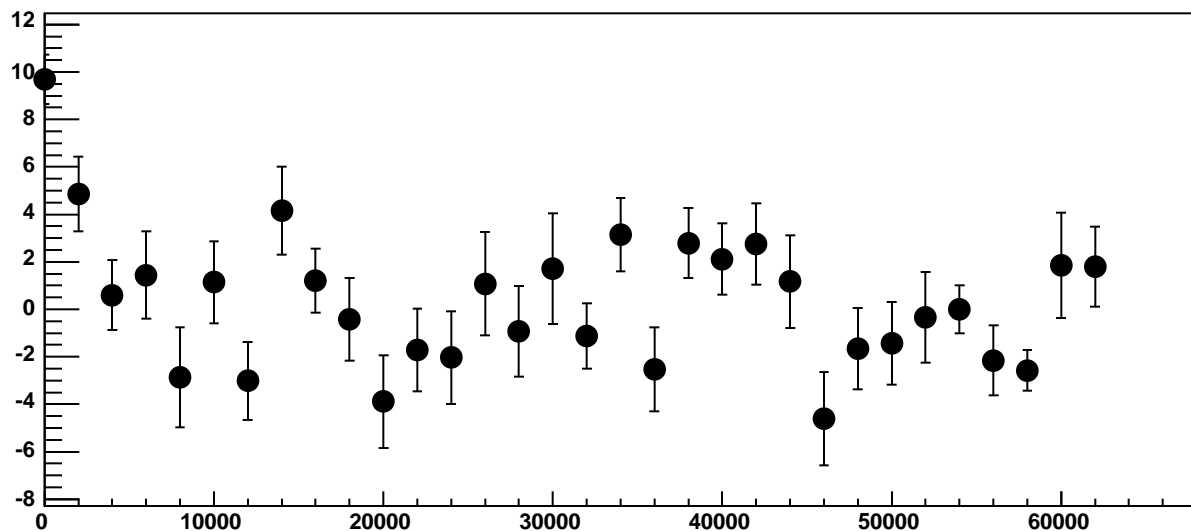


χ^2 / ndf 41.65 / 23
p0 281.9 ± 0.7895
p1 $0.002971 \pm 2.239\text{e-}05$

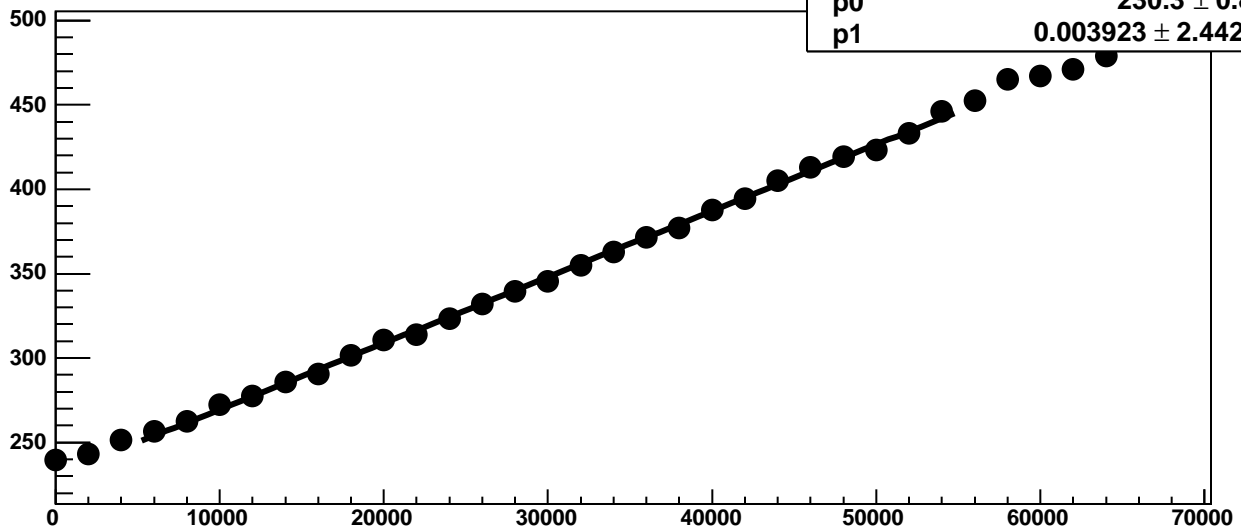
Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

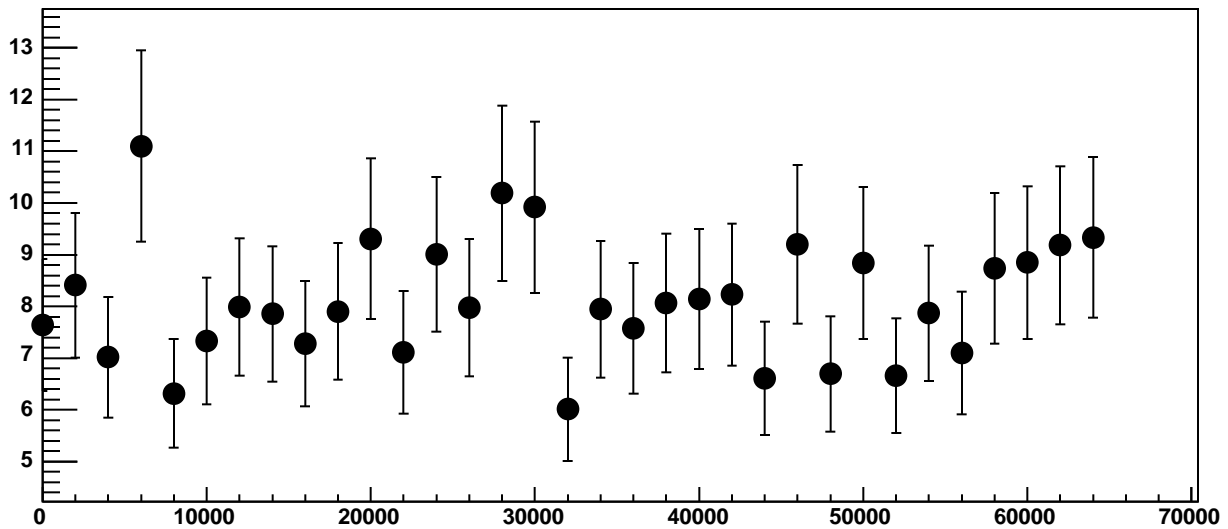


Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

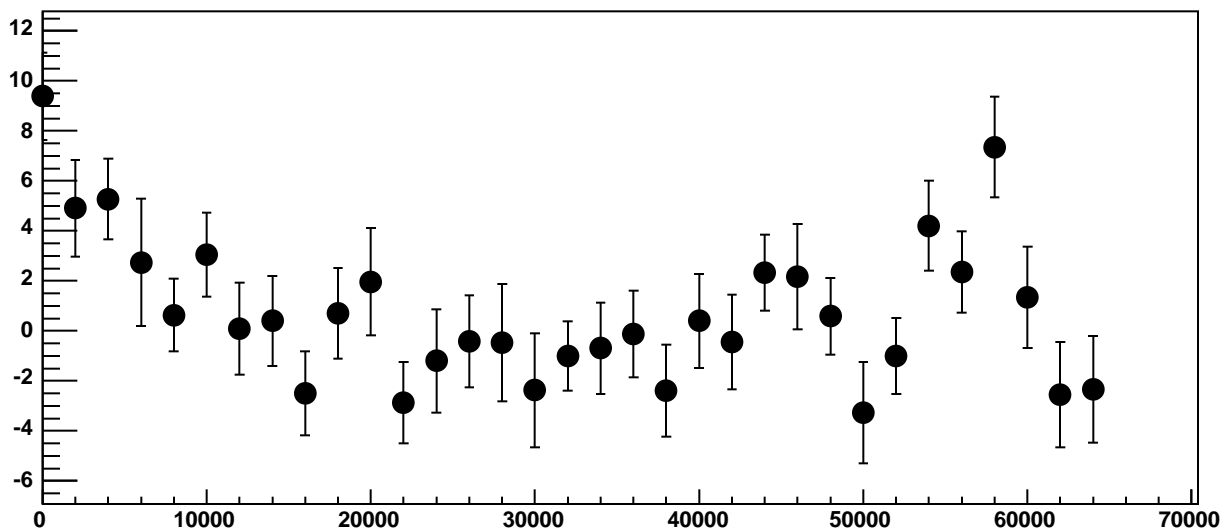


χ^2 / ndf 27.01 / 23
p0 230.3 ± 0.8247
p1 $0.003923 \pm 2.442e-05$

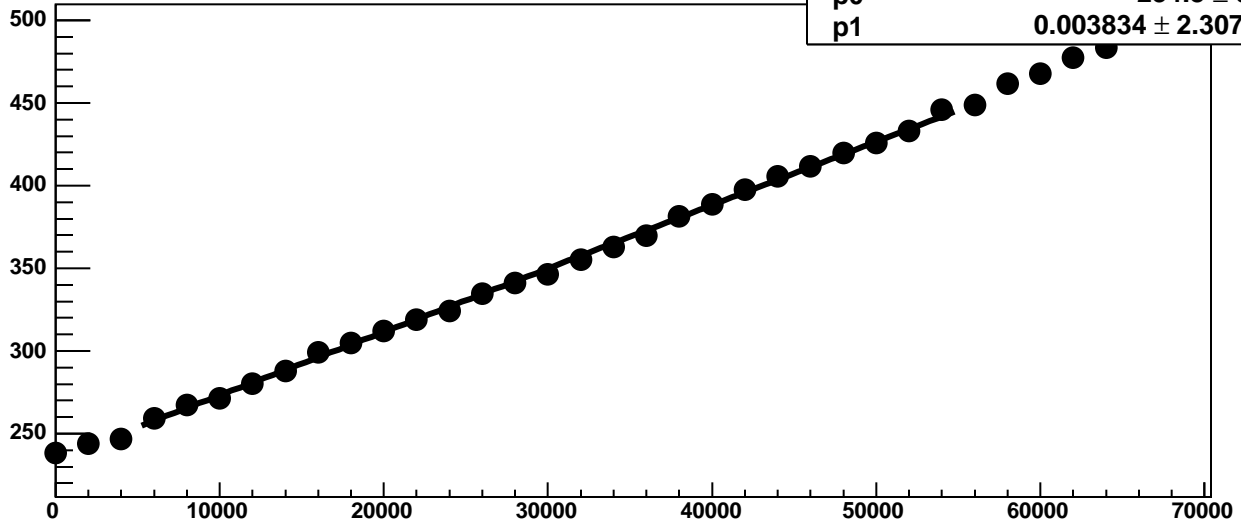
Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

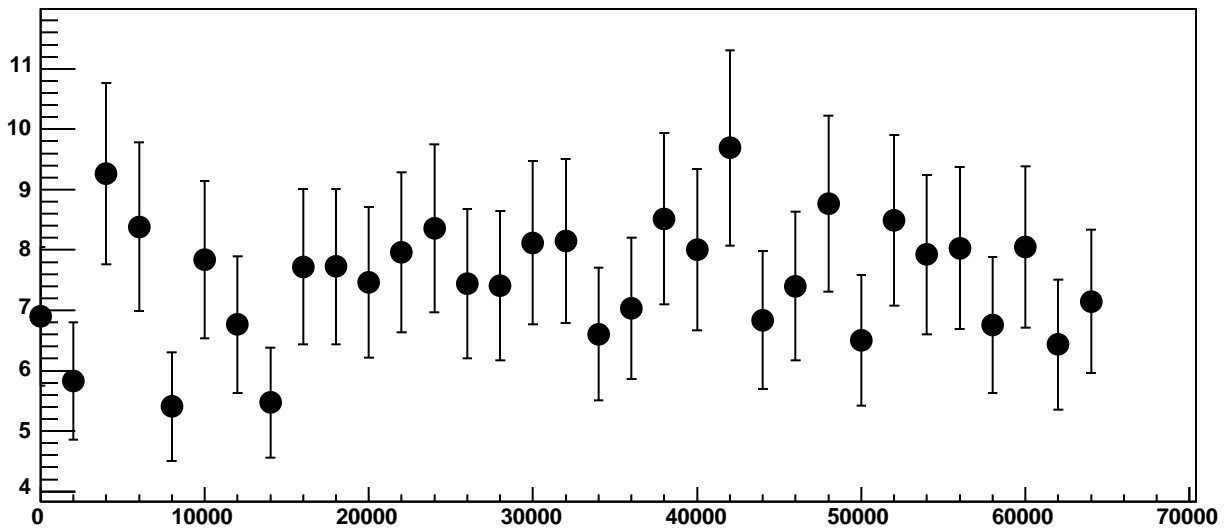


Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

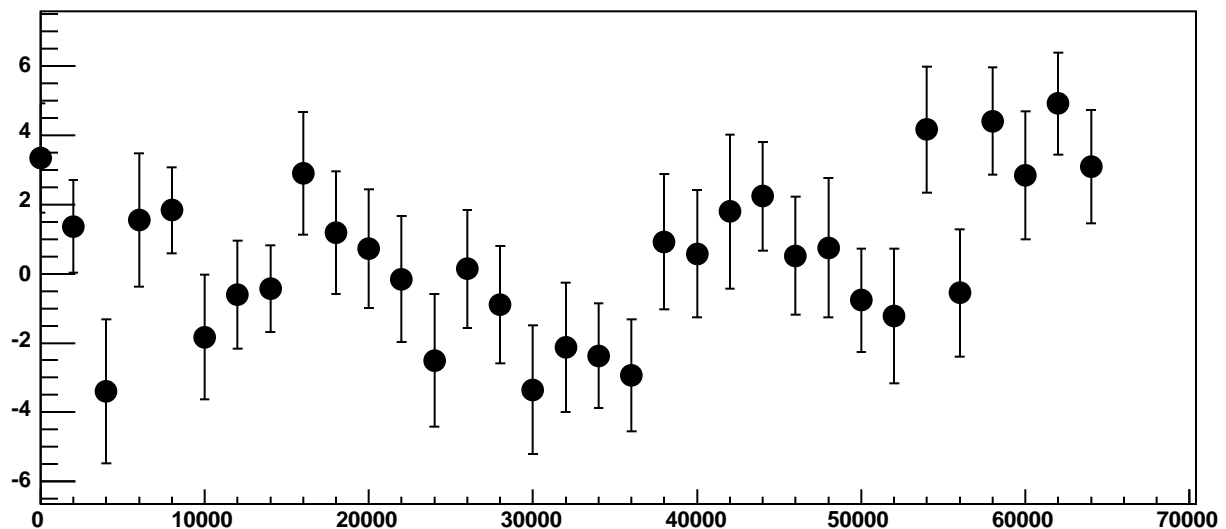


χ^2 / ndf 28.88 / 23
p0 234.8 ± 0.741
p1 $0.003834 \pm 2.307\text{e-}05$

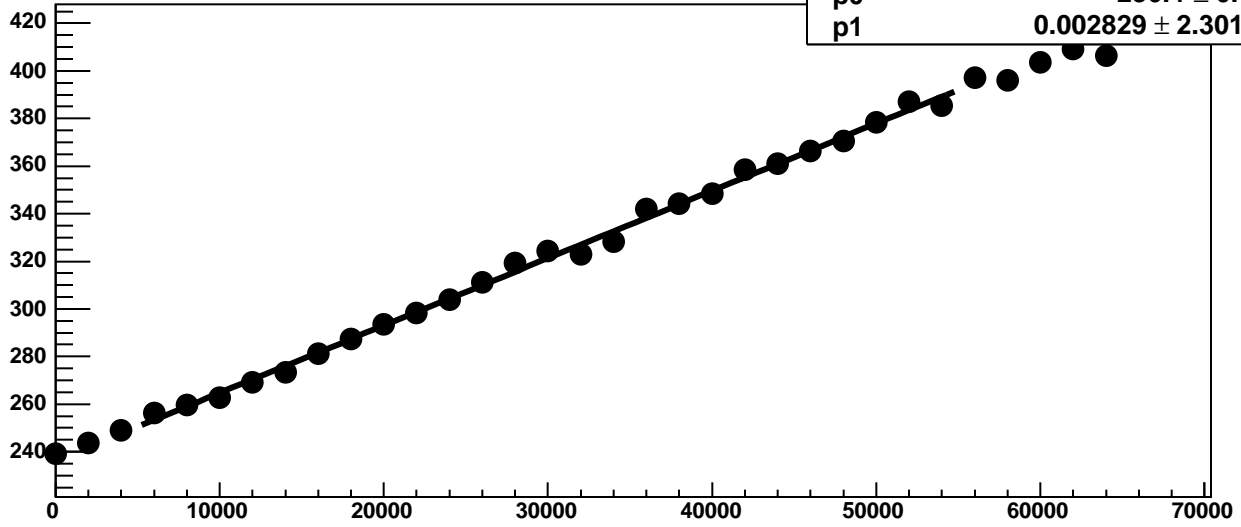
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

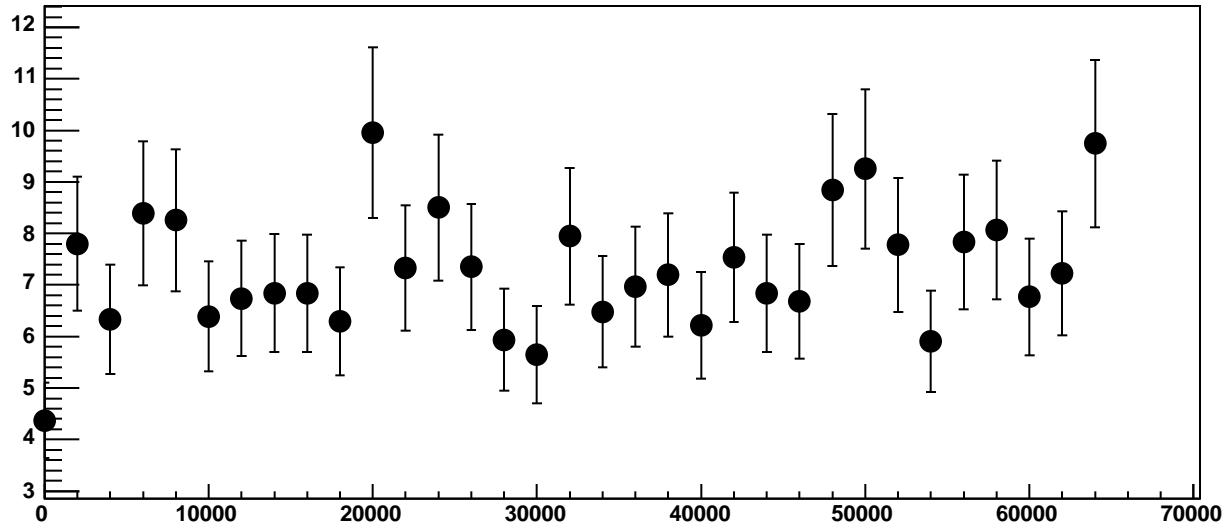


Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

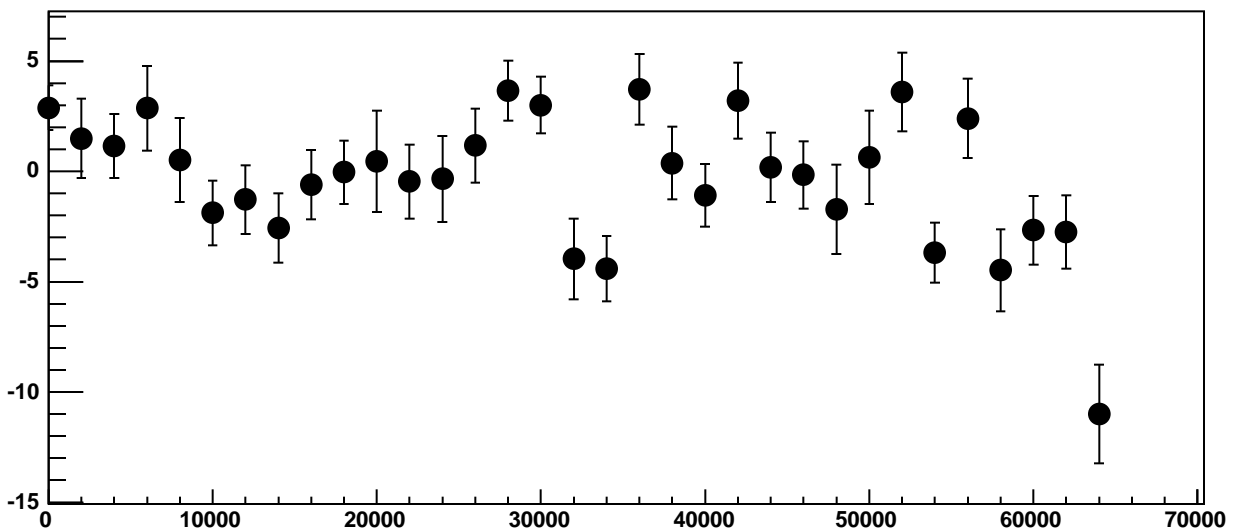


χ^2 / ndf 56.05 / 23
p0 236.4 ± 0.7664
p1 $0.002829 \pm 2.301e-05$

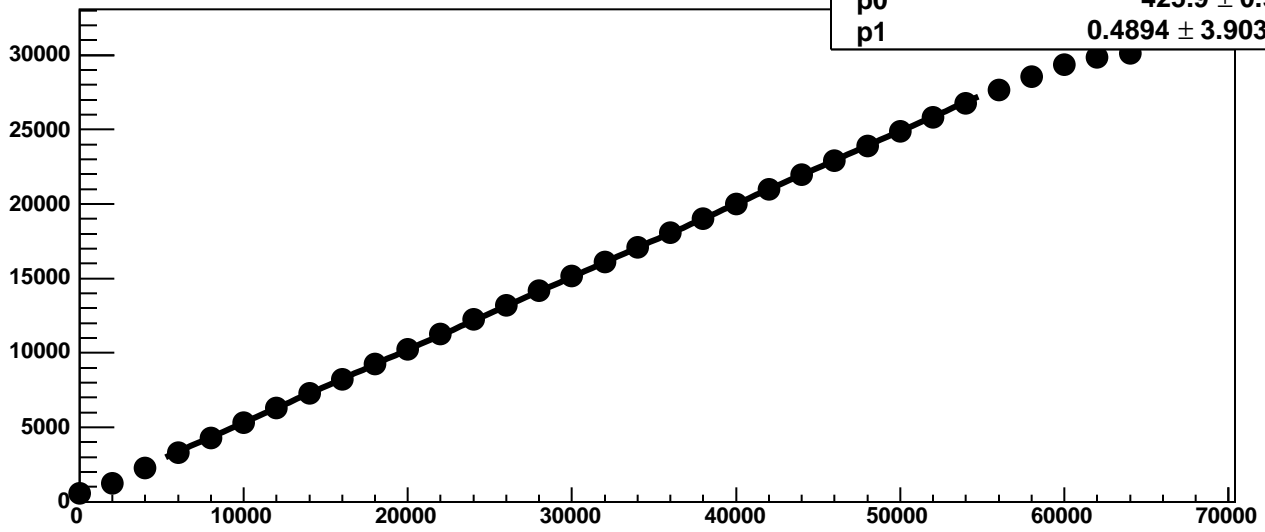
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



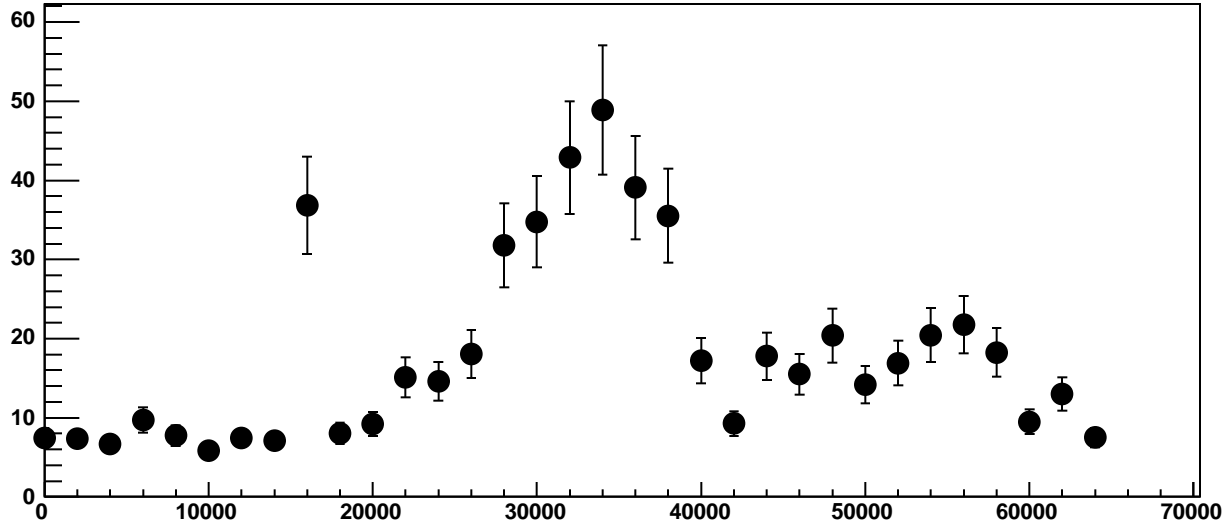
Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



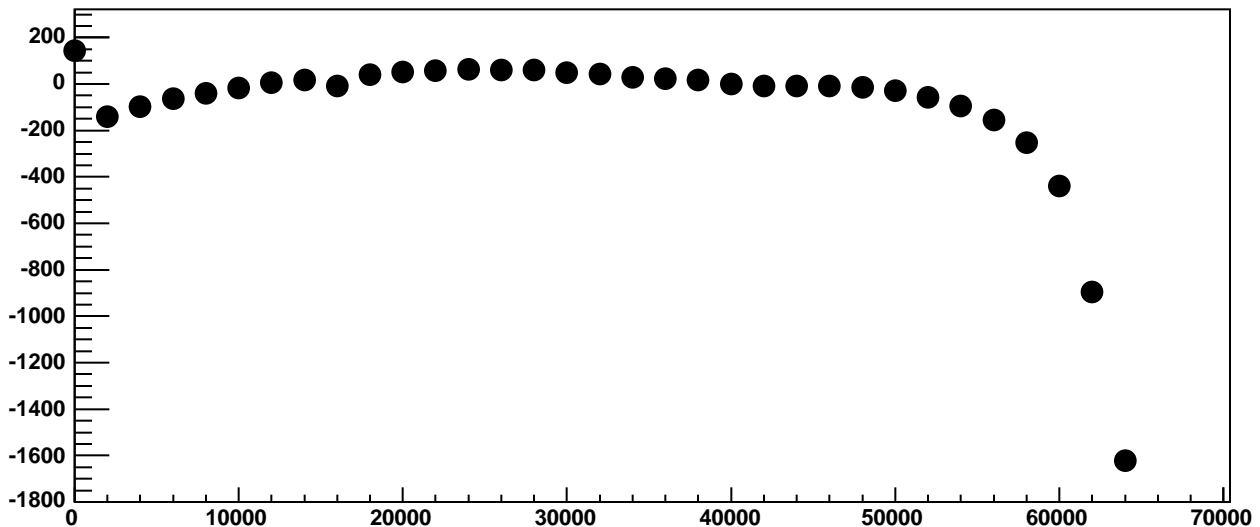
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



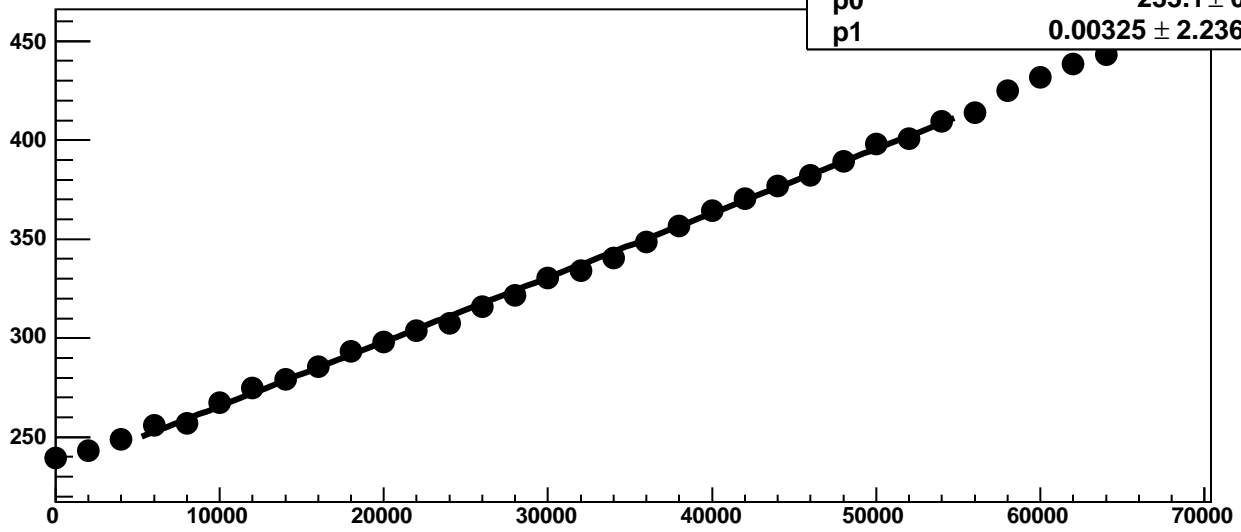
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

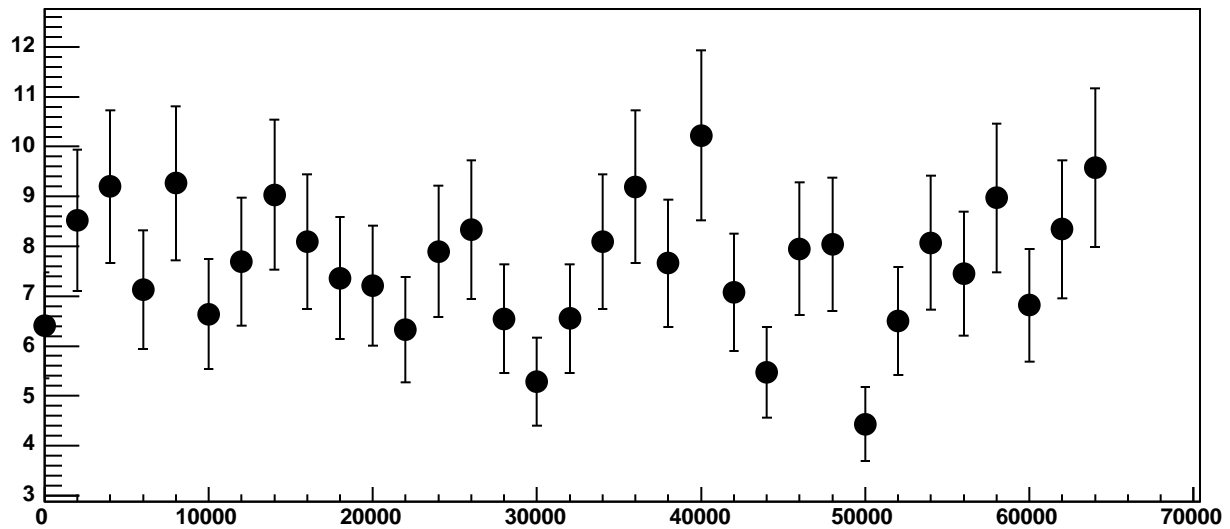


Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

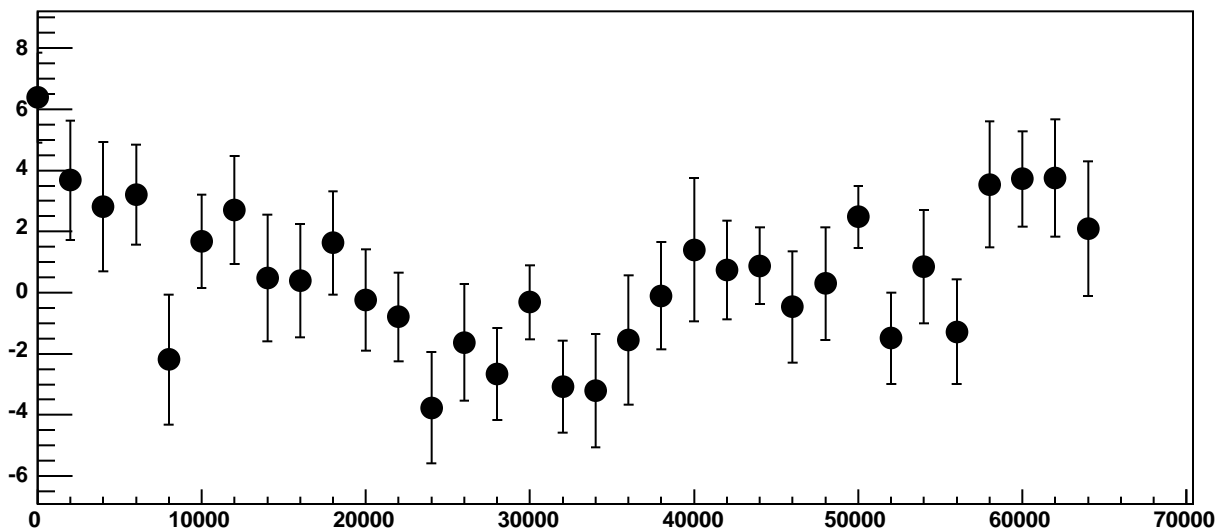


χ^2 / ndf 34.08 / 23
p0 233.1 ± 0.779
p1 $0.00325 \pm 2.236e-05$

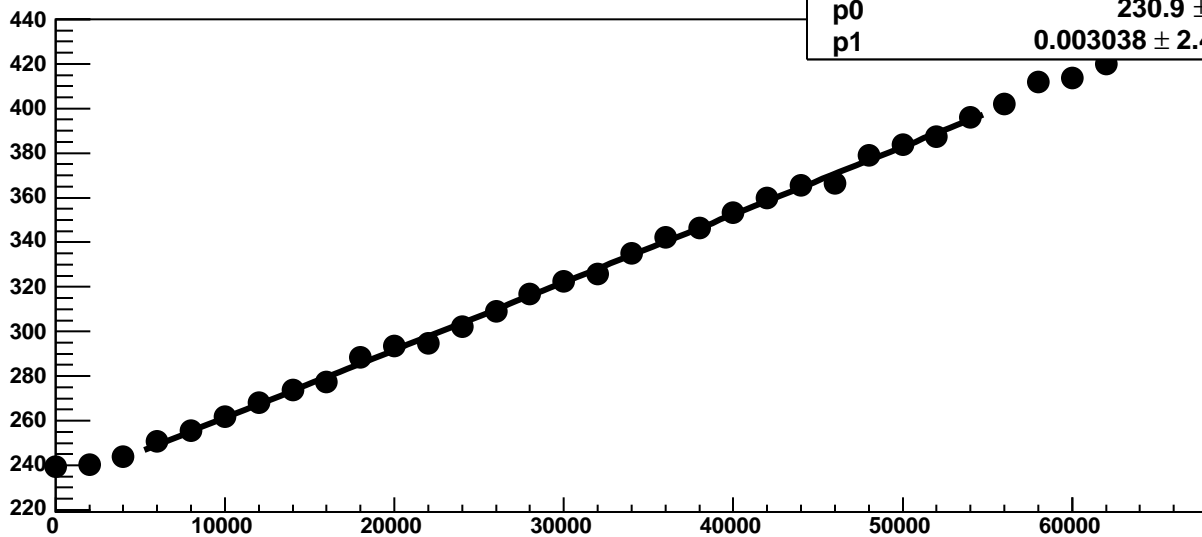
Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



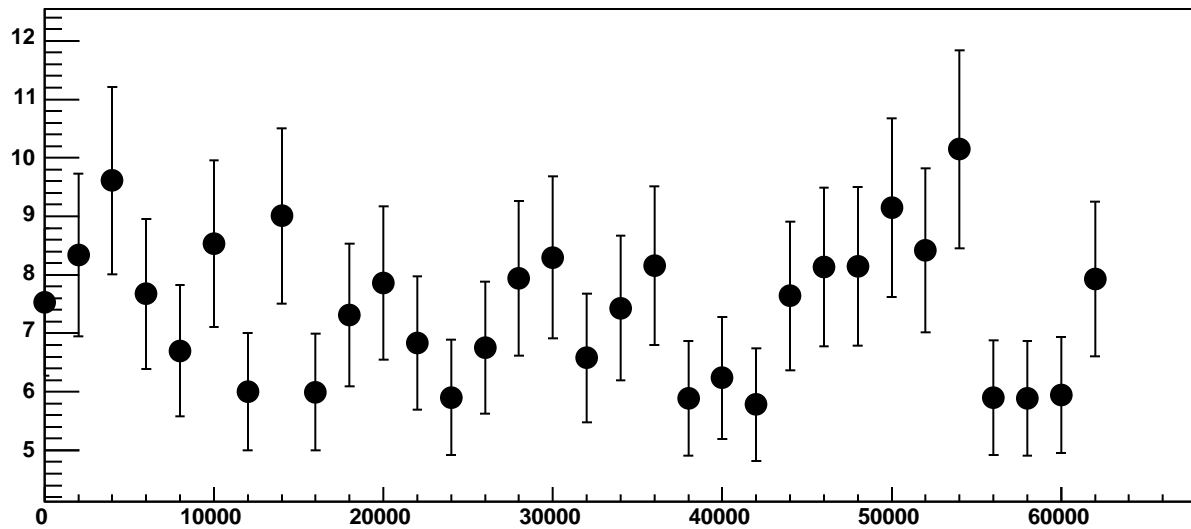
Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



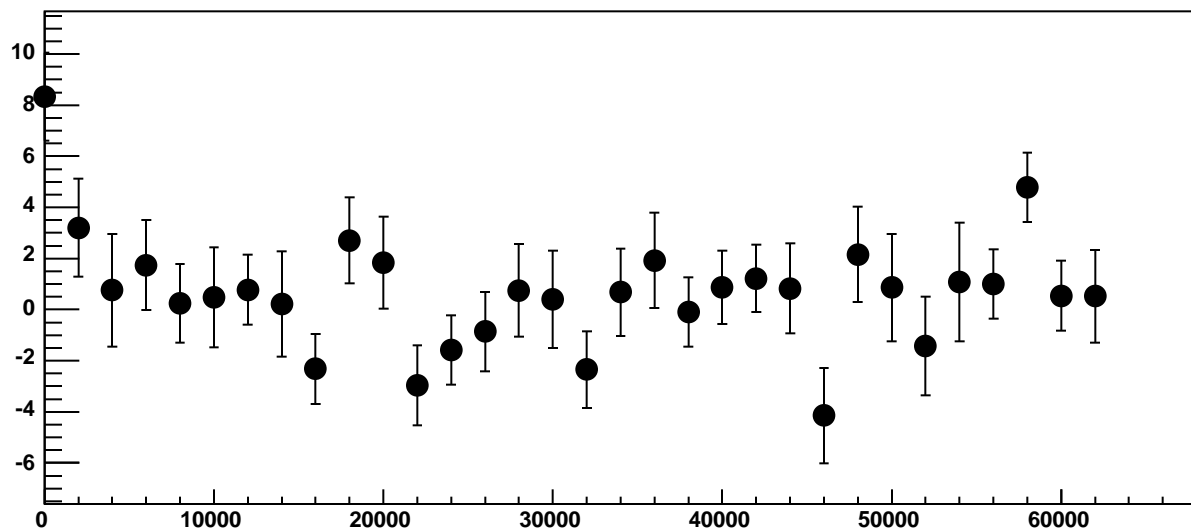
Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



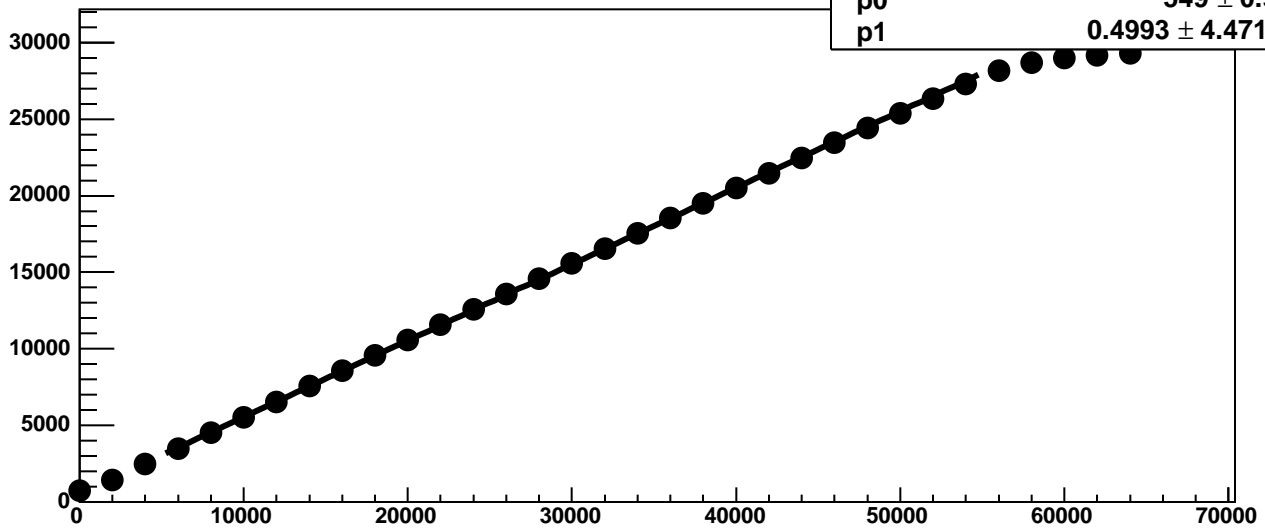
Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



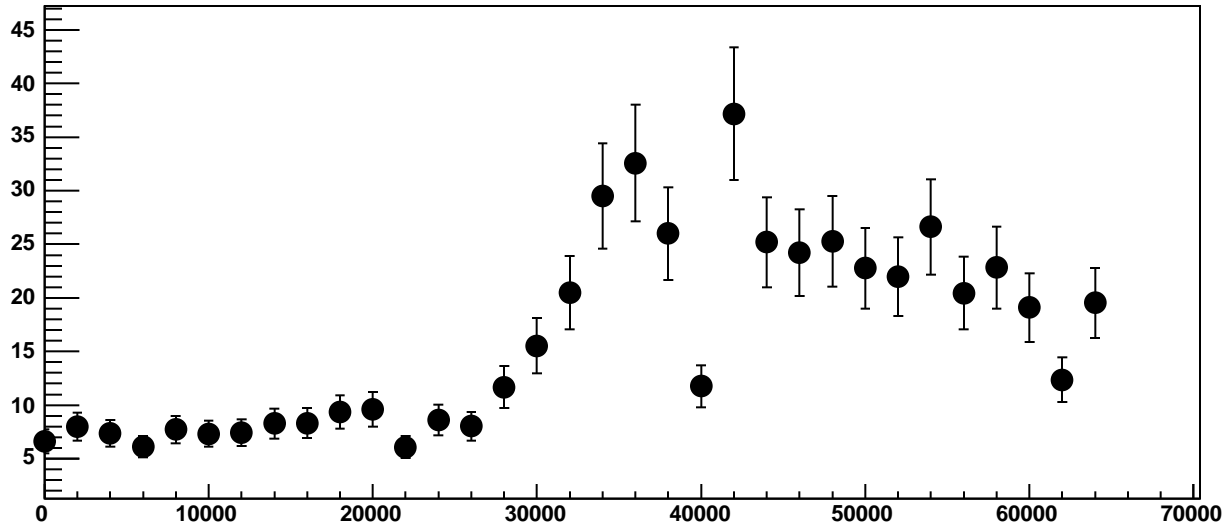
Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



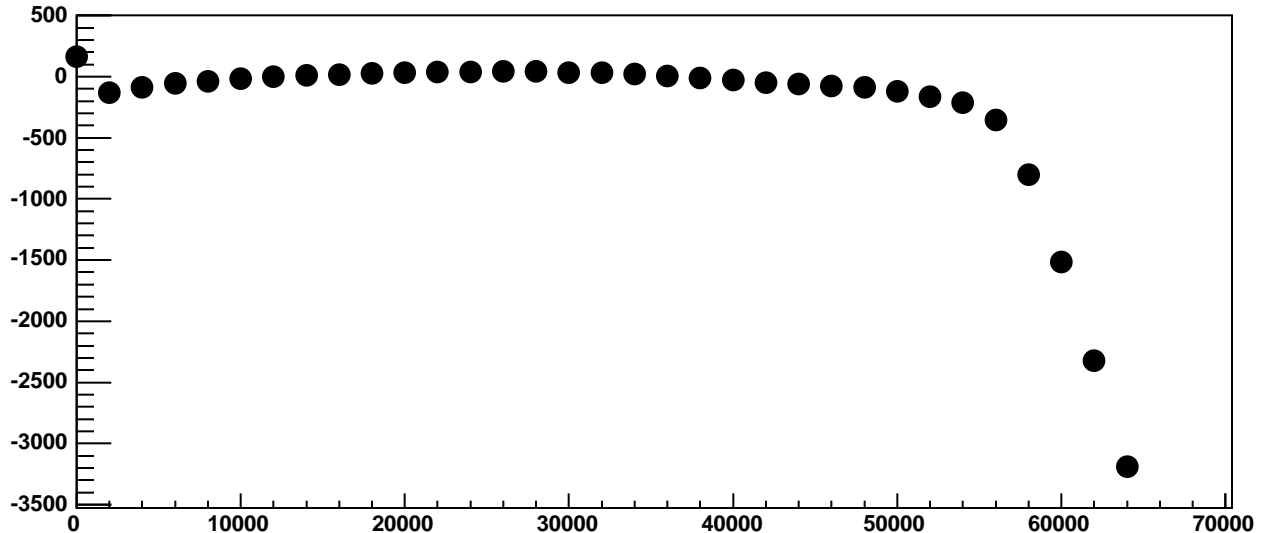
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



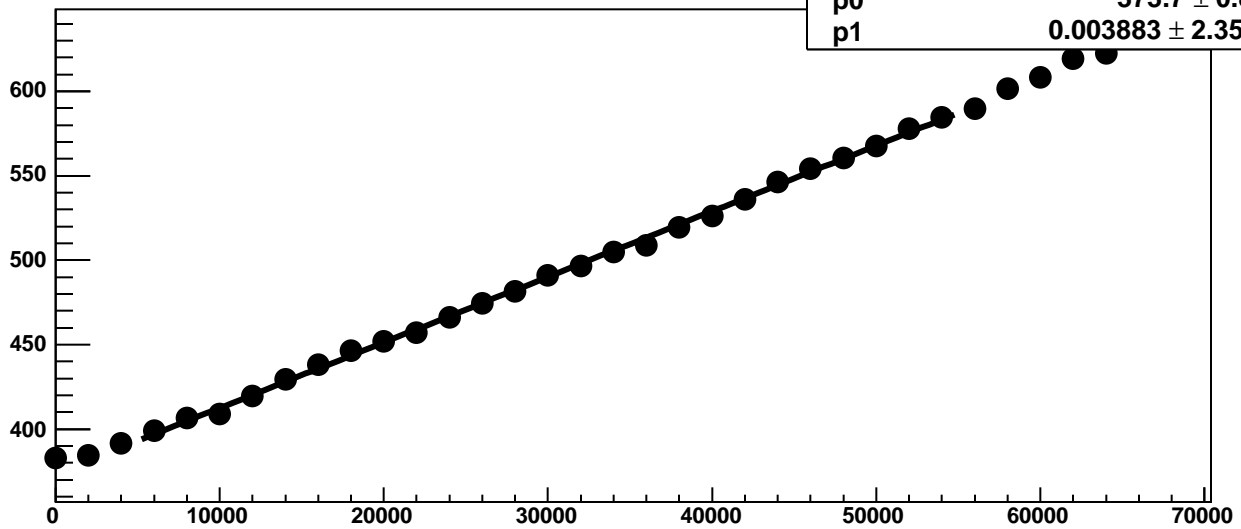
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

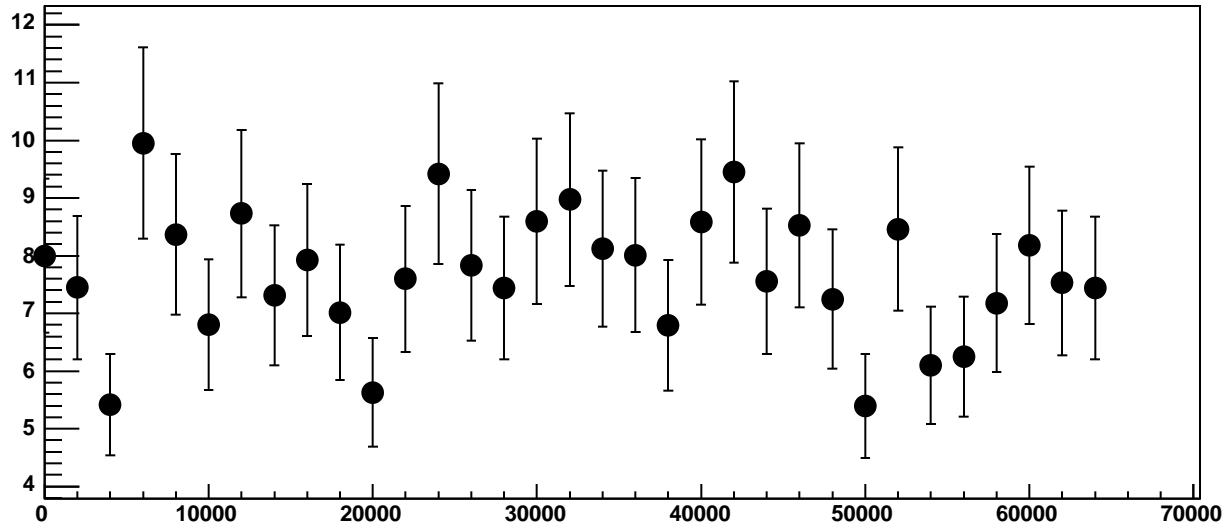


Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

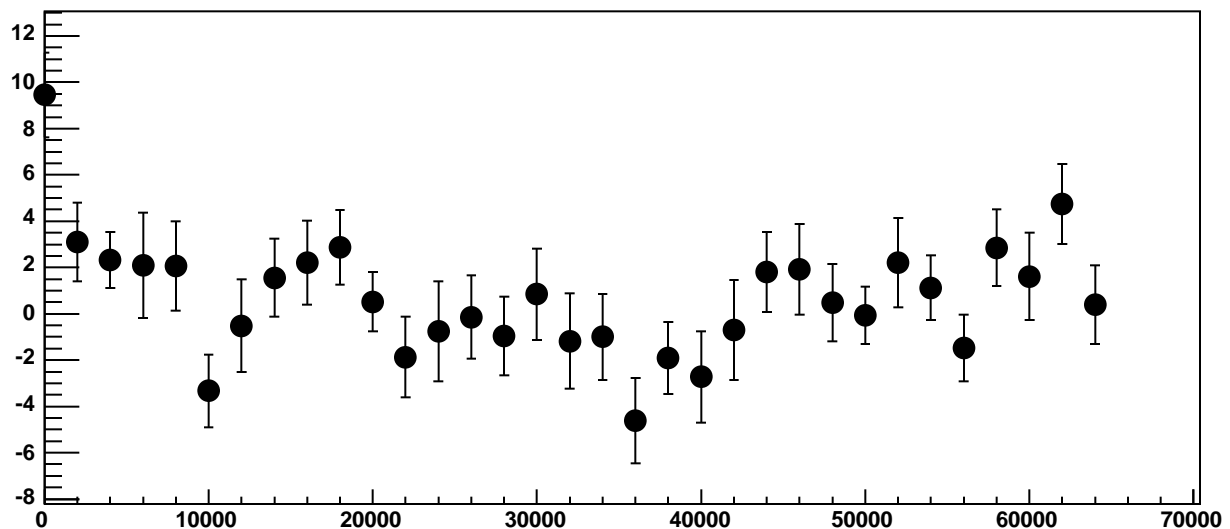


χ^2 / ndf 28.58 / 23
p0 373.7 ± 0.8079
p1 $0.003883 \pm 2.35e-05$

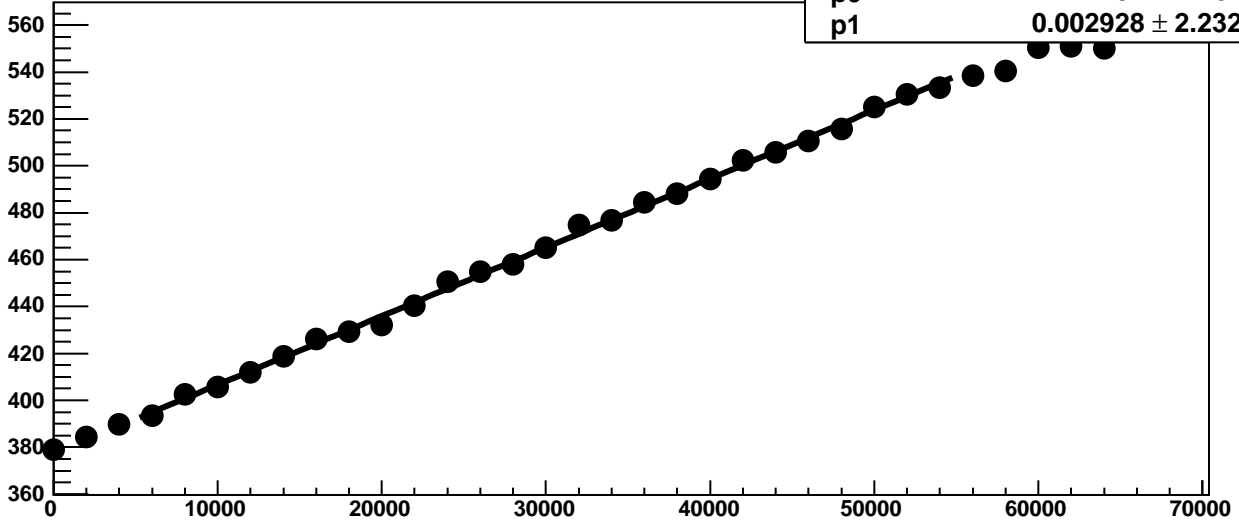
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



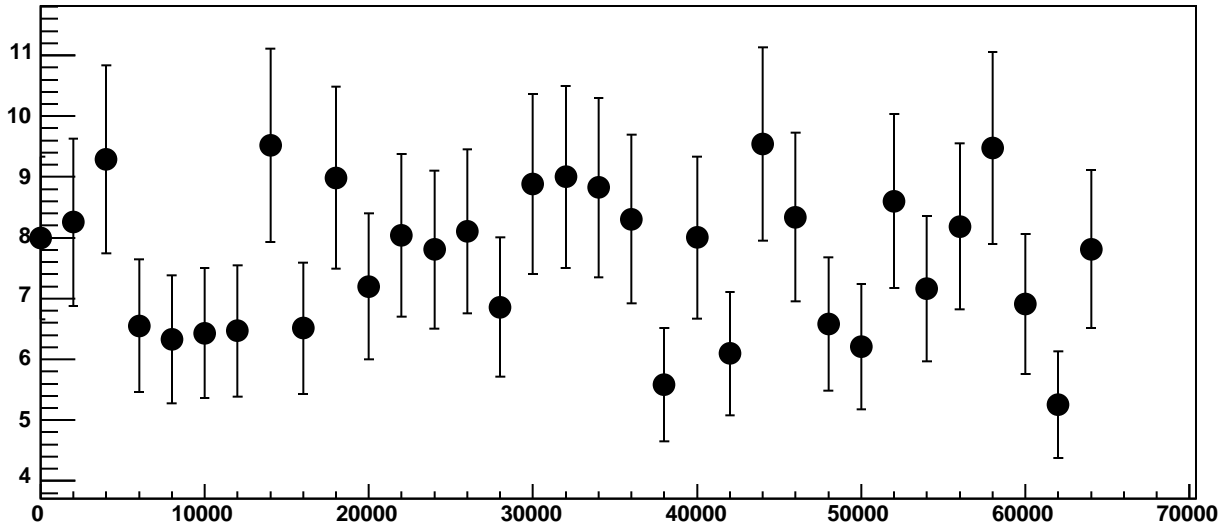
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



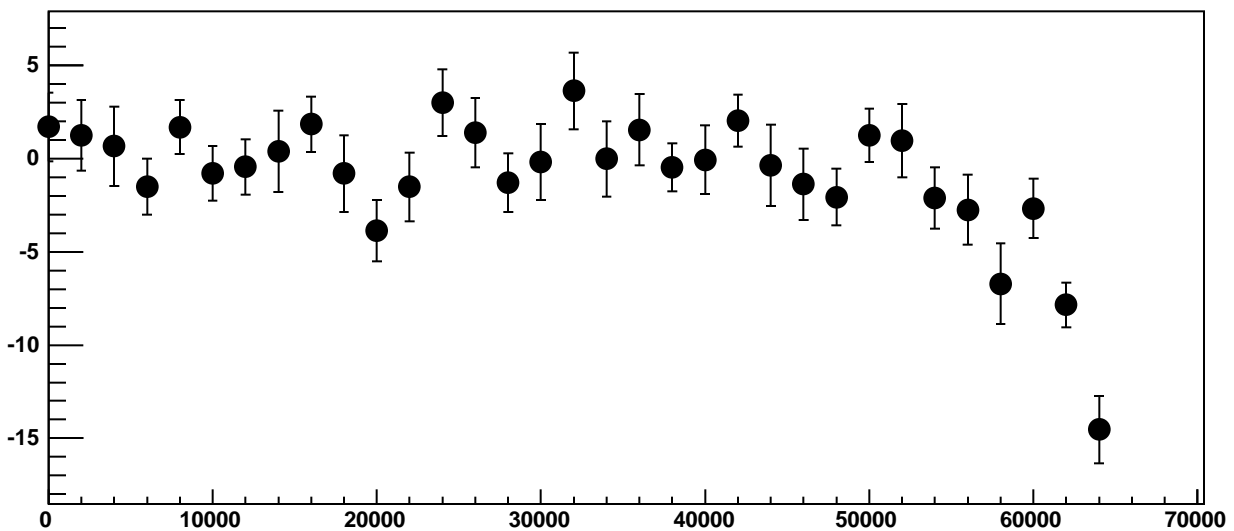
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



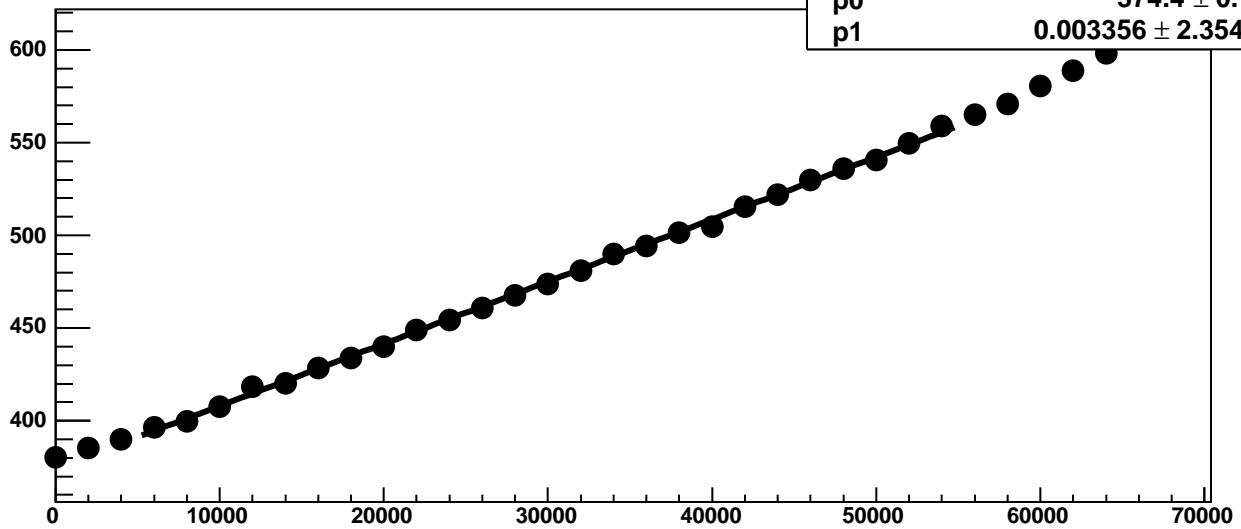
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

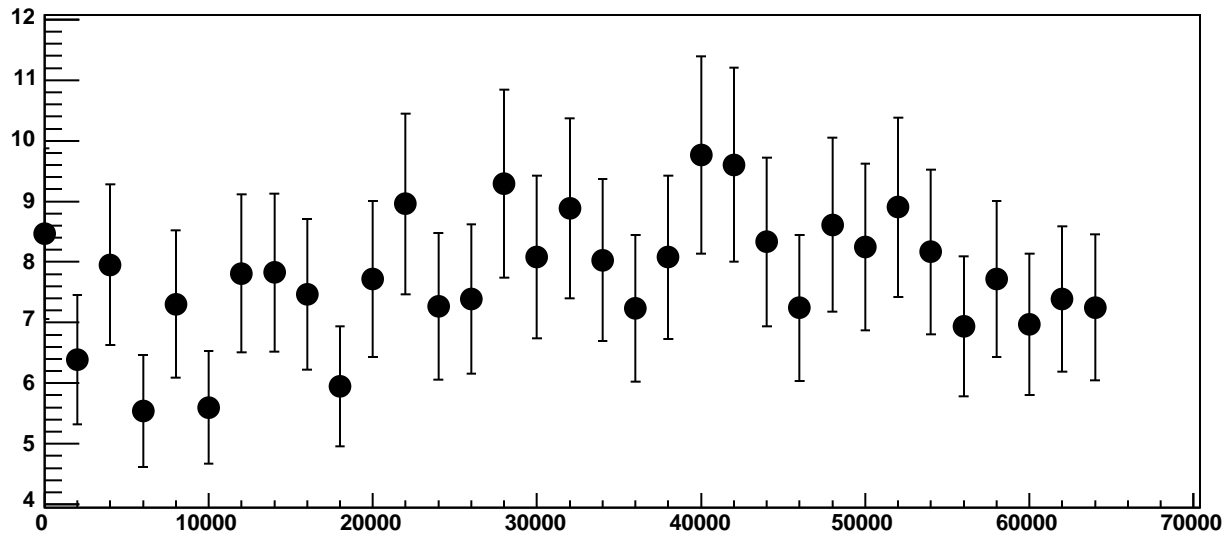


Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

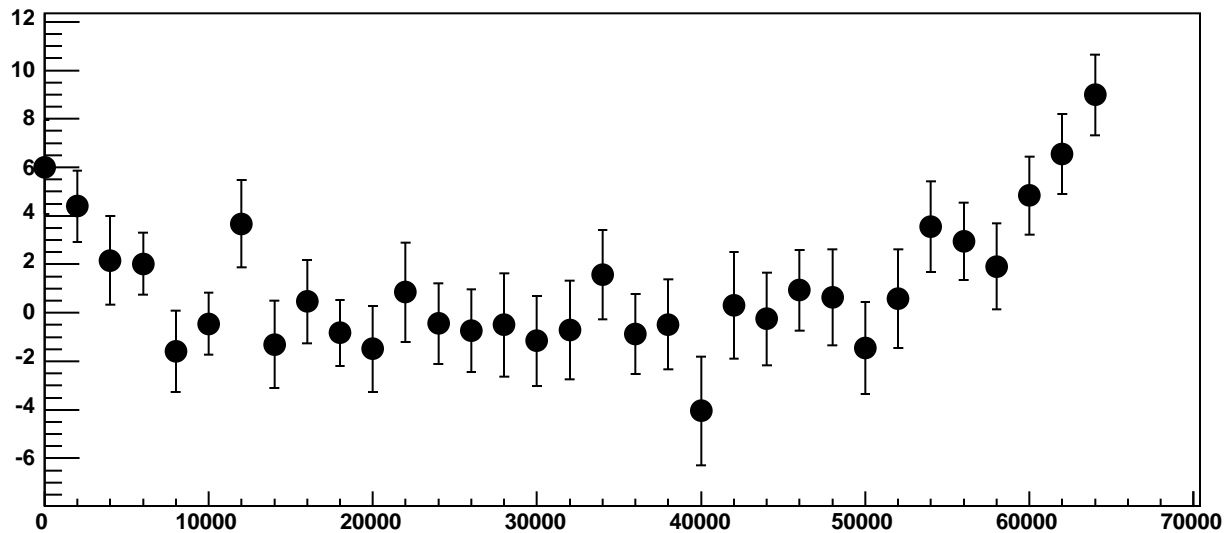


χ^2 / ndf 19.47 / 23
p0 374.4 ± 0.7292
p1 $0.003356 \pm 2.354e-05$

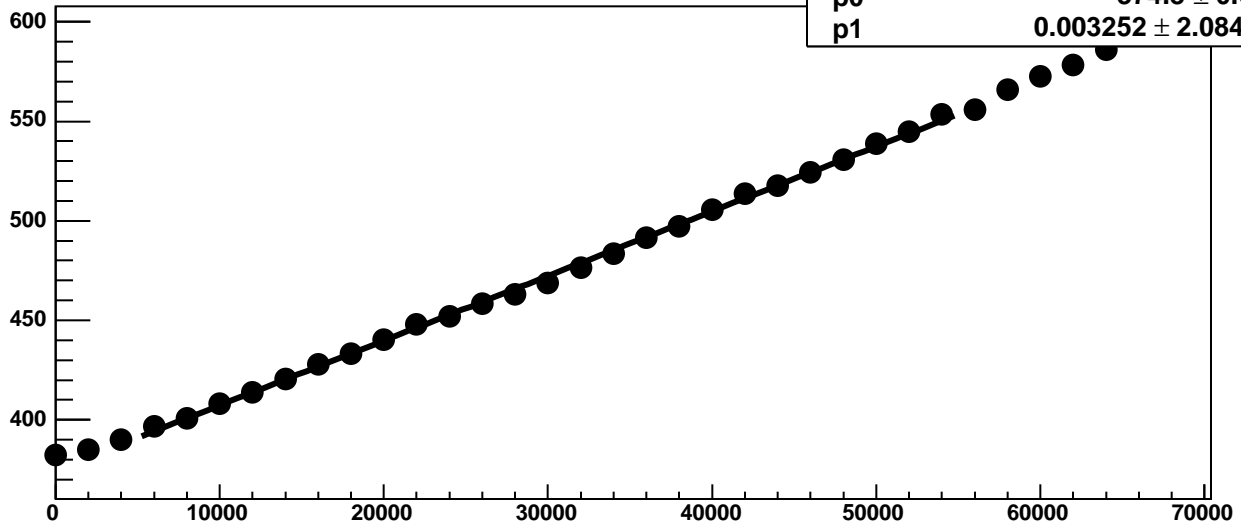
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

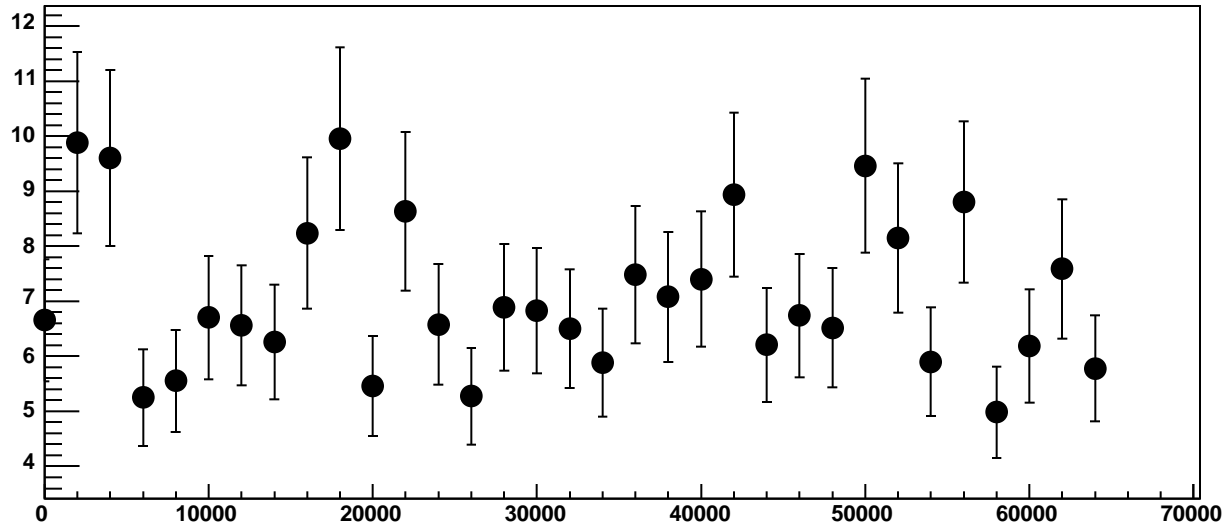


Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

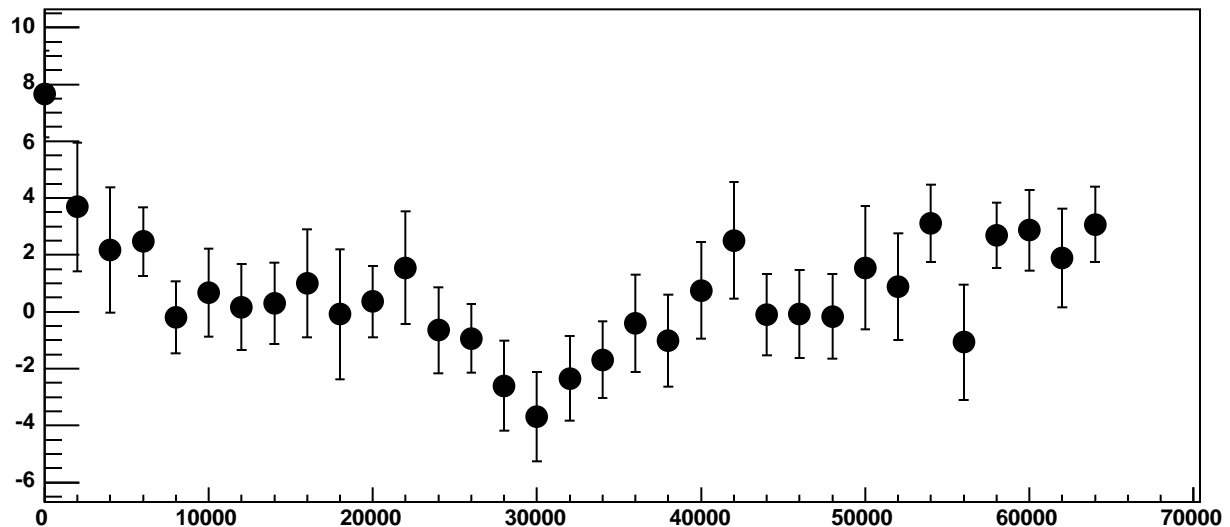


χ^2 / ndf 26.66 / 23
p0 374.8 ± 0.6693
p1 $0.003252 \pm 2.084e-05$

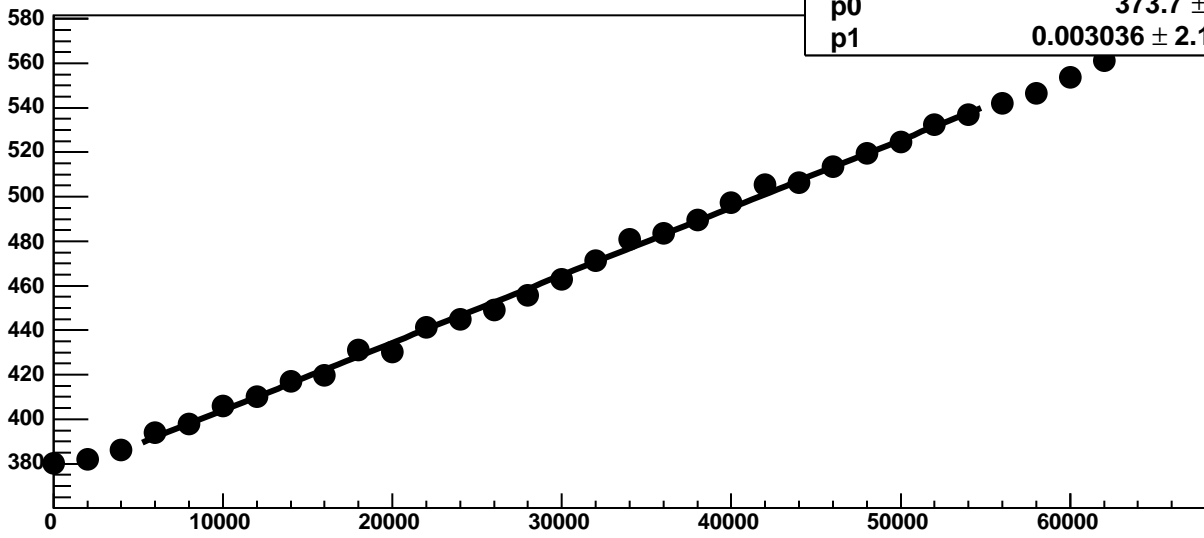
Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

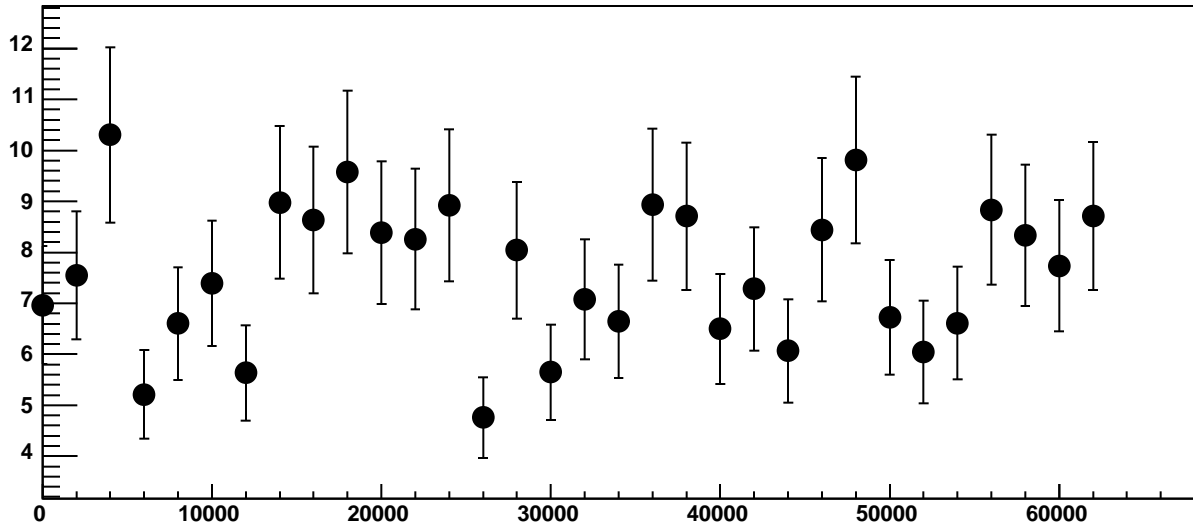


Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

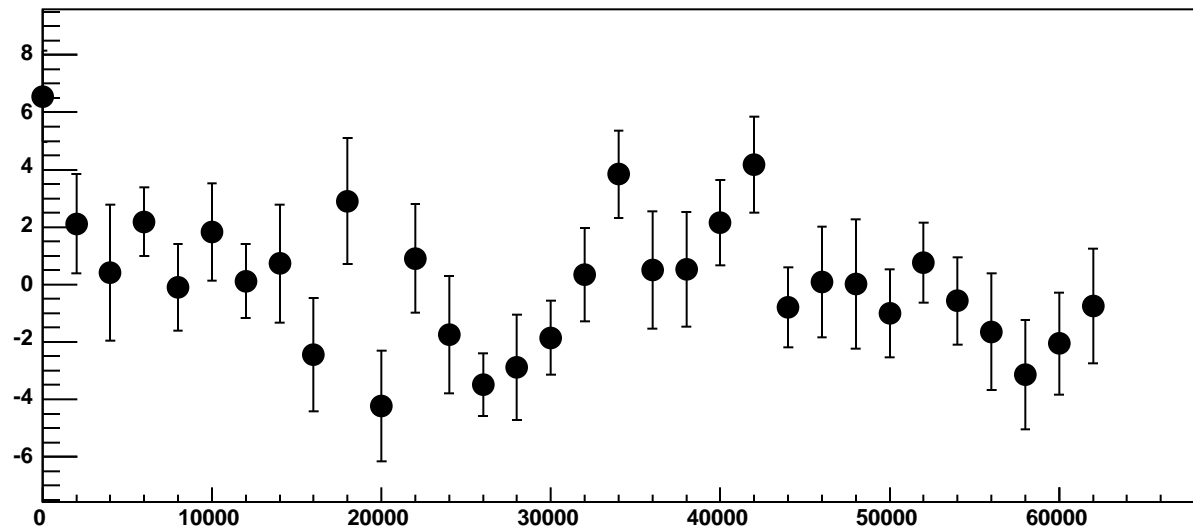


χ^2 / ndf 44.48 / 23
p0 373.7 ± 0.7088
p1 $0.003036 \pm 2.139e-05$

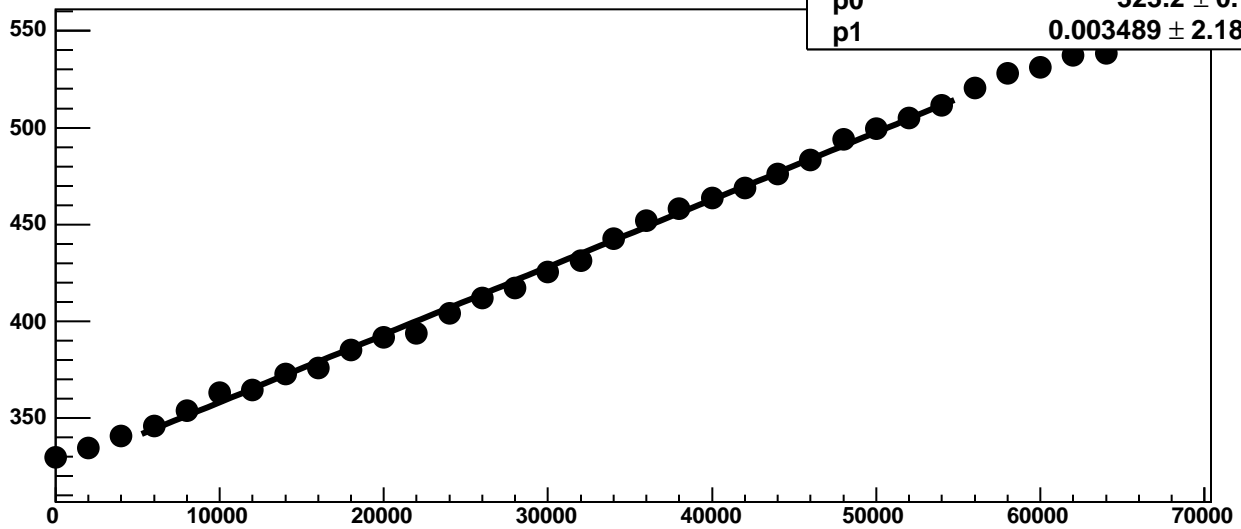
Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

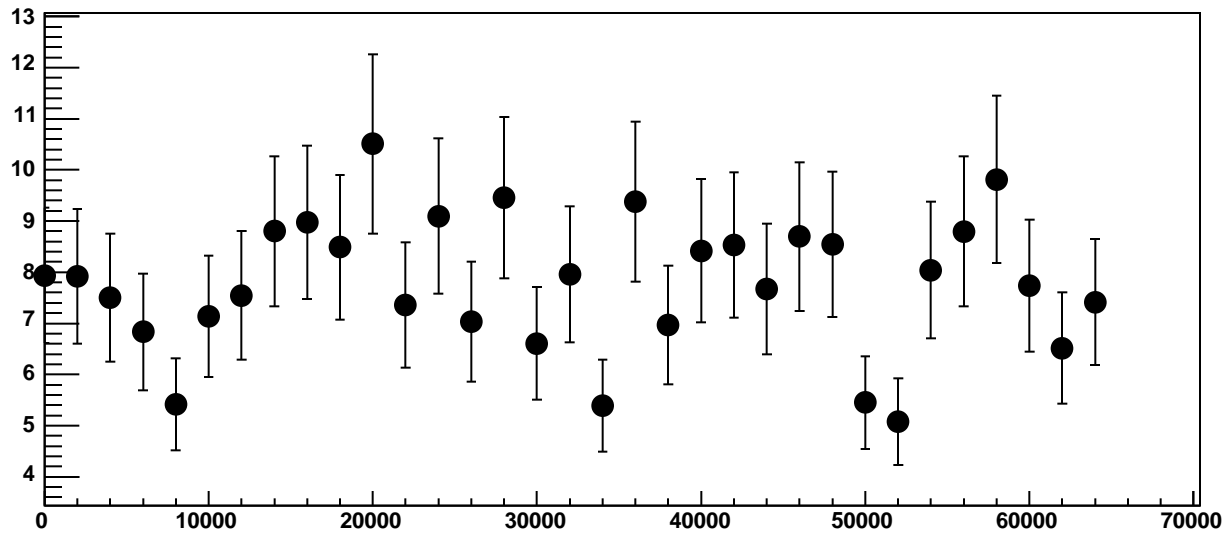


Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

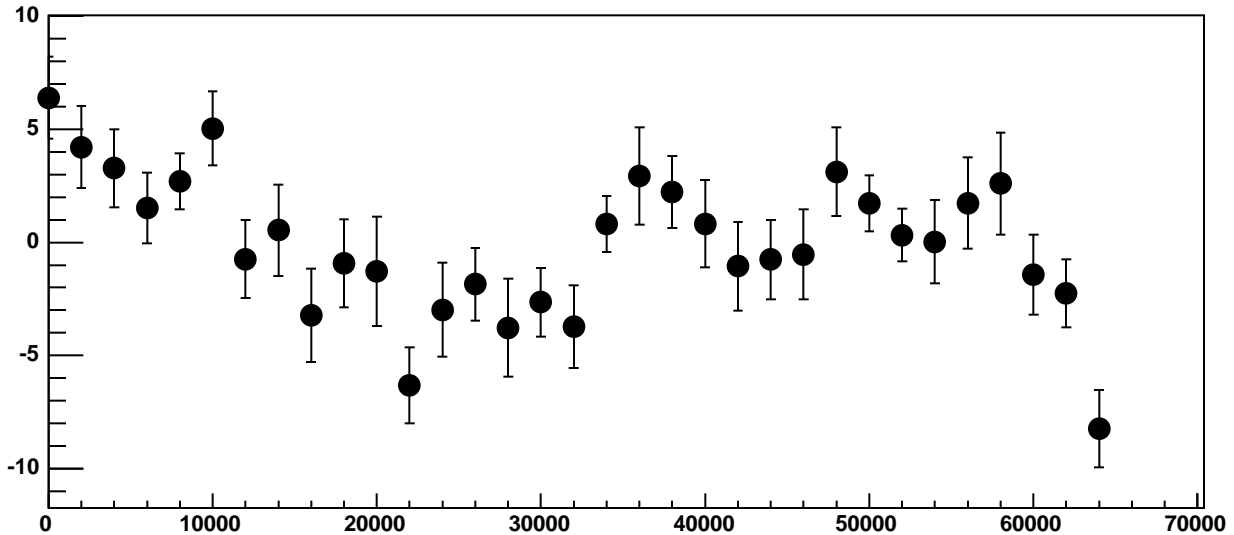


χ^2 / ndf 55.38 / 23
p0 323.2 ± 0.7504
p1 $0.003489 \pm 2.18e-05$

Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

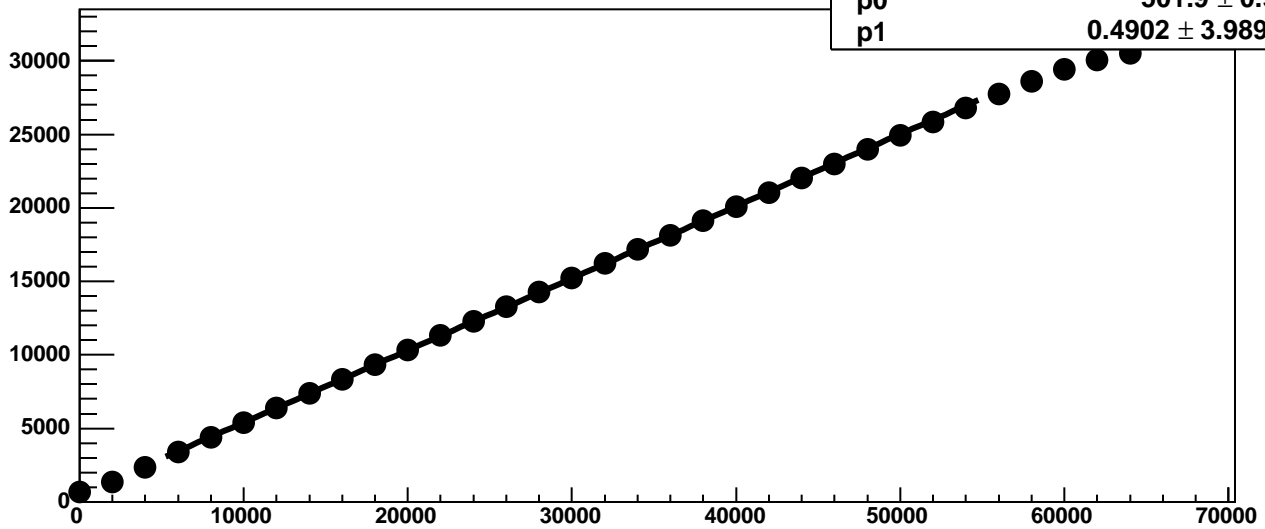


Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

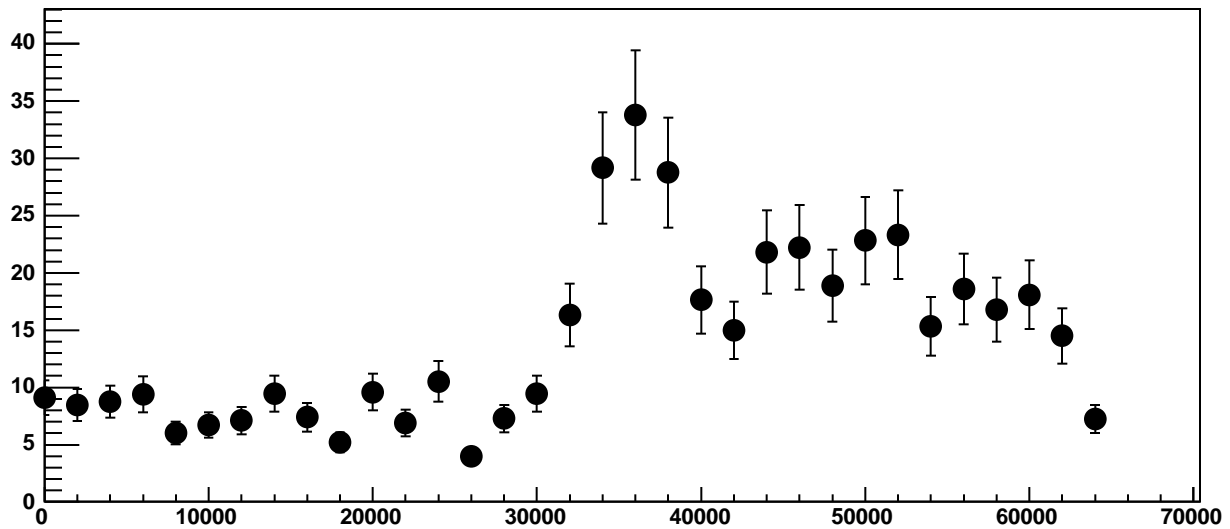


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

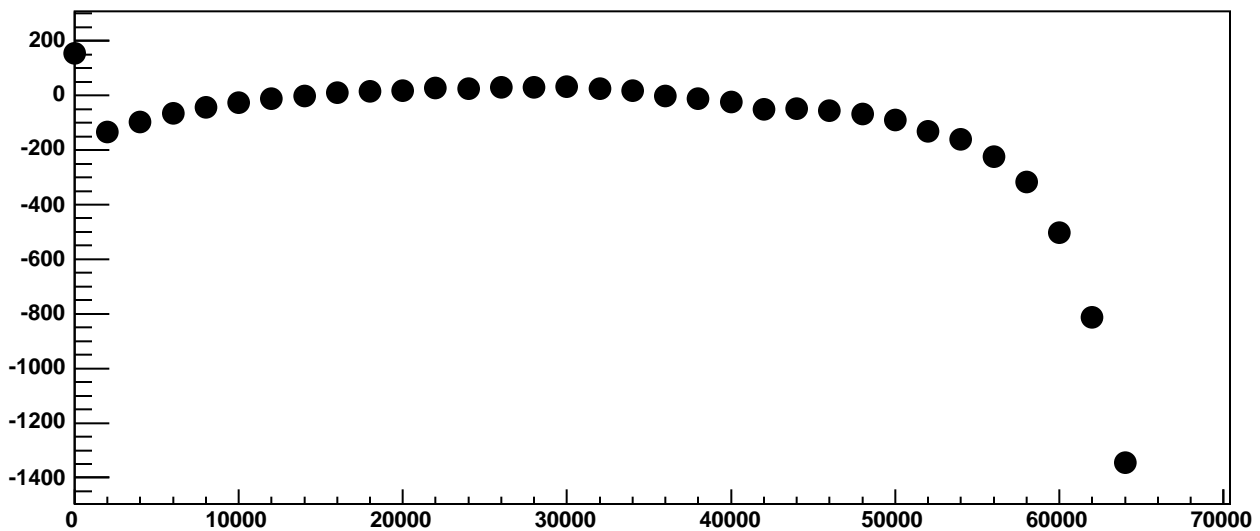
χ^2 / ndf 8260 / 23
p0 501.9 ± 0.9366
p1 $0.4902 \pm 3.989e-05$



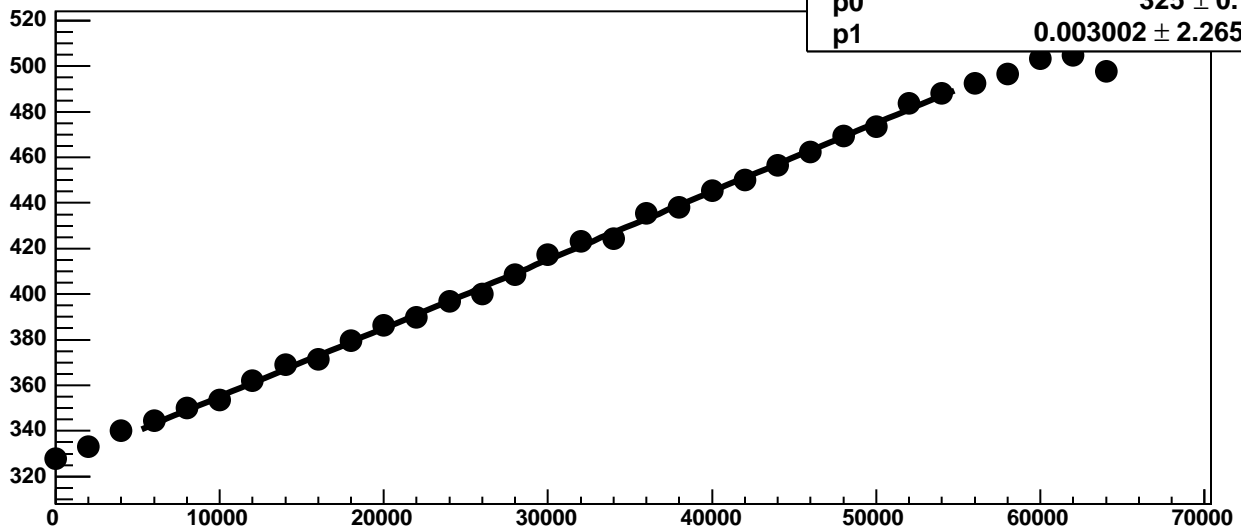
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

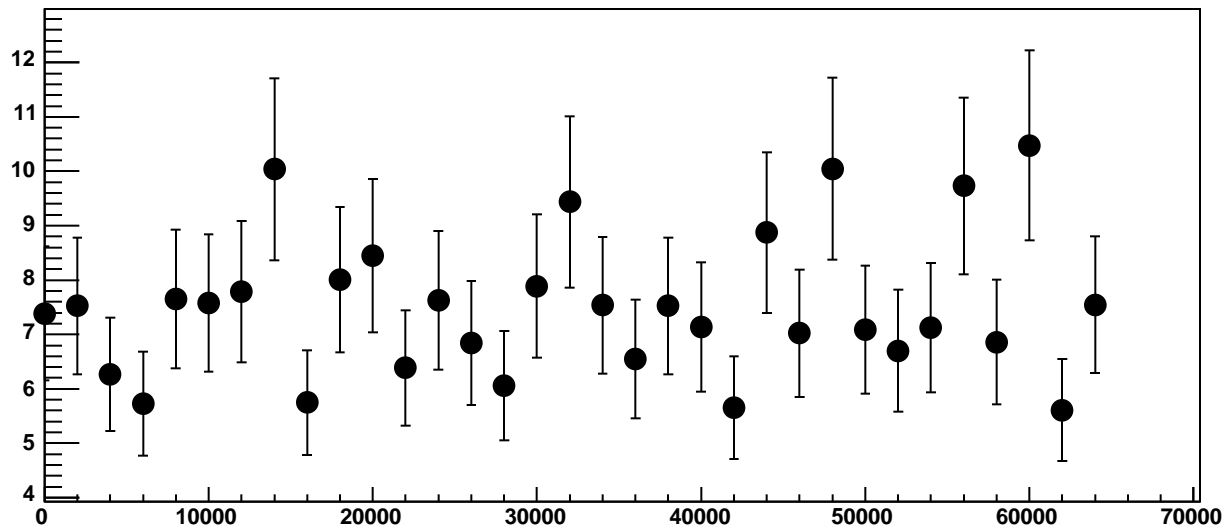


Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

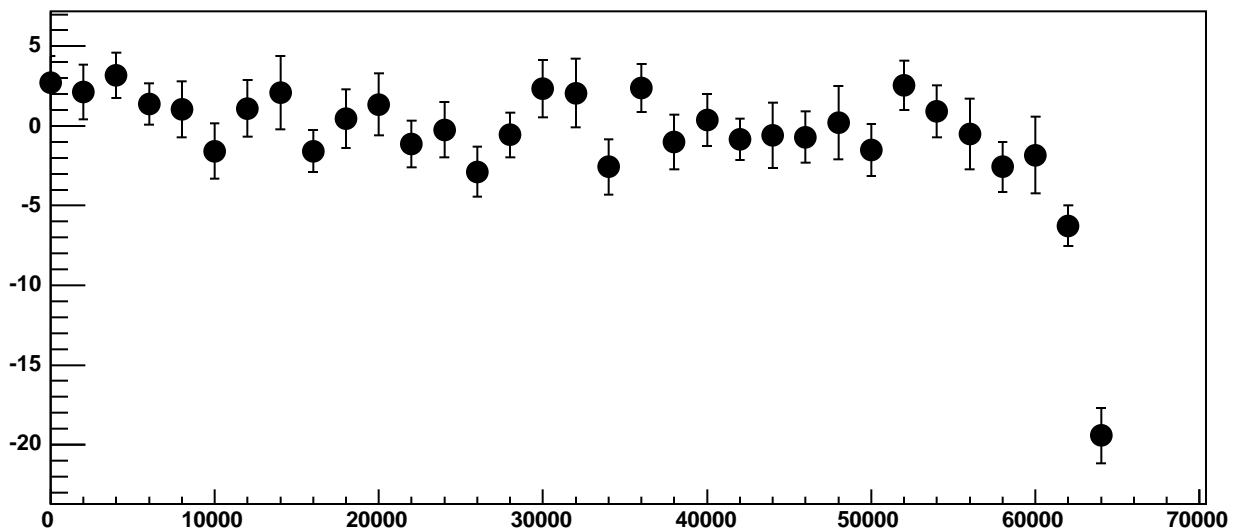


χ^2 / ndf 21.9 / 23
p0 325 ± 0.7507
p1 $0.003002 \pm 2.265e-05$

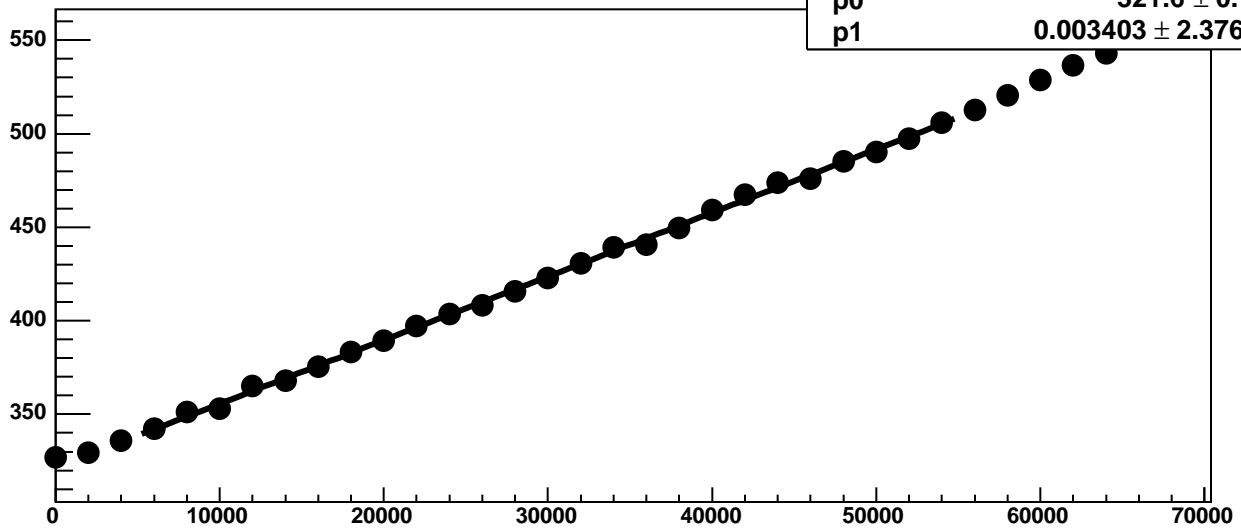
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

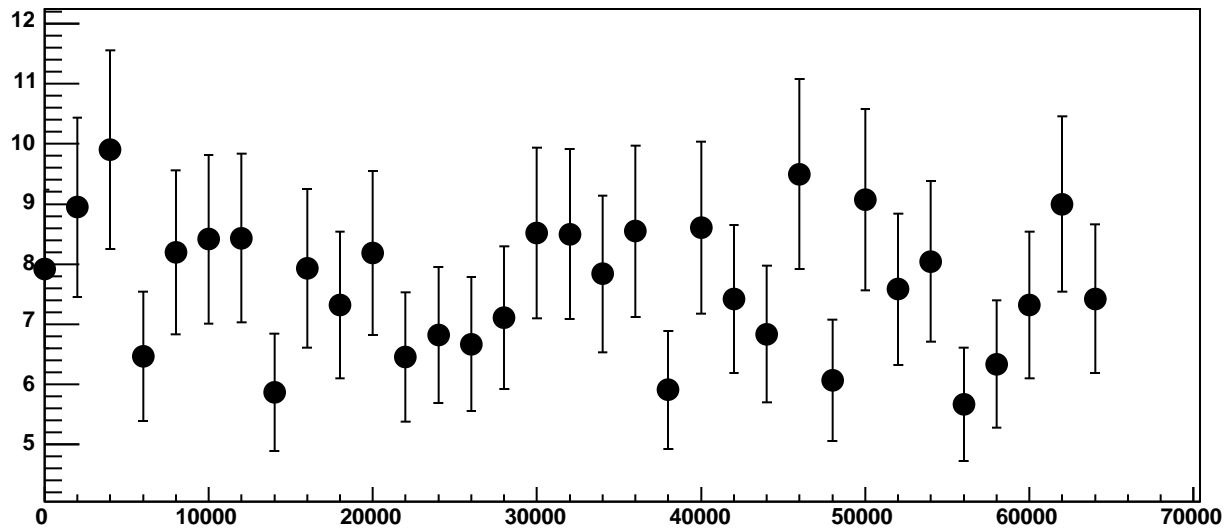


Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

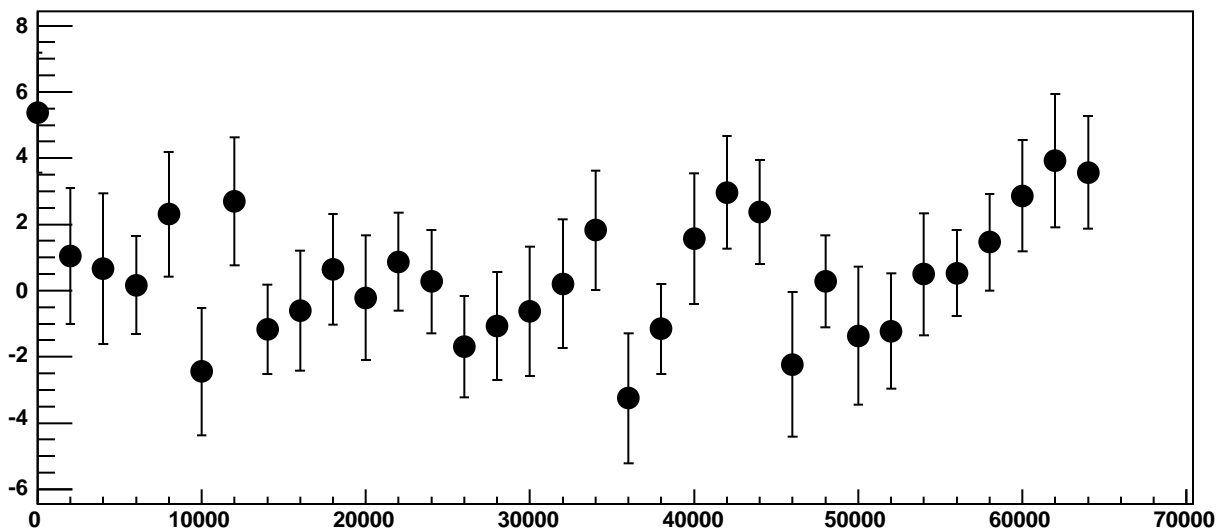


χ^2 / ndf 20.76 / 23
p0 321.6 ± 0.7789
p1 $0.003403 \pm 2.376e-05$

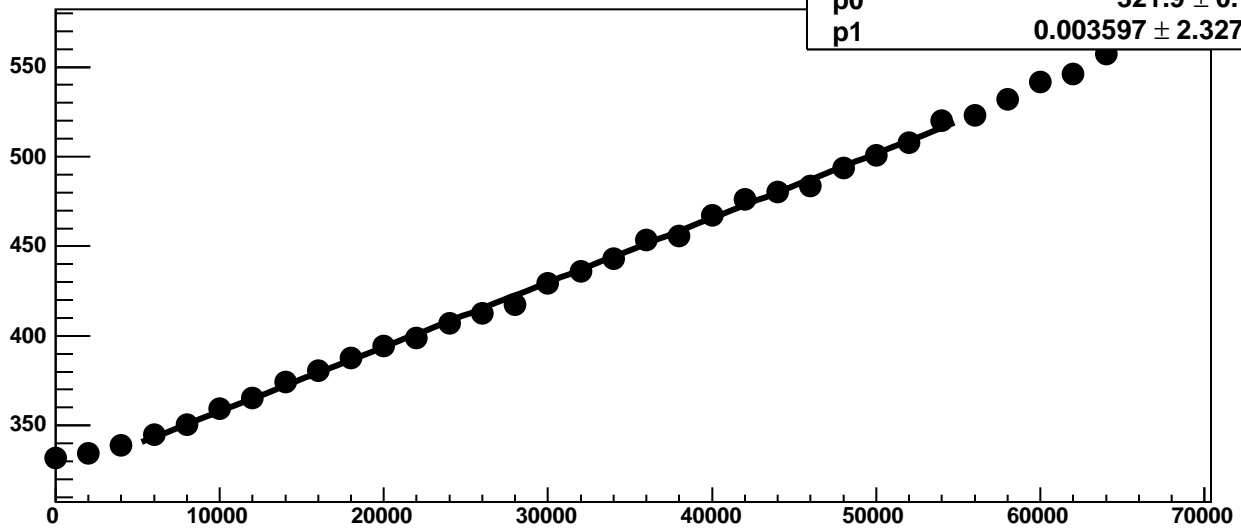
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



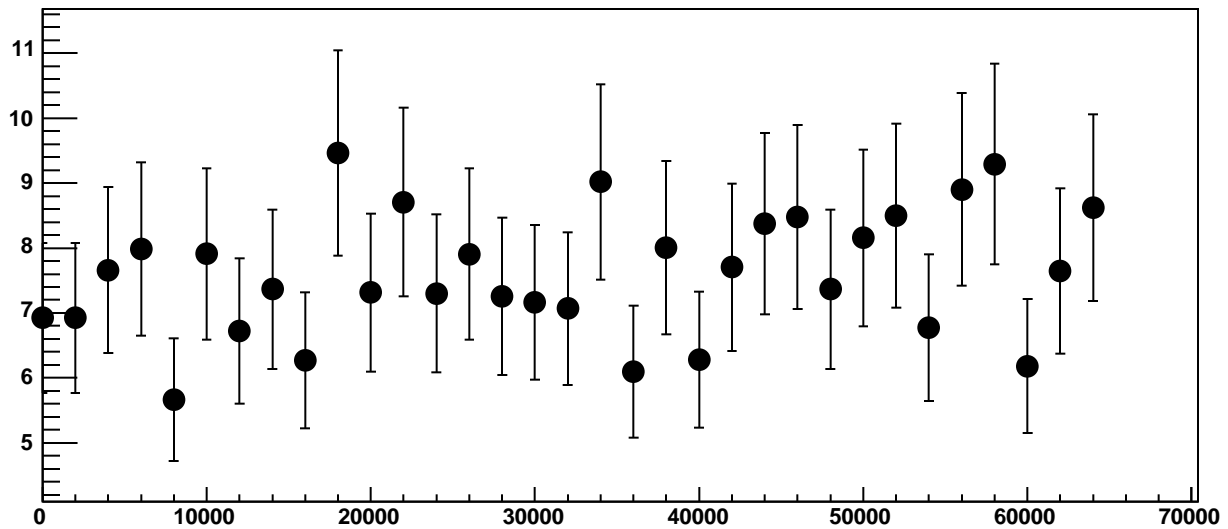
Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



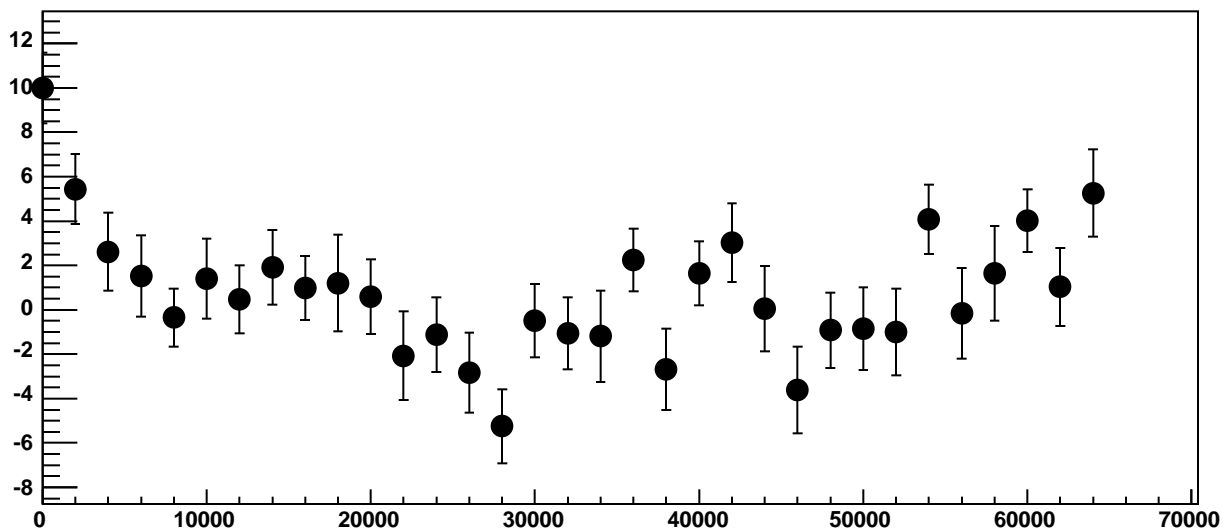
Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



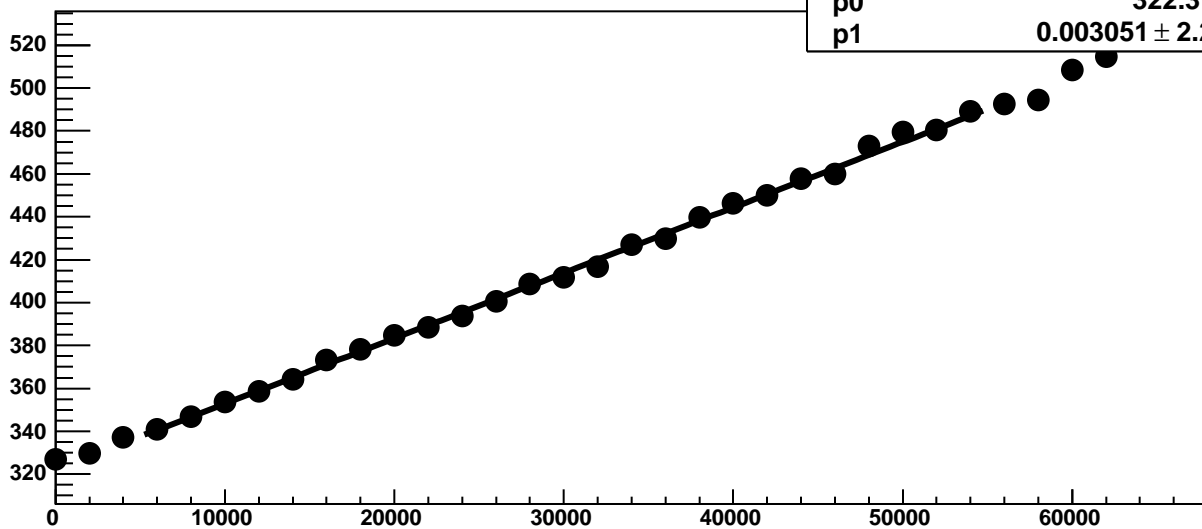
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

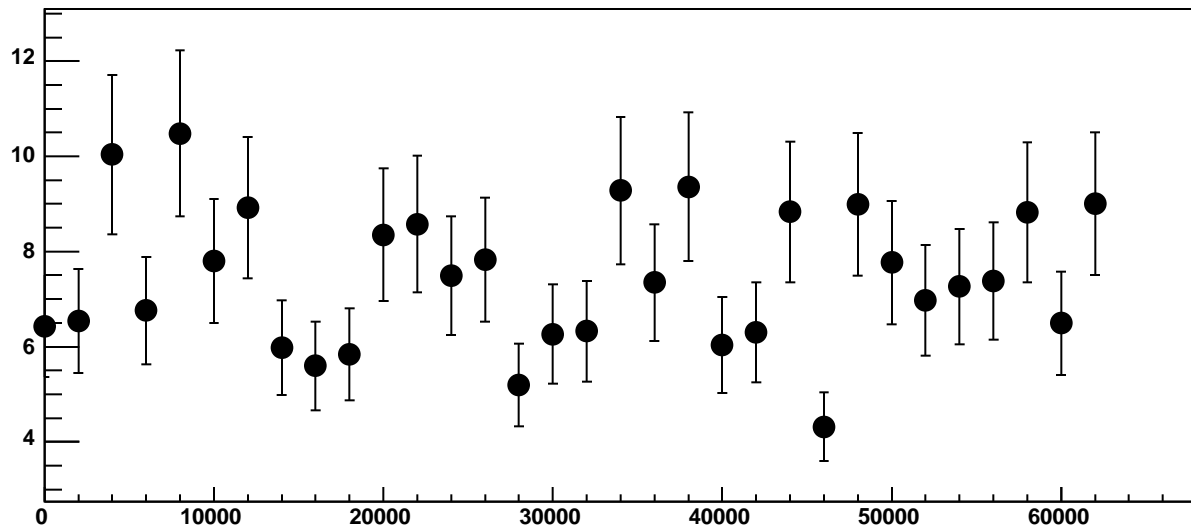


Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

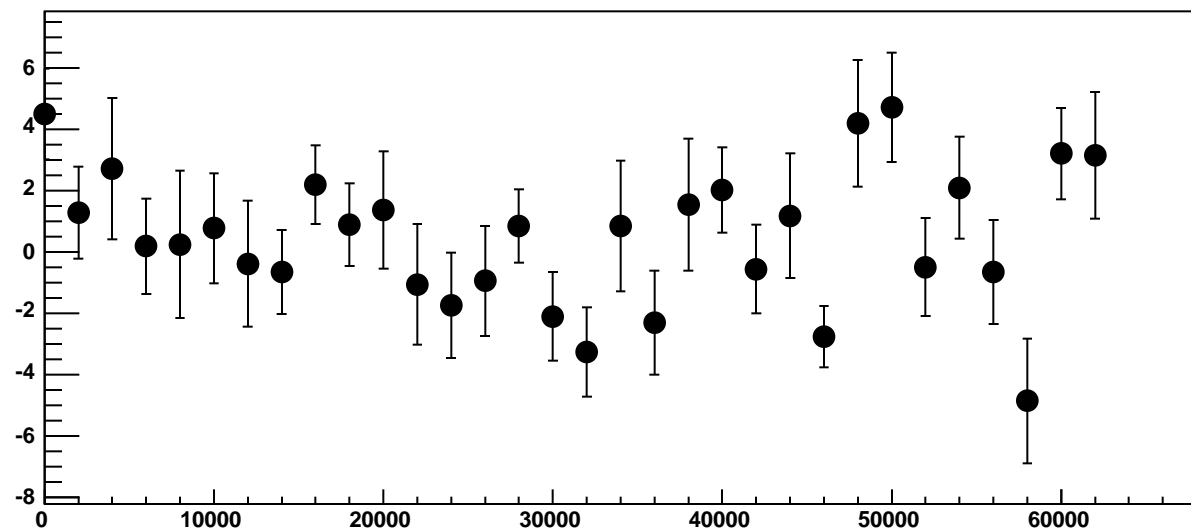


χ^2 / ndf 39.31 / 23
p0 322.3 ± 0.758
p1 $0.003051 \pm 2.248e-05$

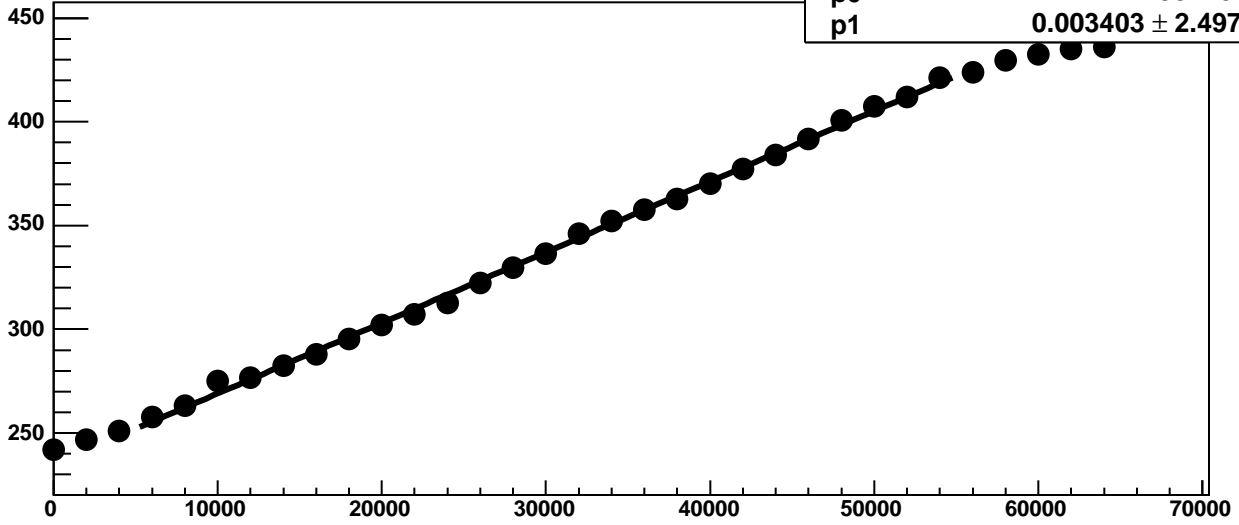
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

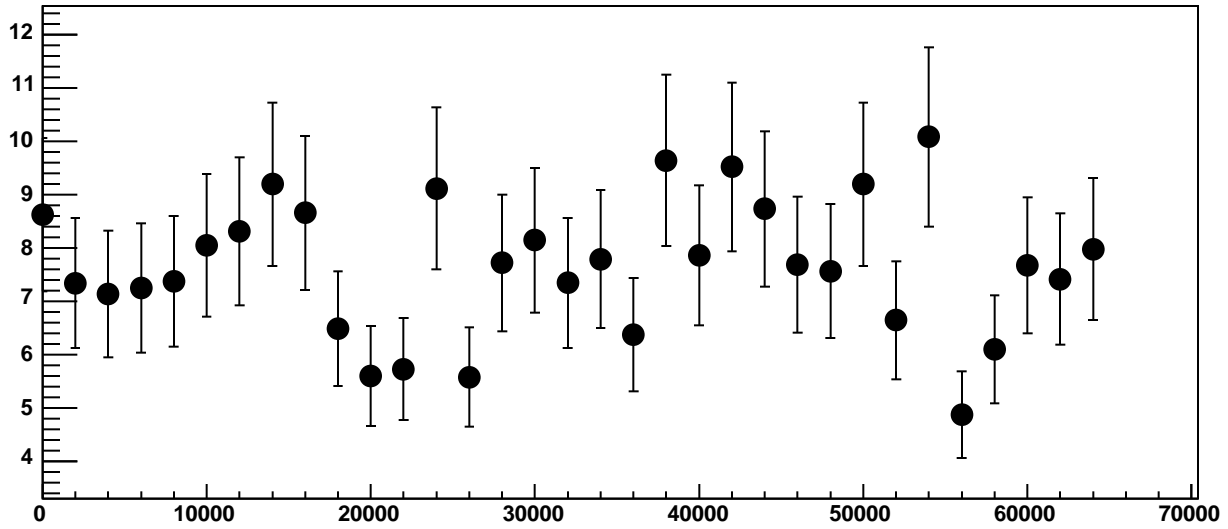


Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

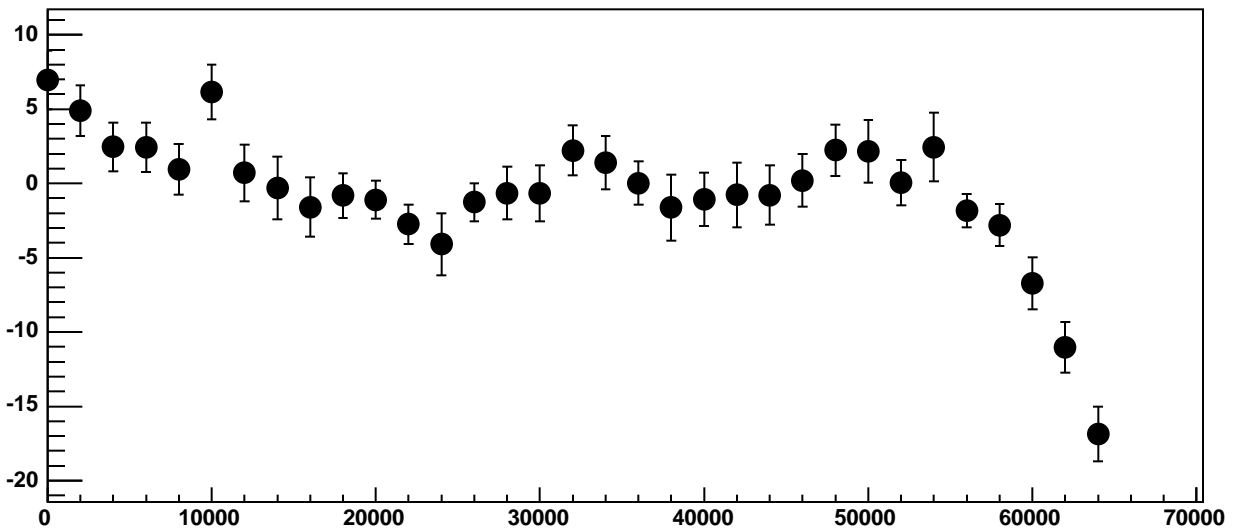


χ^2 / ndf 32.16 / 23
p0 235 ± 0.7981
p1 $0.003403 \pm 2.497e-05$

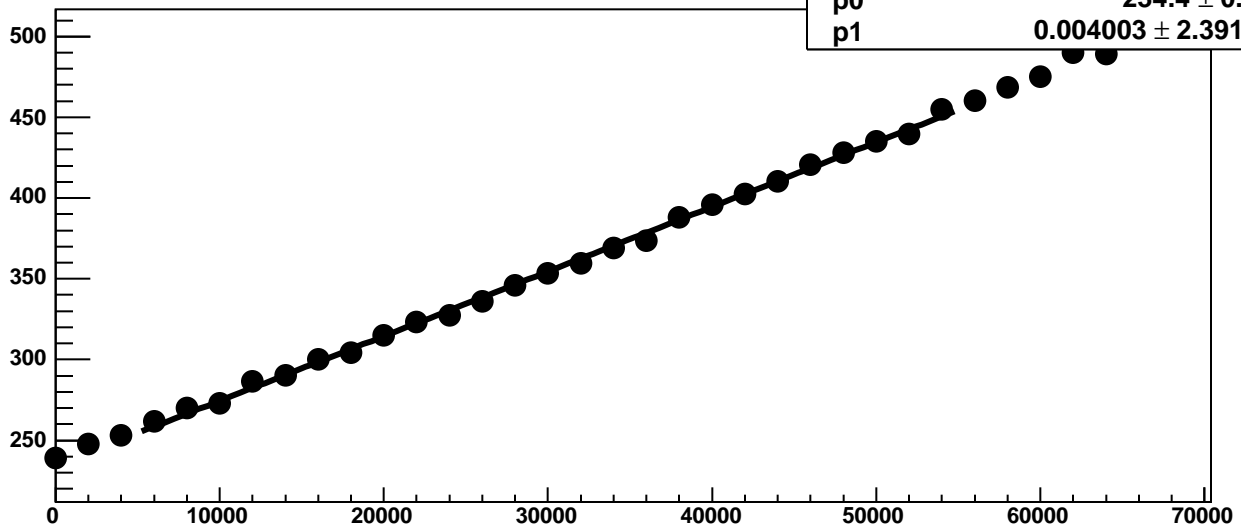
Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

43.26 / 23

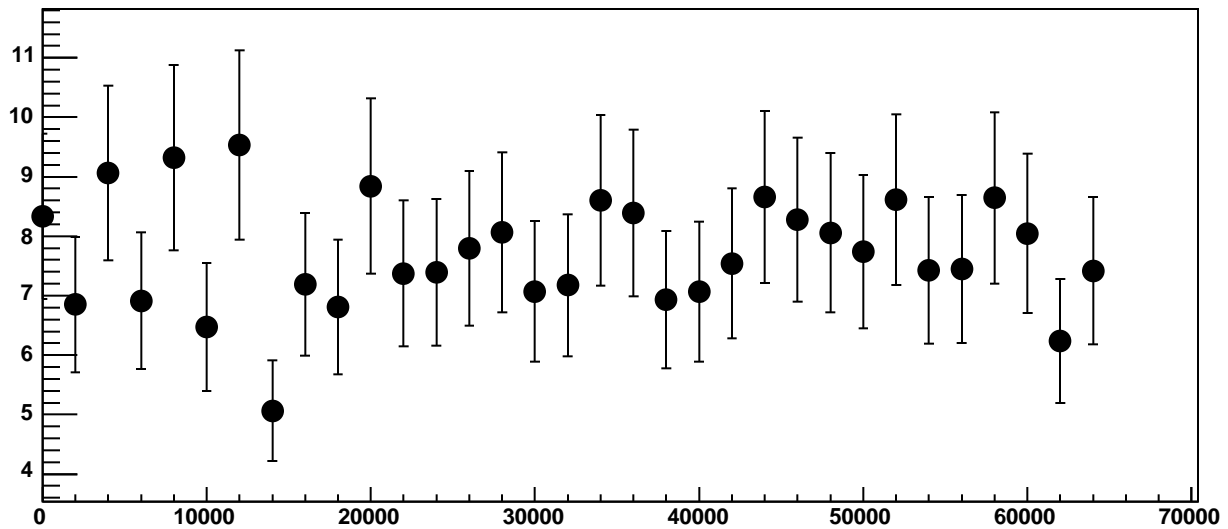
p0

234.4 ± 0.7691

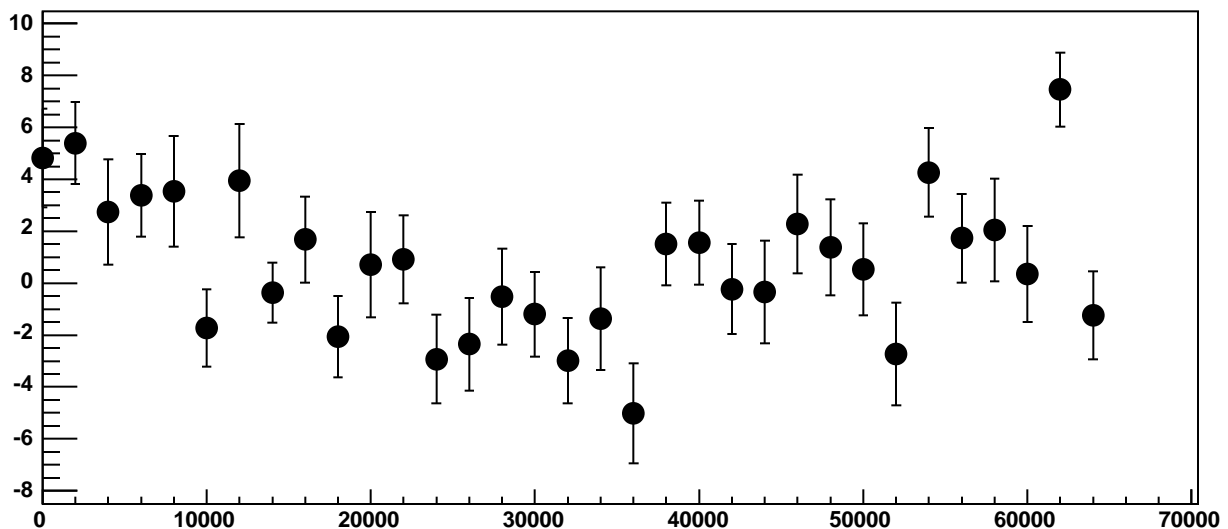
p1

$0.004003 \pm 2.391e-05$

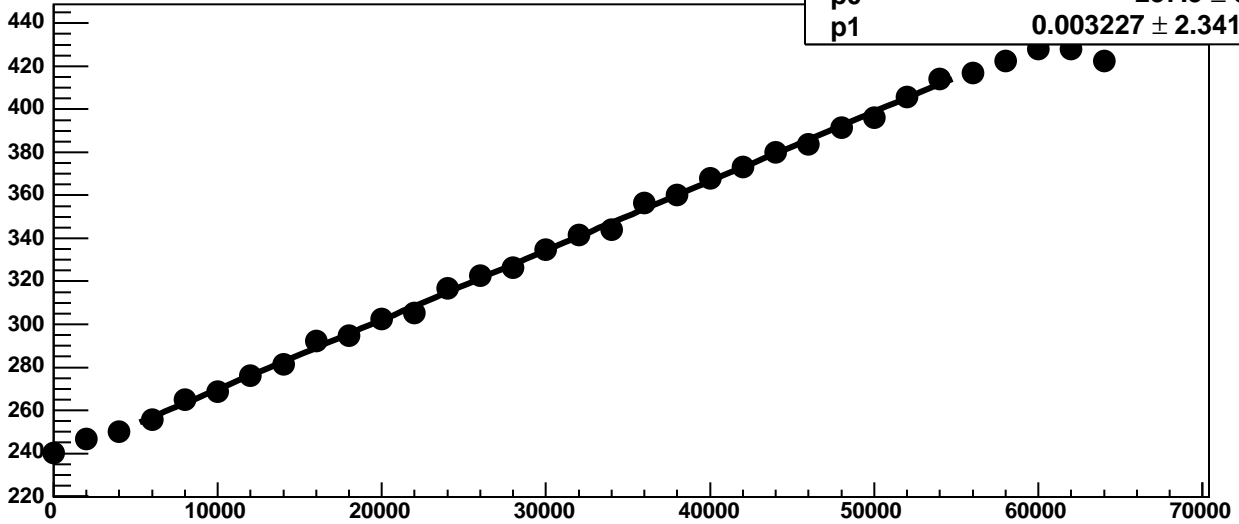
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

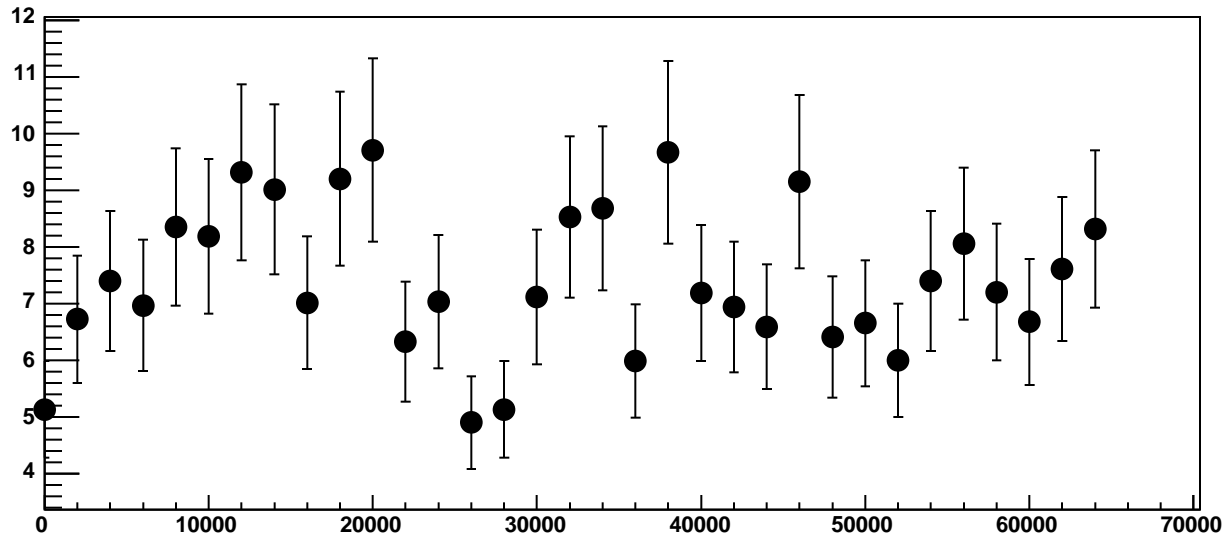


Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

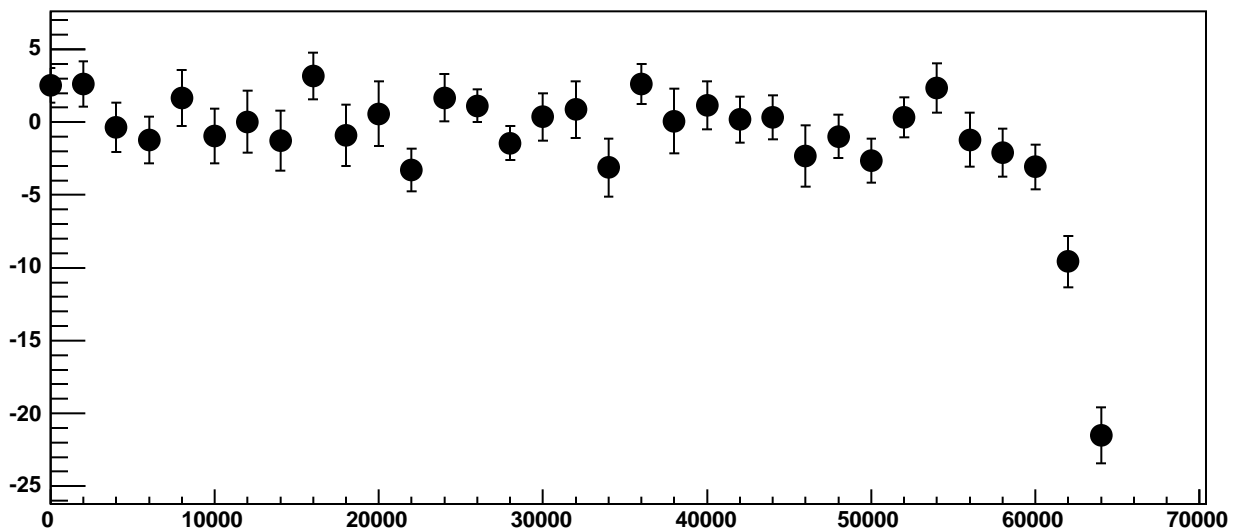


χ^2 / ndf 28.38 / 23
p0 237.5 ± 0.801
p1 $0.003227 \pm 2.341\text{e-}05$

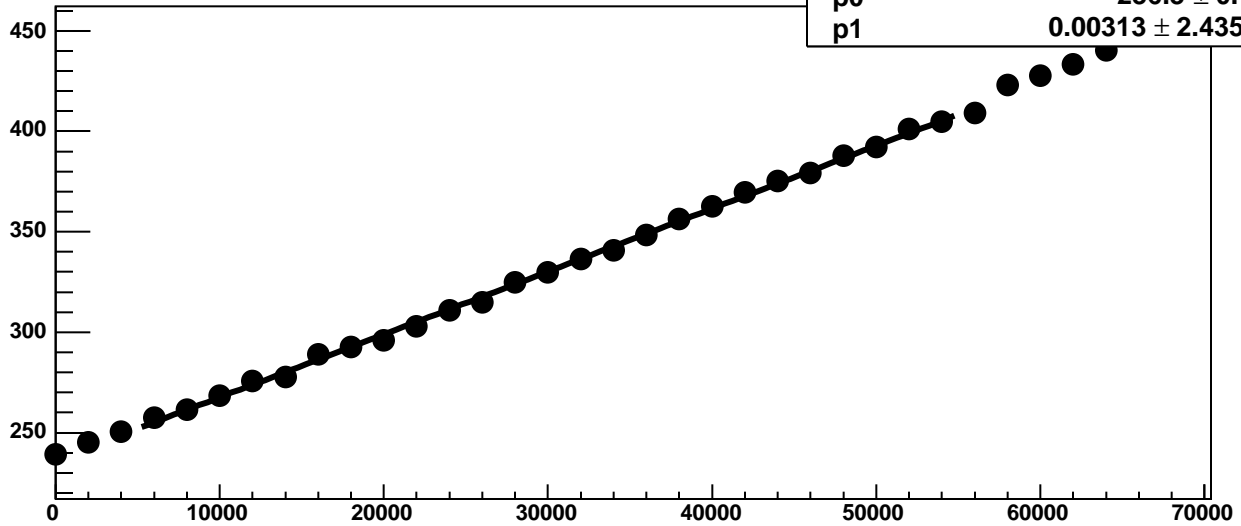
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

24.84 / 23

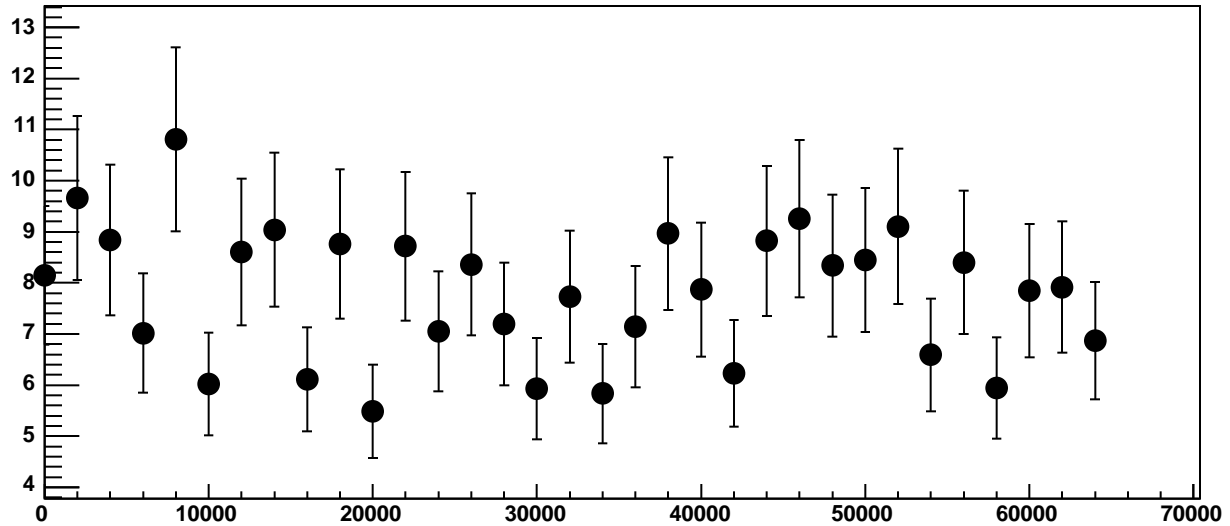
p0

236.3 ± 0.7914

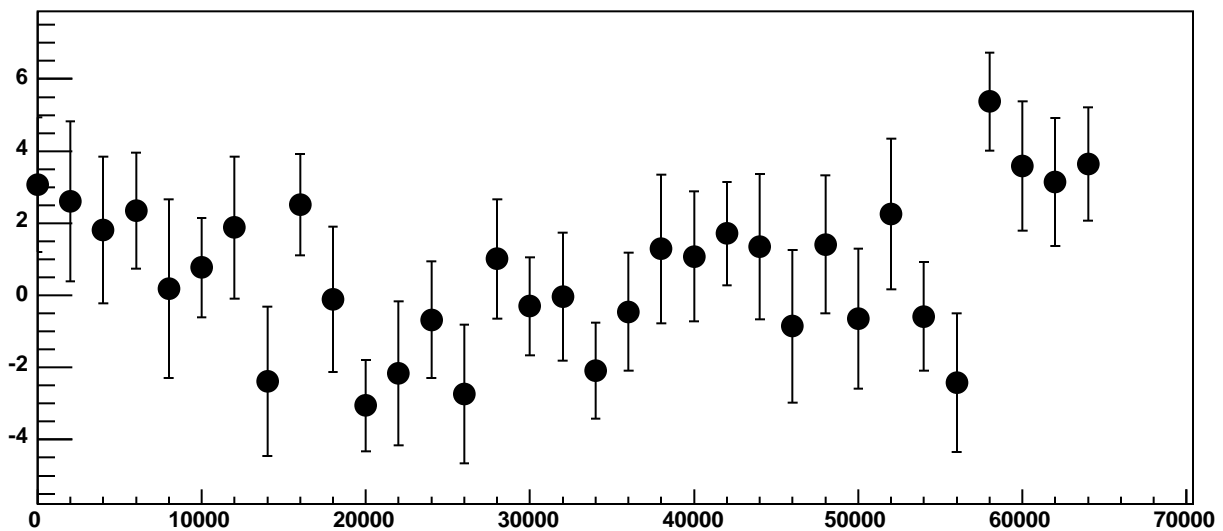
p1

$0.00313 \pm 2.435e-05$

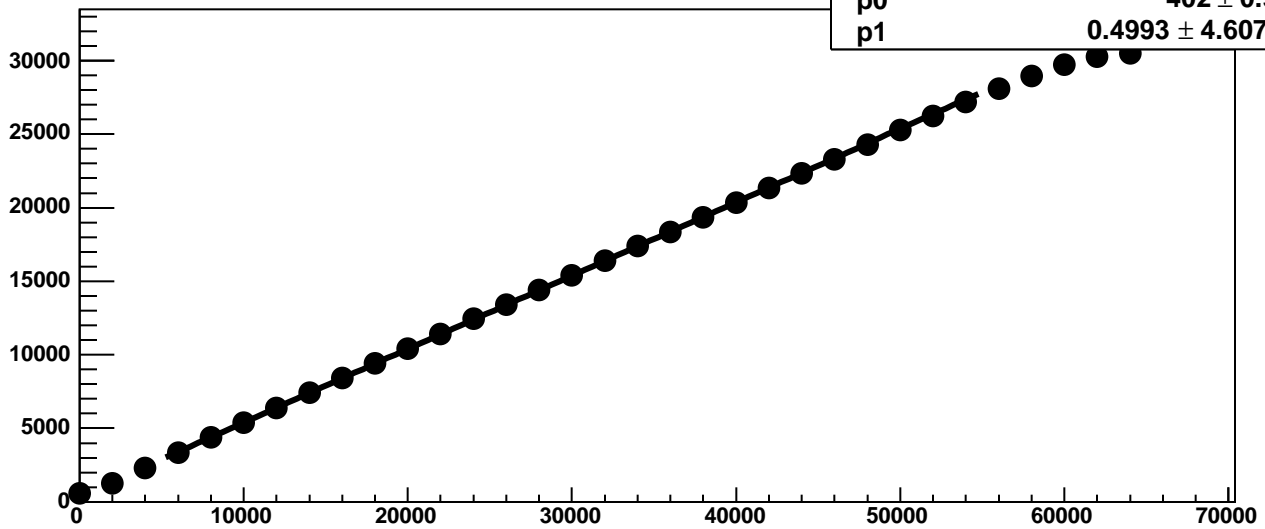
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



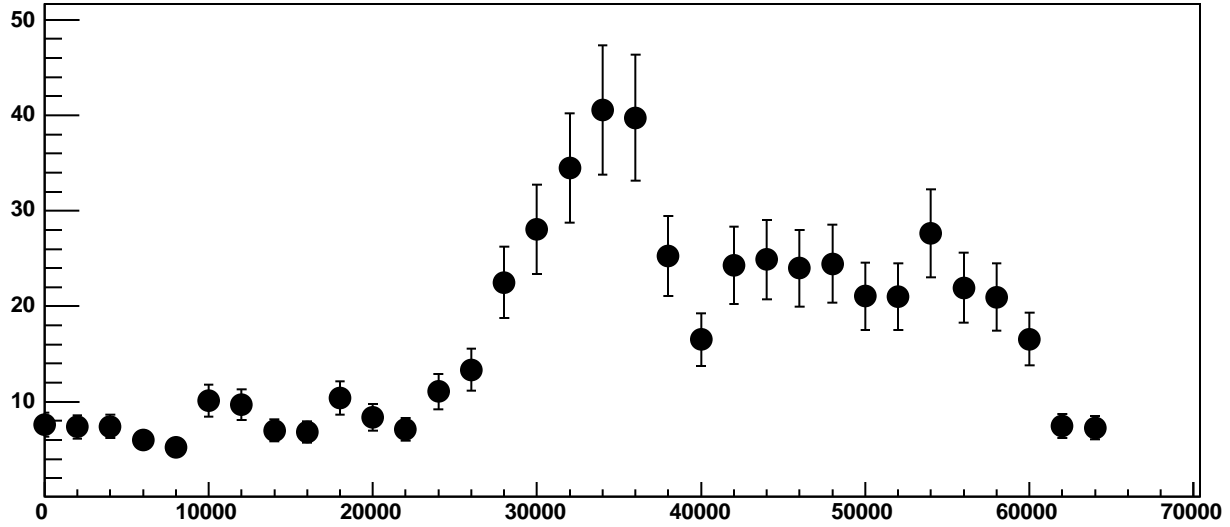
Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



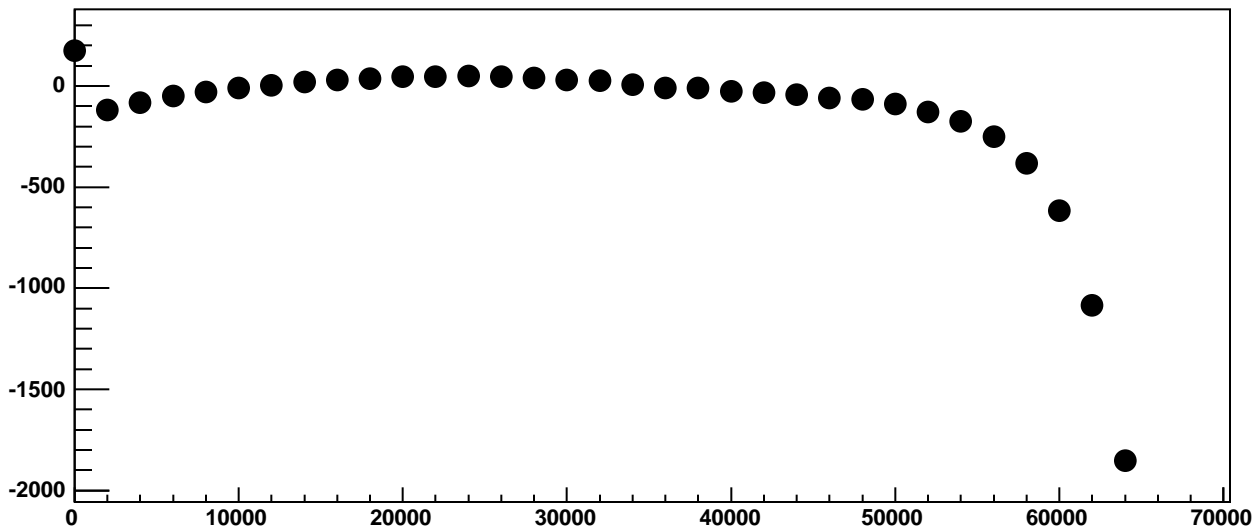
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



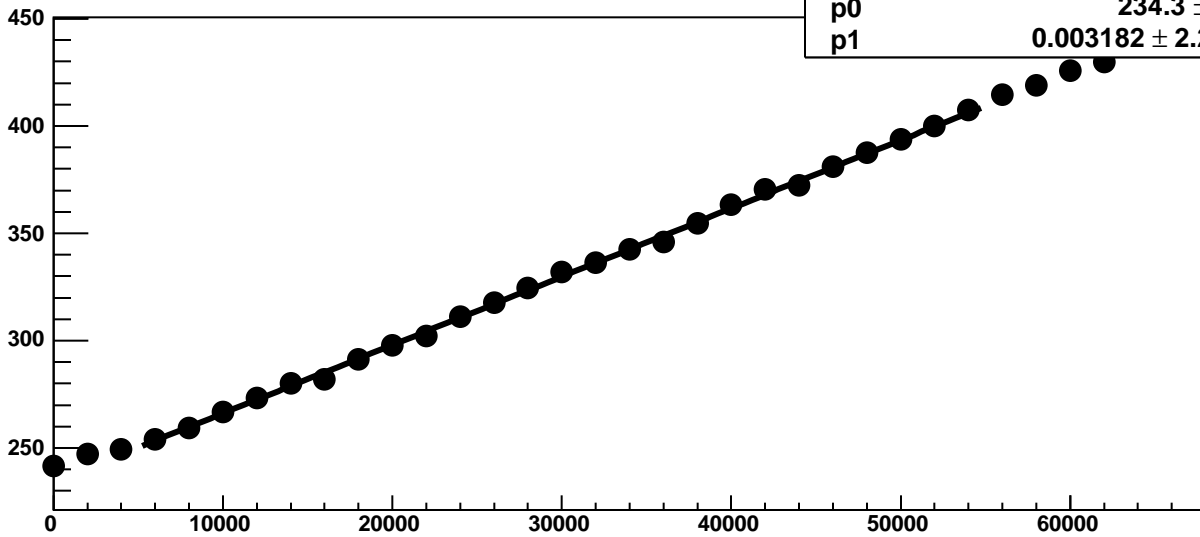
Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

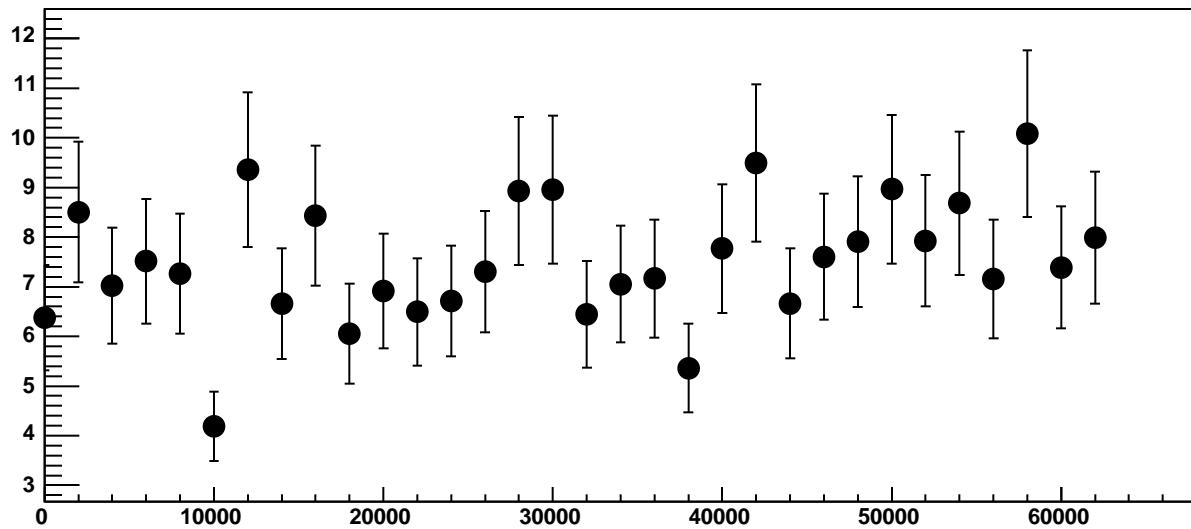


Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

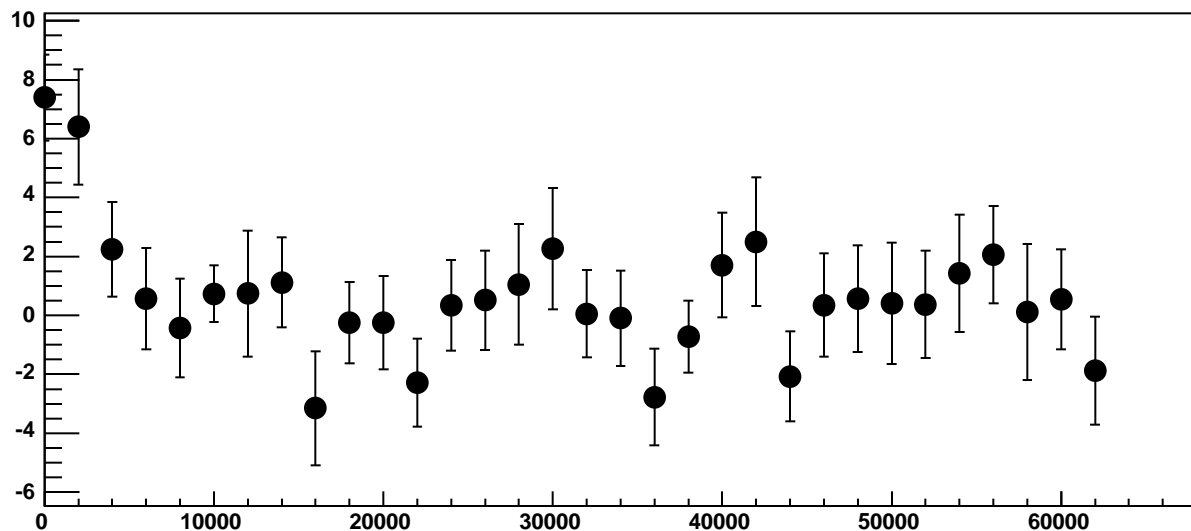


χ^2 / ndf 16.09 / 23
p0 234.3 ± 0.7112
p1 $0.003182 \pm 2.275e-05$

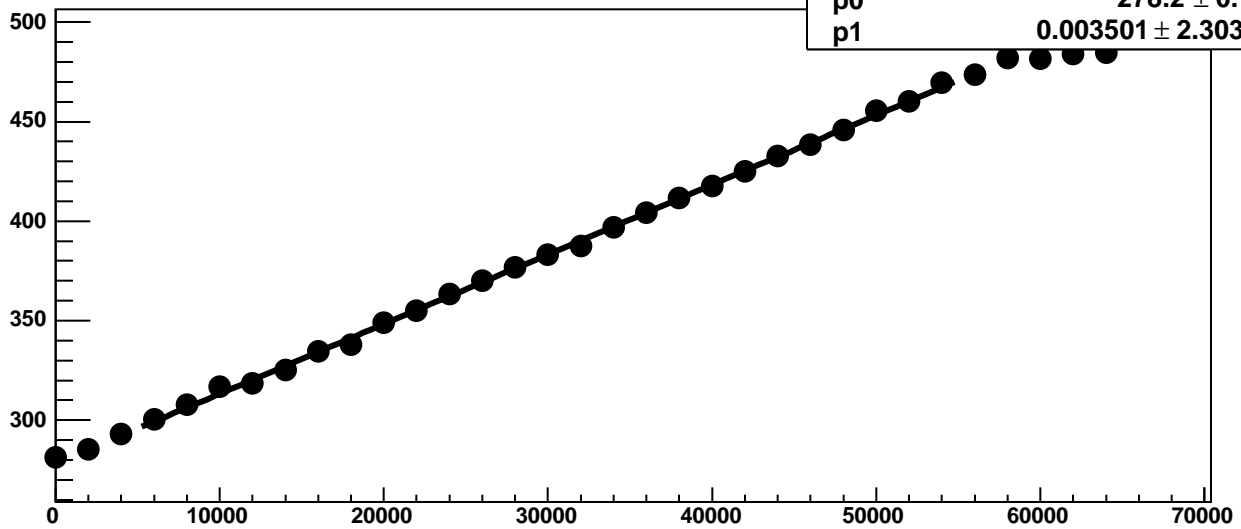
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

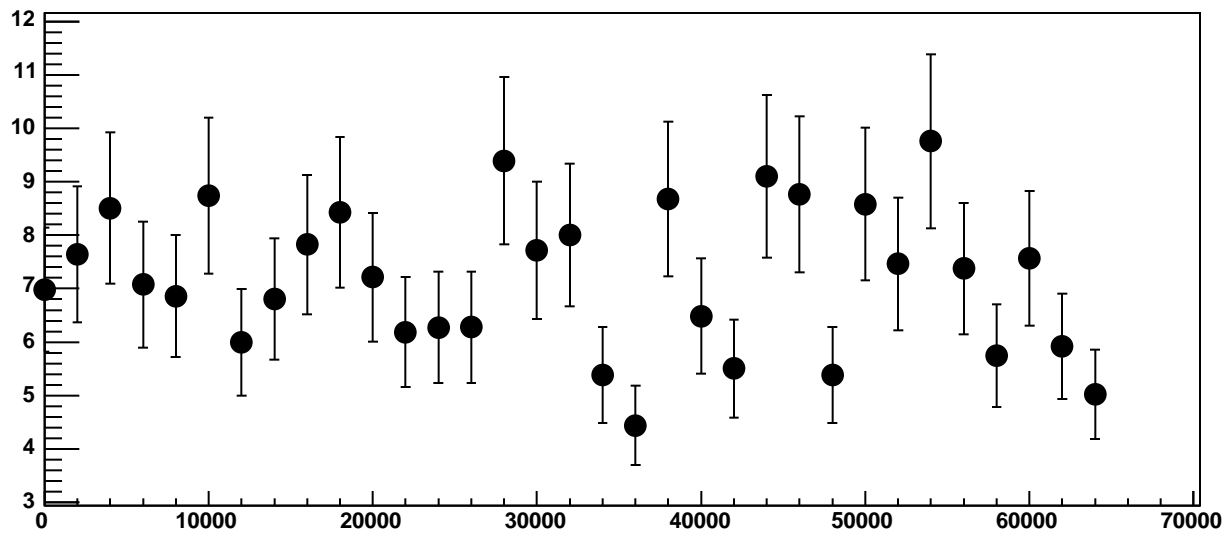


Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

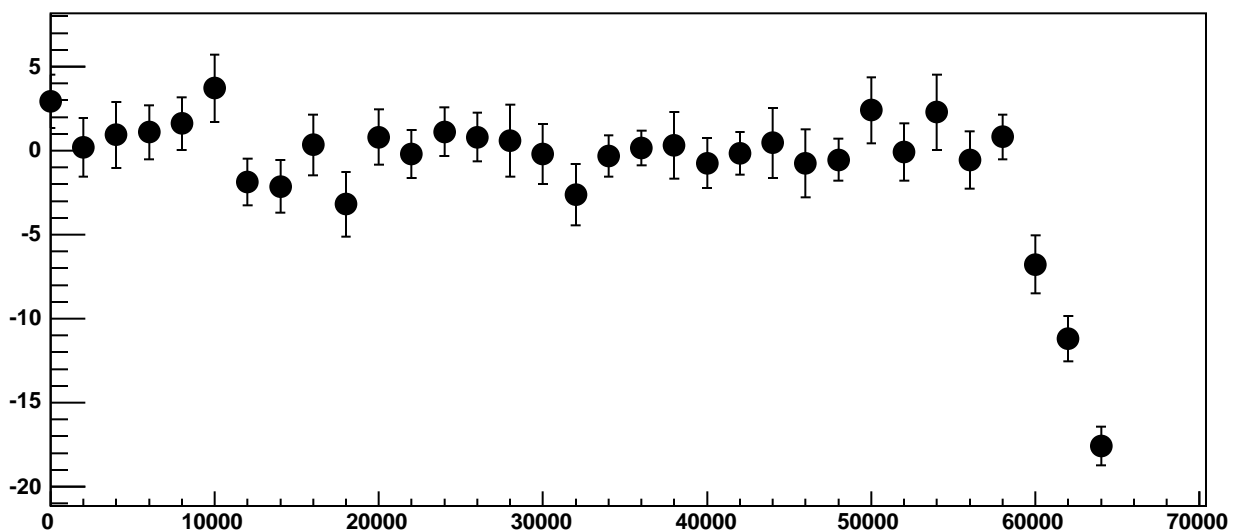


χ^2 / ndf 18 / 23
p0 278.2 ± 0.7593
p1 $0.003501 \pm 2.303e-05$

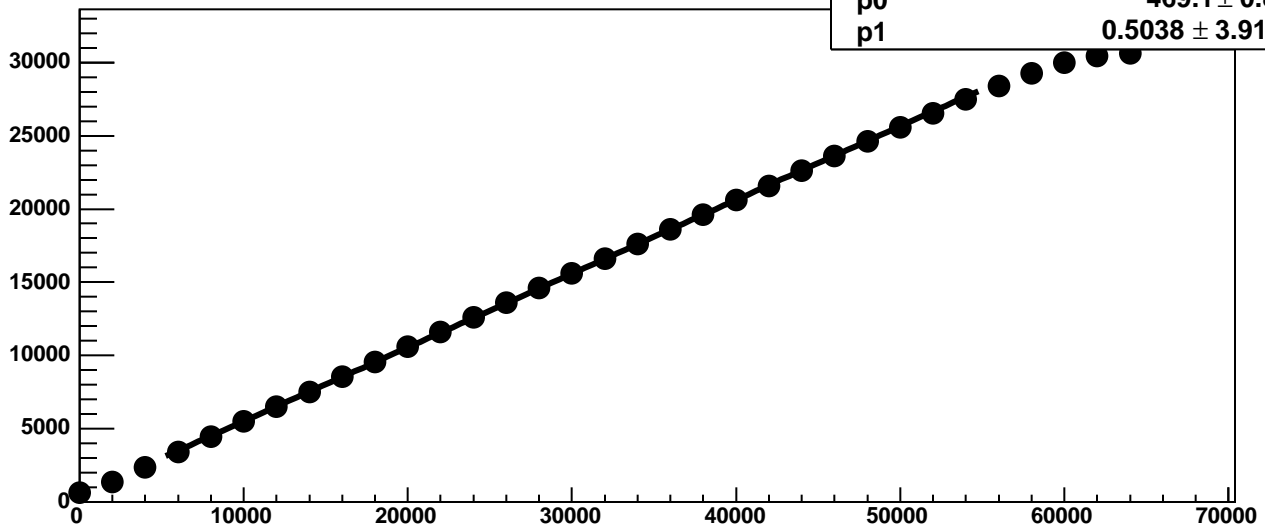
Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



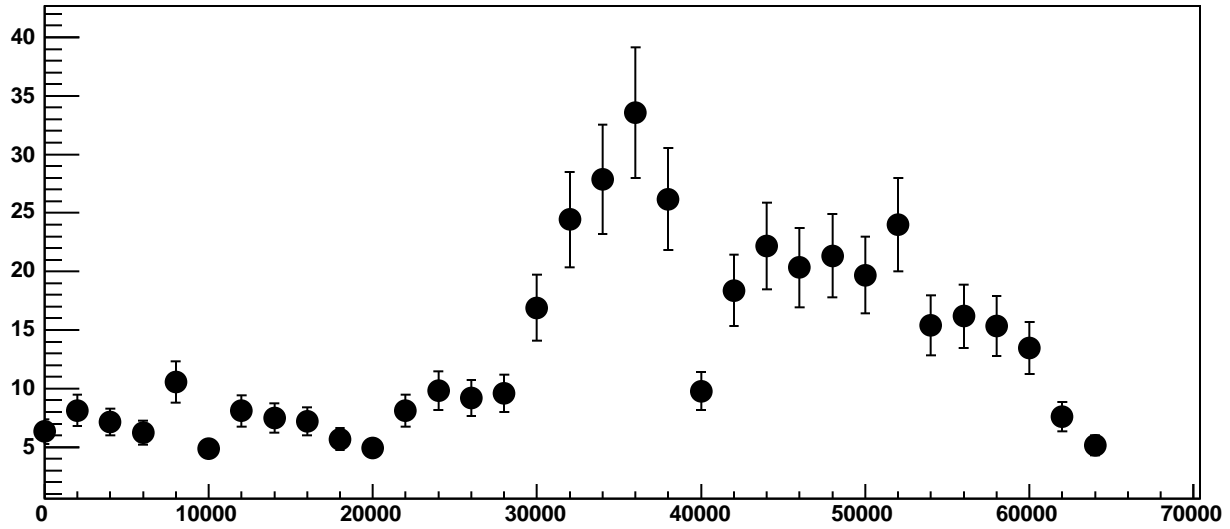
Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



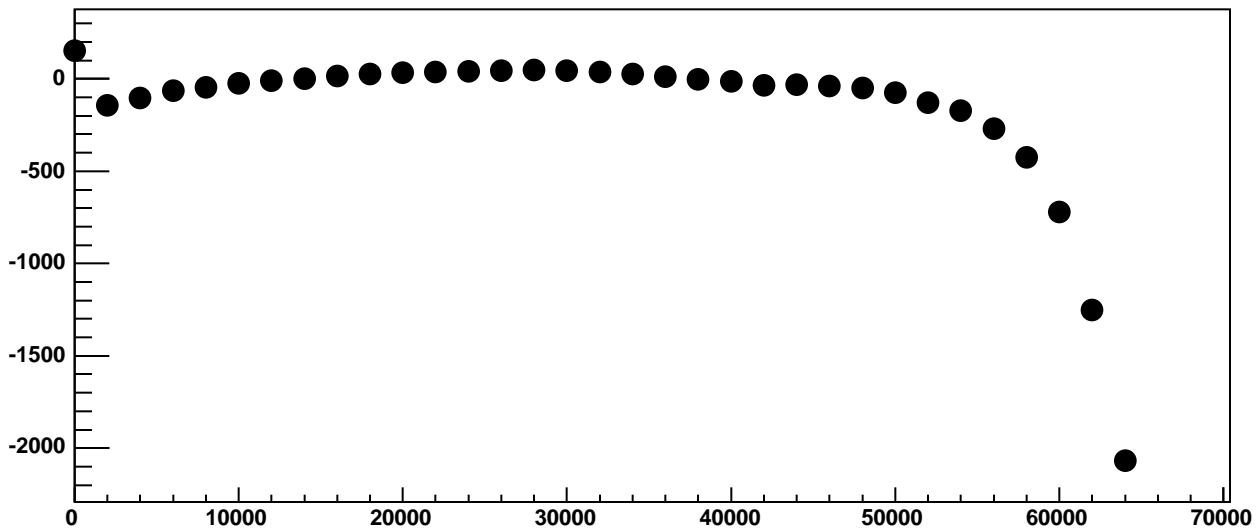
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



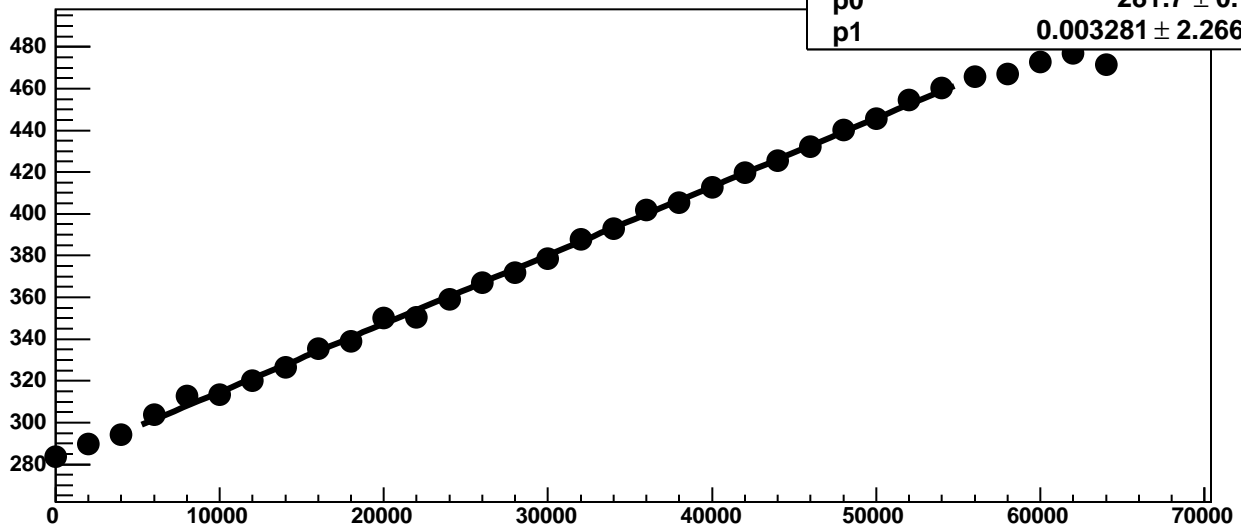
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

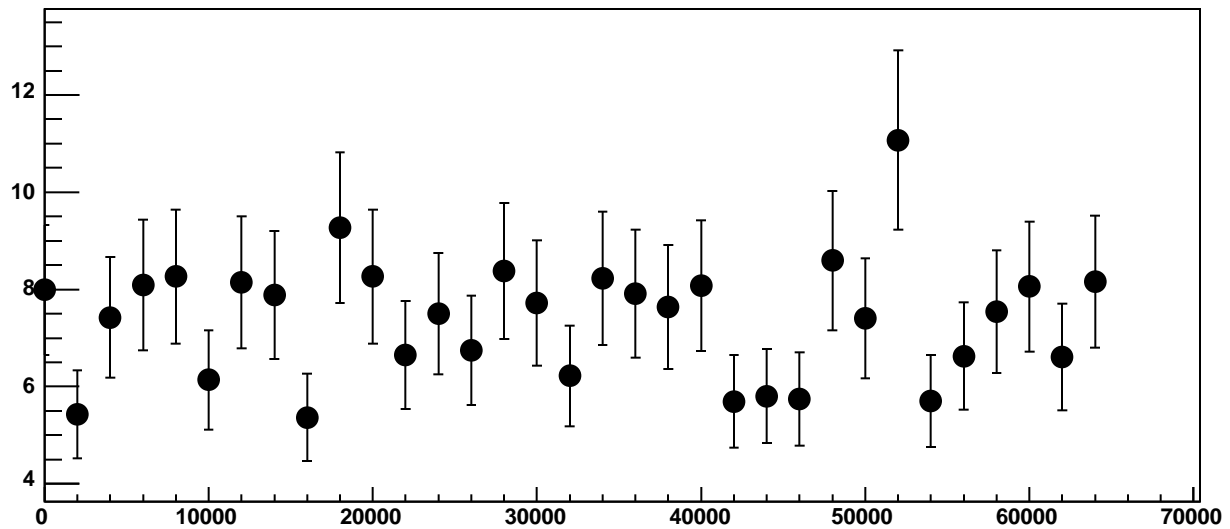


Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

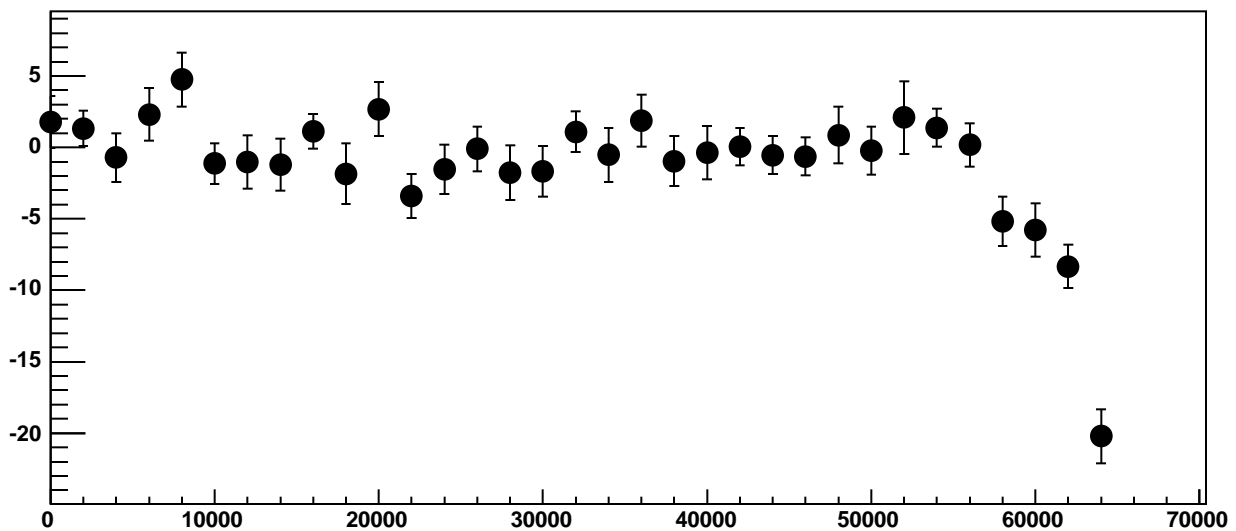


χ^2 / ndf 24.78 / 23
p0 281.7 ± 0.7696
p1 $0.003281 \pm 2.266e-05$

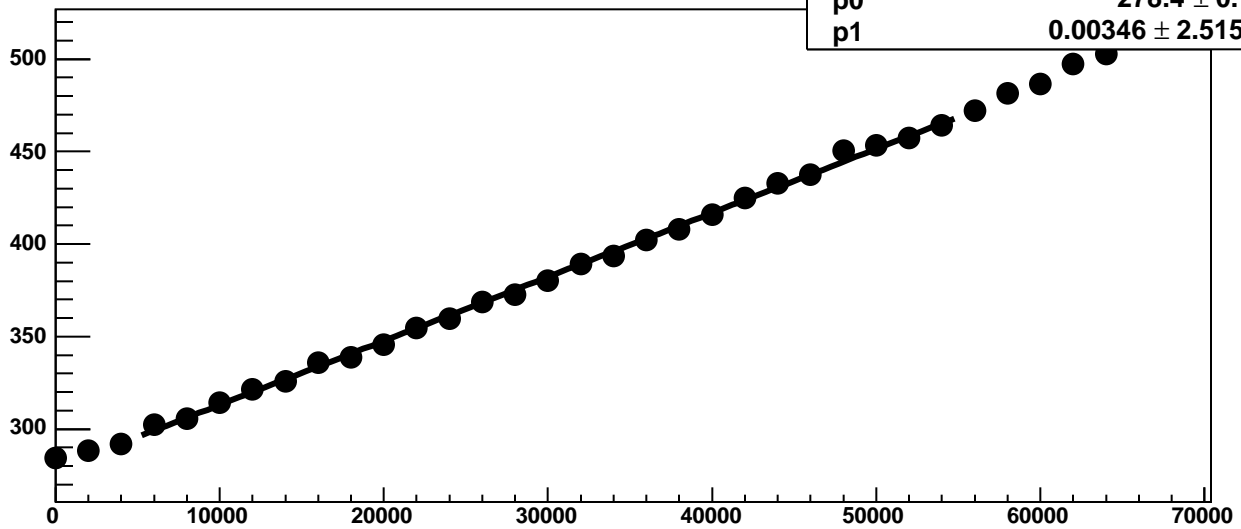
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.91 / 23

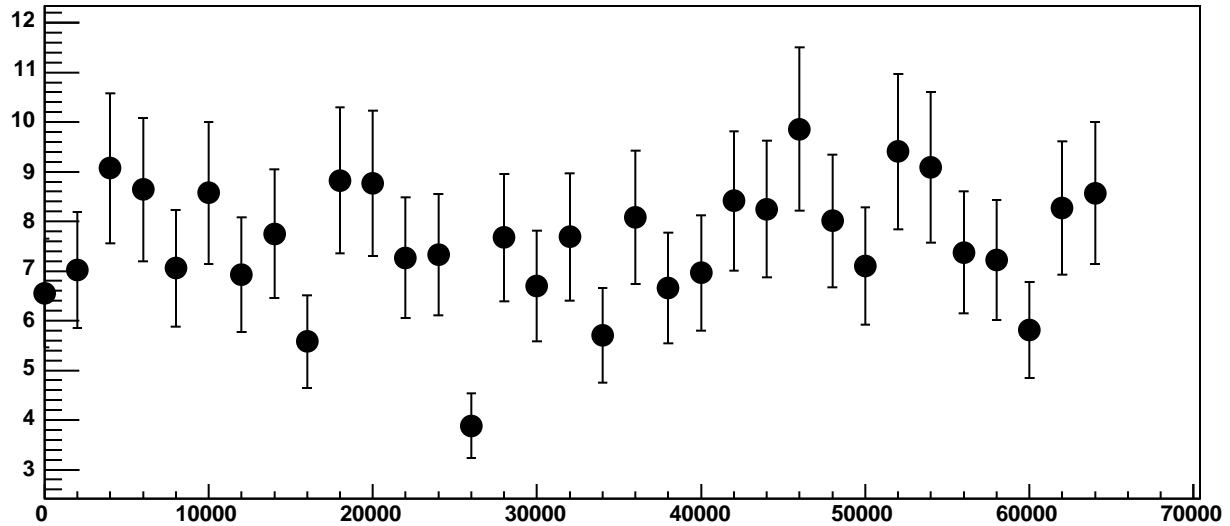
p0

278.4 ± 0.7953

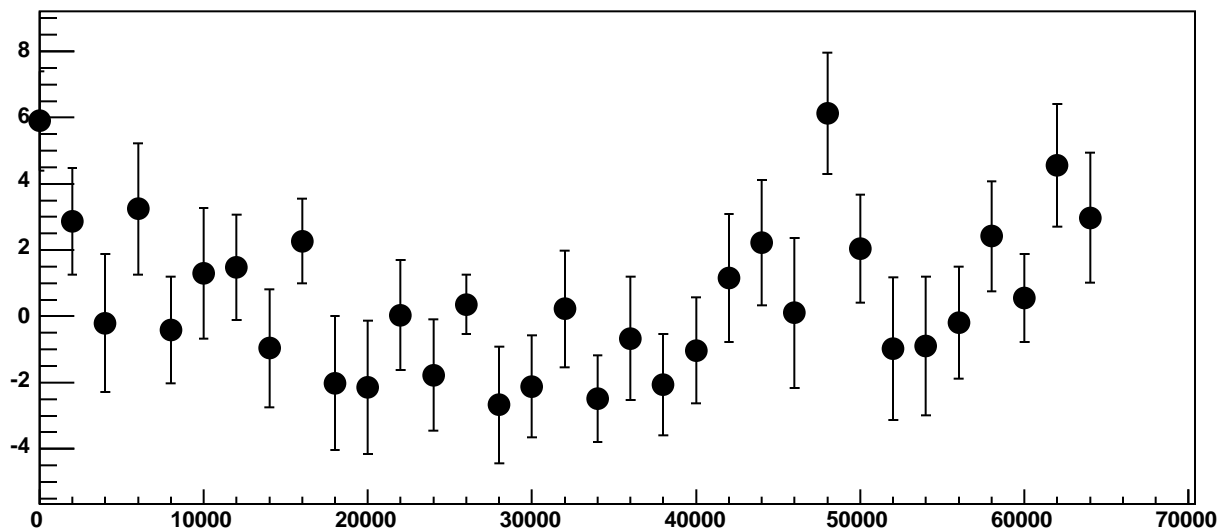
p1

$0.00346 \pm 2.515e-05$

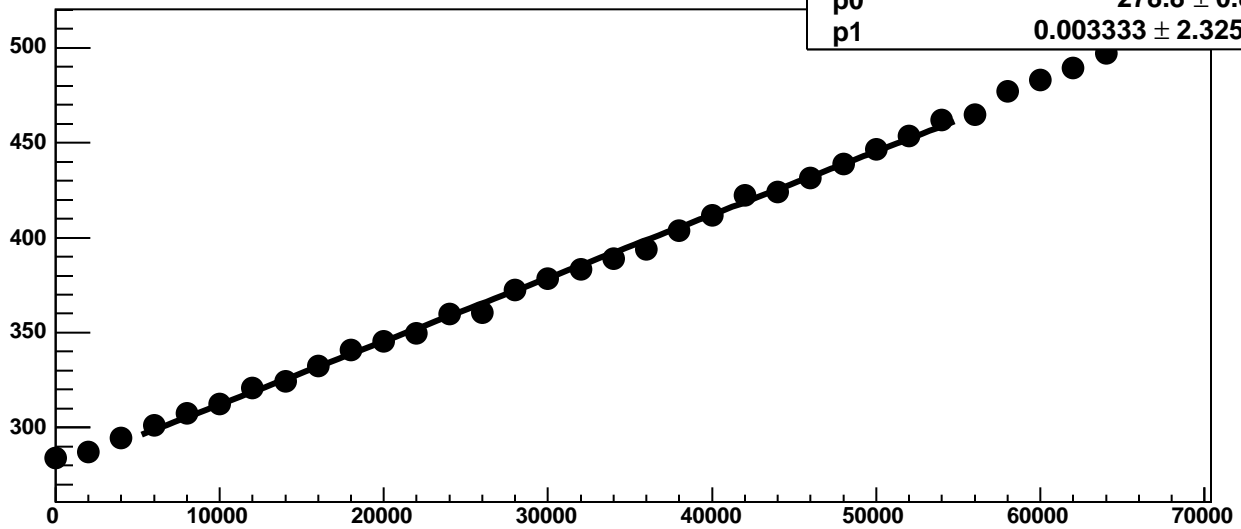
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

39.58 / 23

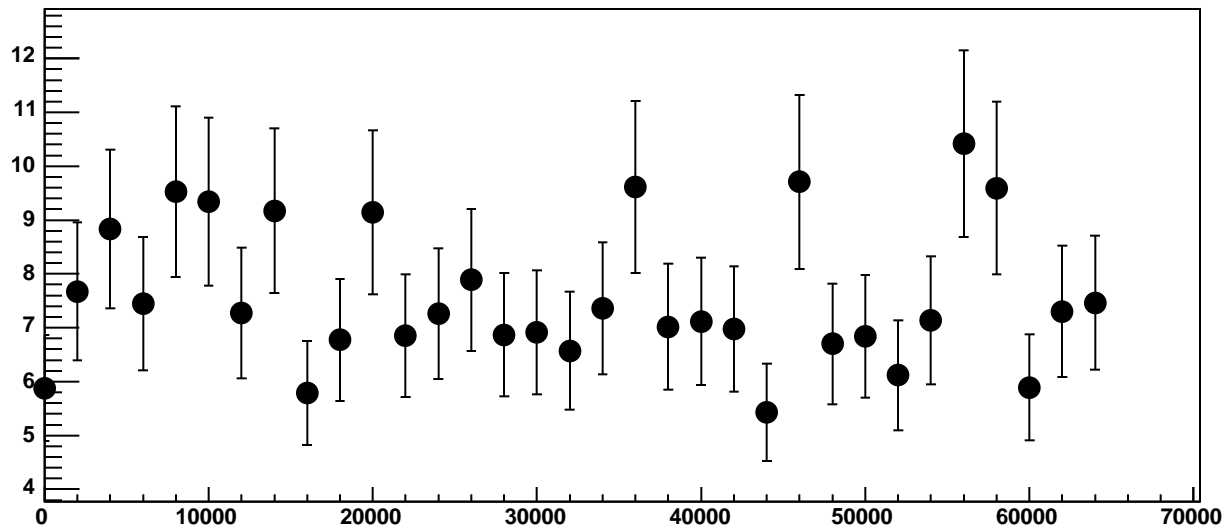
p0

278.8 ± 0.8026

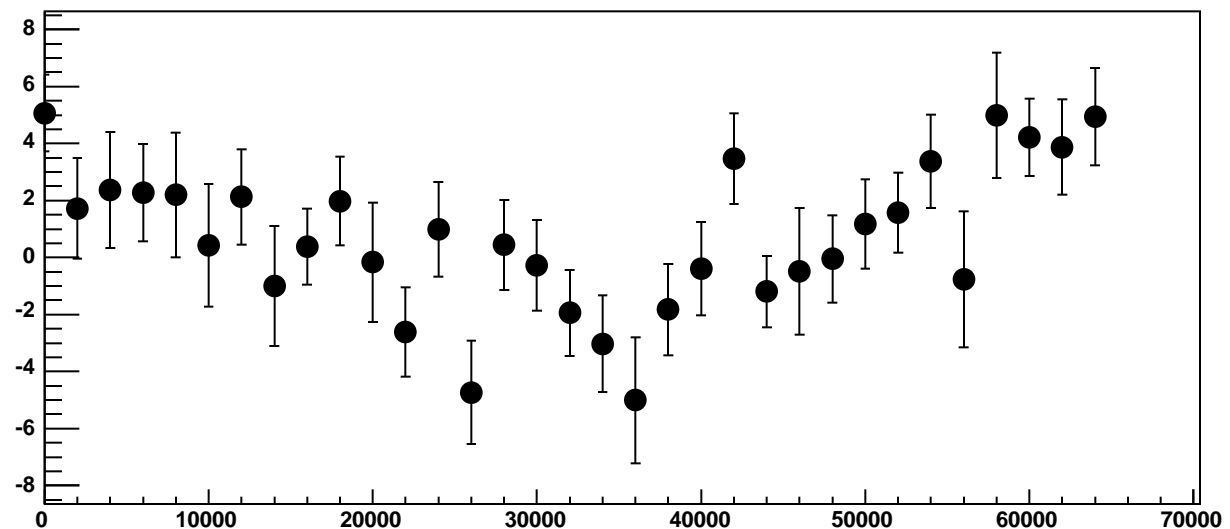
p1

$0.003333 \pm 2.325e-05$

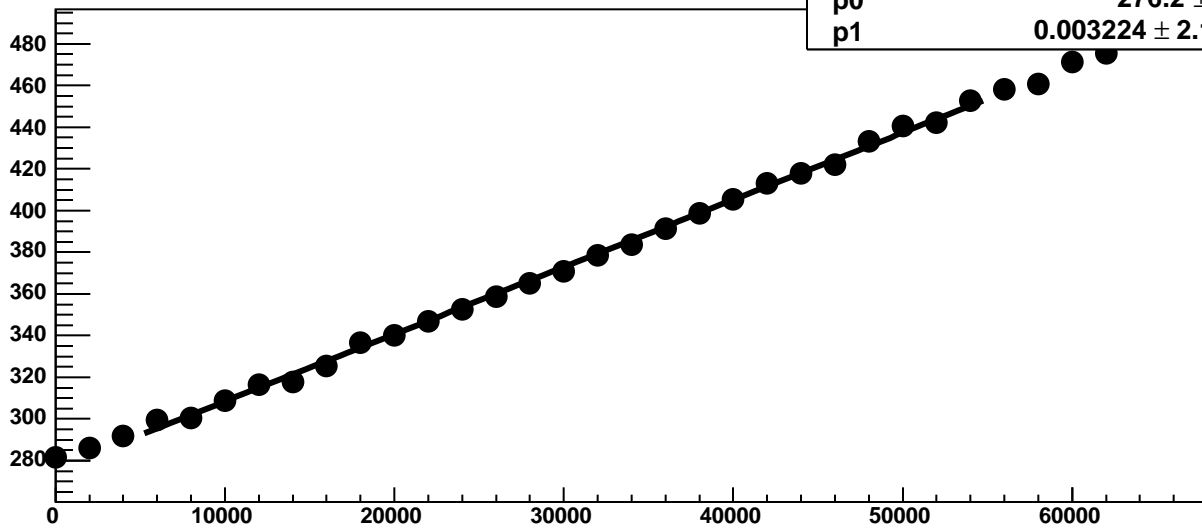
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.77 / 23

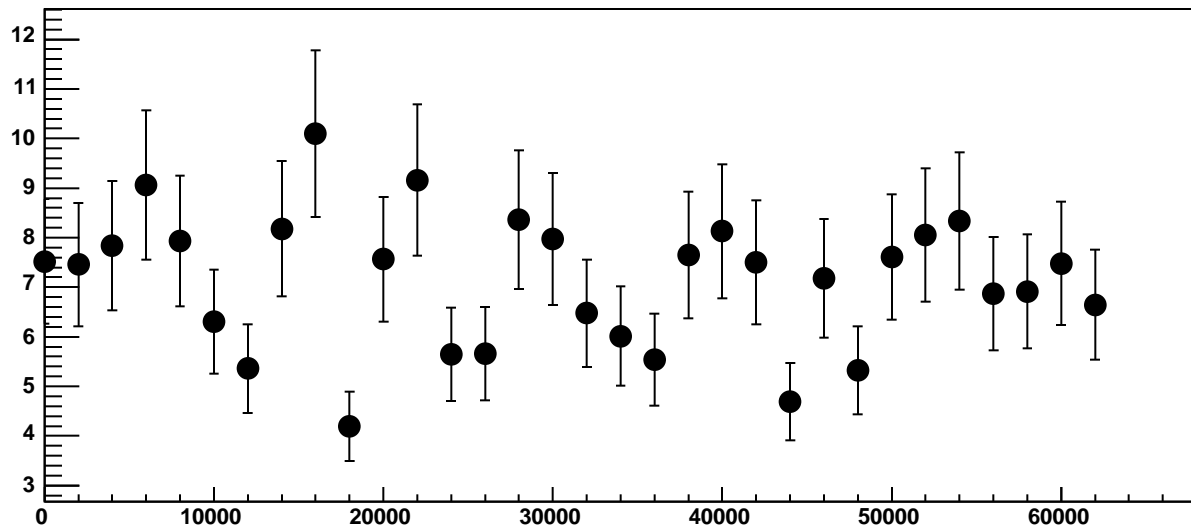
p0

276.2 ± 0.7263

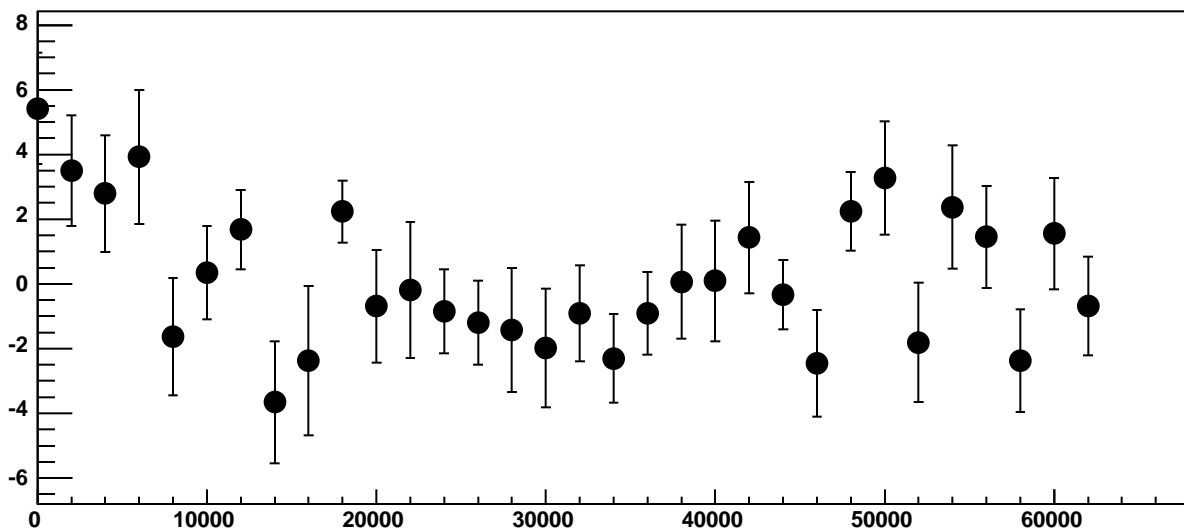
p1

$0.003224 \pm 2.197\text{e-}05$

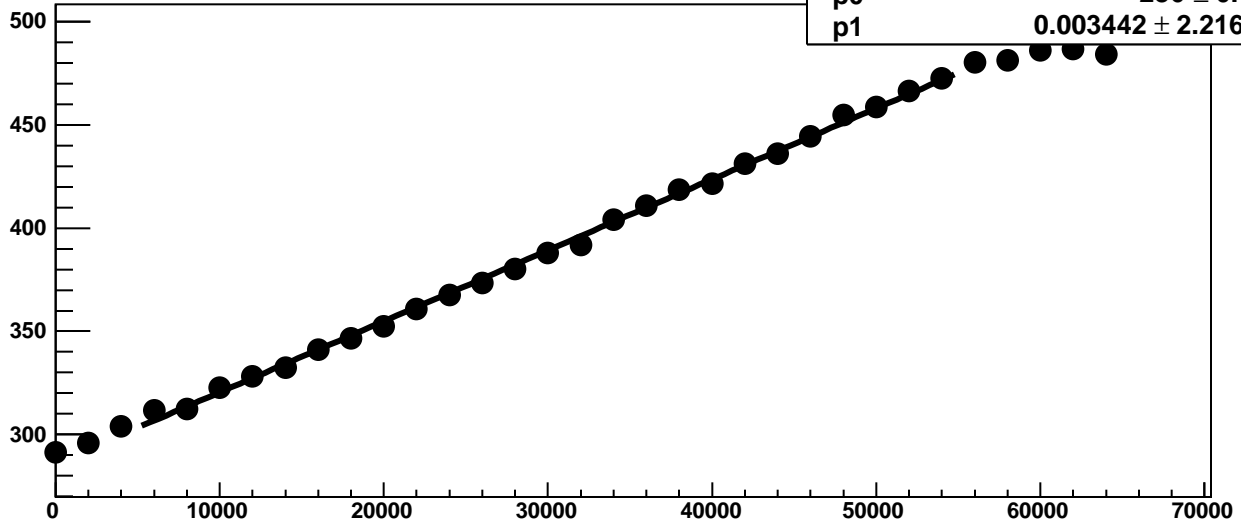
Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

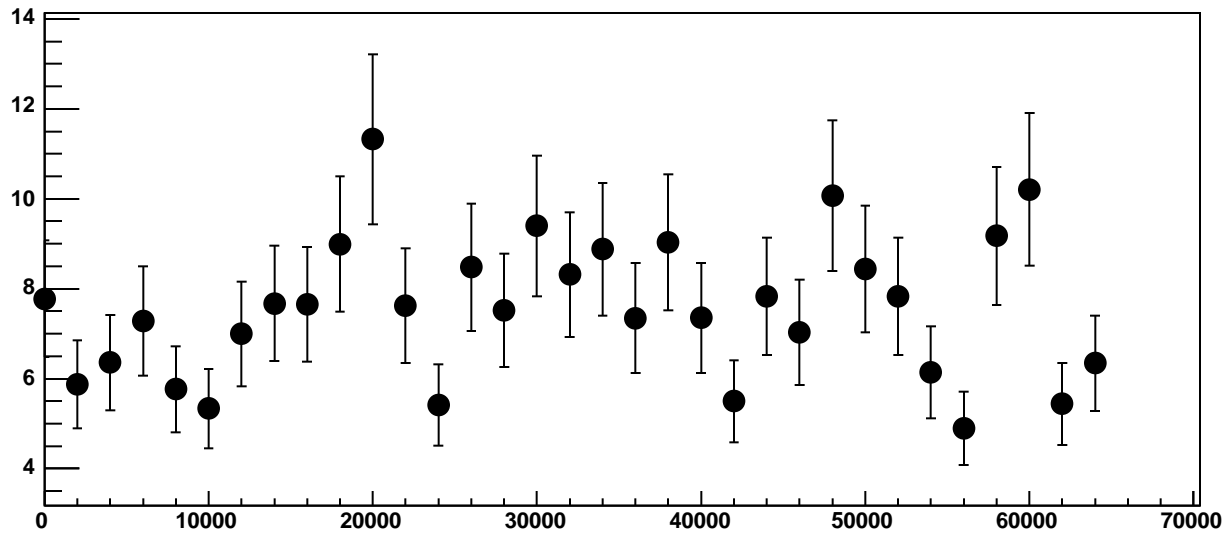


Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

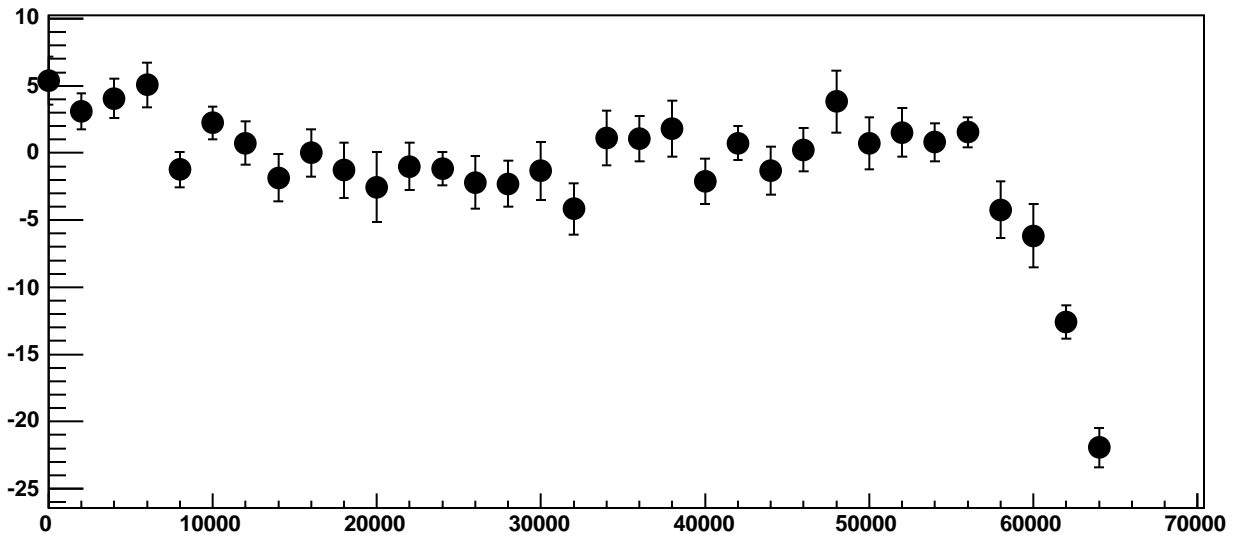


χ^2 / ndf 33.31 / 23
p0 286 ± 0.7247
p1 $0.003442 \pm 2.216e-05$

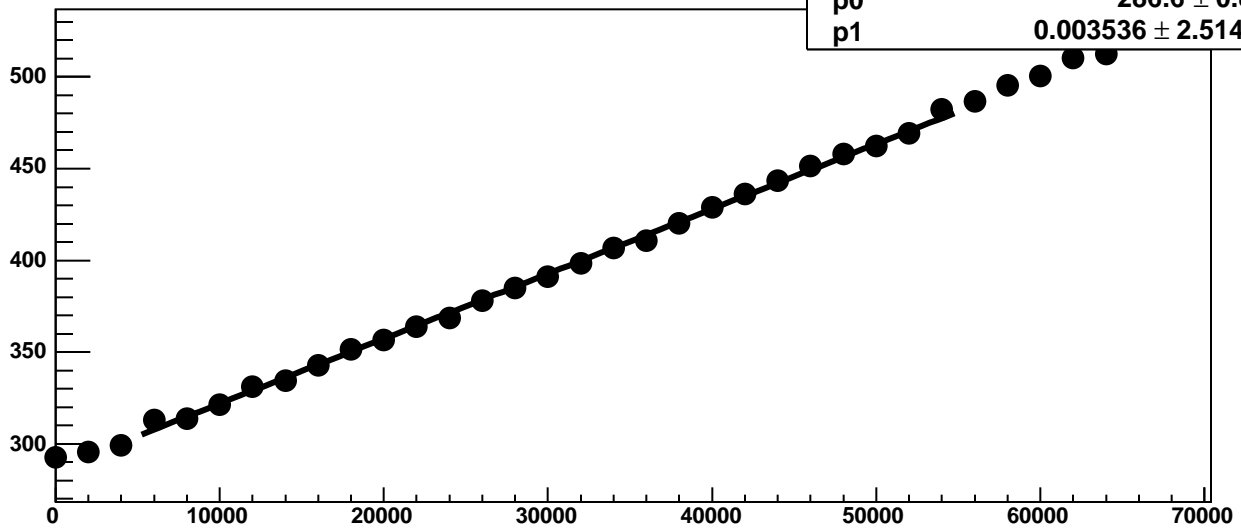
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

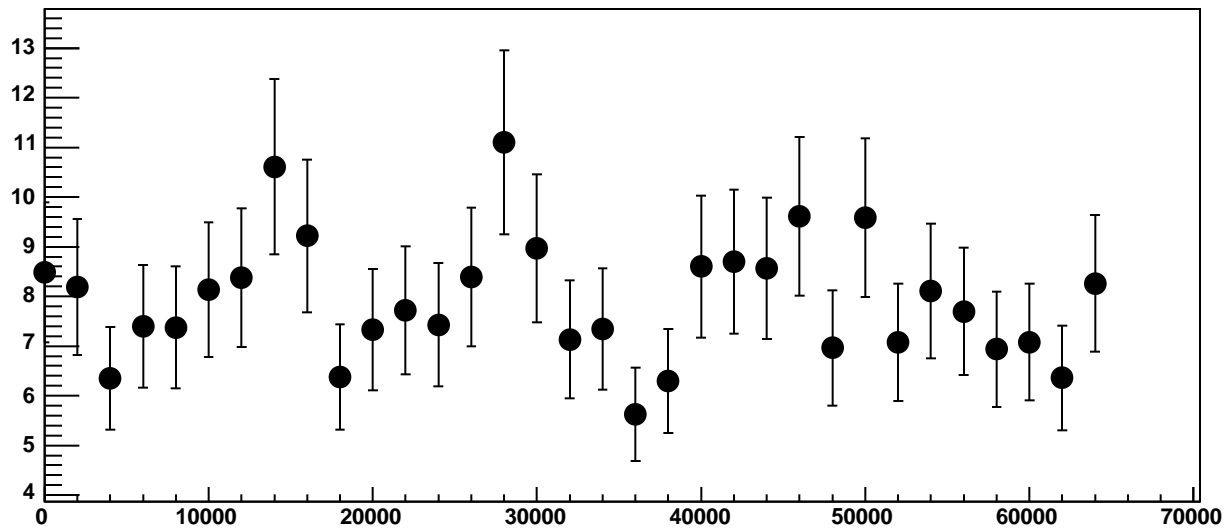


Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

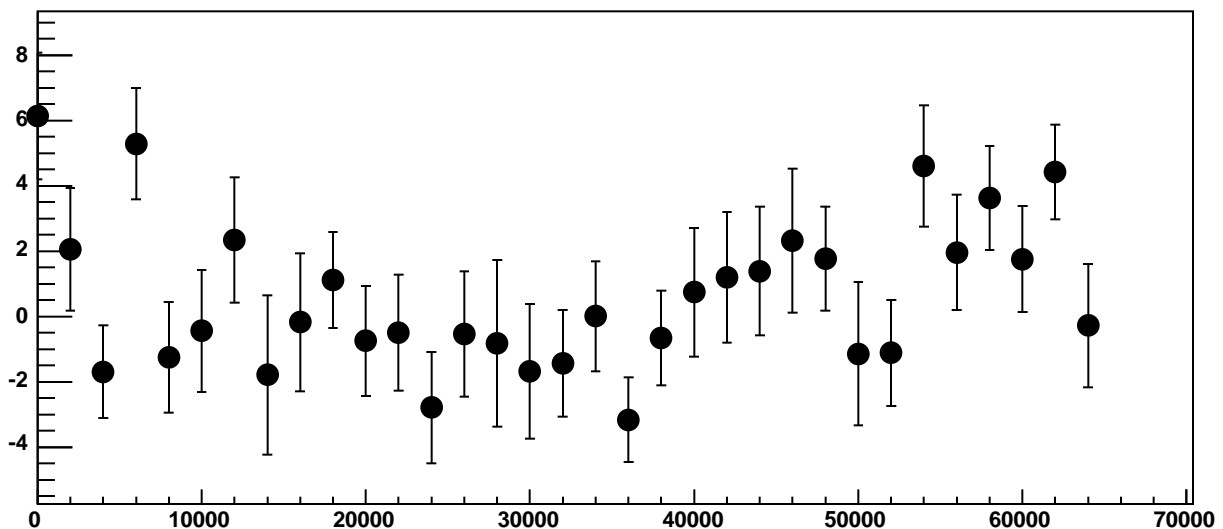


χ^2 / ndf 33.9 / 23
p0 286.6 ± 0.8372
p1 $0.003536 \pm 2.514e-05$

Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

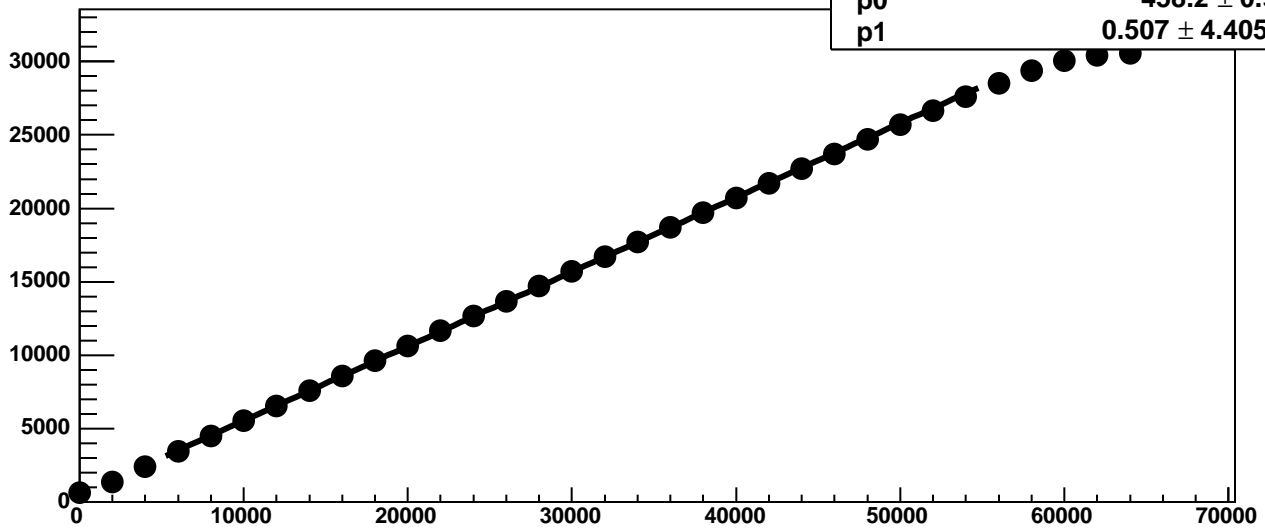


Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

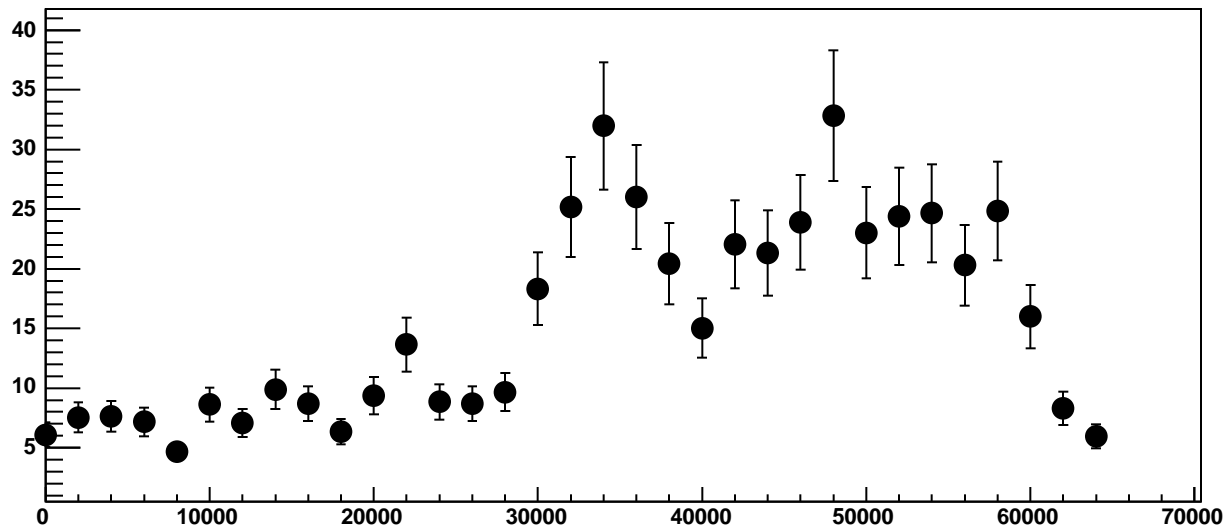


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

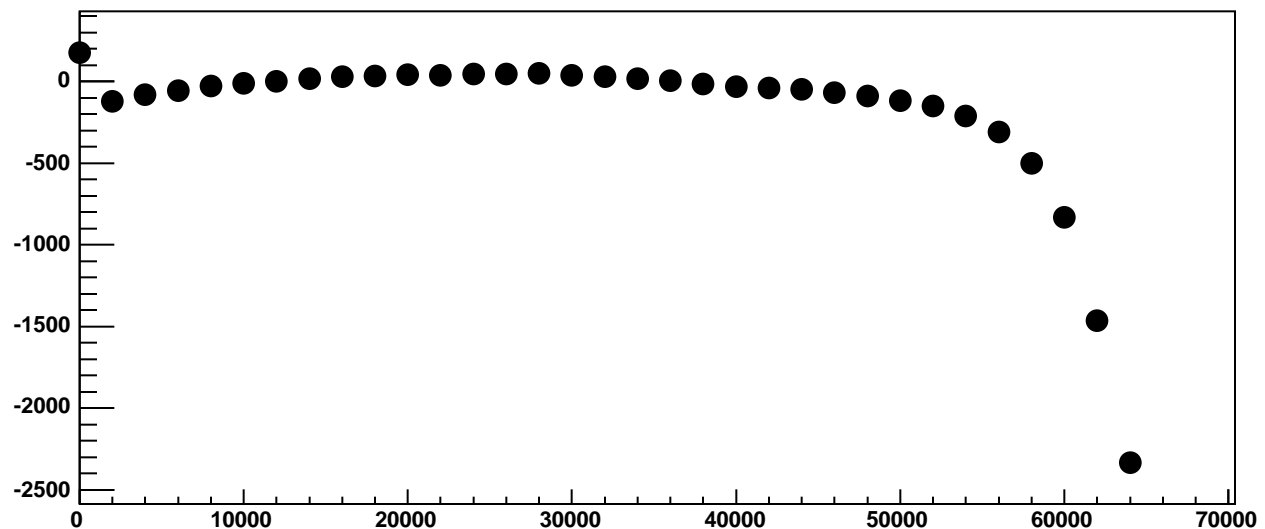
χ^2 / ndf 7886 / 23
p0 458.2 ± 0.9205
p1 $0.507 \pm 4.405e-05$



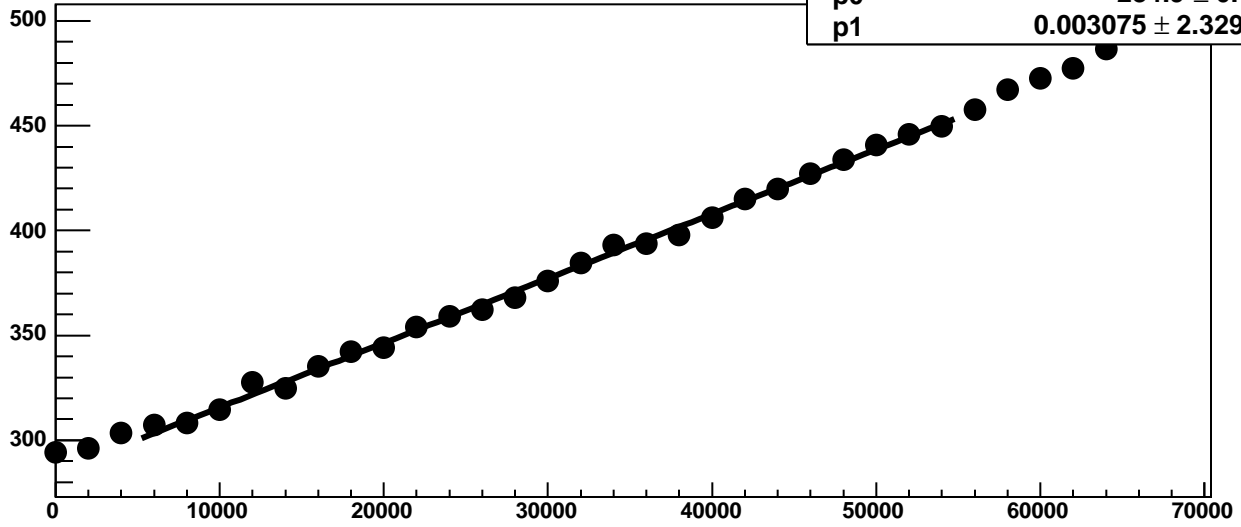
Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.46 / 23

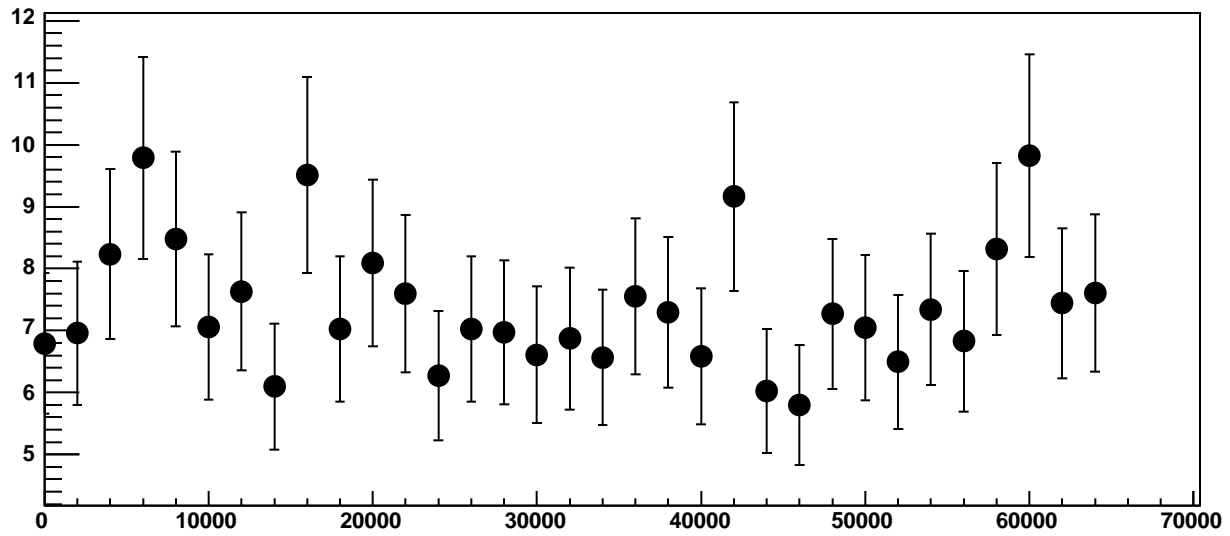
p0

284.9 ± 0.7998

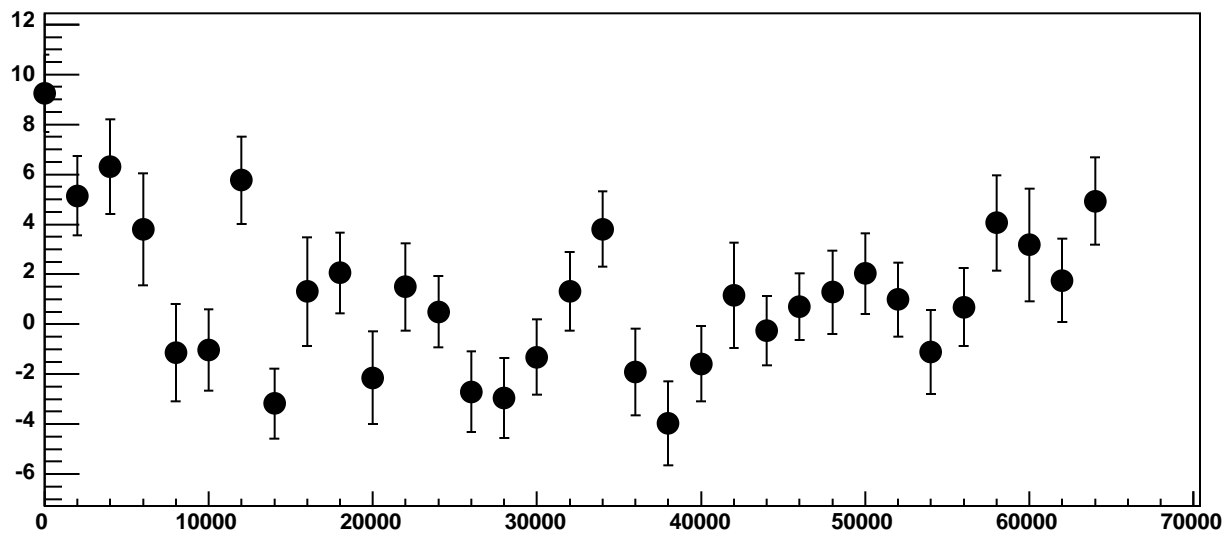
p1

$0.003075 \pm 2.329e-05$

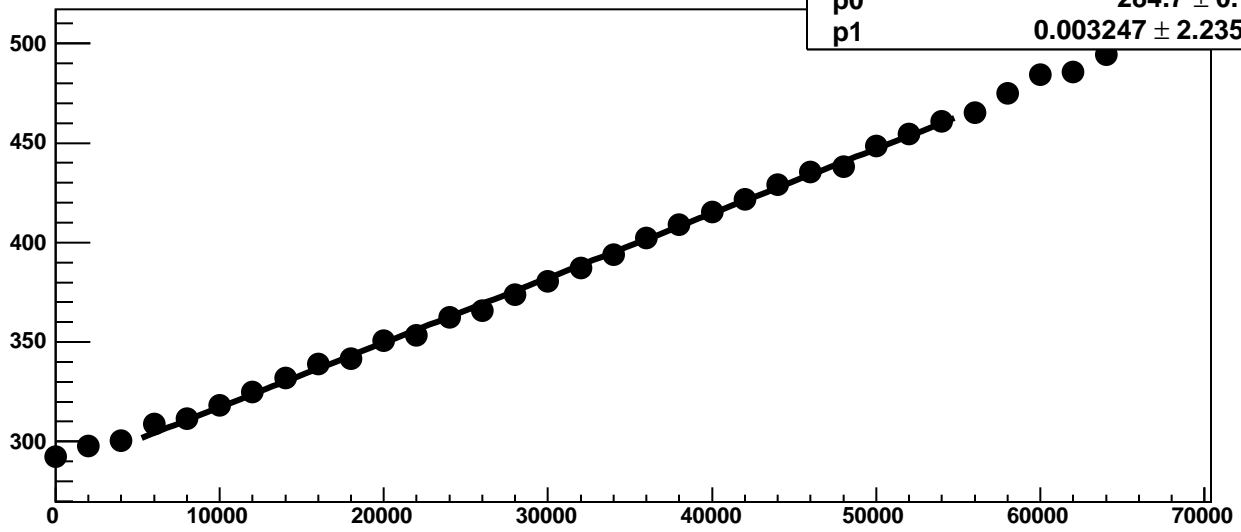
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30.8 / 23

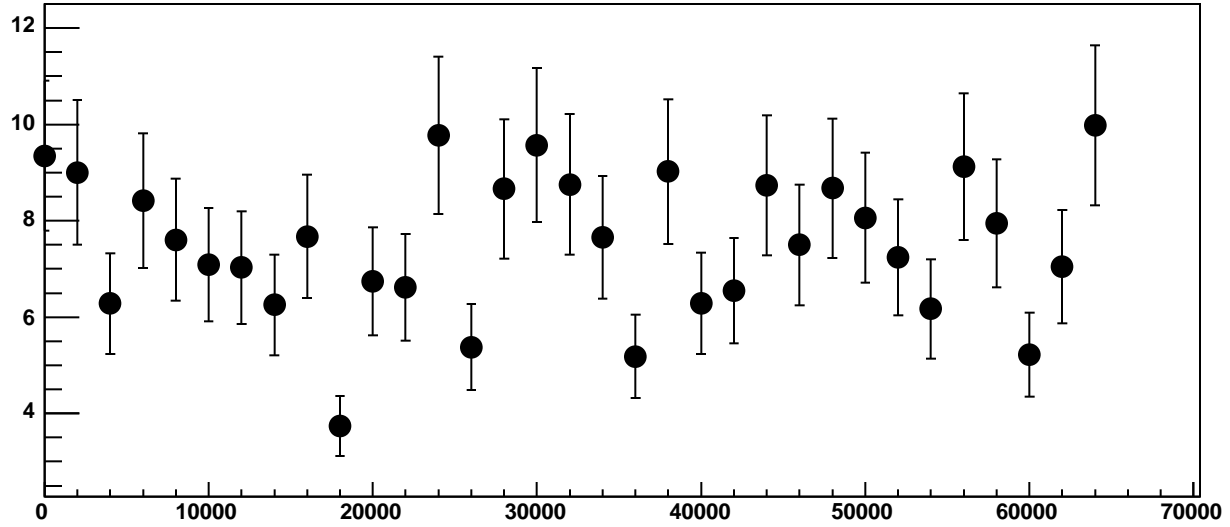
p0

284.7 ± 0.7176

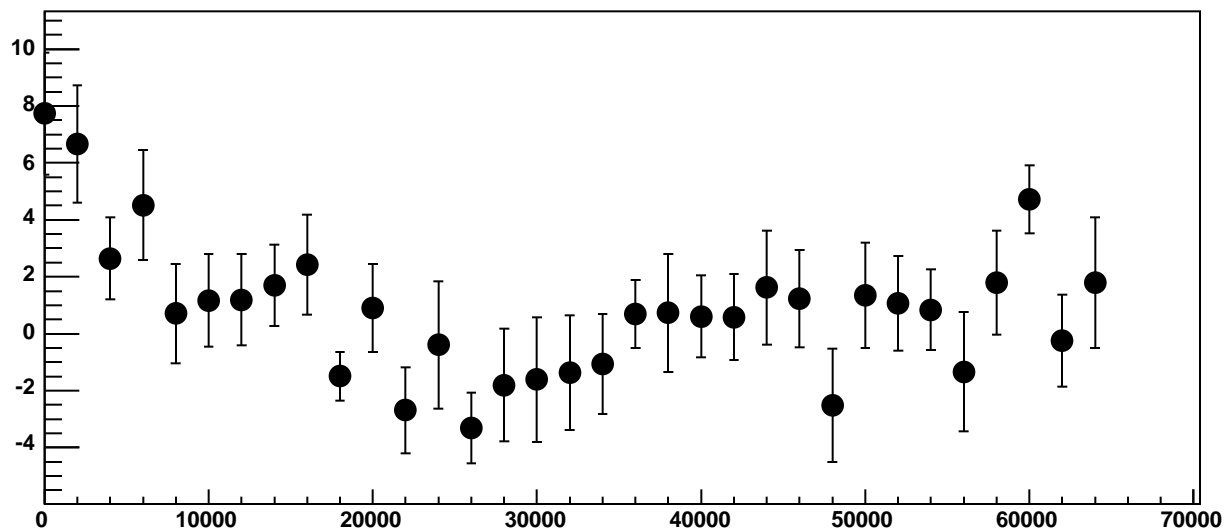
p1

$0.003247 \pm 2.235e-05$

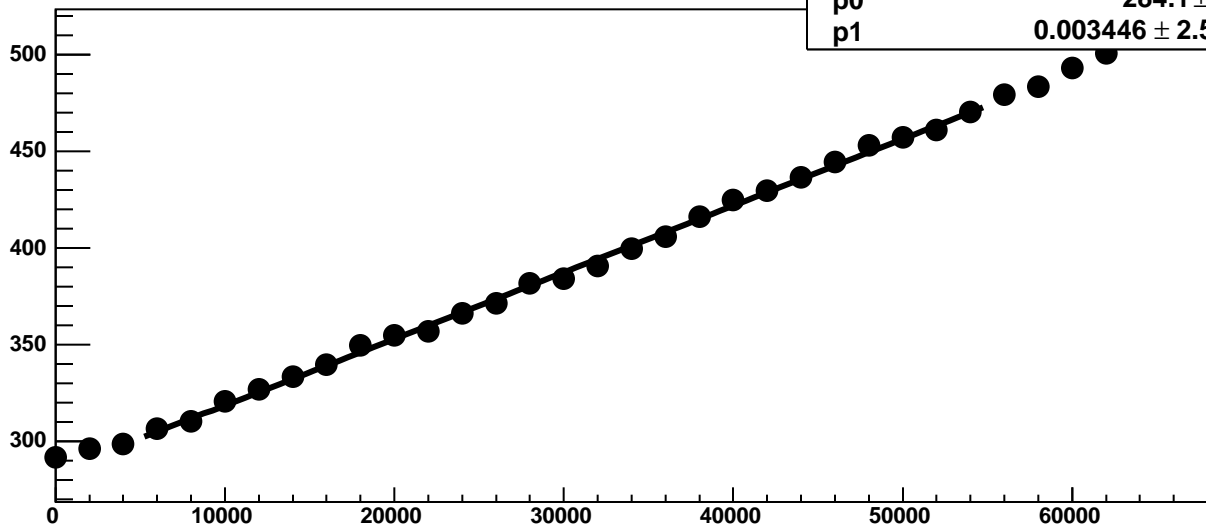
Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

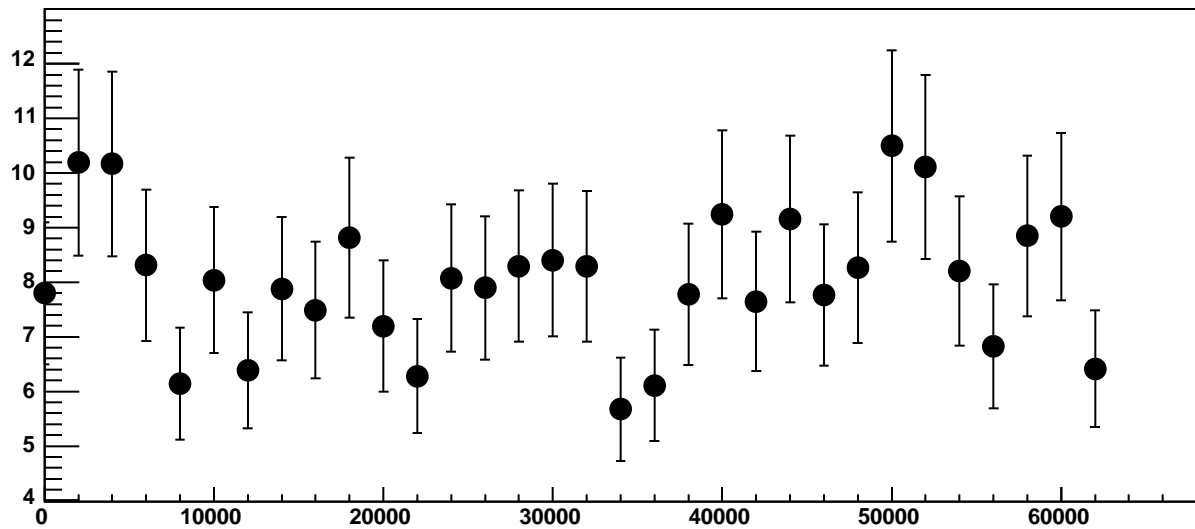


Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

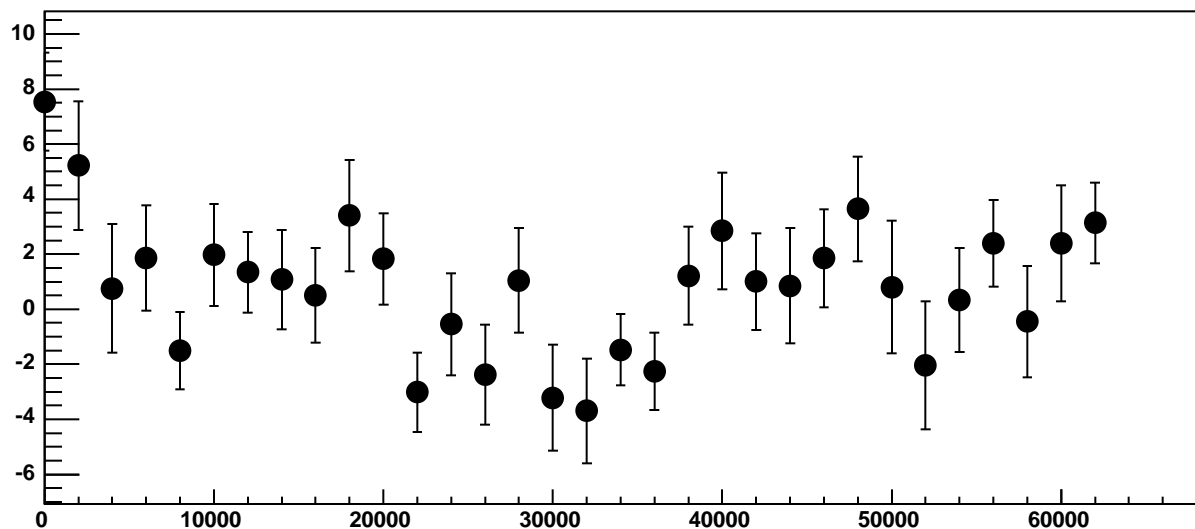


χ^2 / ndf 33.95 / 23
p0 284.1 ± 0.7974
p1 0.003446 ± 2.526e-05

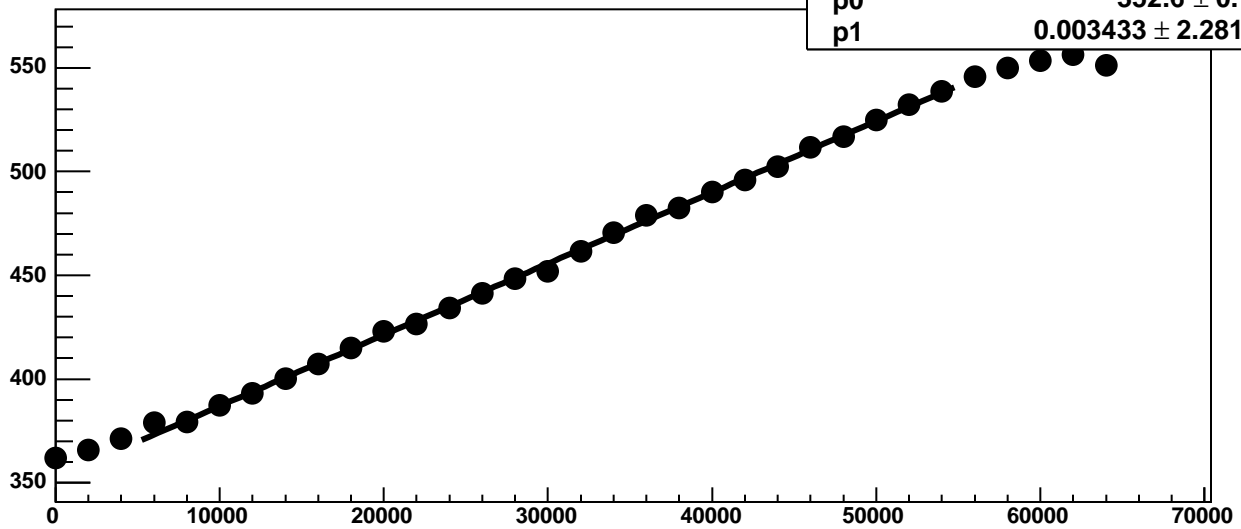
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

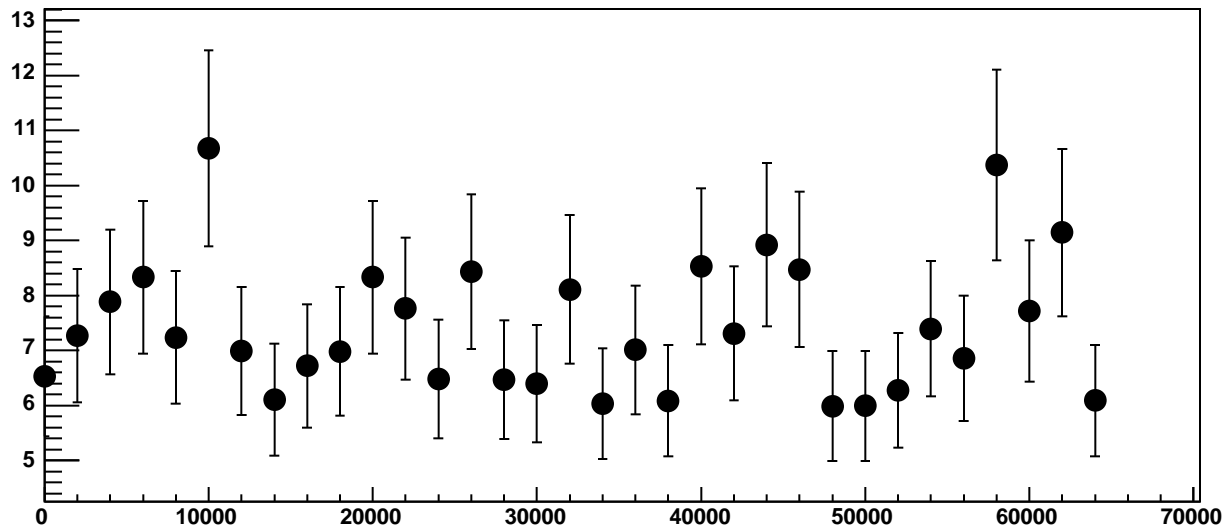


Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

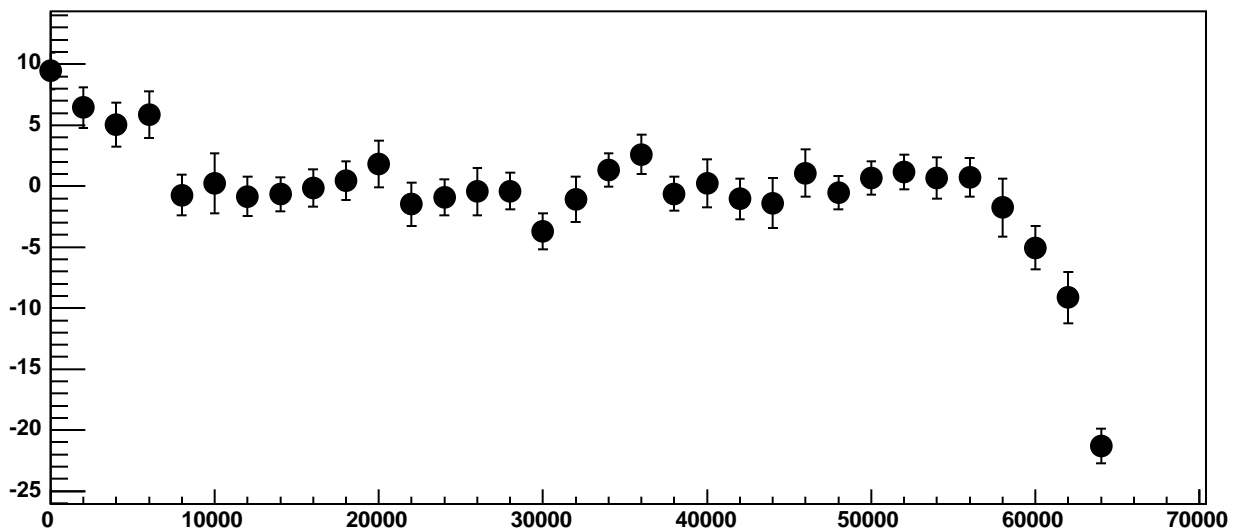


χ^2 / ndf 25.01 / 23
p0 352.6 ± 0.7775
p1 $0.003433 \pm 2.281e-05$

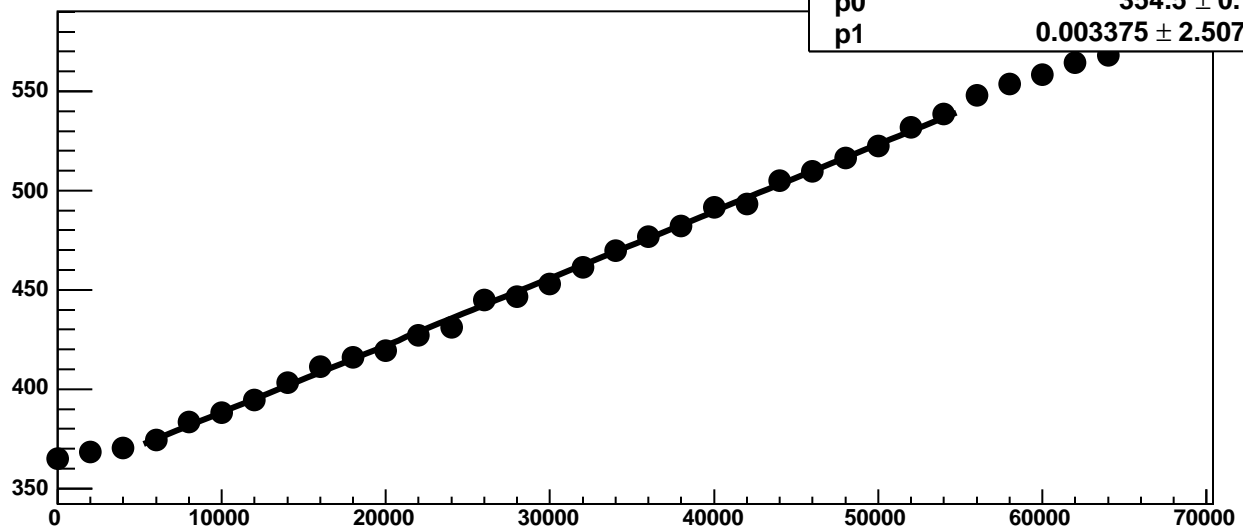
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

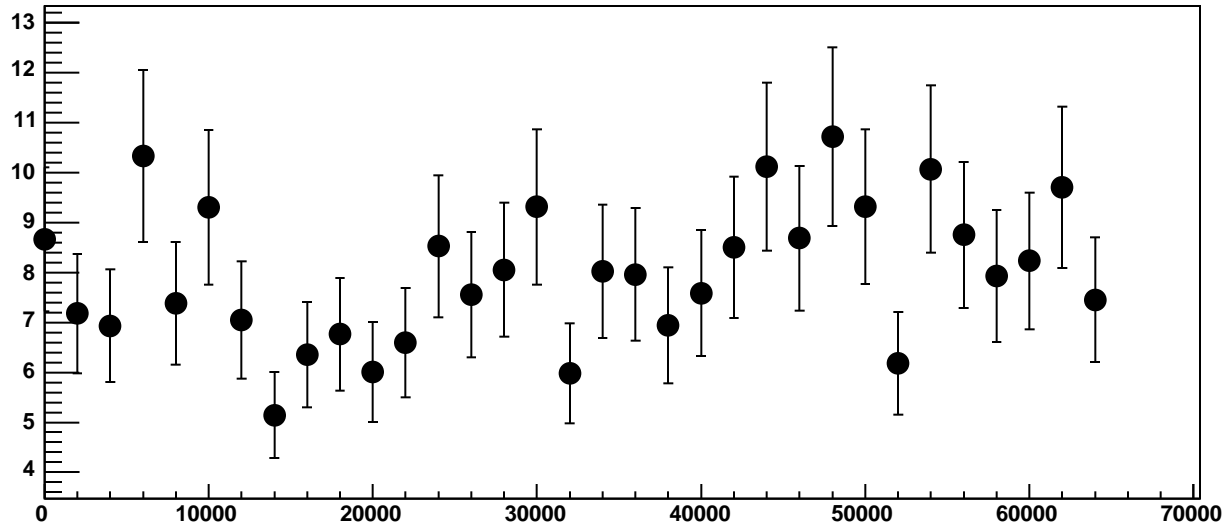


Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

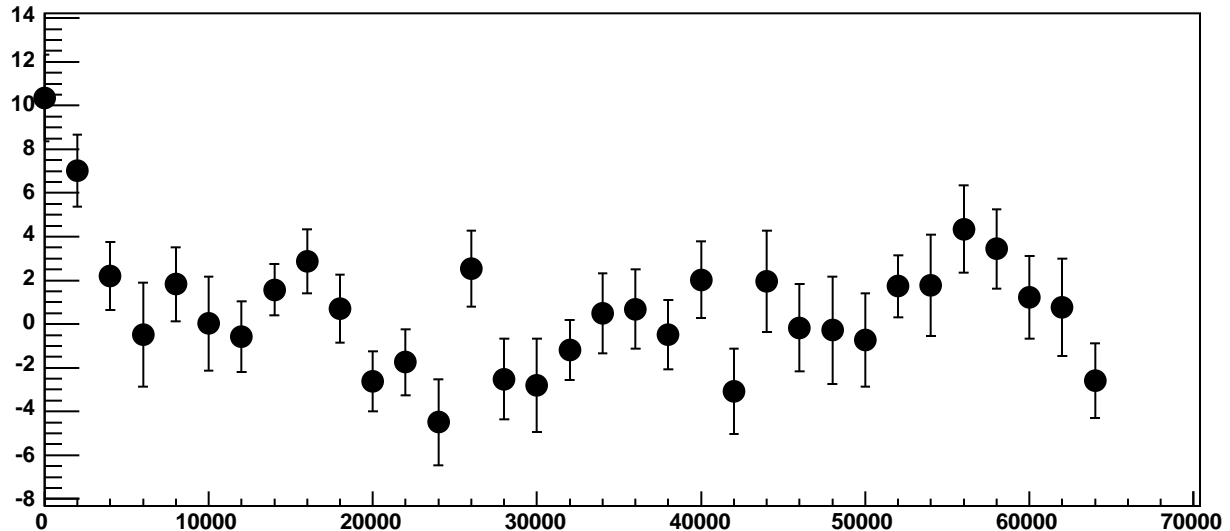


χ^2 / ndf 31.01 / 23
p0 354.5 ± 0.7857
p1 $0.003375 \pm 2.507e-05$

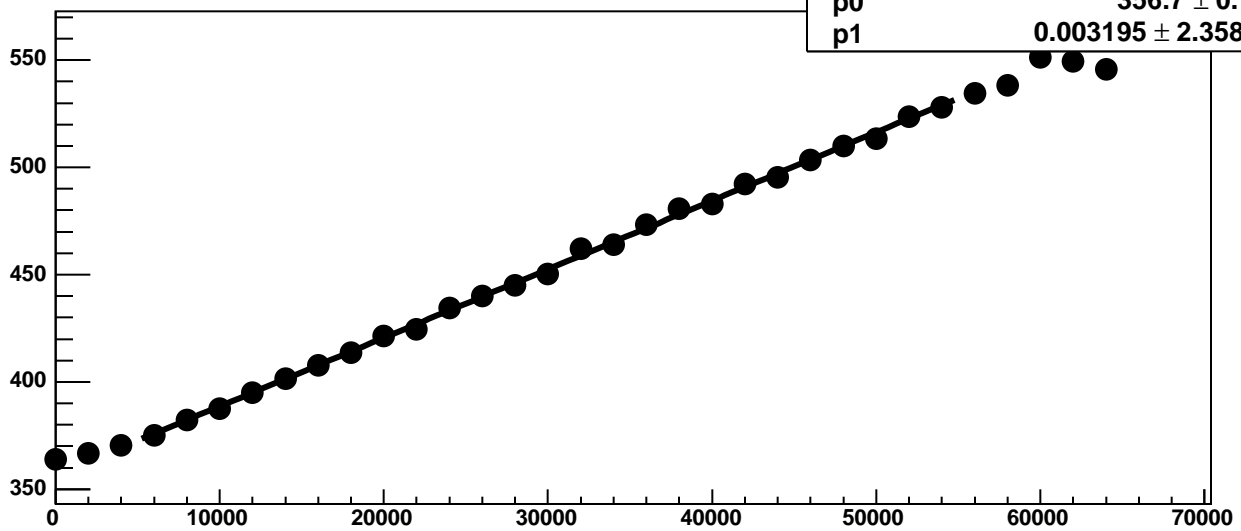
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

22 / 23

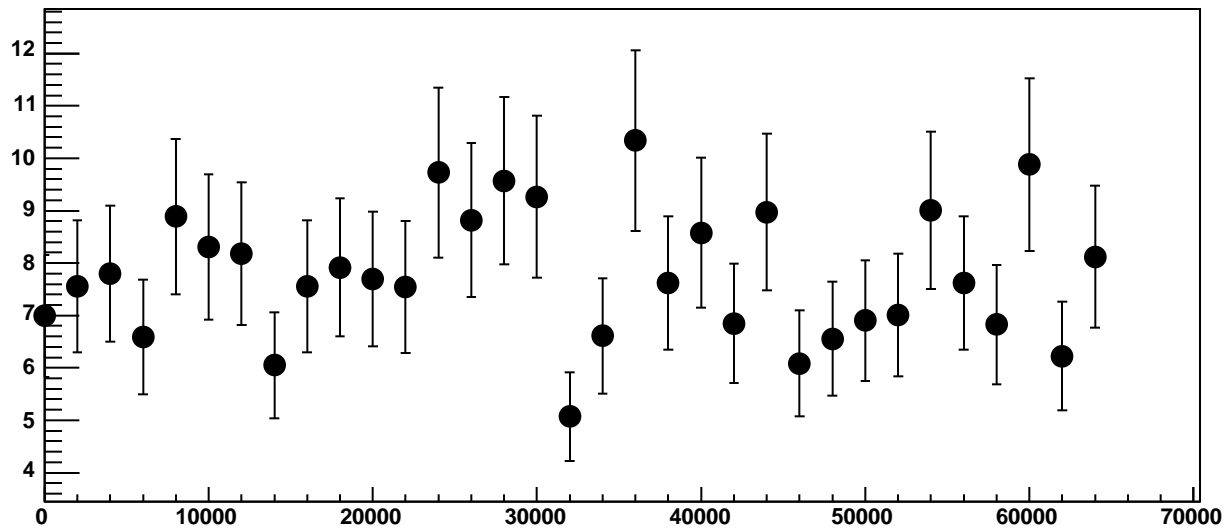
p0

356.7 ± 0.7974

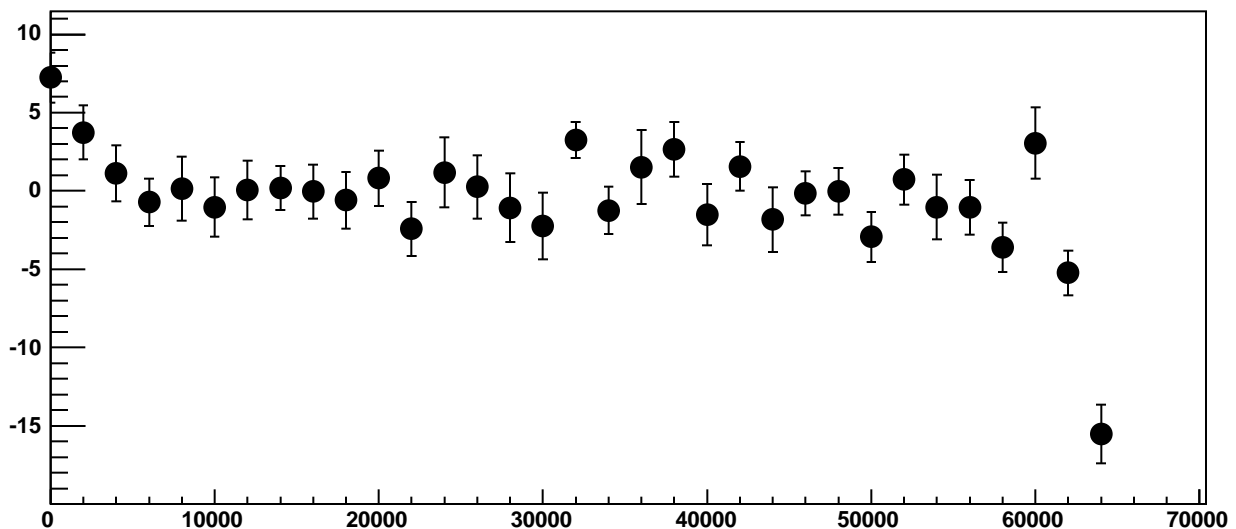
p1

$0.003195 \pm 2.358e-05$

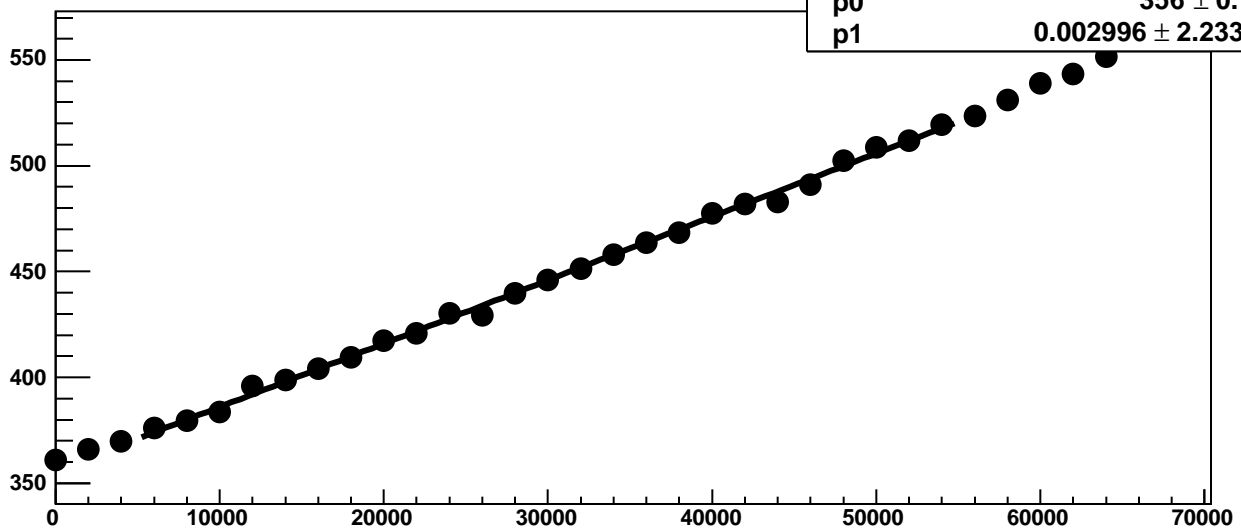
Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



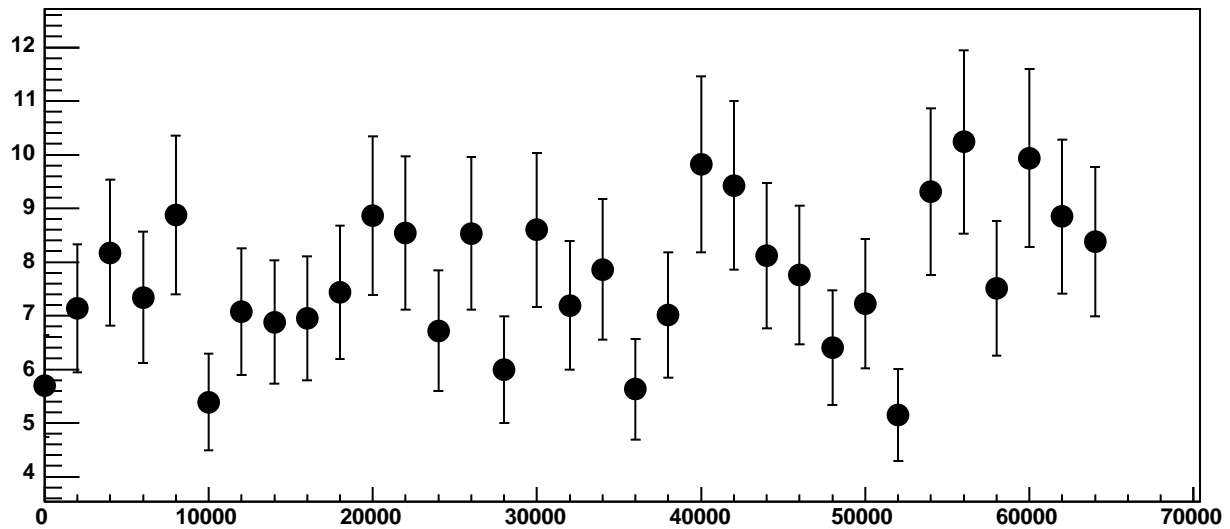
Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



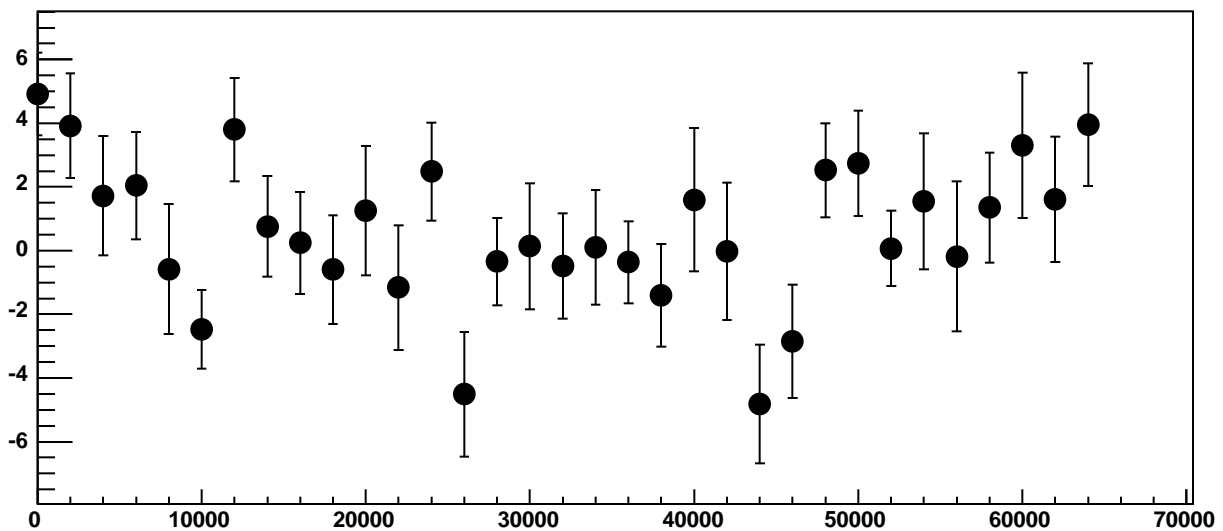
Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



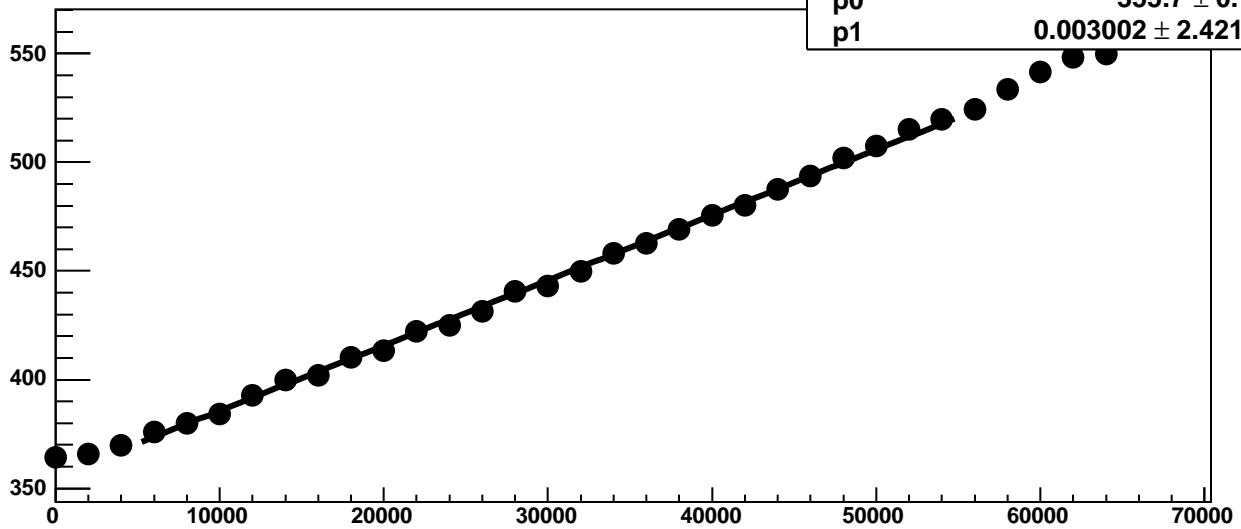
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

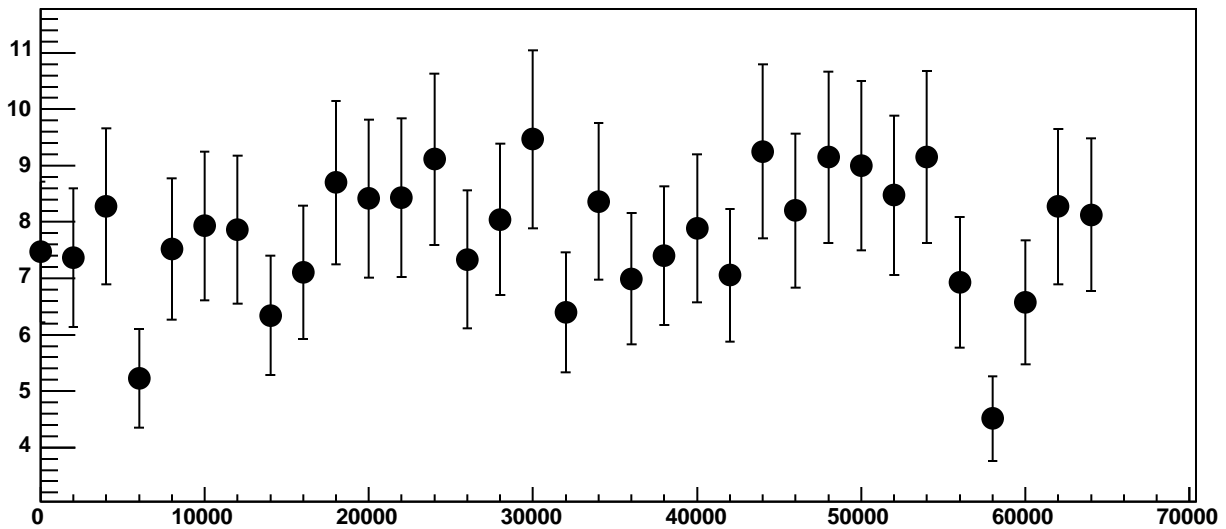


Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

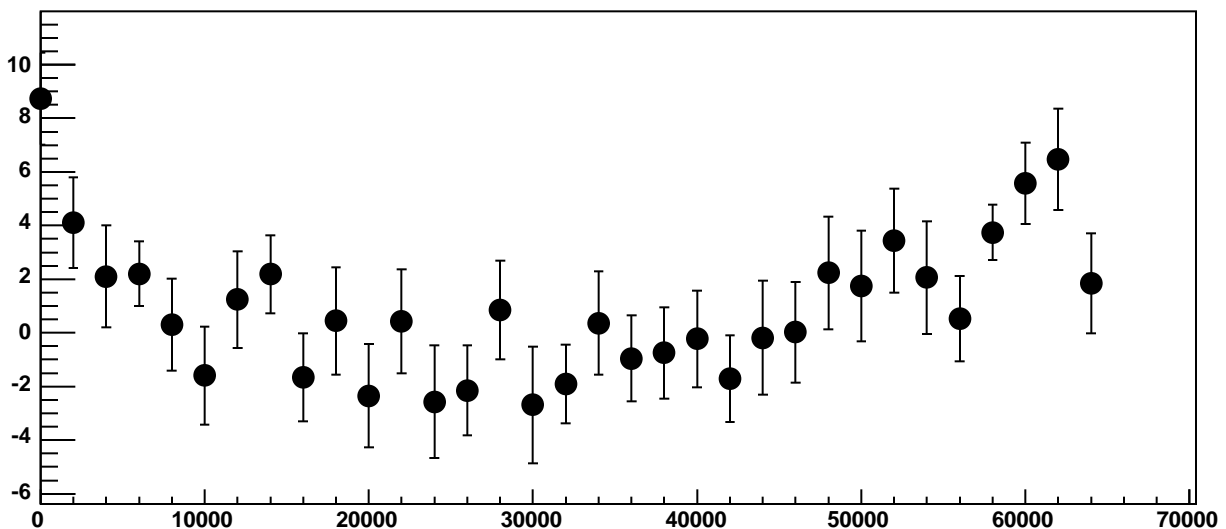


χ^2 / ndf 23.73 / 23
p0 355.7 ± 0.7613
p1 $0.003002 \pm 2.421\text{e-}05$

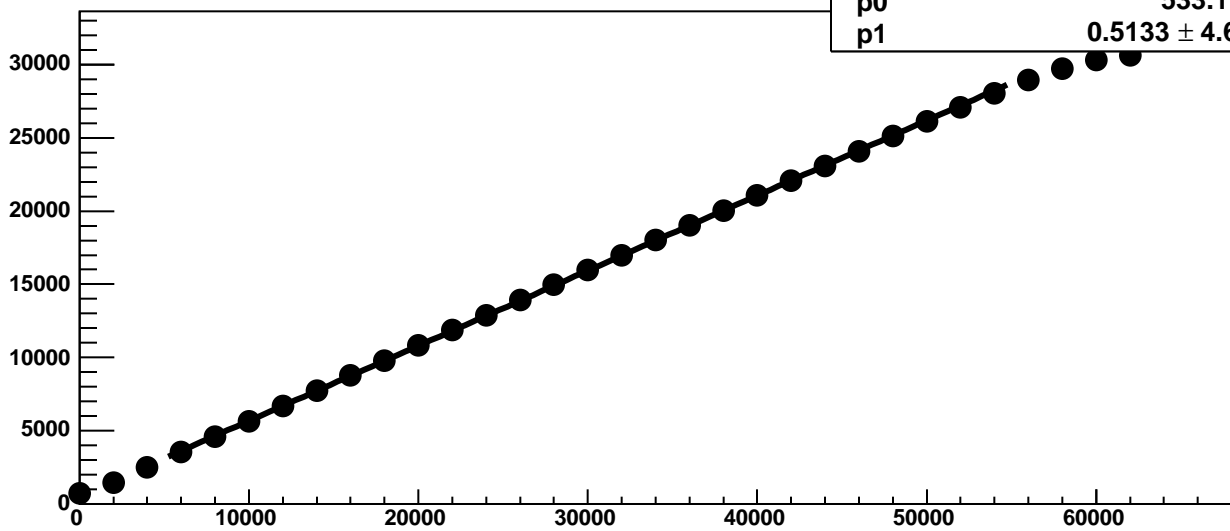
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



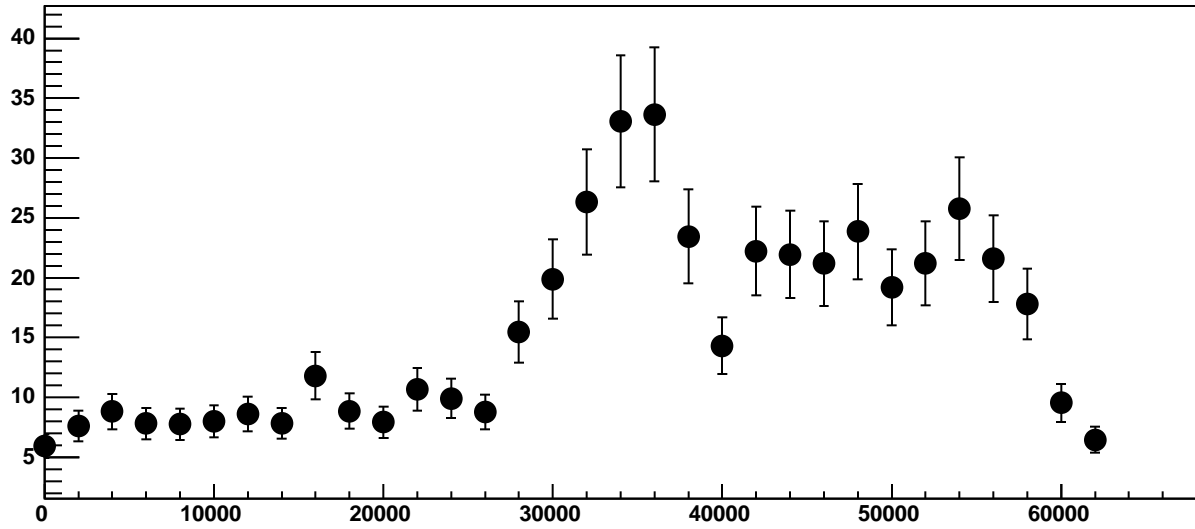
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



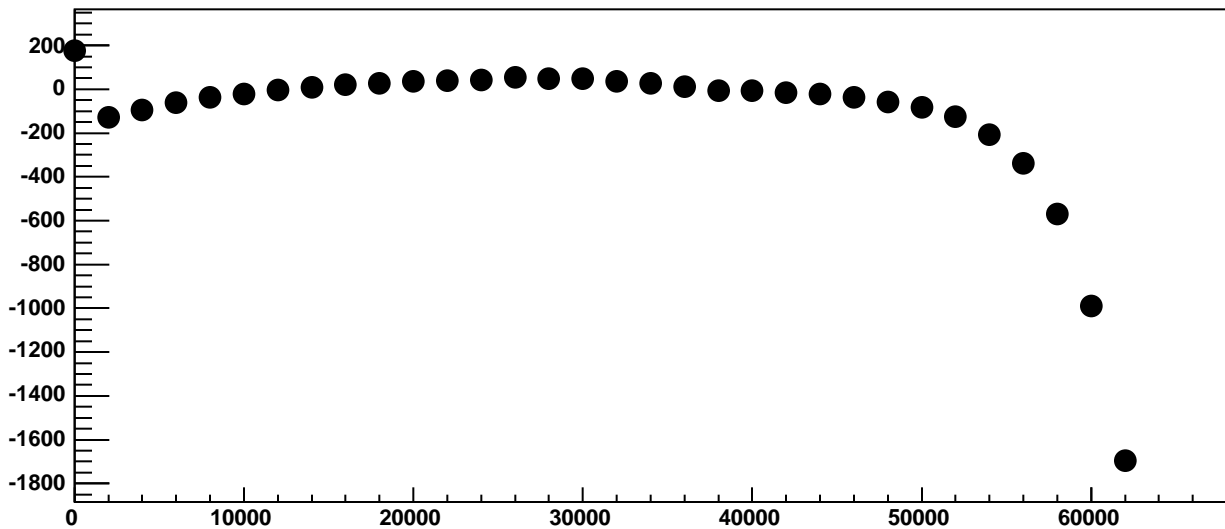
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



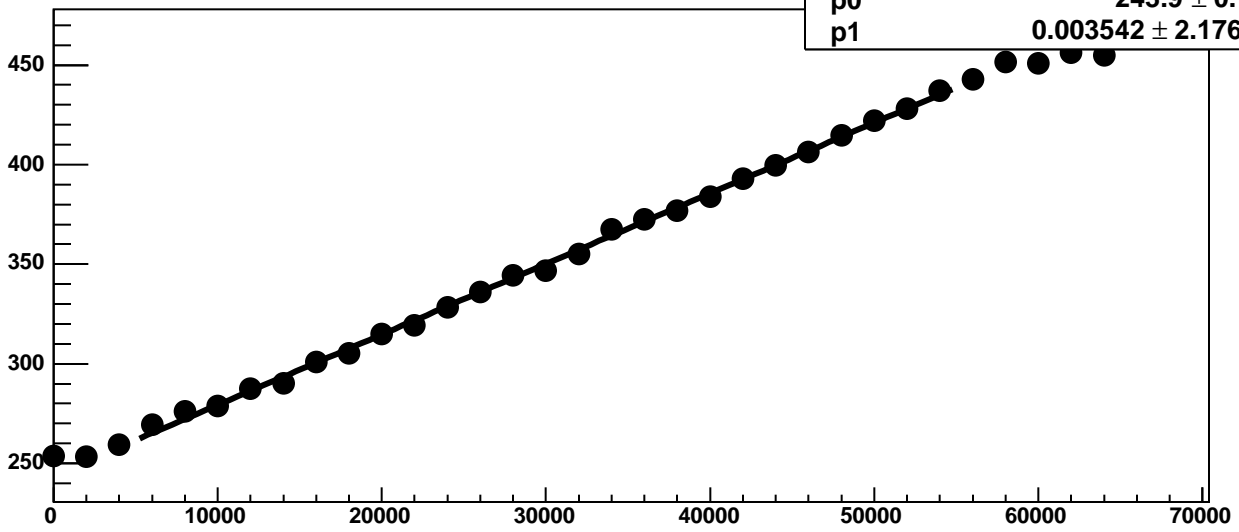
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.37 / 23

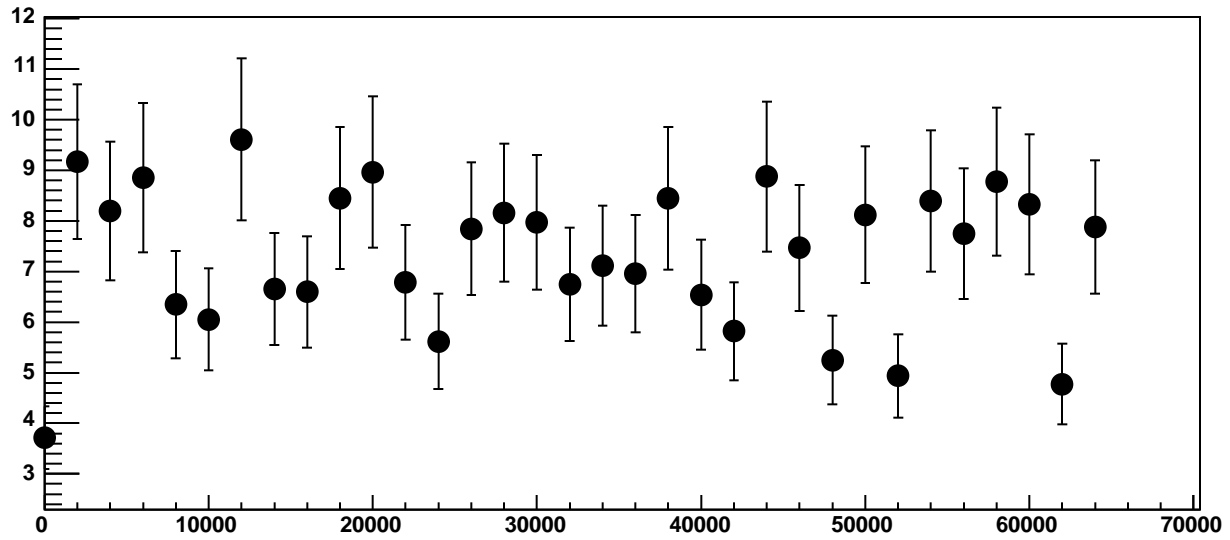
p0

243.9 ± 0.7493

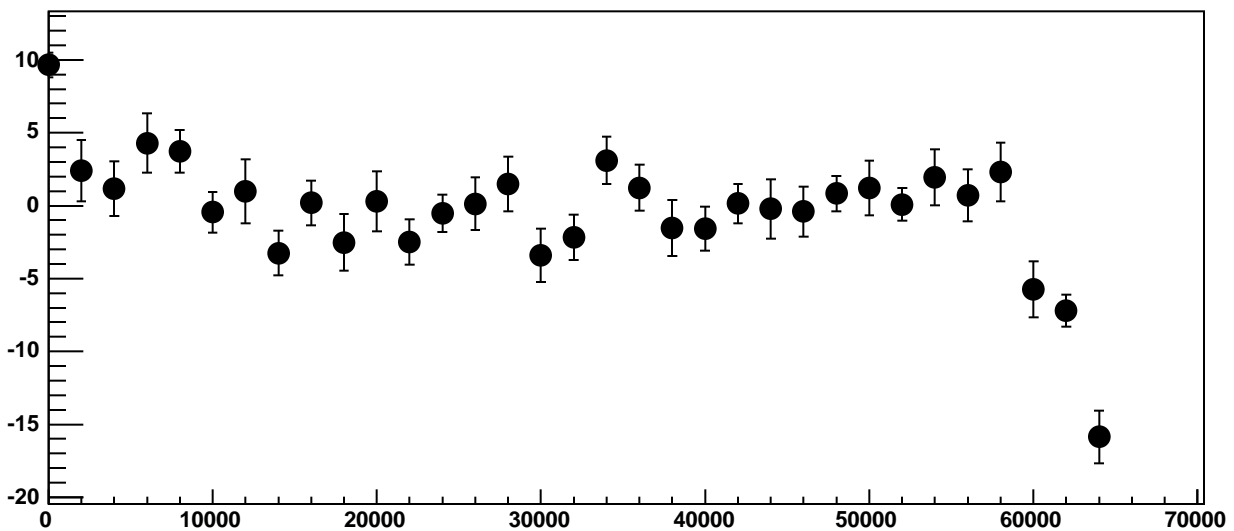
p1

$0.003542 \pm 2.176e-05$

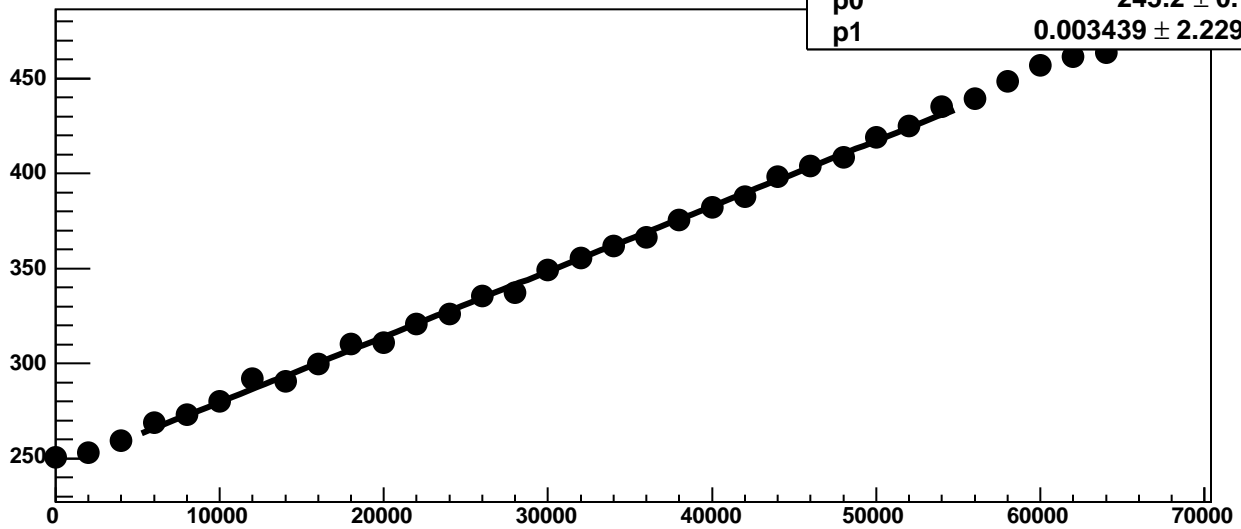
Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

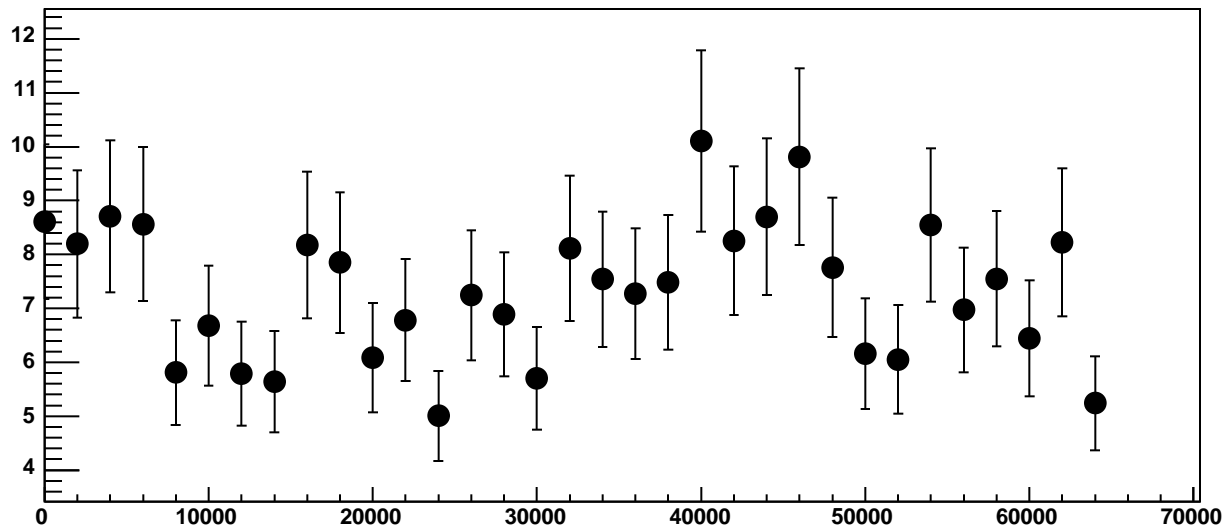


Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

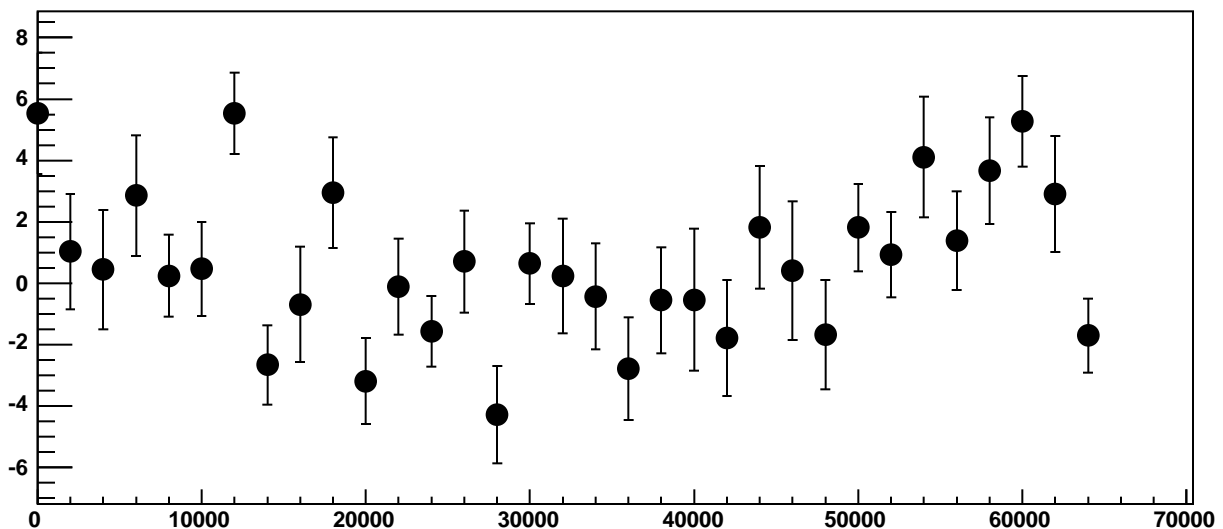


χ^2 / ndf 53.59 / 23
p0 245.2 ± 0.7046
p1 0.003439 ± 2.229e-05

Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

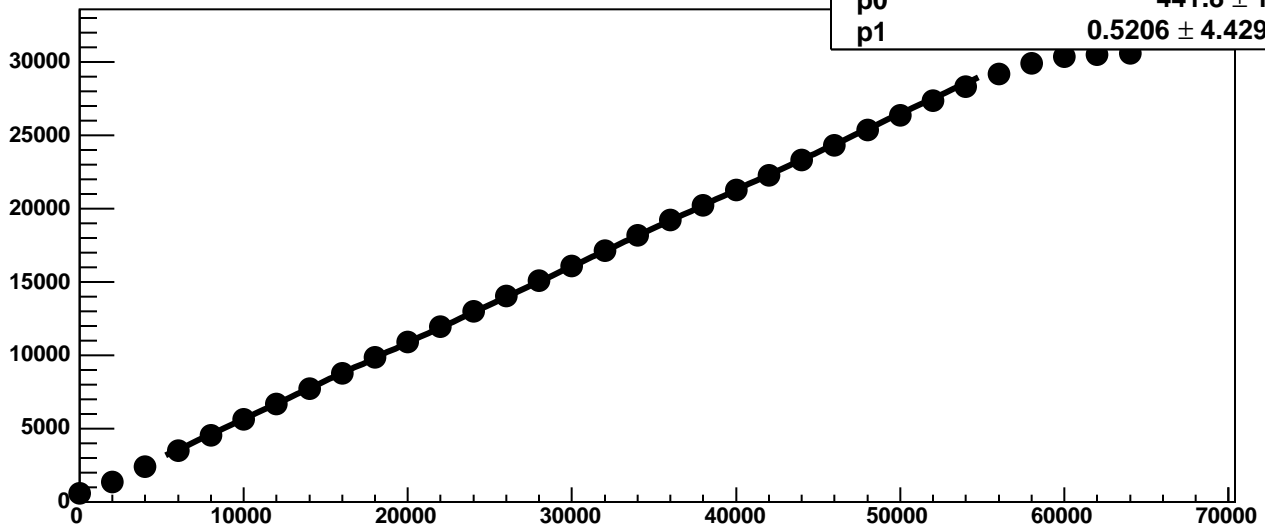


Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

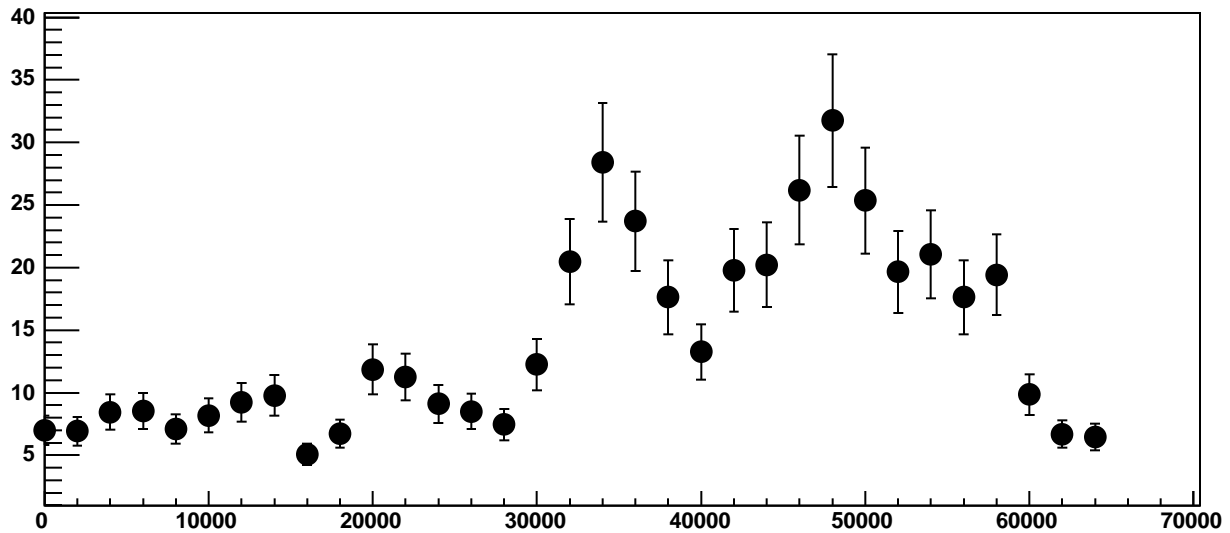


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

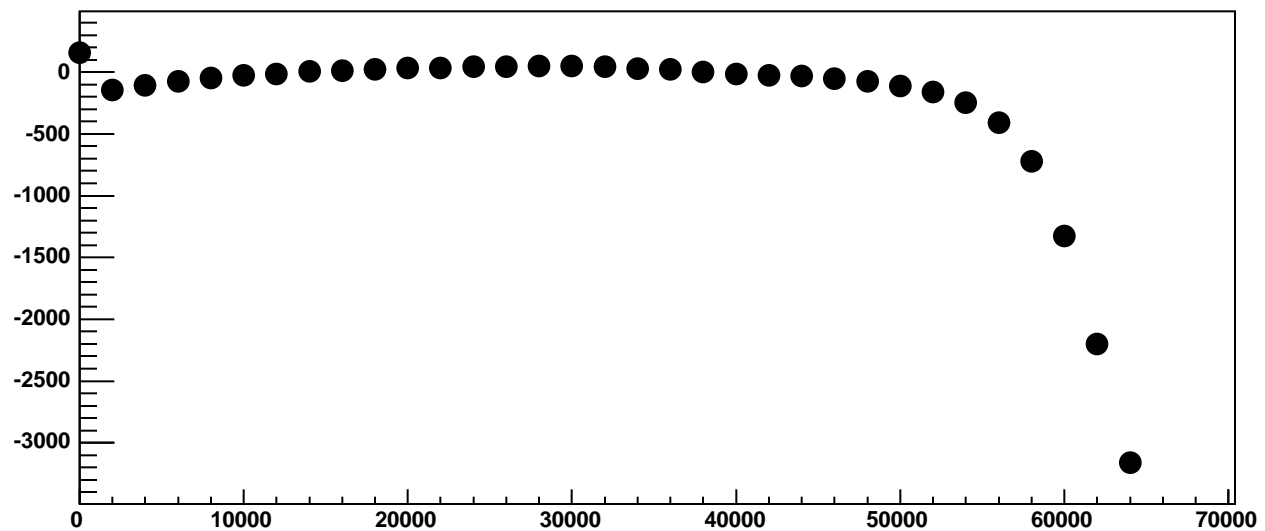
χ^2 / ndf 9920 / 23
p0 441.8 ± 1.019
p1 $0.5206 \pm 4.429\text{e-}05$



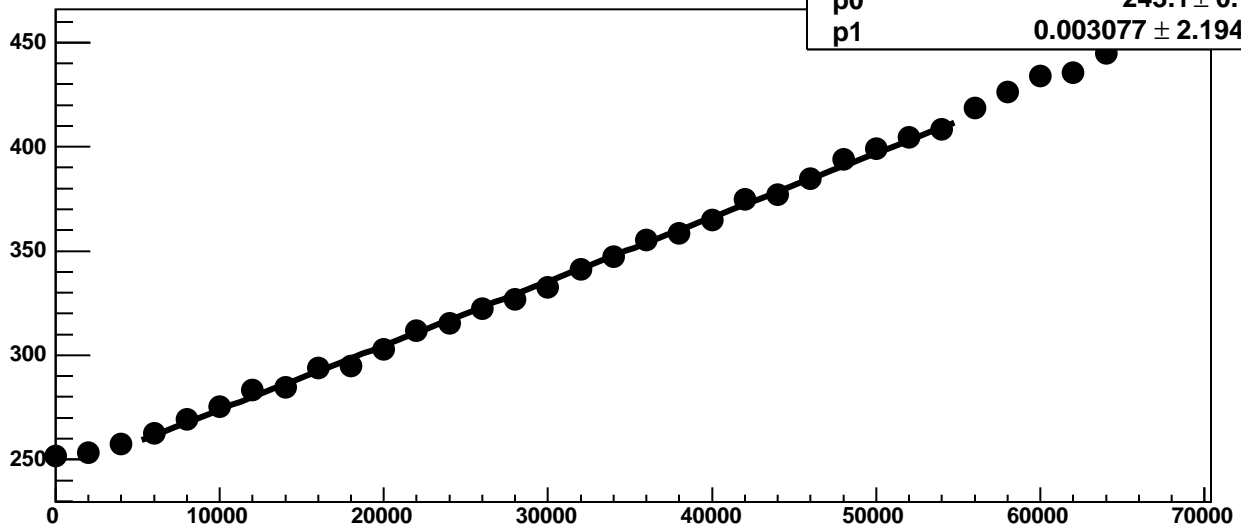
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

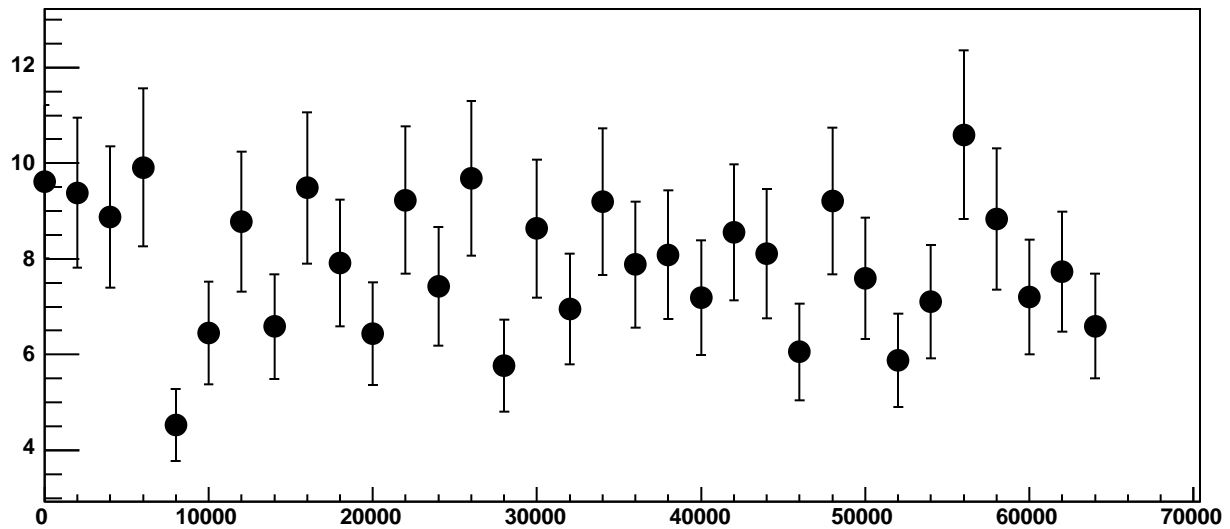


Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

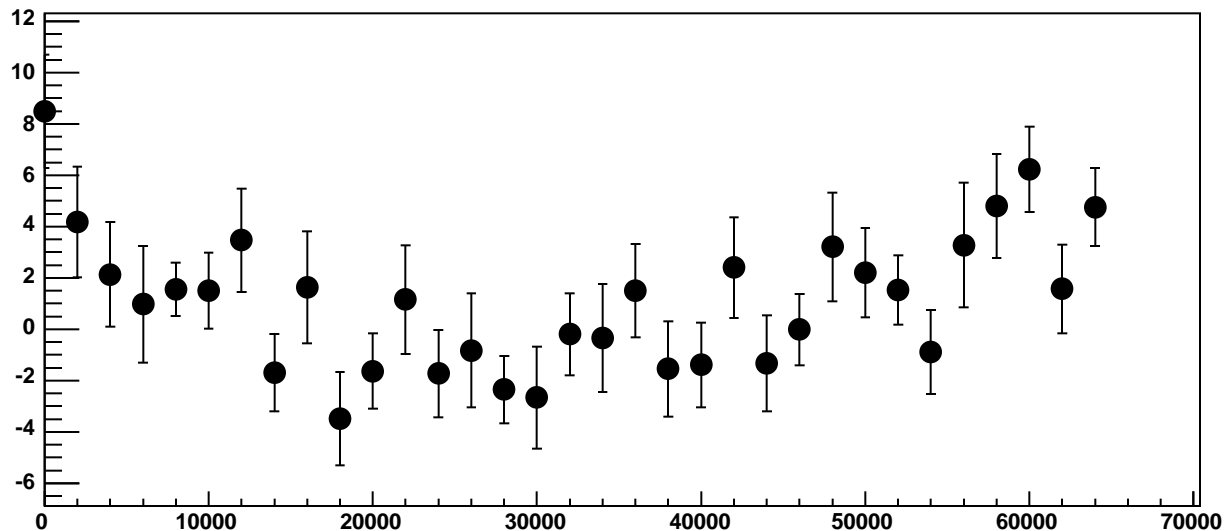


χ^2 / ndf 29.22 / 23
p0 243.1 ± 0.7253
p1 $0.003077 \pm 2.194e-05$

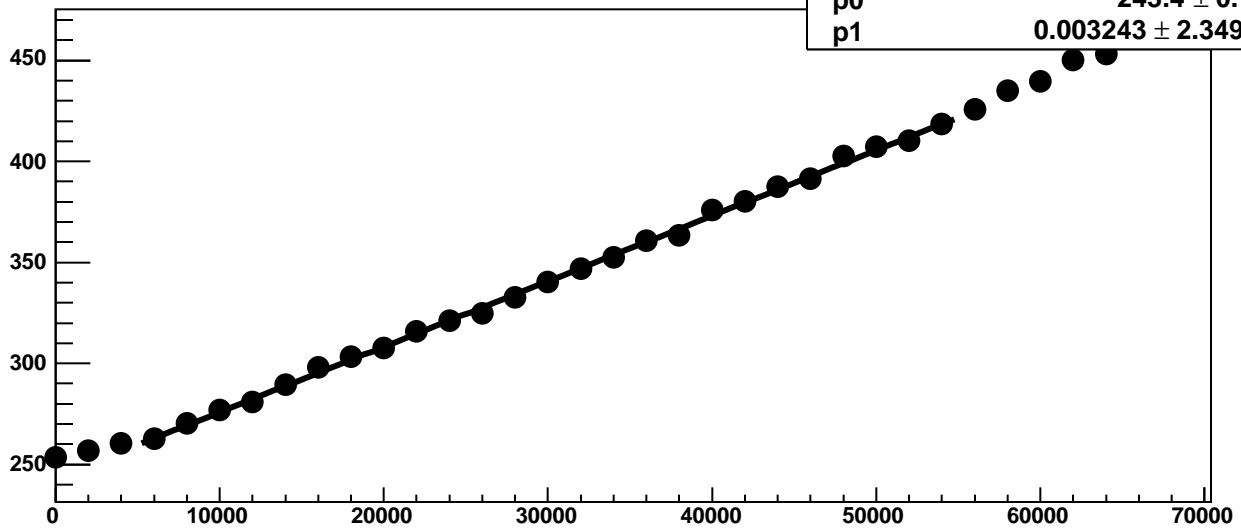
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

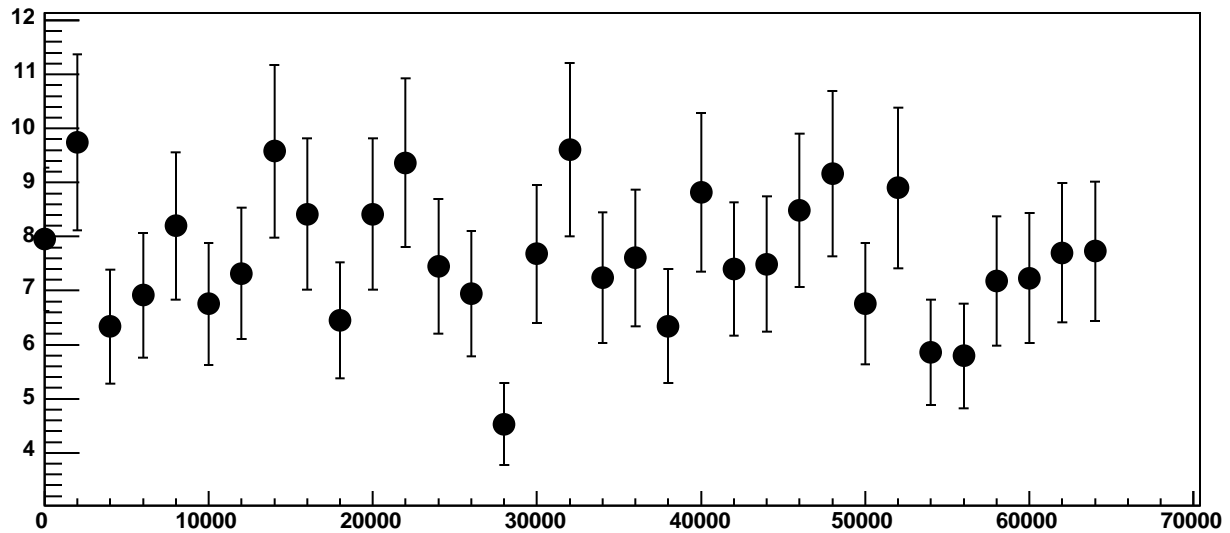


Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

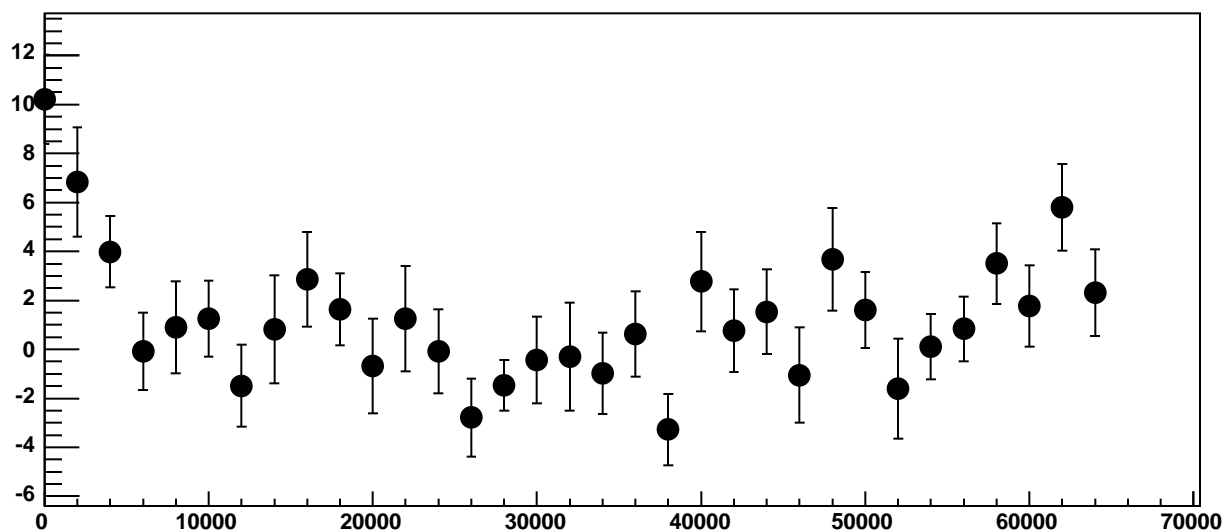


χ^2 / ndf 24.29 / 23
p0 243.4 ± 0.7837
p1 $0.003243 \pm 2.349\text{e-}05$

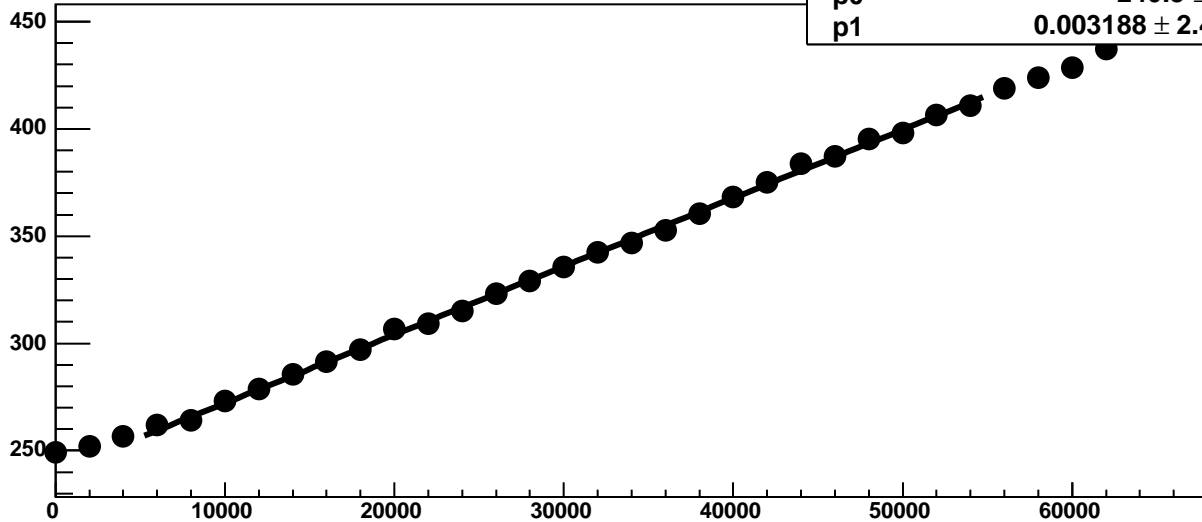
Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

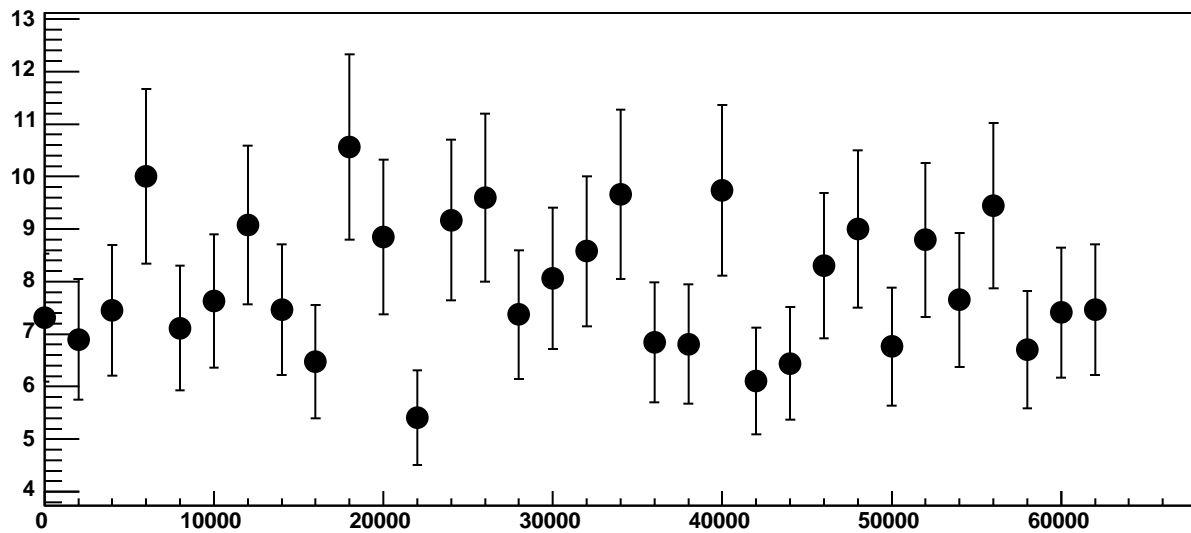


Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

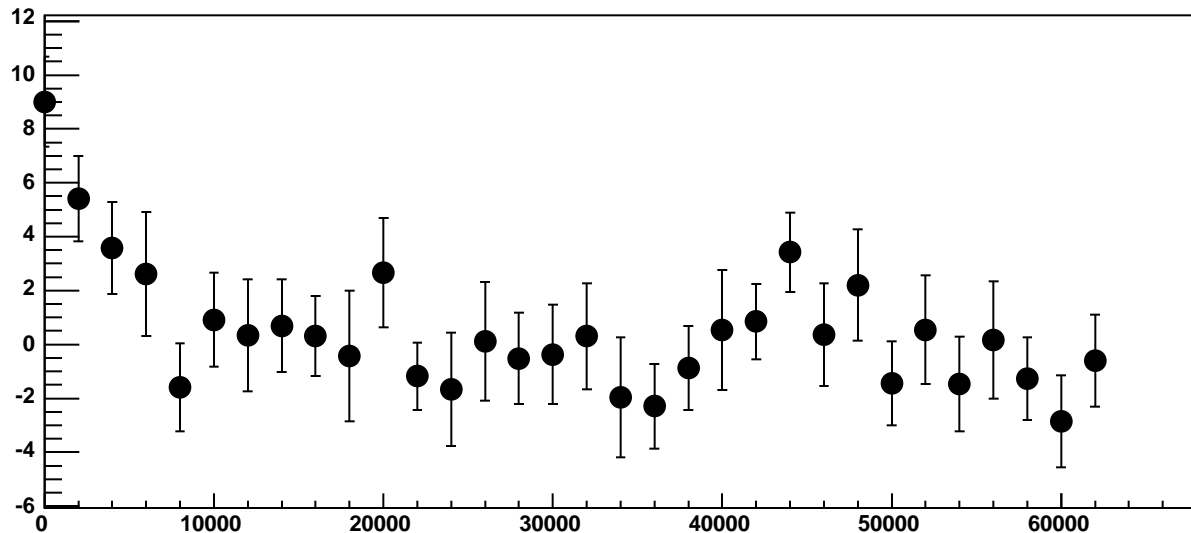


χ^2 / ndf 18.01 / 23
p0 240.3 ± 0.8358
p1 $0.003188 \pm 2.484e-05$

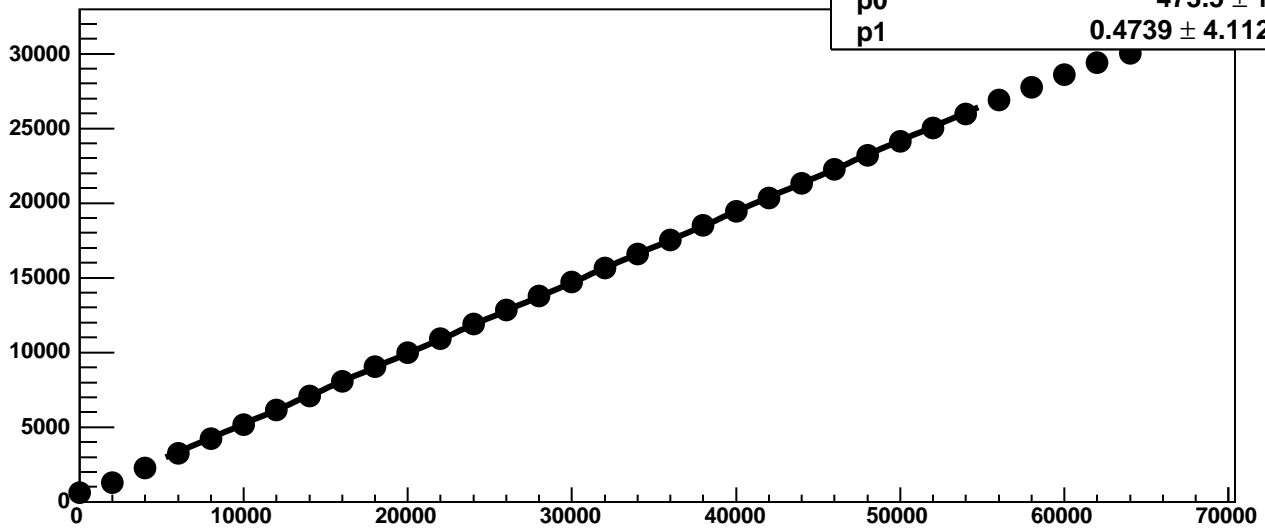
Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



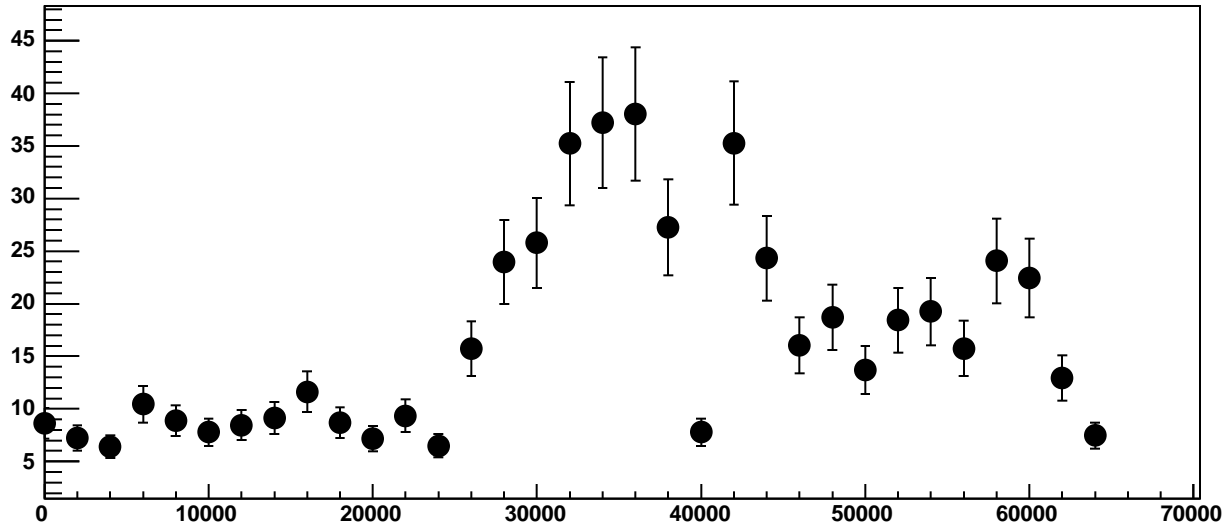
Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



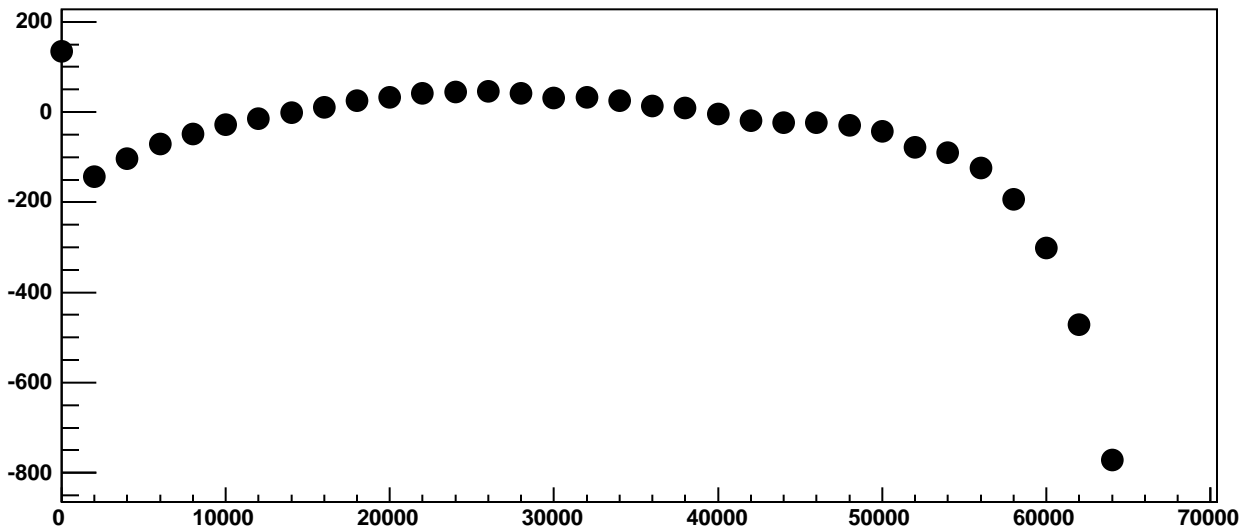
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



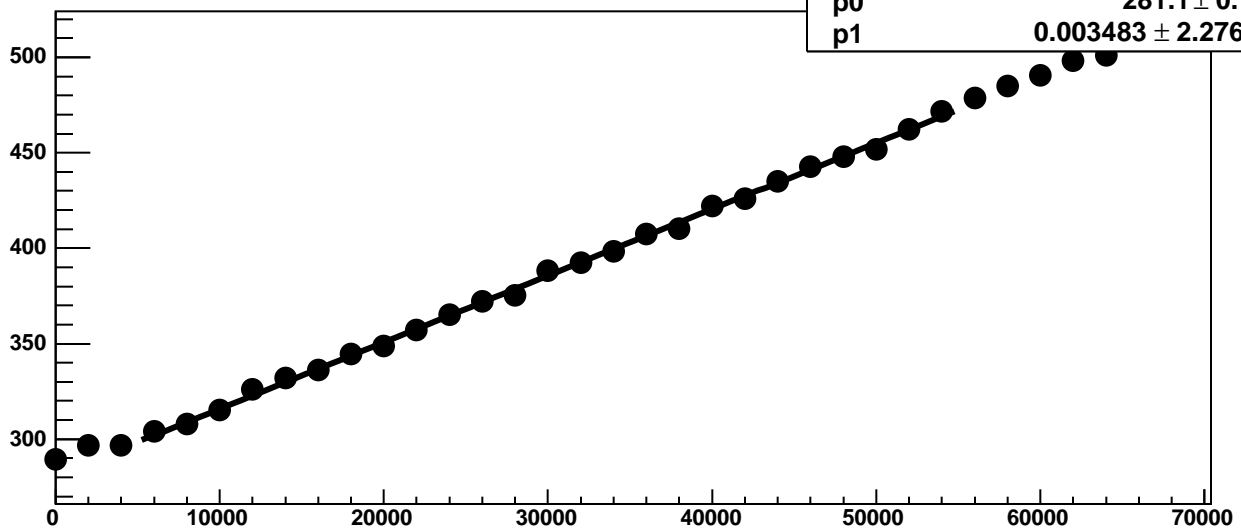
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

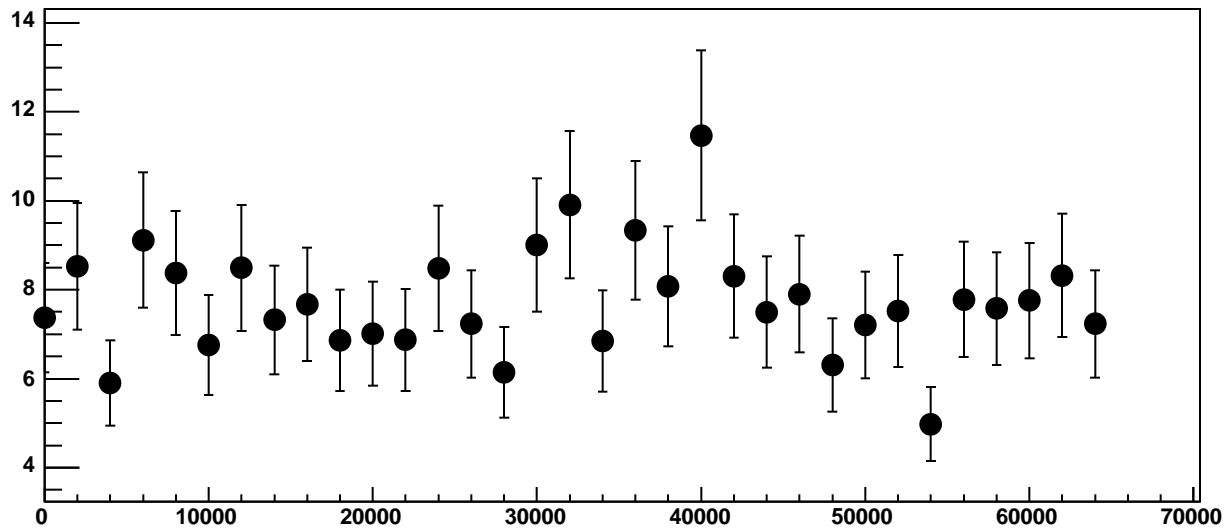


Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

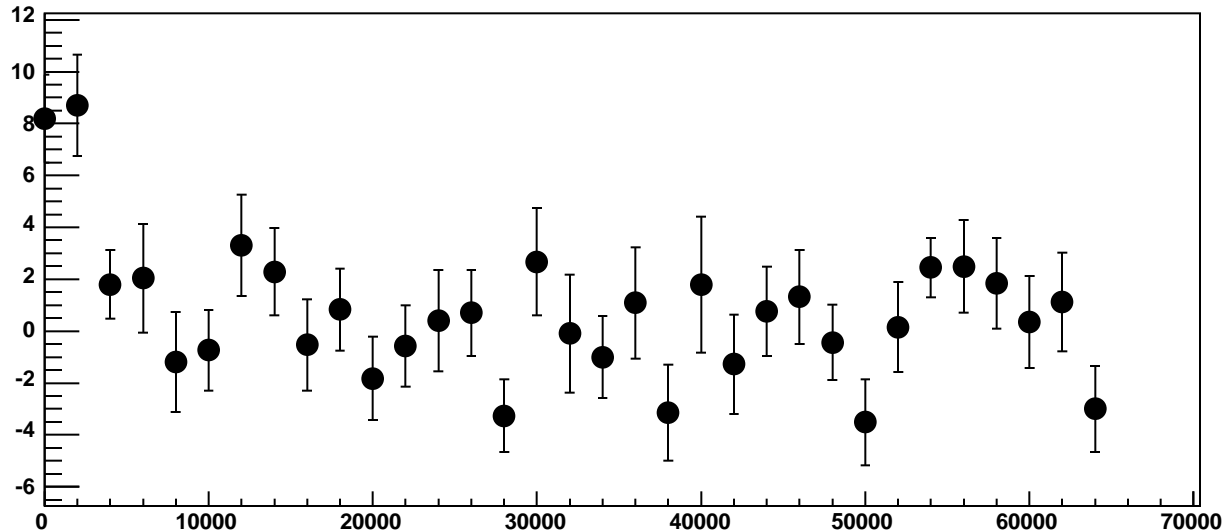


χ^2 / ndf 29.7 / 23
p0 281.1 ± 0.7902
p1 $0.003483 \pm 2.276e-05$

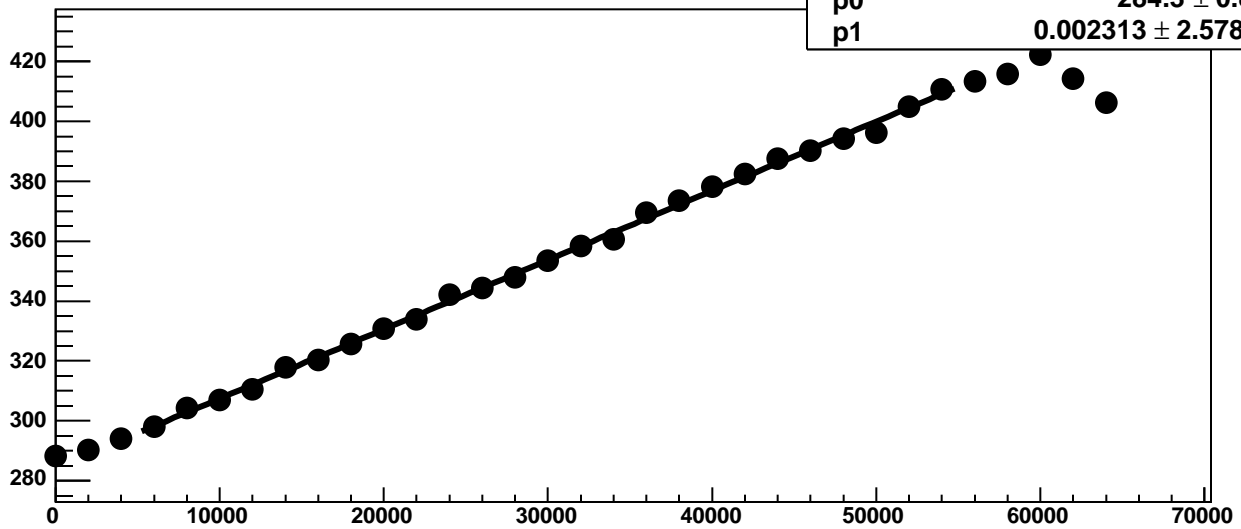
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



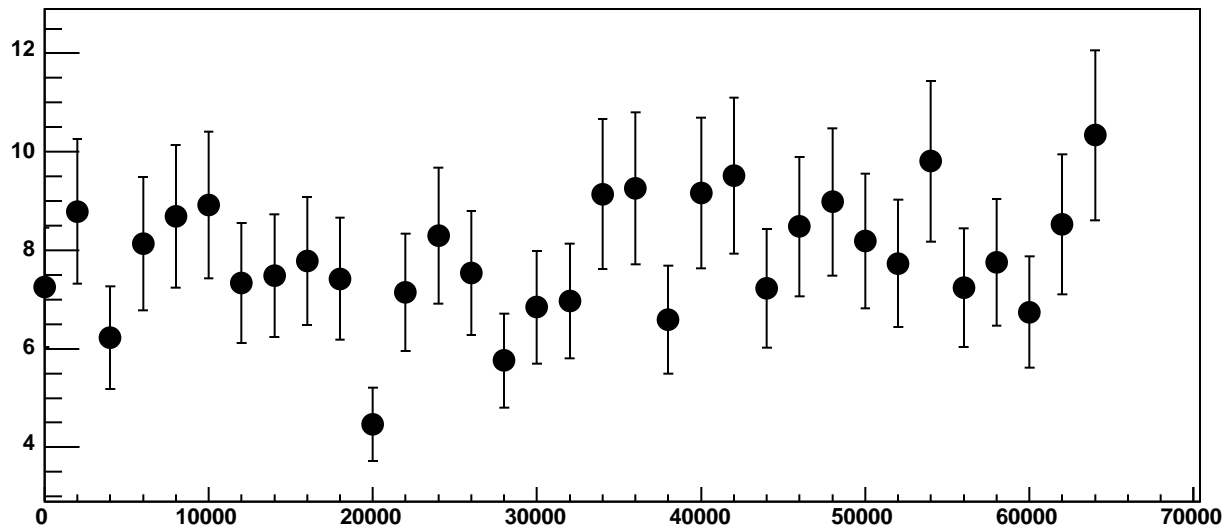
Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



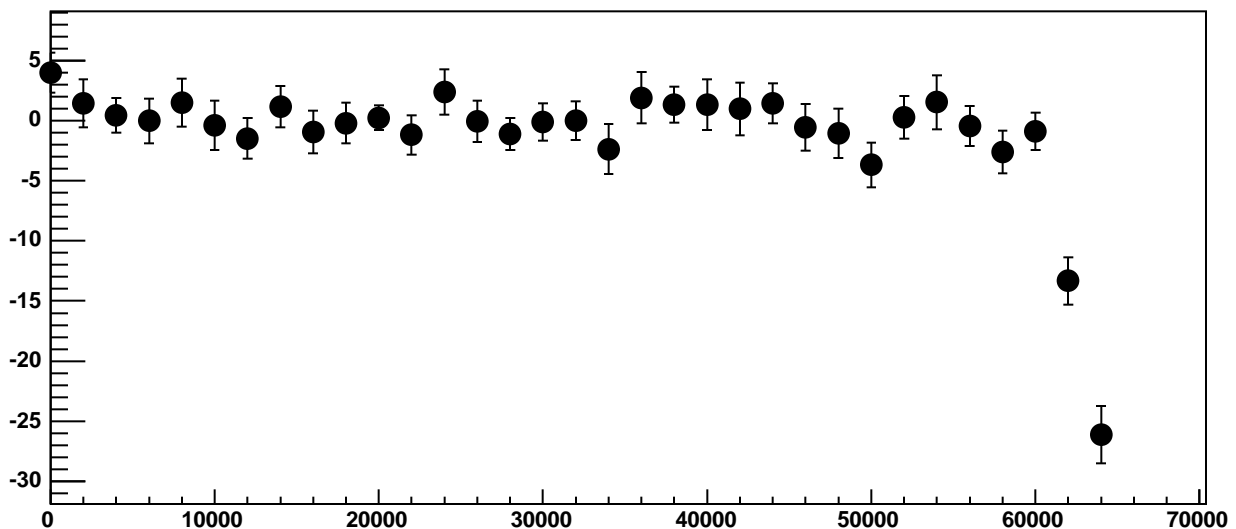
Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



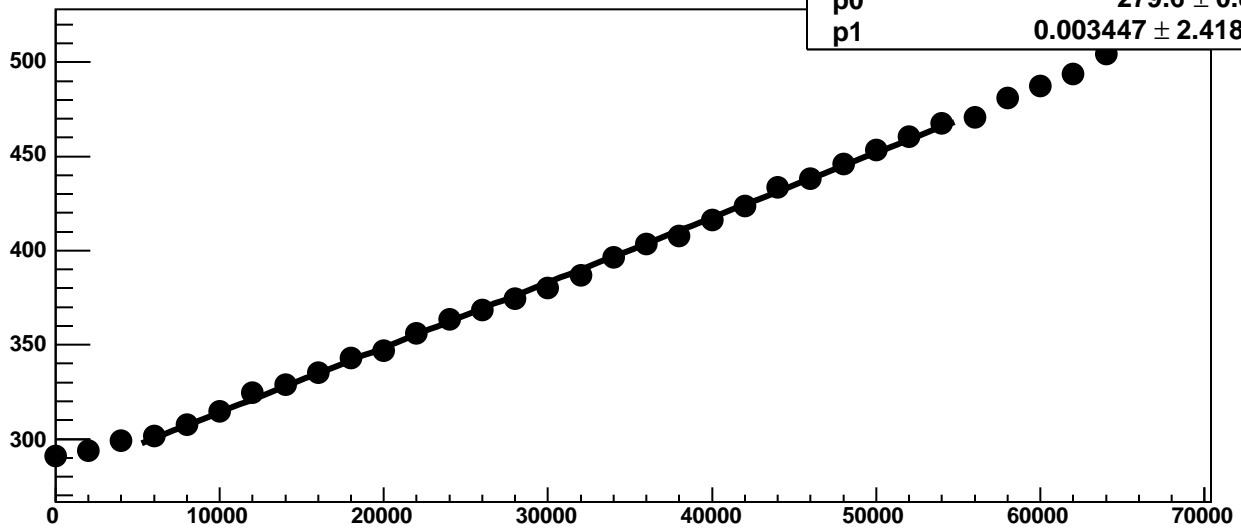
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

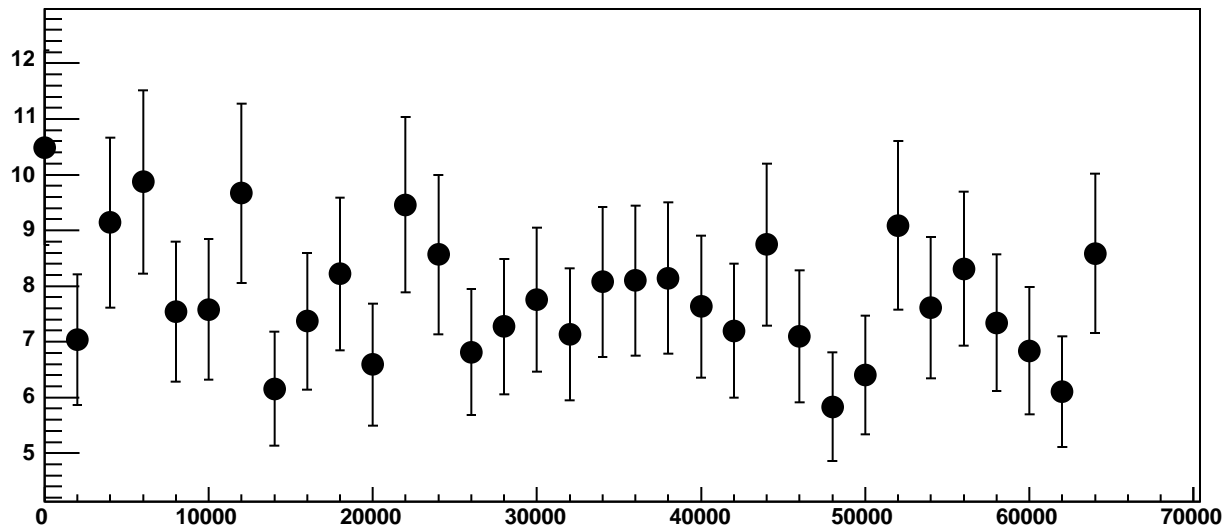


Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

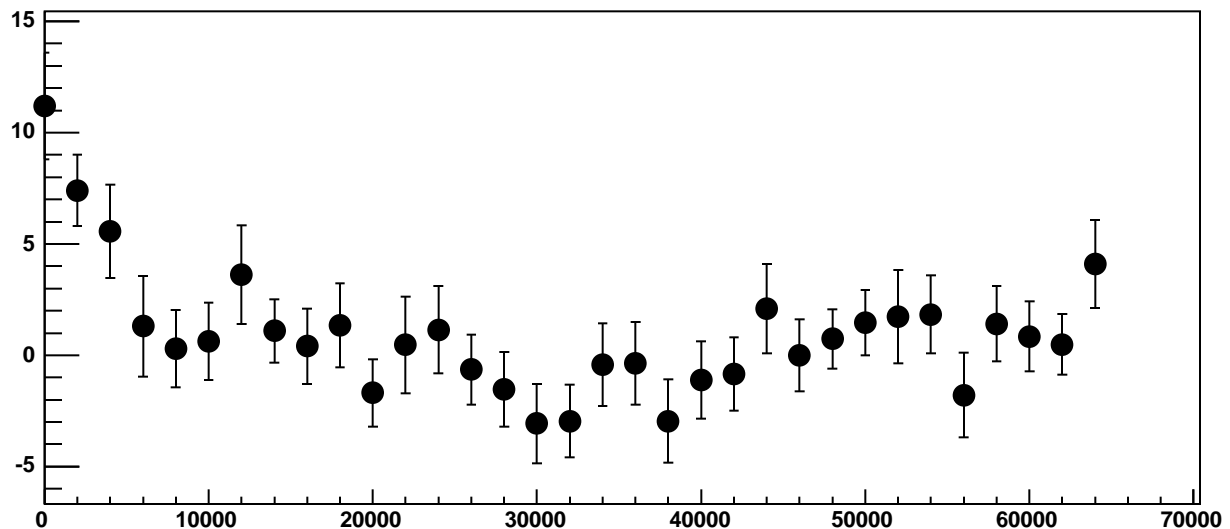


χ^2 / ndf 20.66 / 23
p0 279.6 ± 0.8217
p1 $0.003447 \pm 2.418e-05$

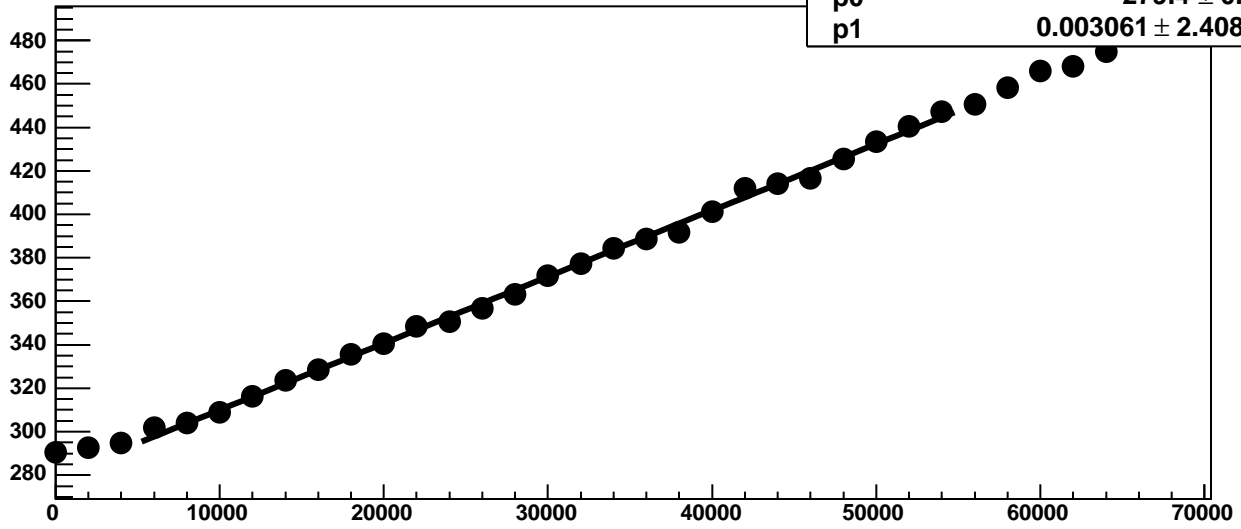
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.93 / 23

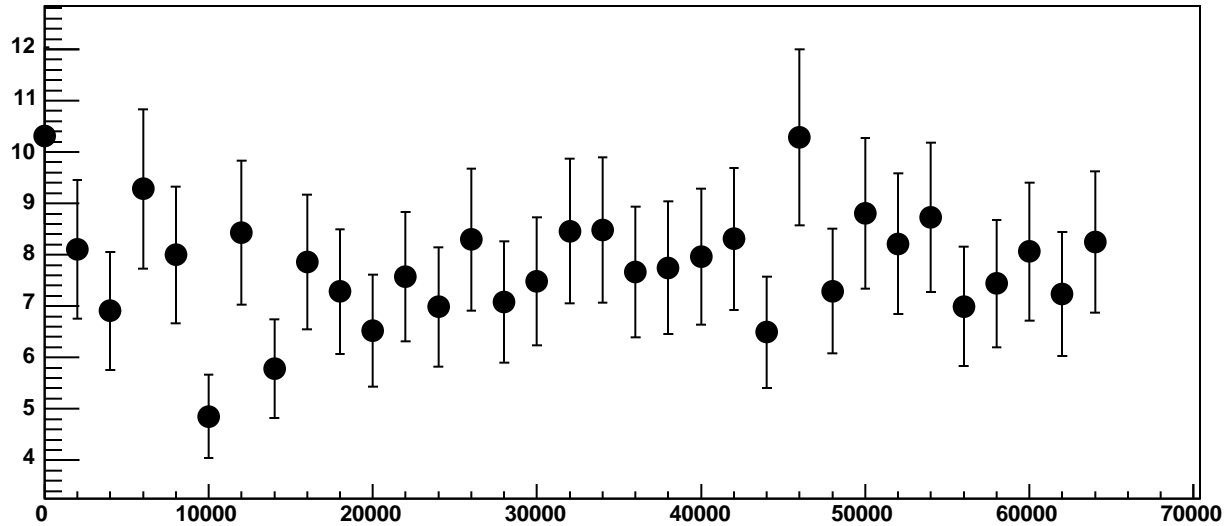
p0

279.4 ± 0.7571

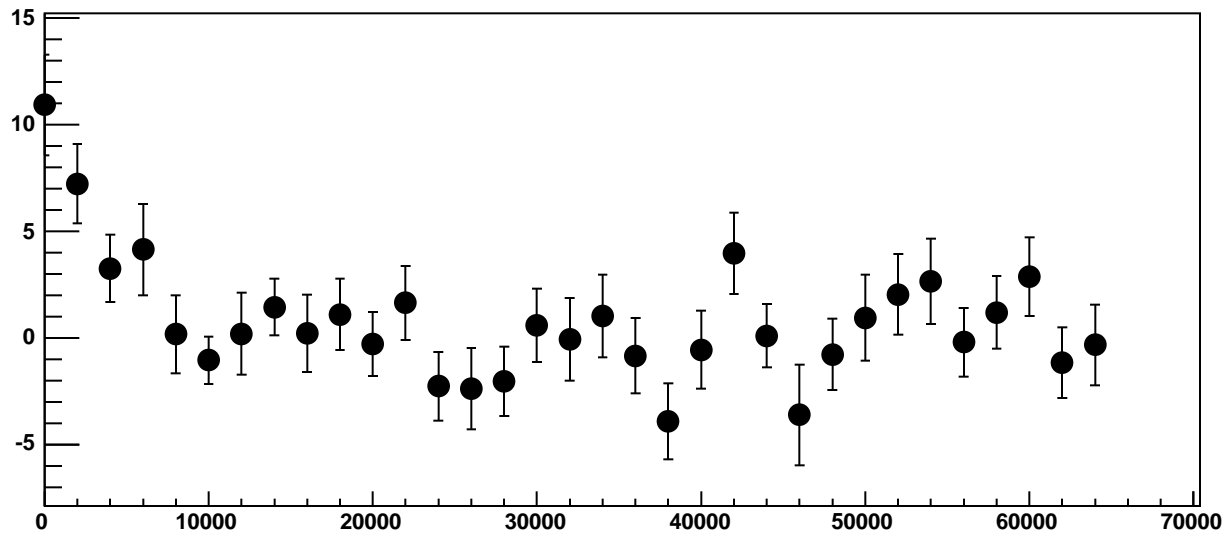
p1

$0.003061 \pm 2.408e-05$

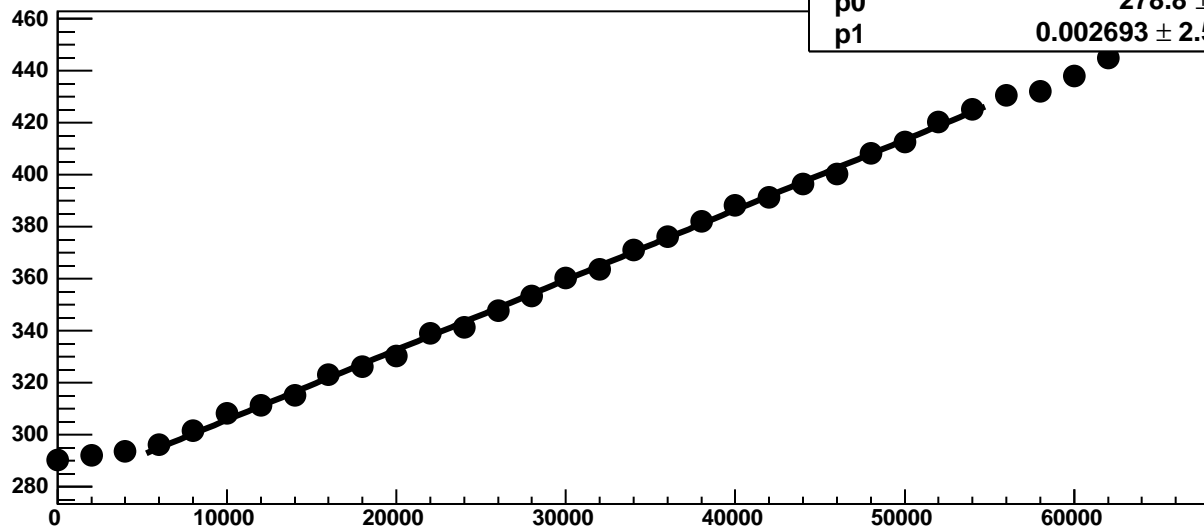
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

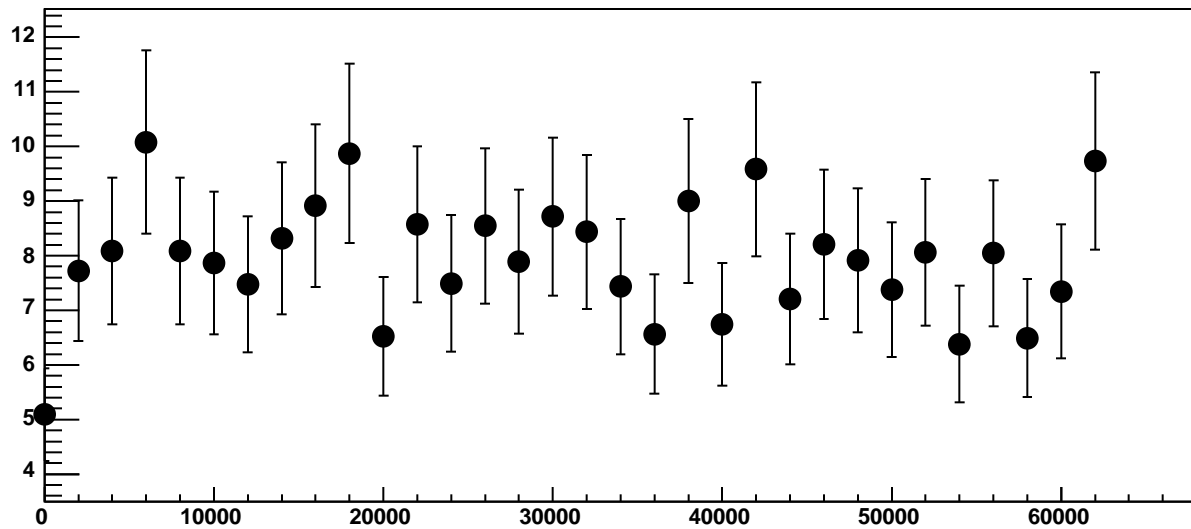


Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

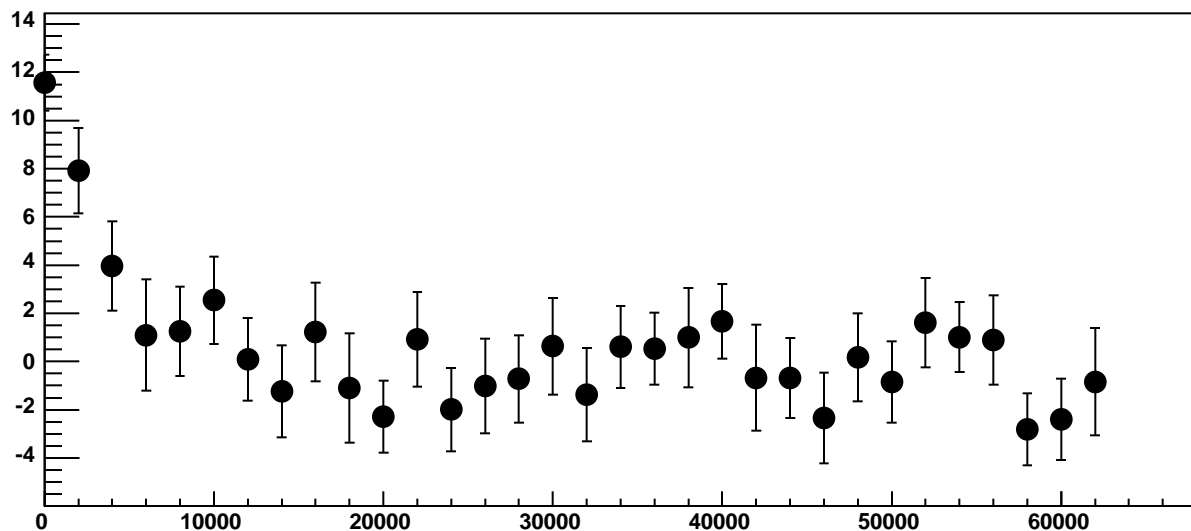


χ^2 / ndf 13.53 / 23
p0 278.8 ± 0.8613
p1 $0.002693 \pm 2.504e-05$

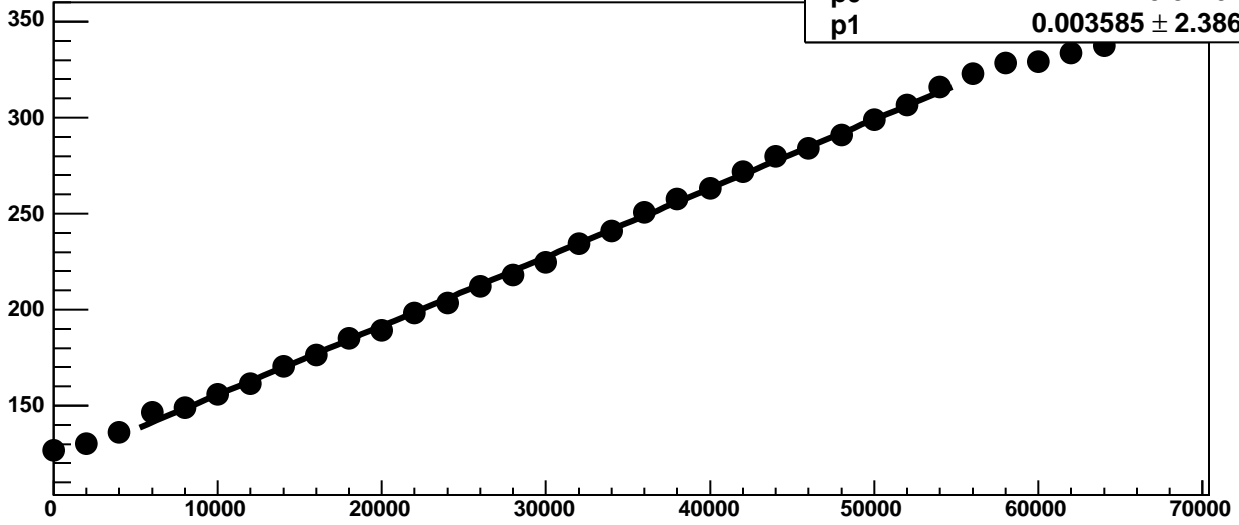
Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

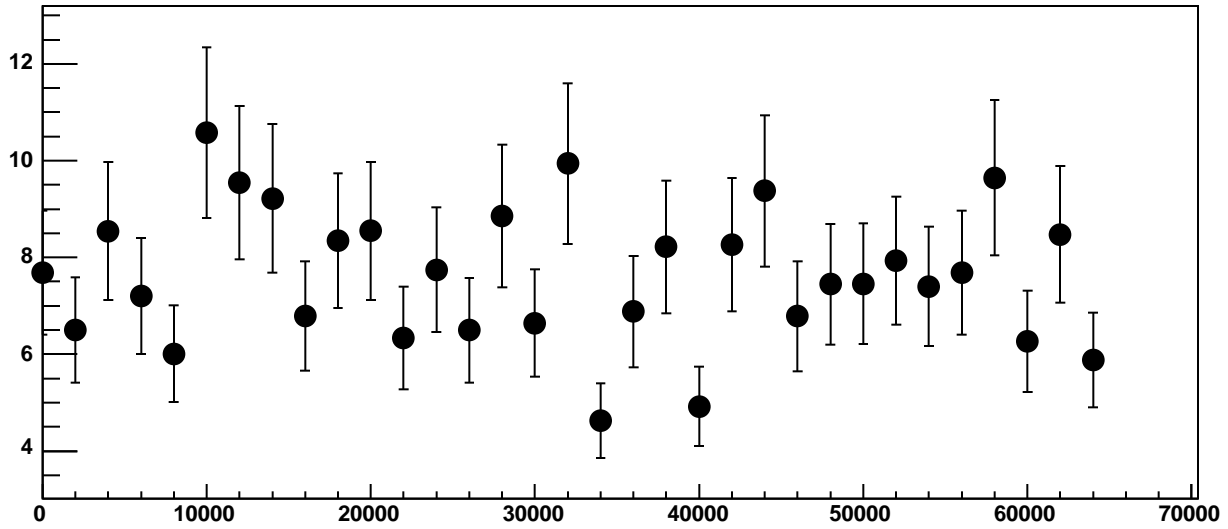


Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

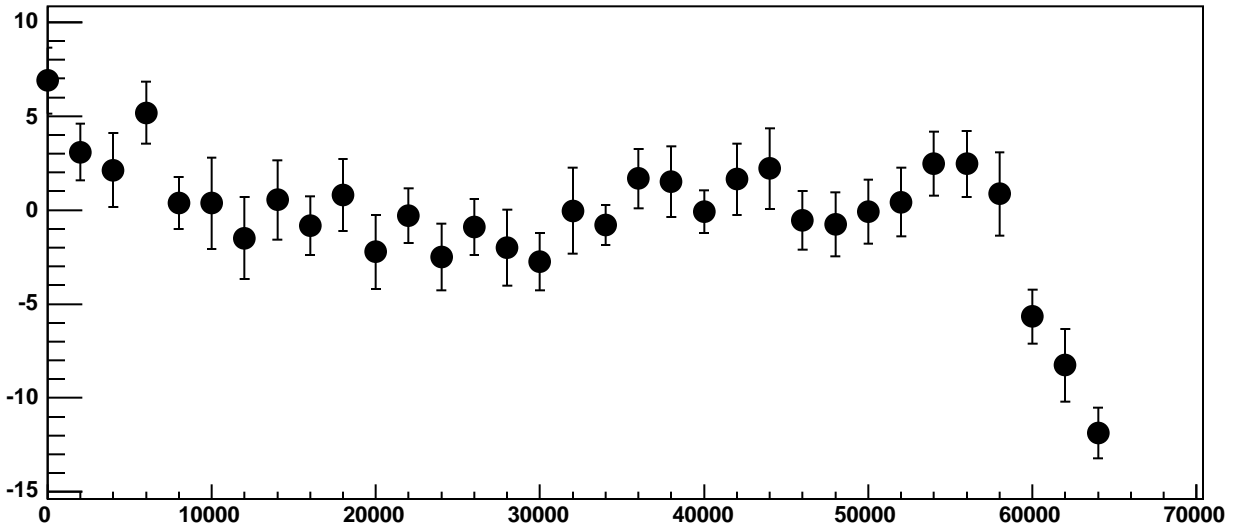


χ^2 / ndf 25.43 / 23
p0 119.8 ± 0.8045
p1 0.003585 ± 2.386e-05

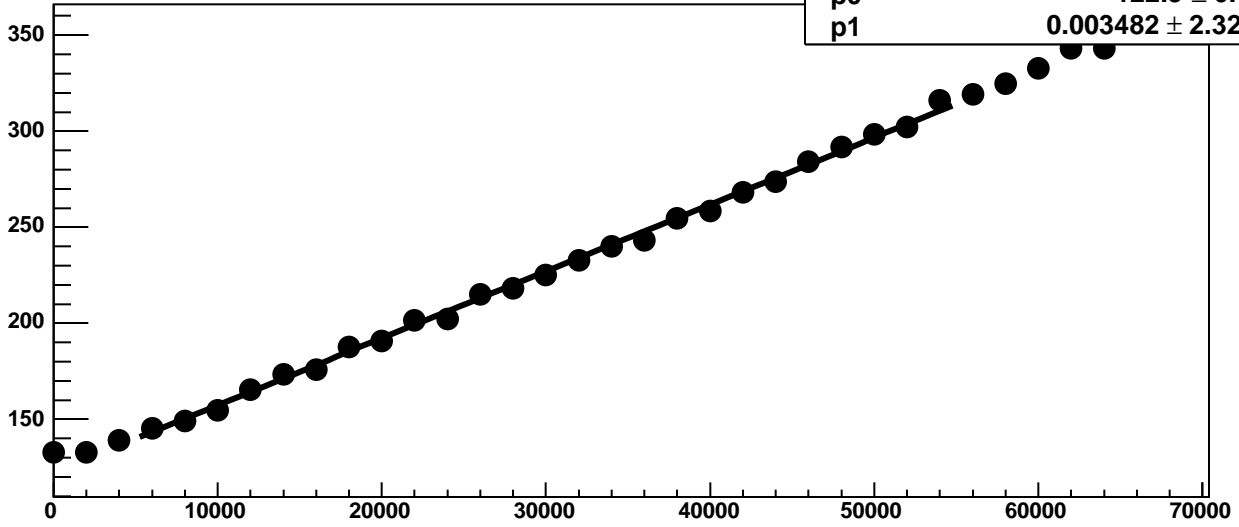
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



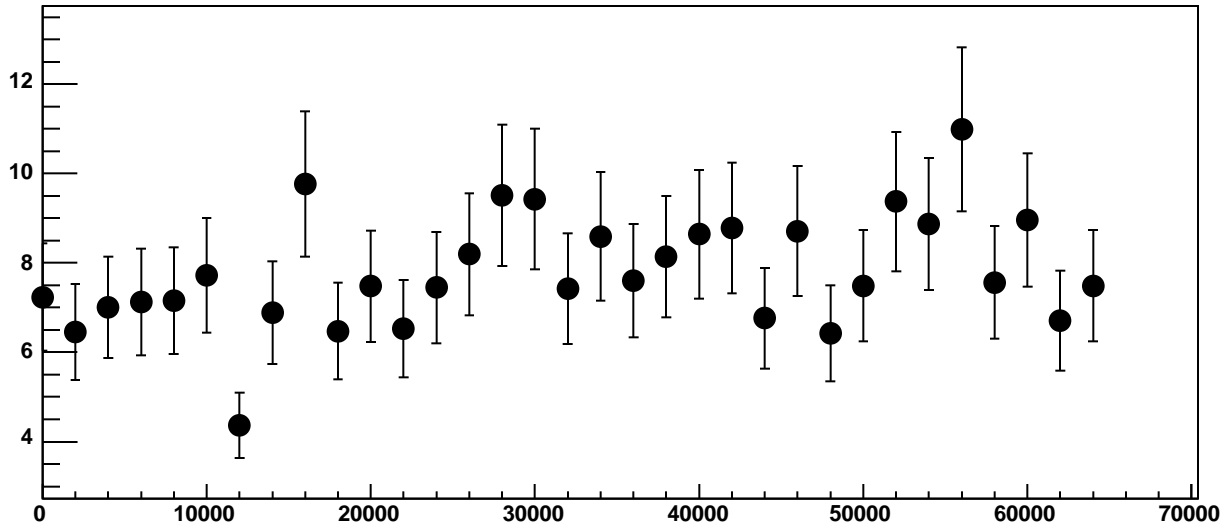
Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



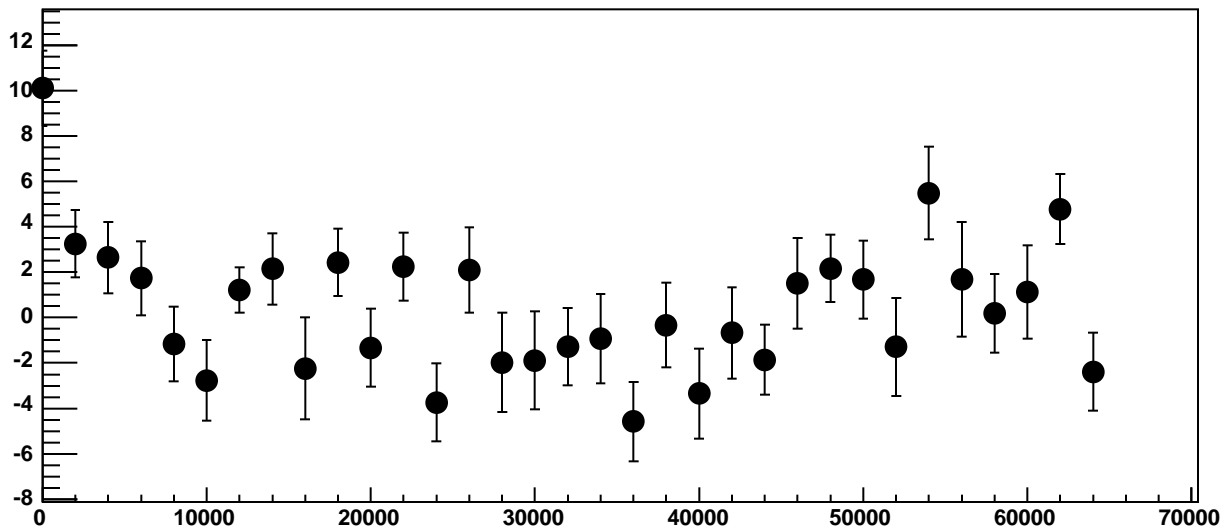
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



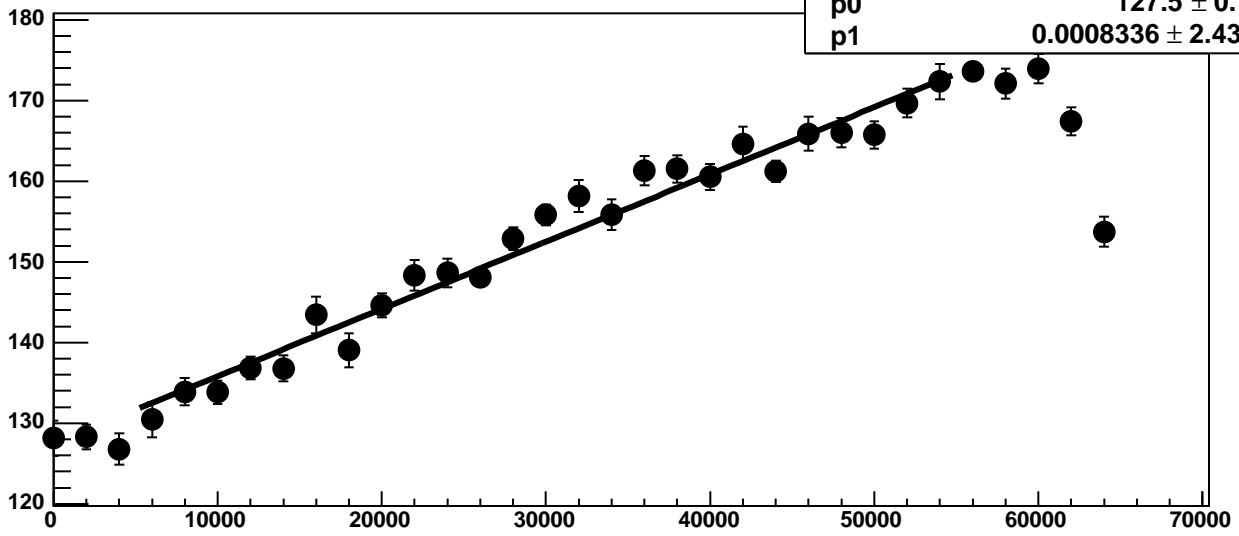
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

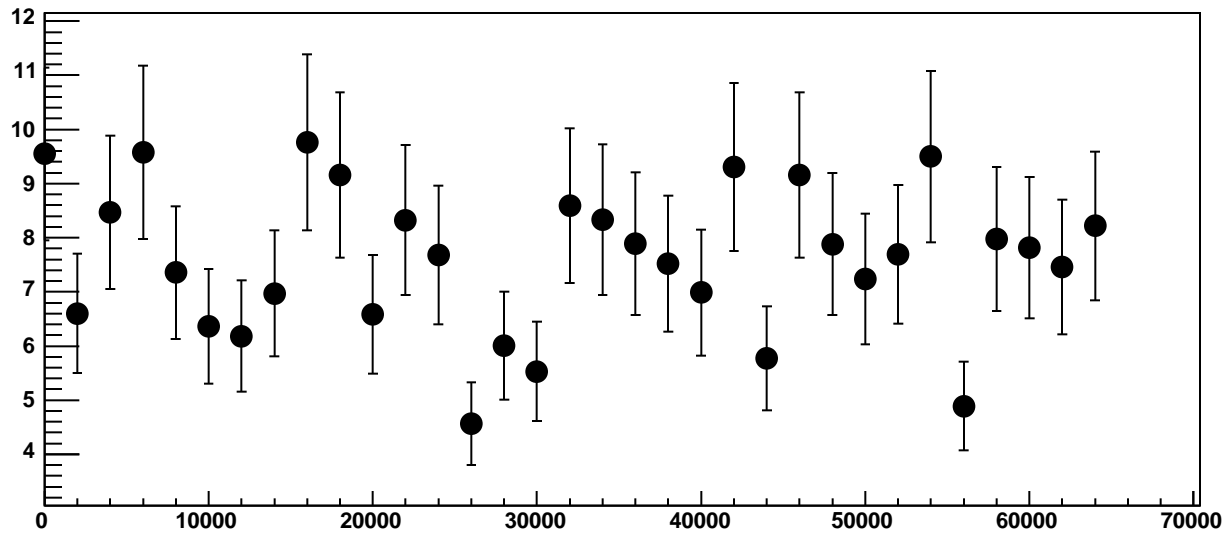


Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

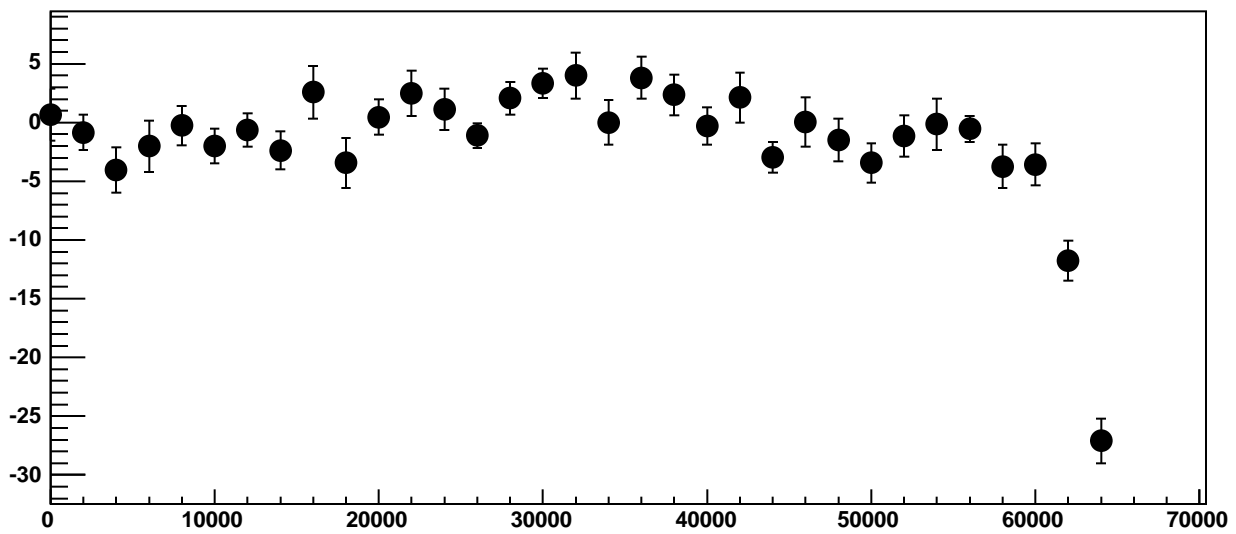


χ^2 / ndf 43.52 / 23
p0 127.5 ± 0.7835
p1 $0.0008336 \pm 2.43e-05$

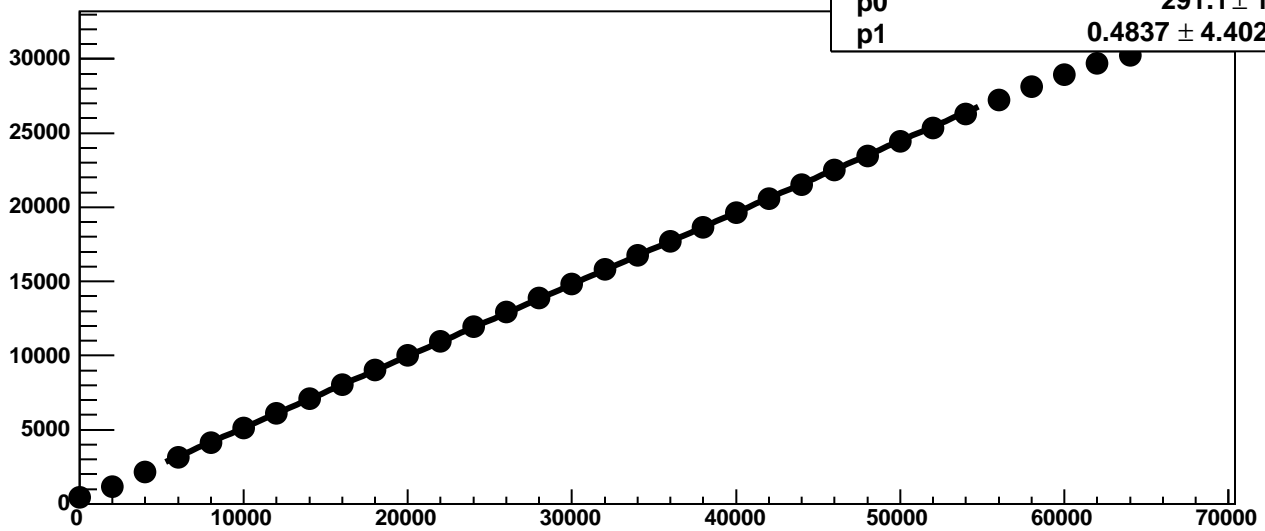
Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



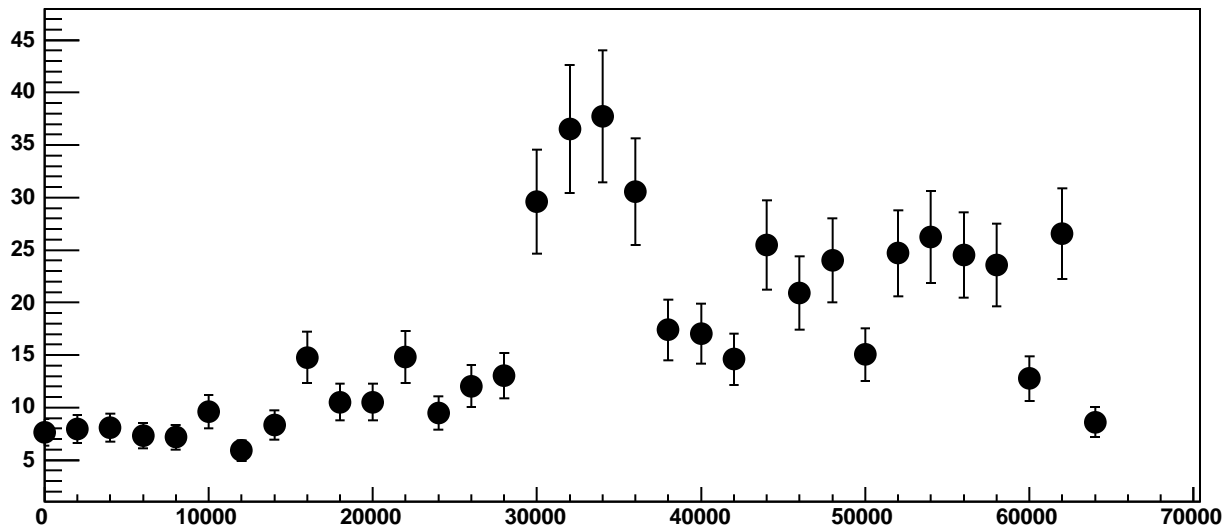
Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



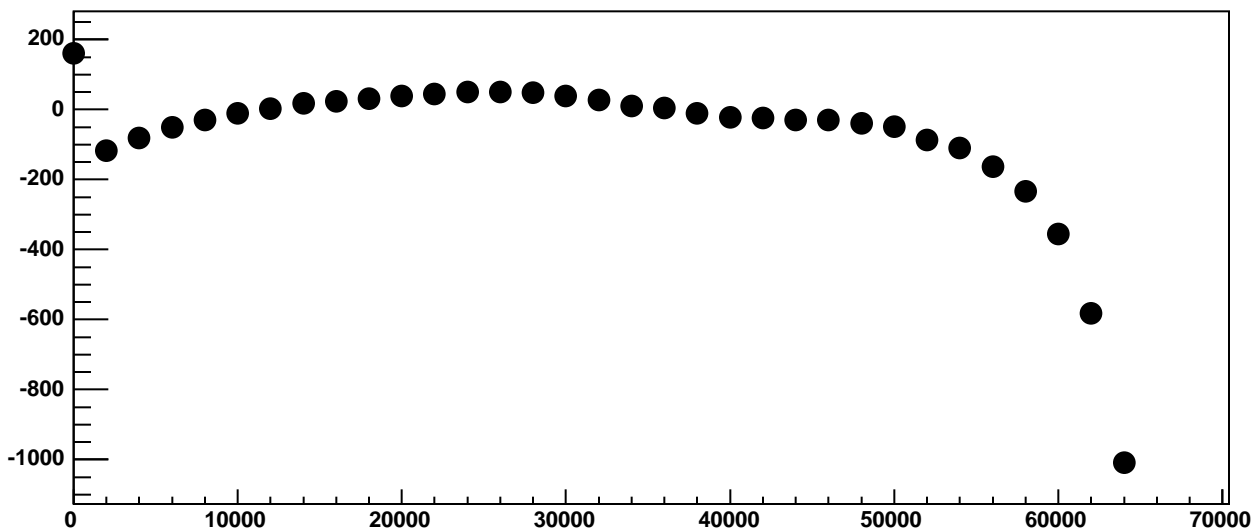
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



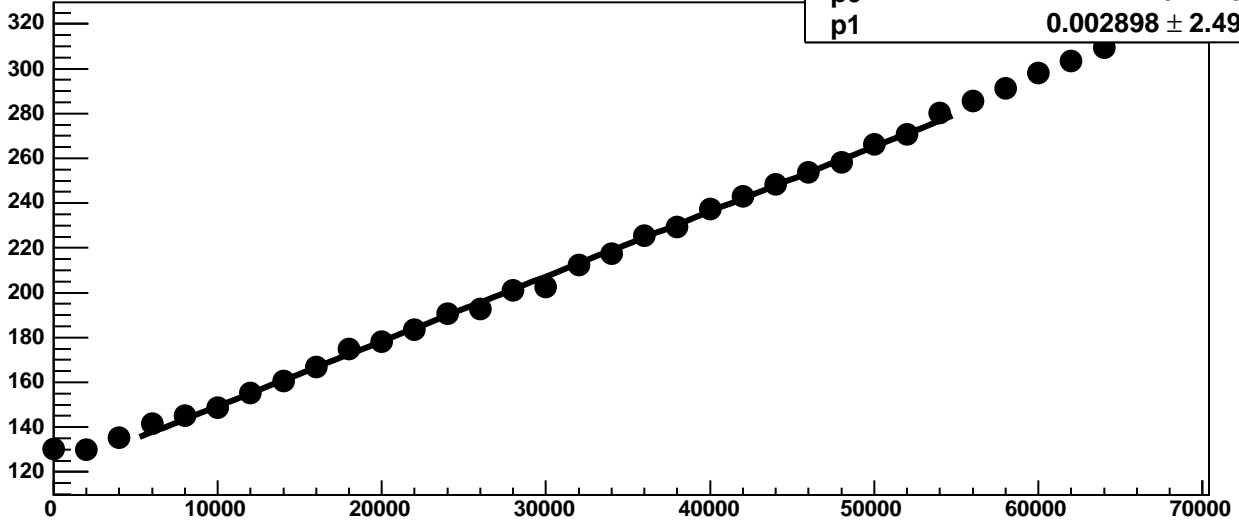
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.27 / 23

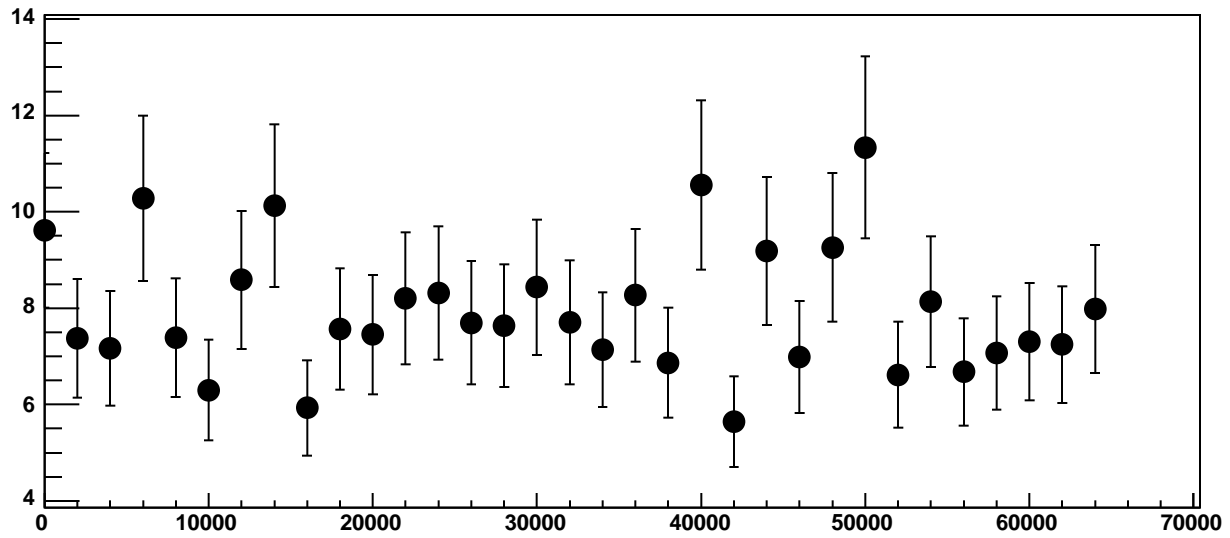
p0

120.4 ± 0.823

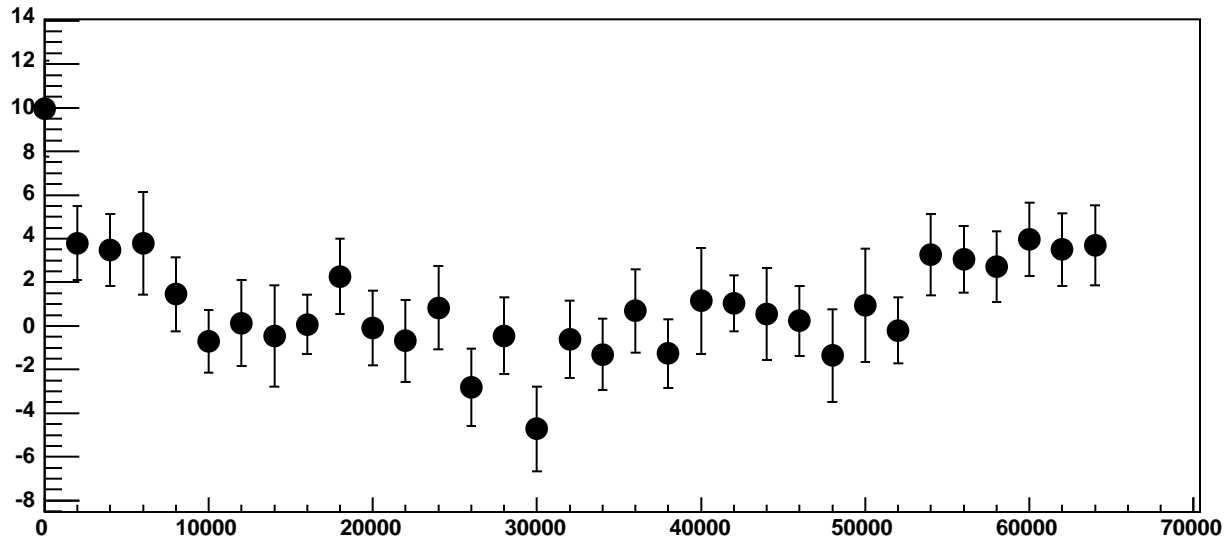
p1

$0.002898 \pm 2.49\text{e-}05$

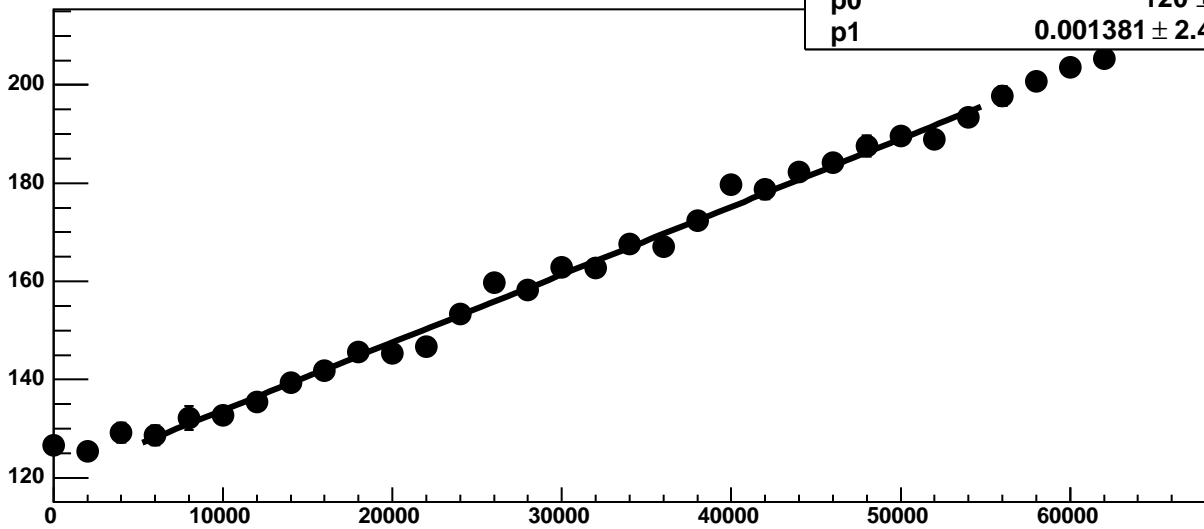
Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

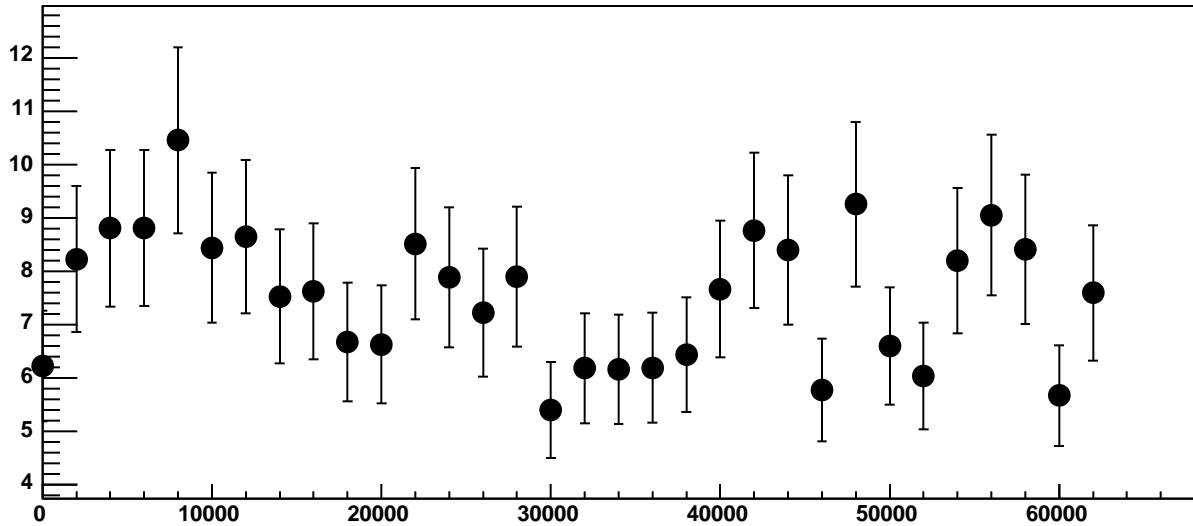


Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

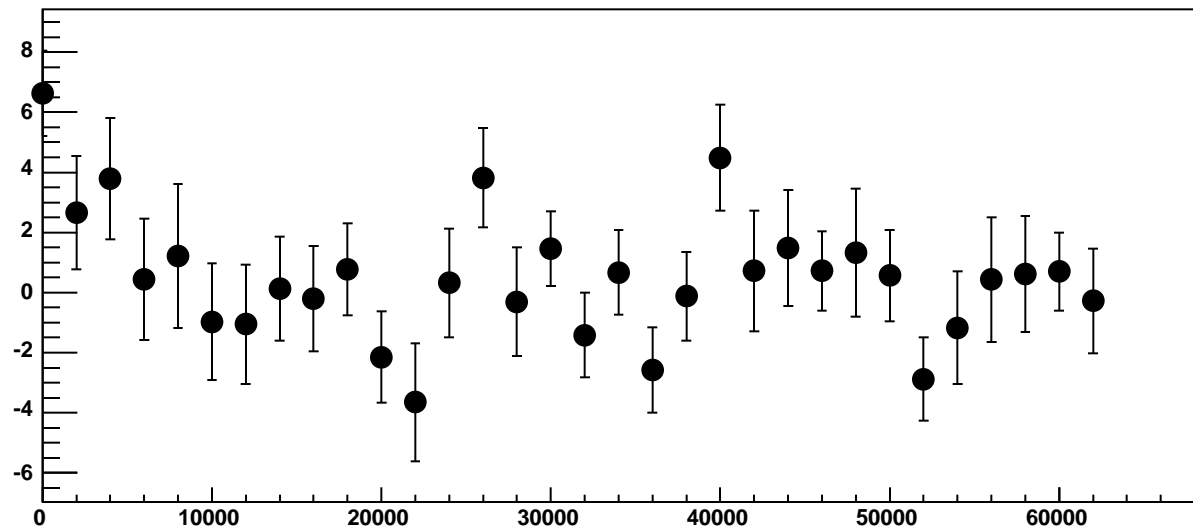


χ^2 / ndf 30.62 / 23
p0 120 ± 0.8421
p1 $0.001381 \pm 2.451e-05$

Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

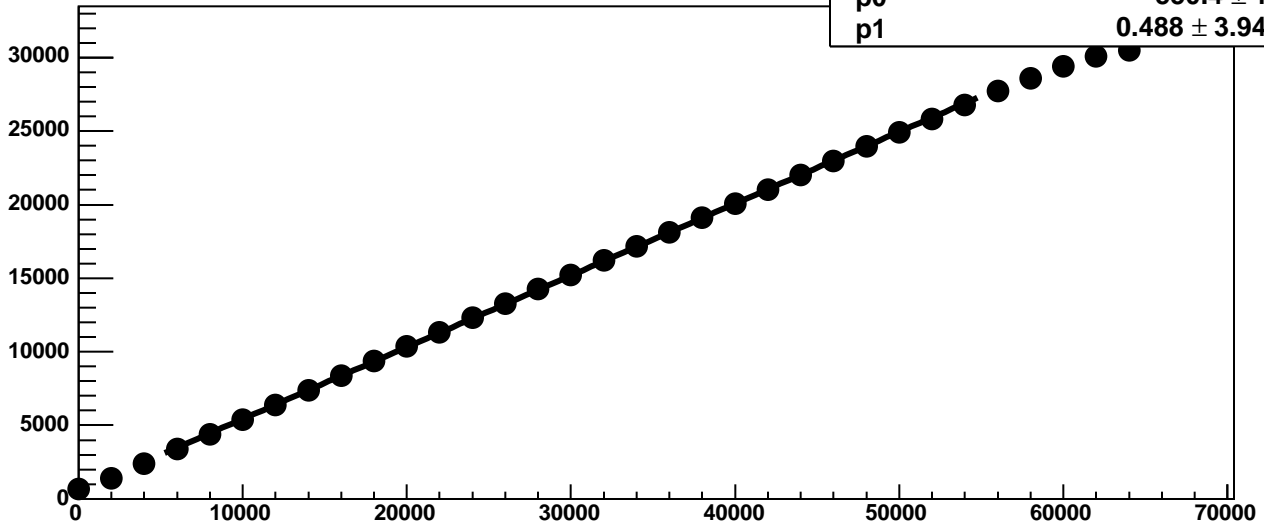


Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

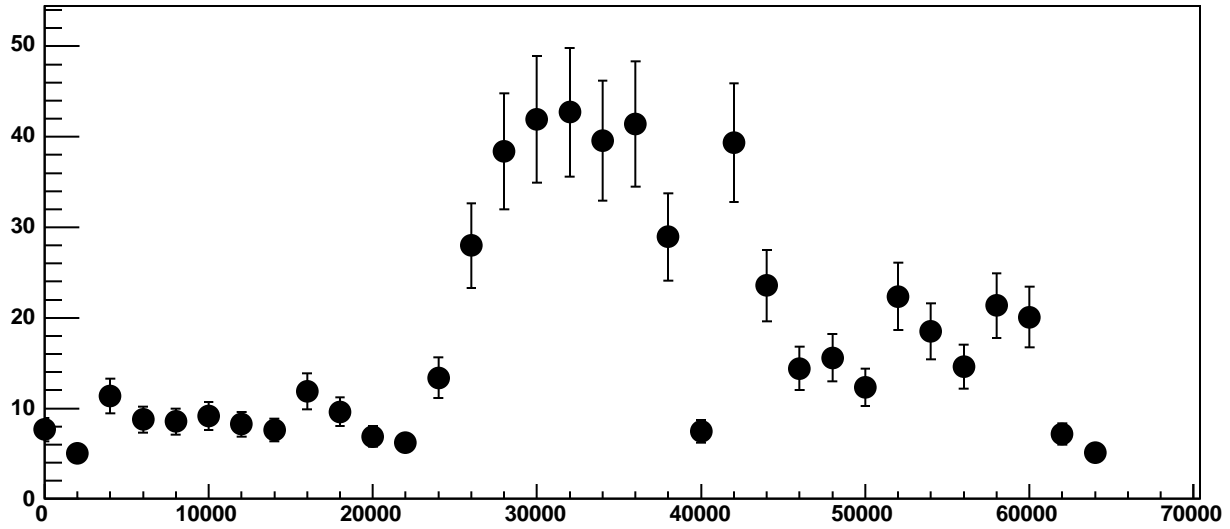


Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

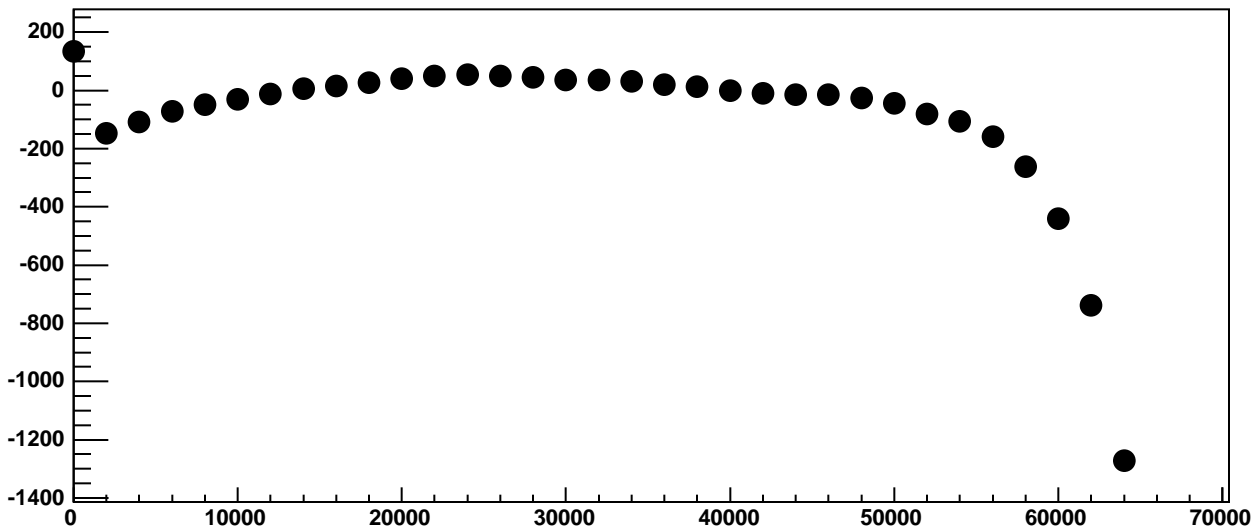
χ^2 / ndf 5840 / 23
p0 550.4 ± 1.025
p1 $0.488 \pm 3.94e-05$



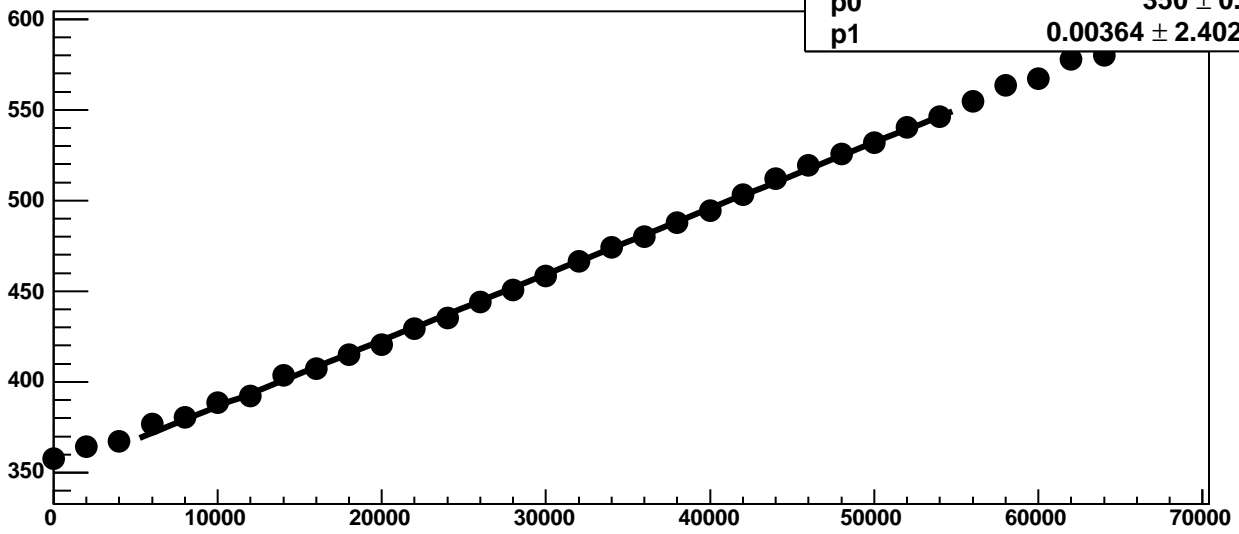
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

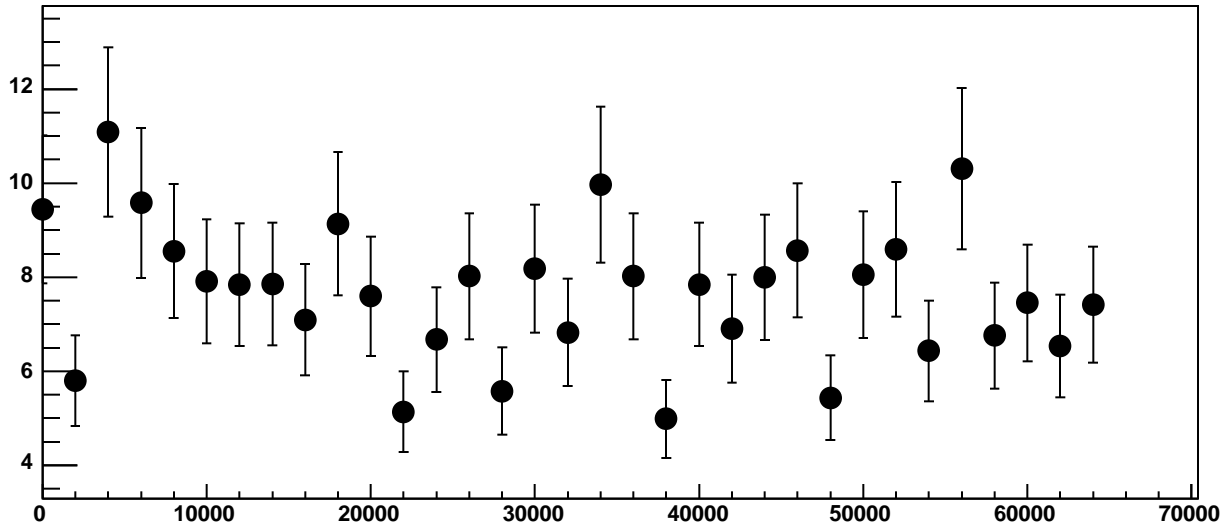


Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

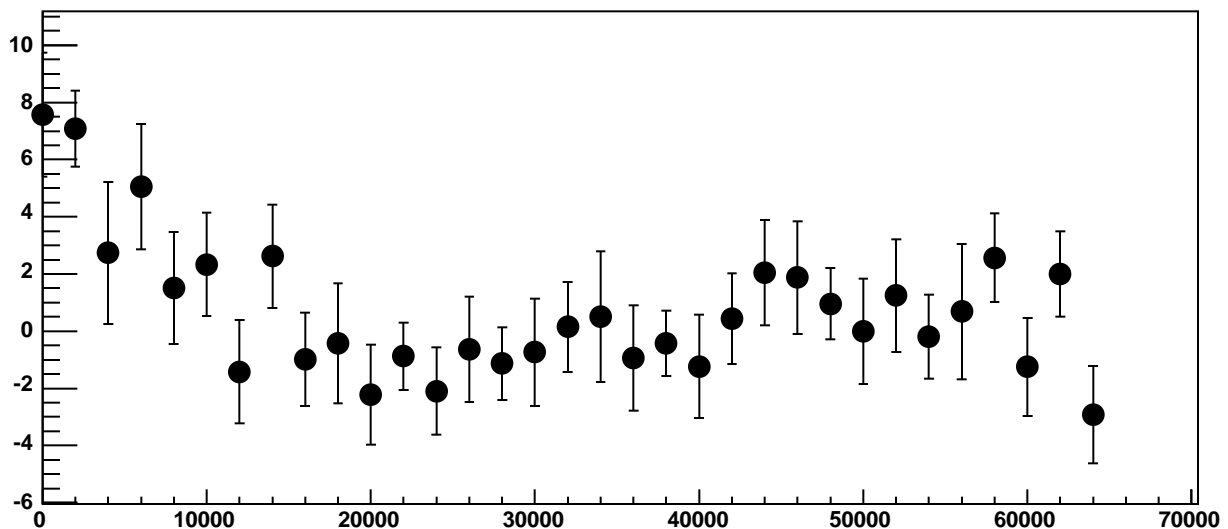


χ^2 / ndf 19.94 / 23
p0 350 ± 0.8171
p1 $0.00364 \pm 2.402e-05$

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

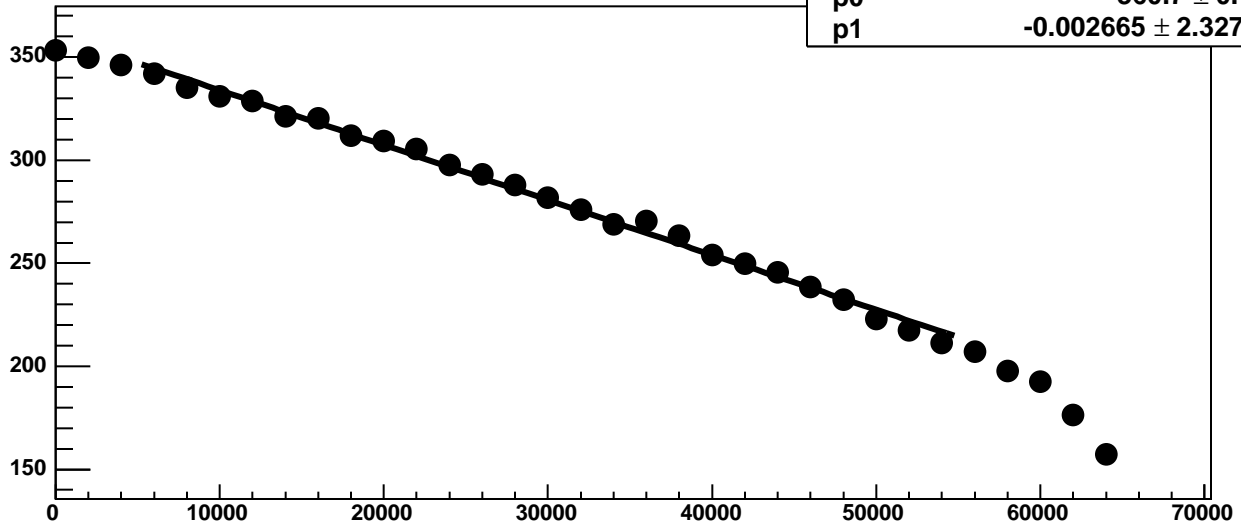


Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

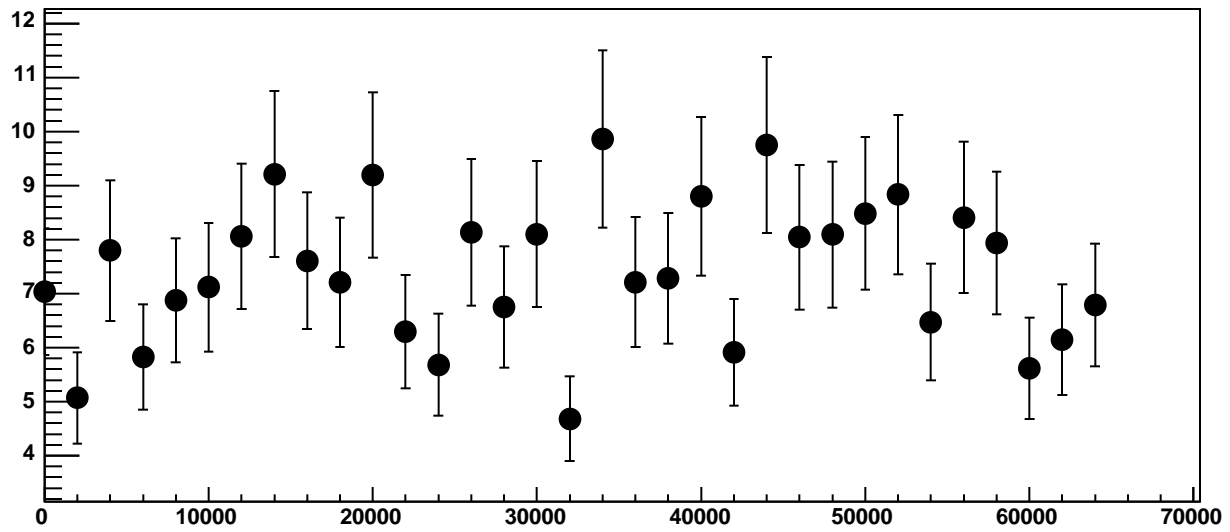


Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

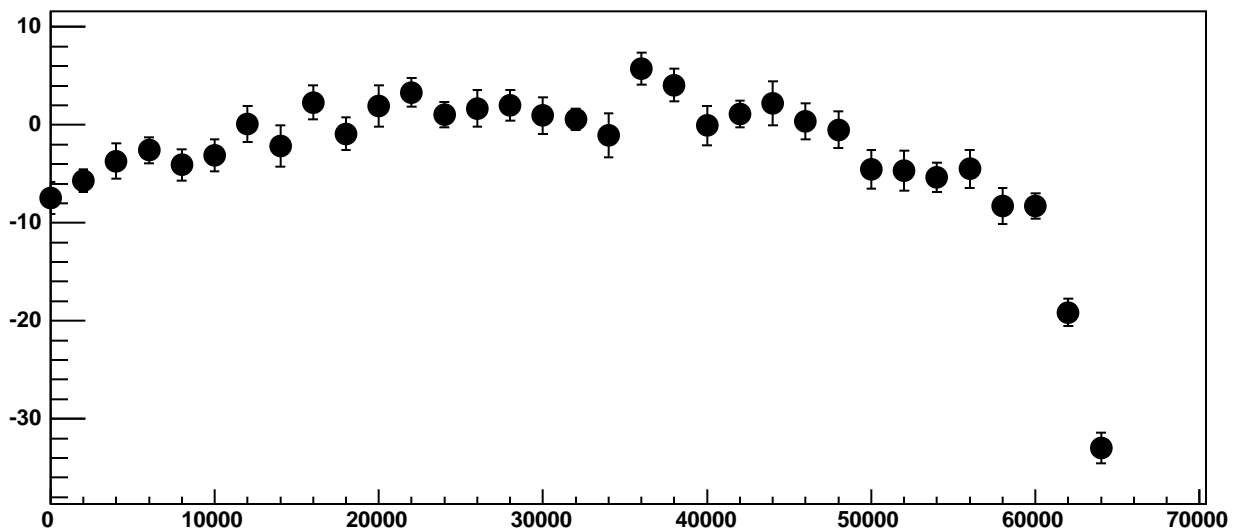
χ^2 / ndf 70.53 / 23
p0 360.7 ± 0.7532
p1 $-0.002665 \pm 2.327\text{e-}05$



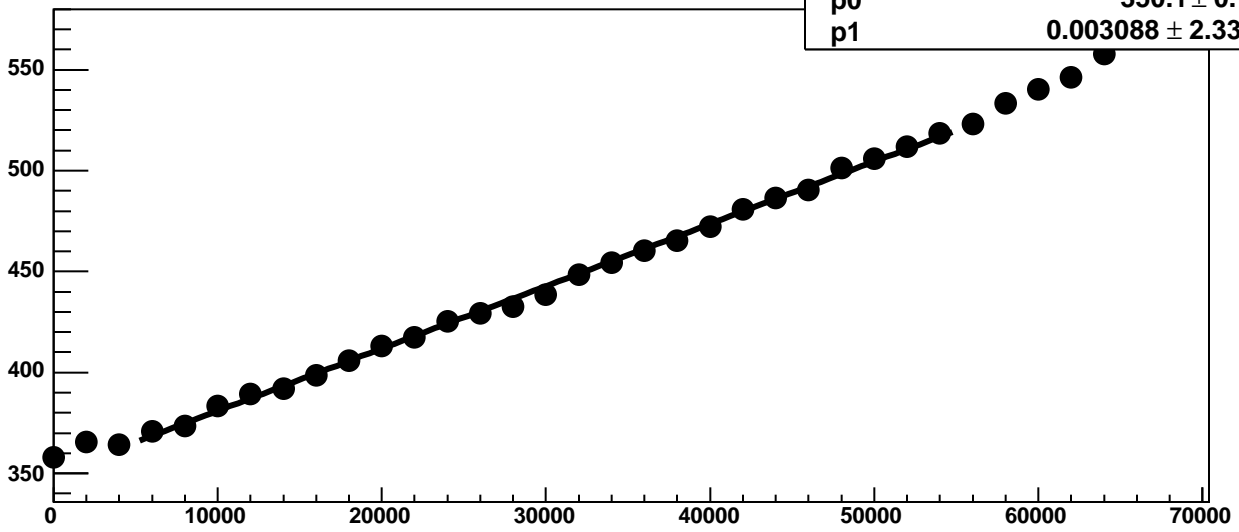
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



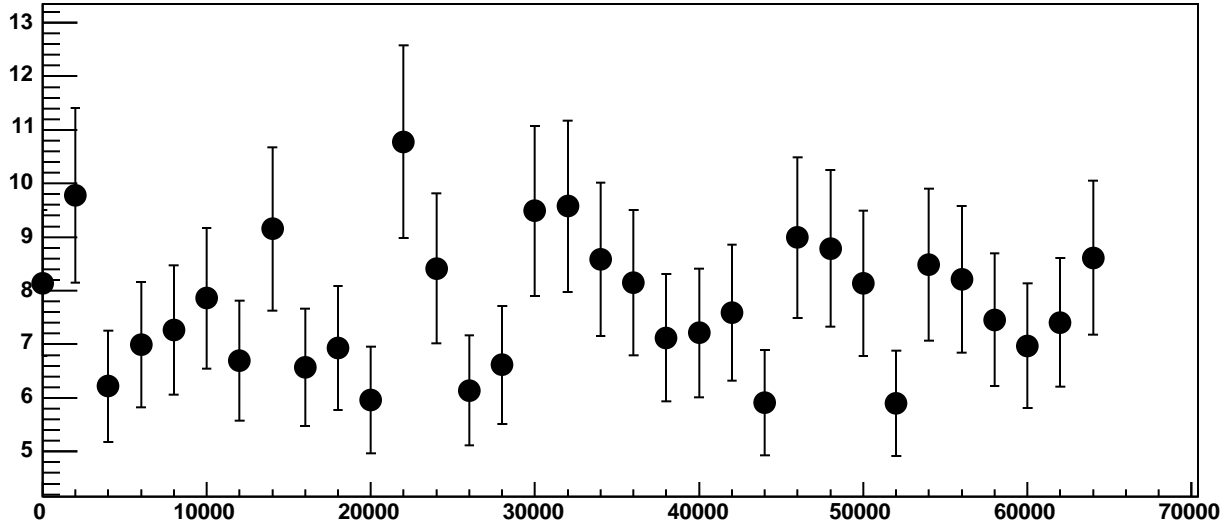
Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



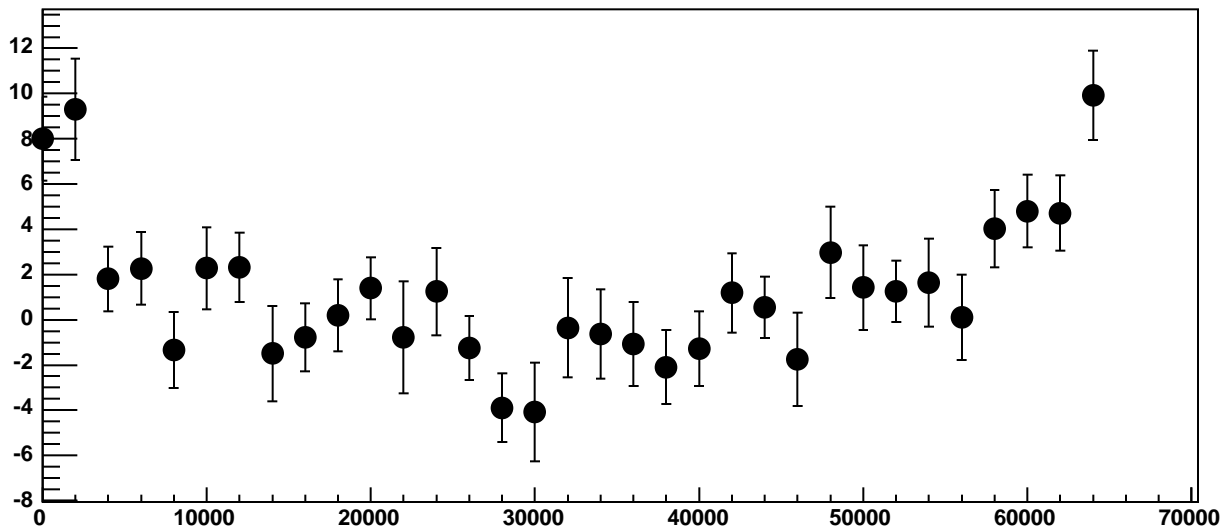
Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



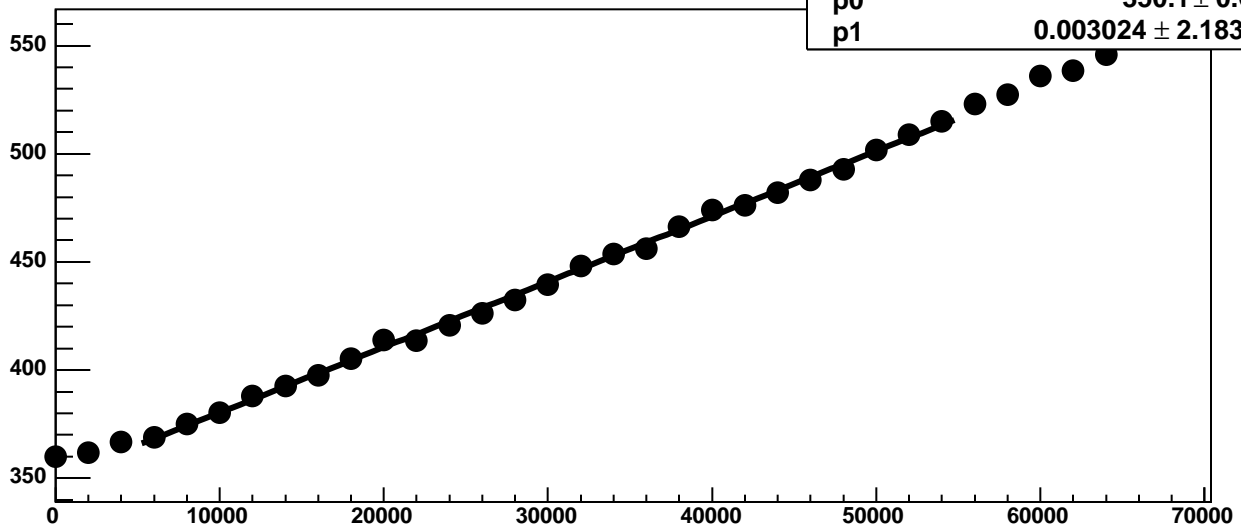
Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.77 / 23

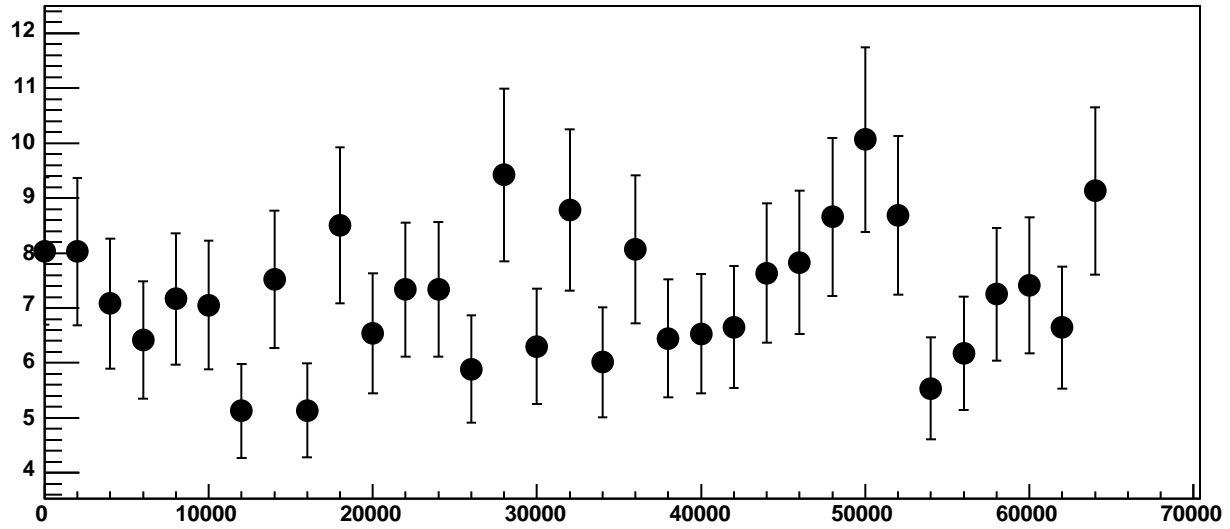
p0

350.1 ± 0.6986

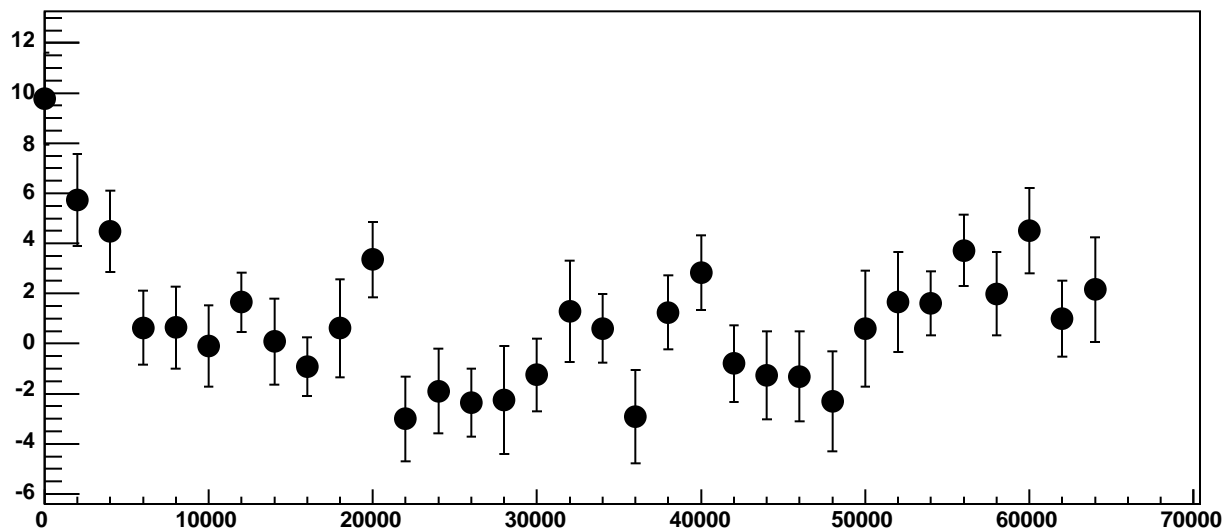
p1

$0.003024 \pm 2.183e-05$

Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

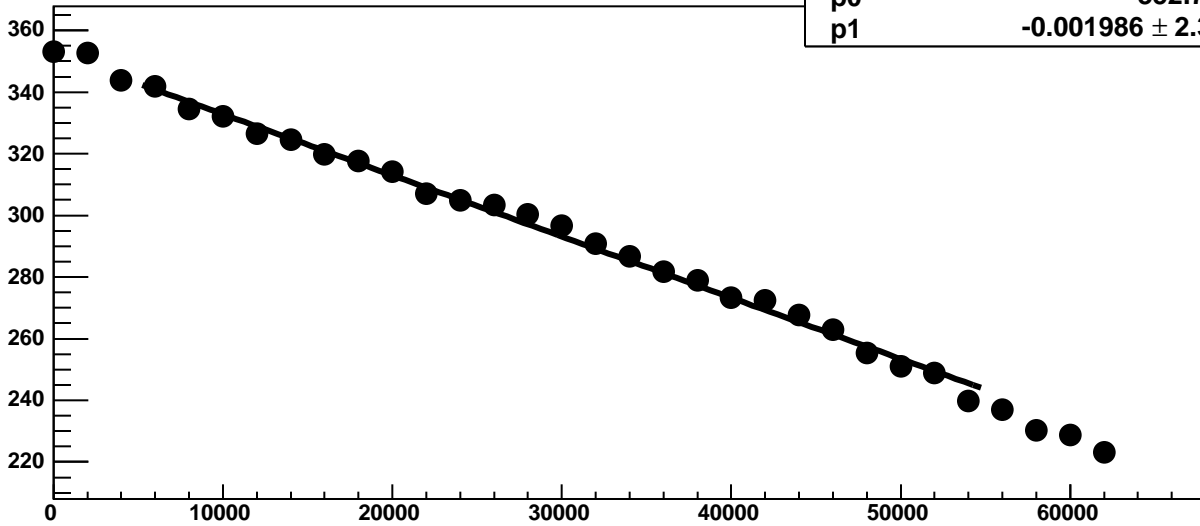


Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

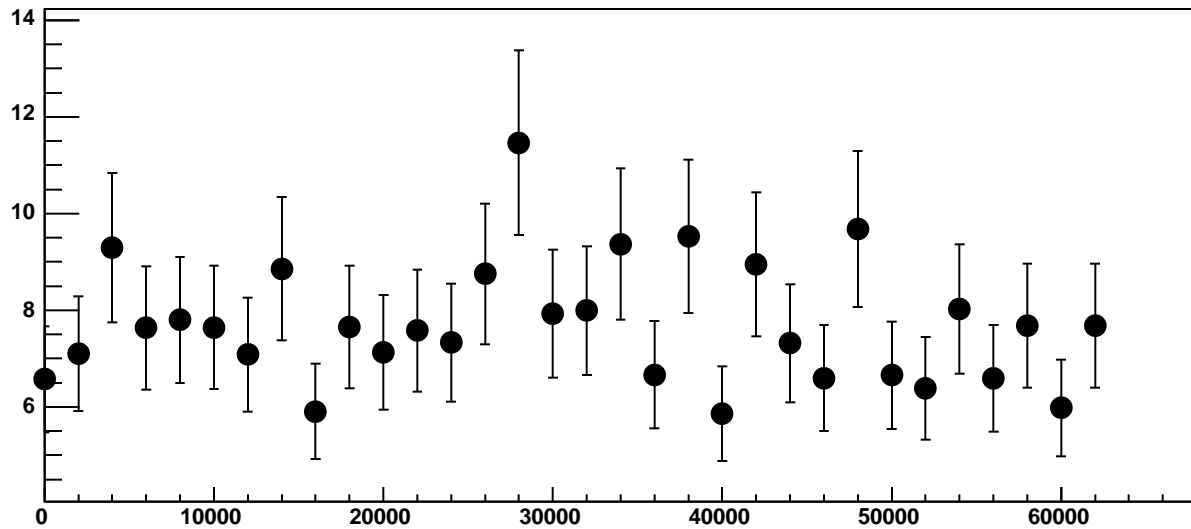


Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

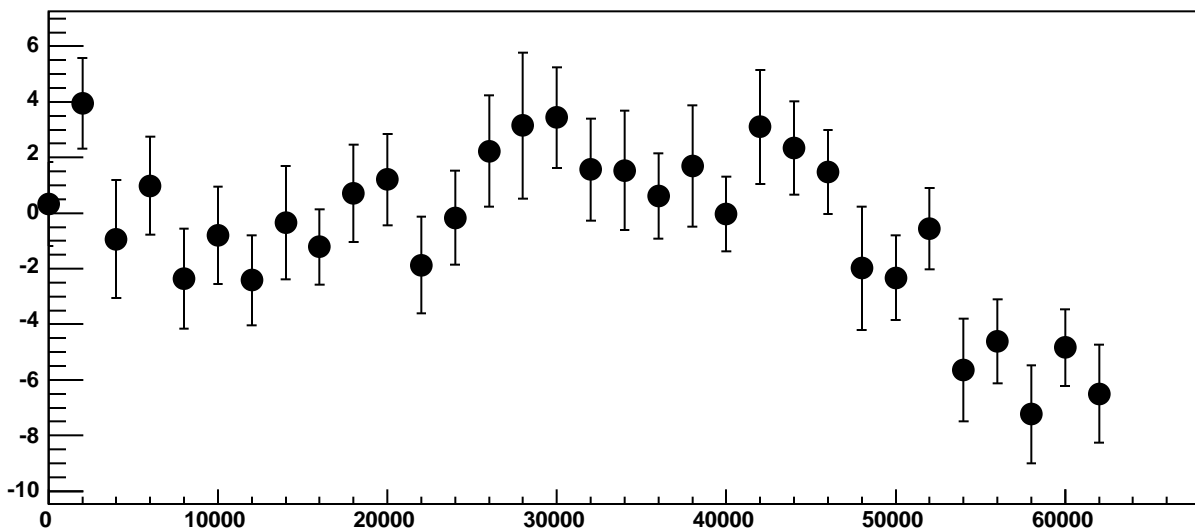
χ^2 / ndf 33.24 / 23
p0 352.7 ± 0.79
p1 $-0.001986 \pm 2.344\text{e-}05$



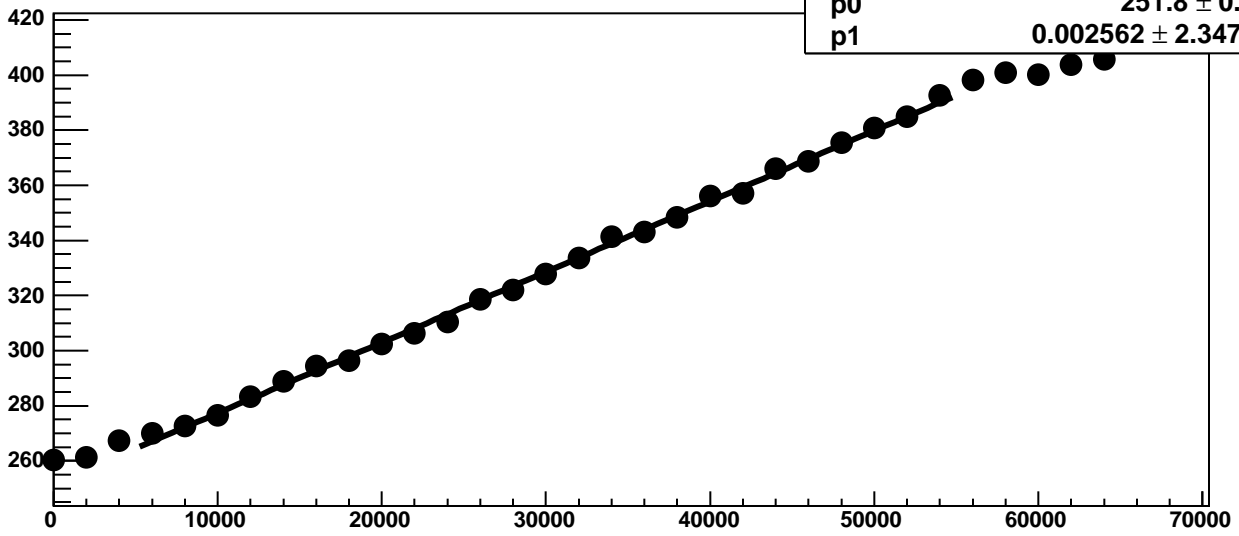
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

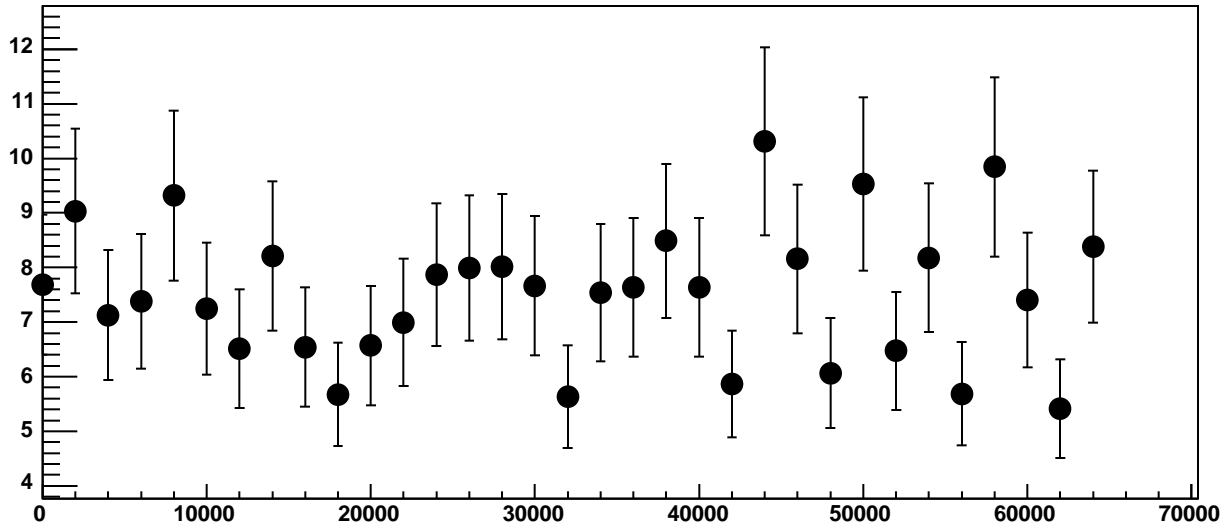


Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

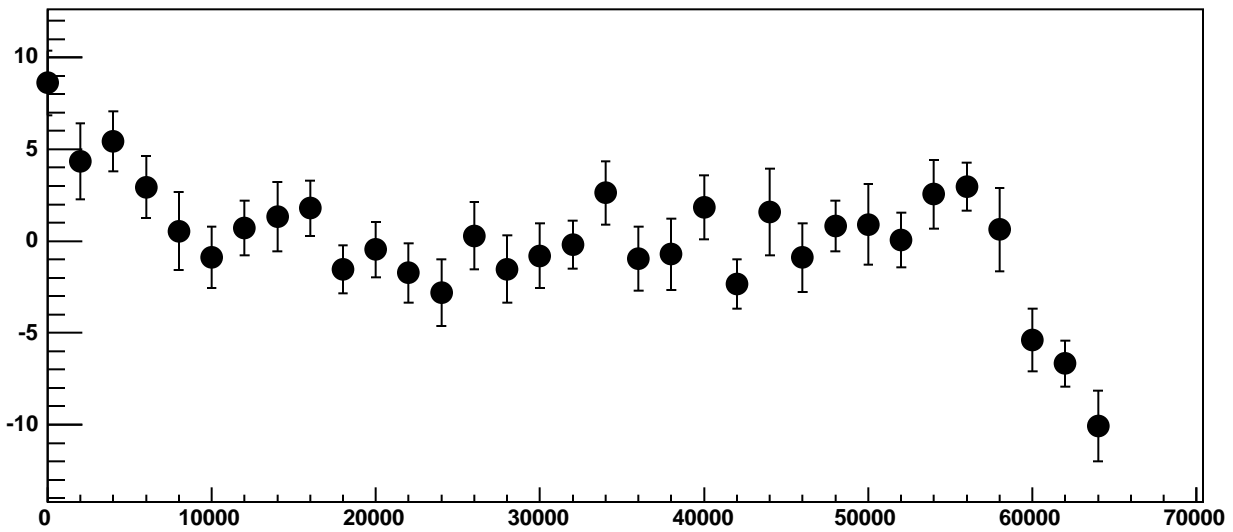


χ^2 / ndf 21.47 / 23
p0 251.8 ± 0.7701
p1 $0.002562 \pm 2.347\text{e-}05$

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

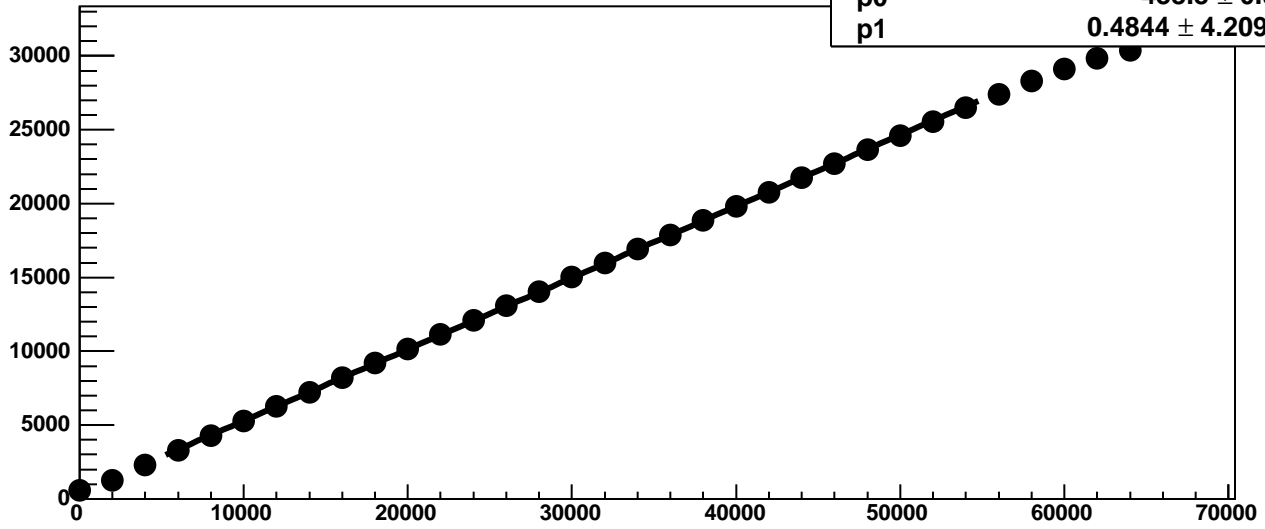


Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

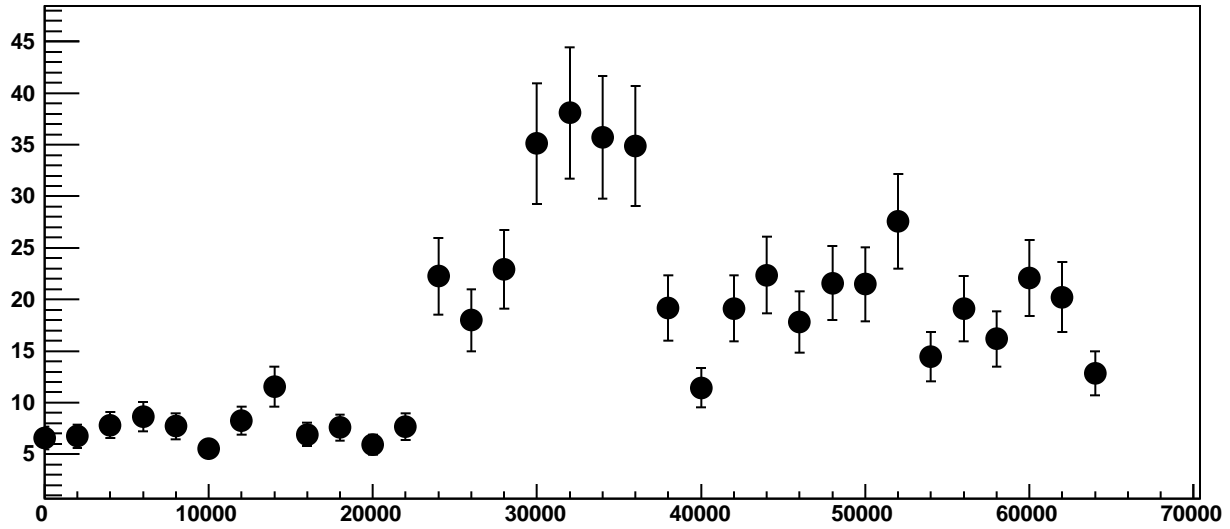


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

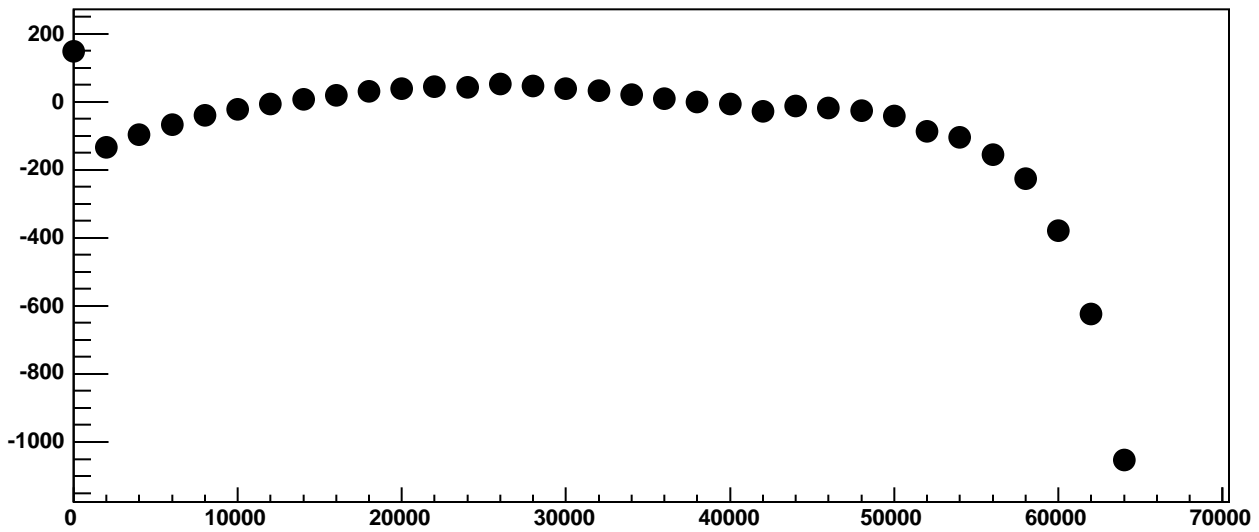
χ^2 / ndf 5517 / 23
p0 438.8 ± 0.9542
p1 $0.4844 \pm 4.209e-05$



Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC

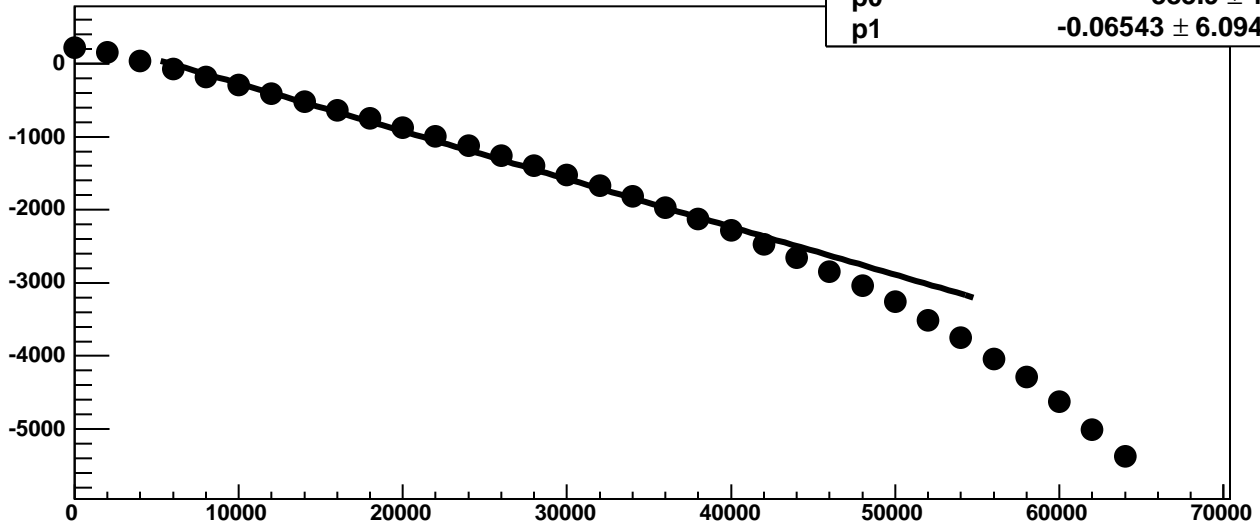


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

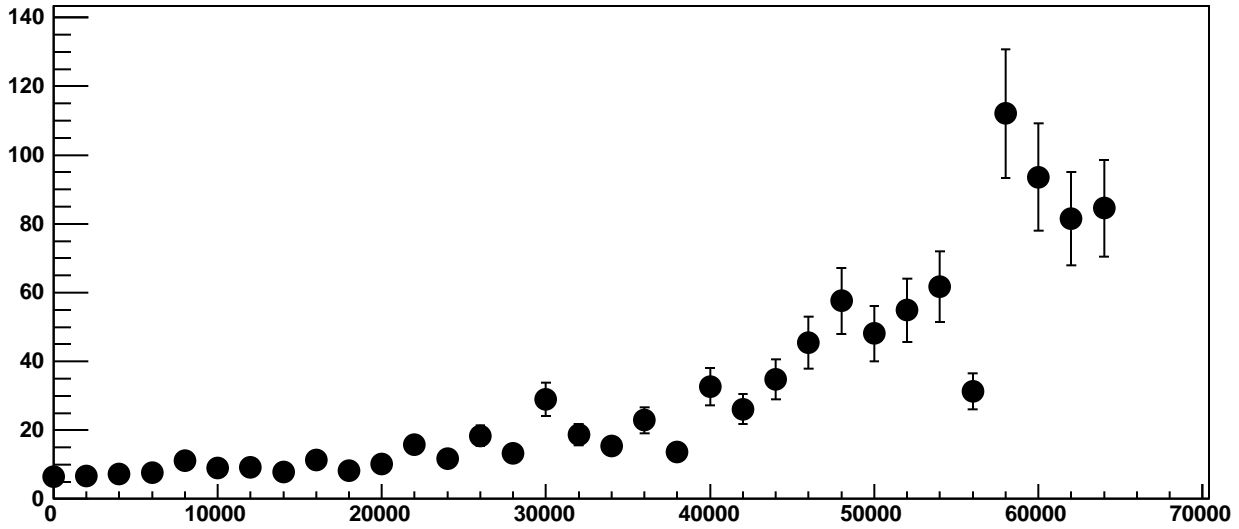


Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

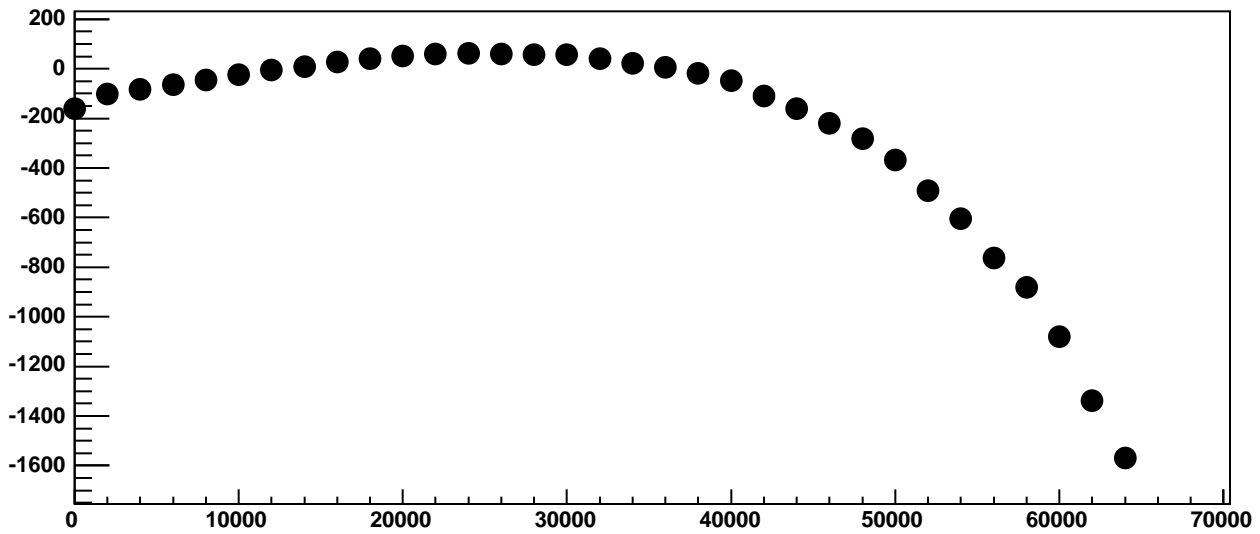
χ^2 / ndf 1.068e+04 / 23
p0 383.9 ± 1.266
p1 -0.06543 ± 6.094e-05



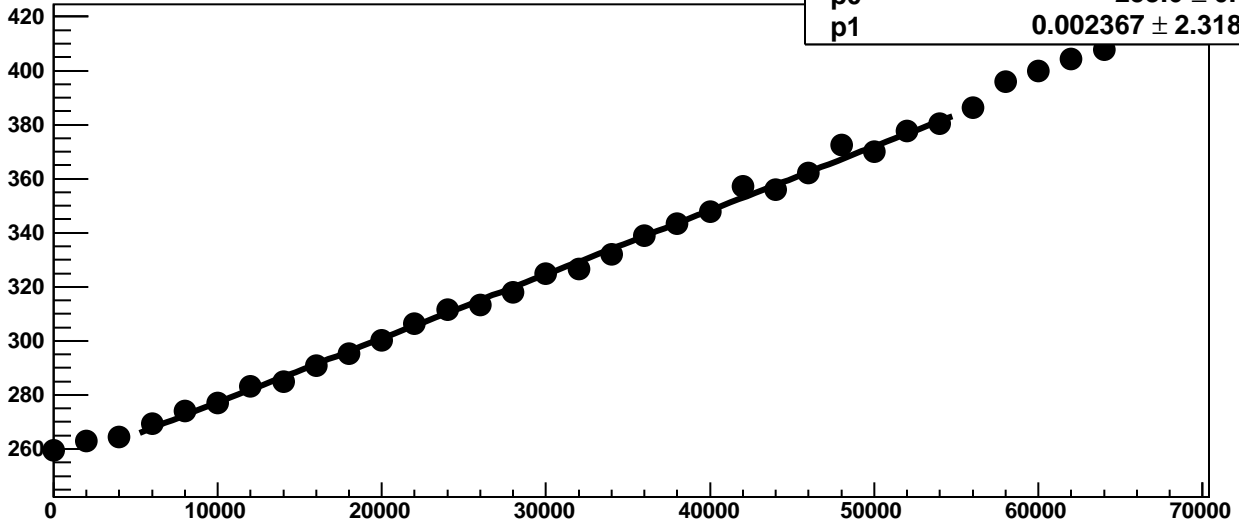
Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



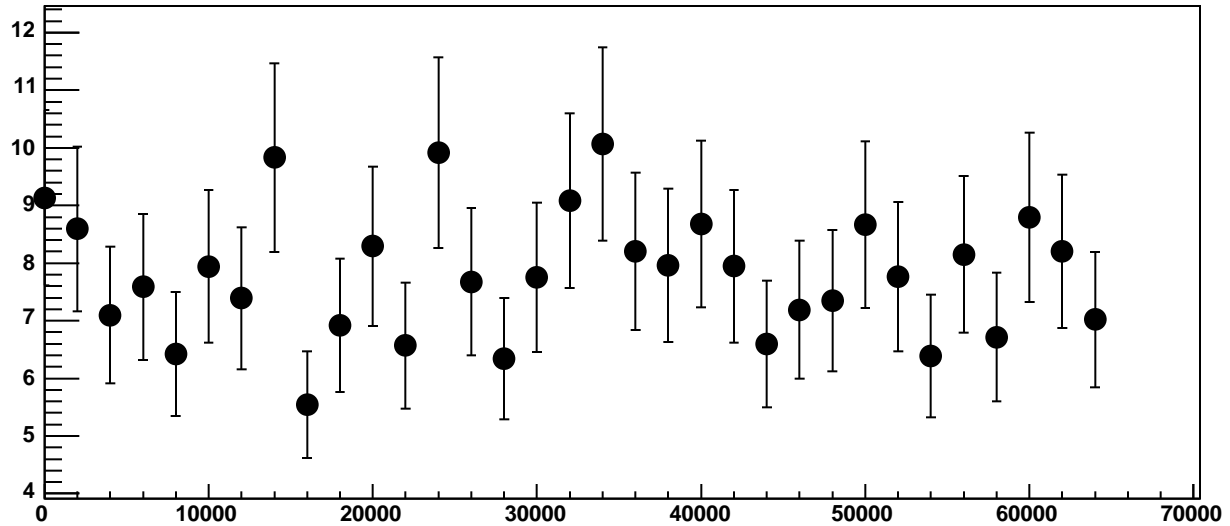
Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



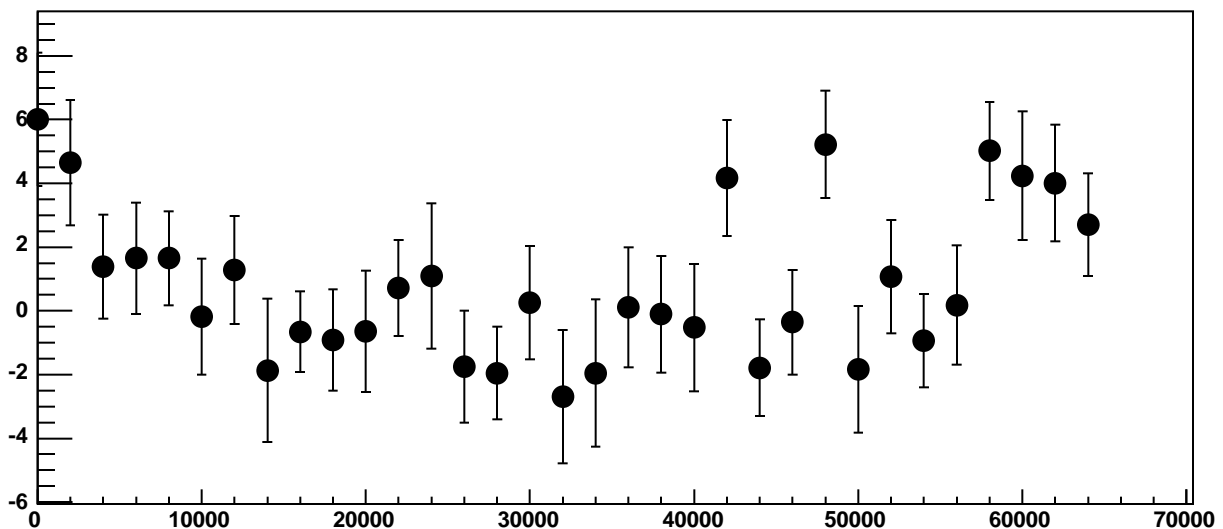
Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



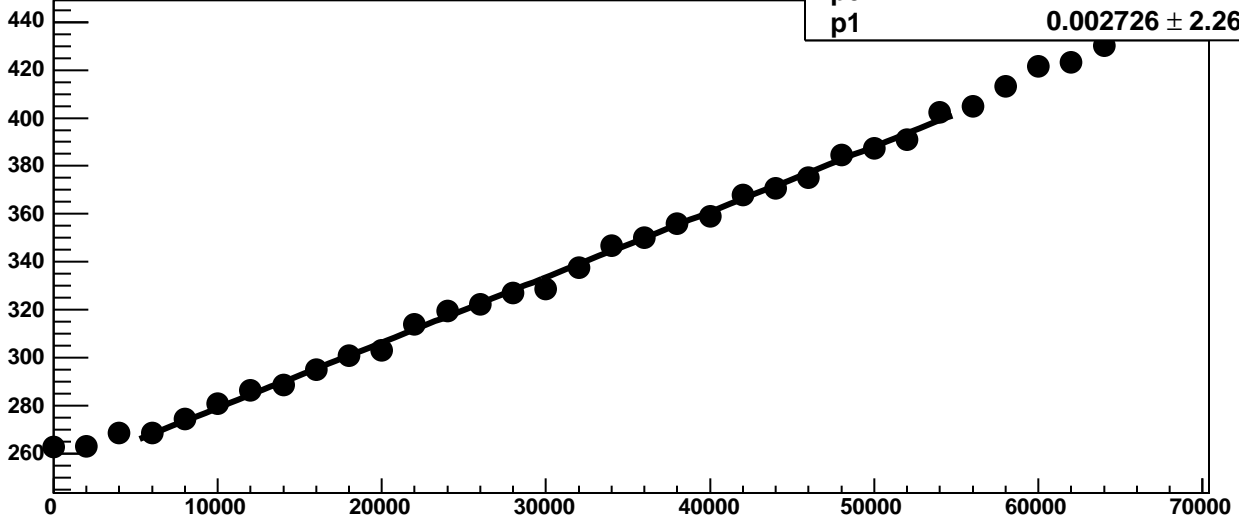
Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.45 / 23

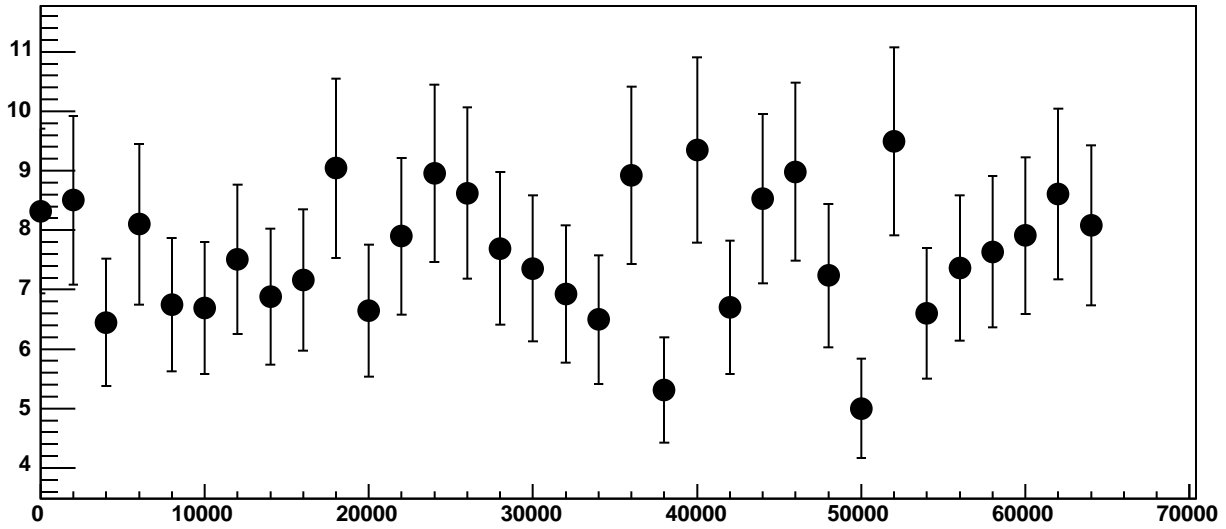
p0

251.8 ± 0.7686

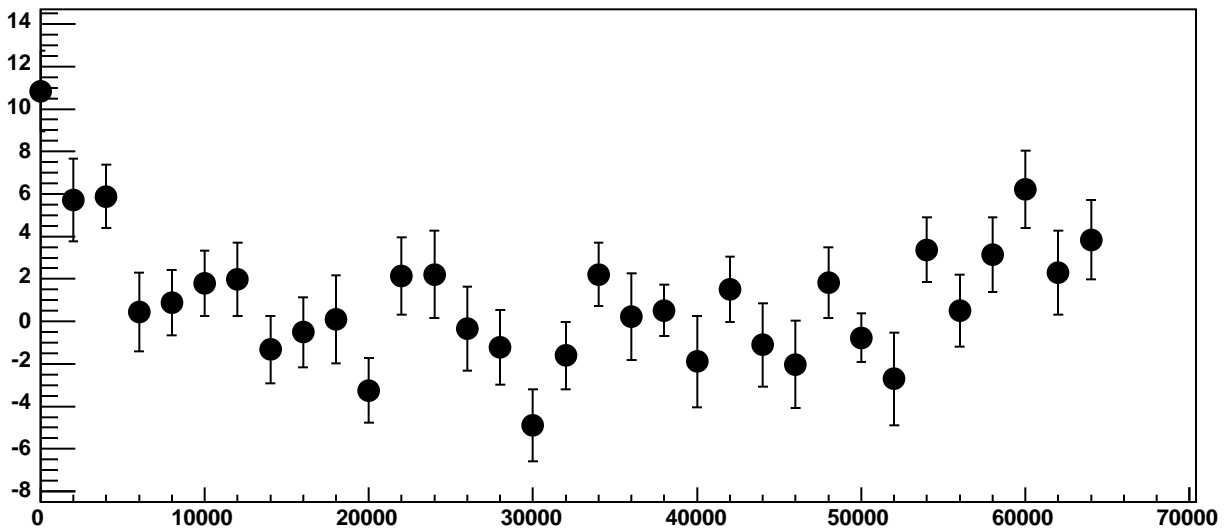
p1

$0.002726 \pm 2.26e-05$

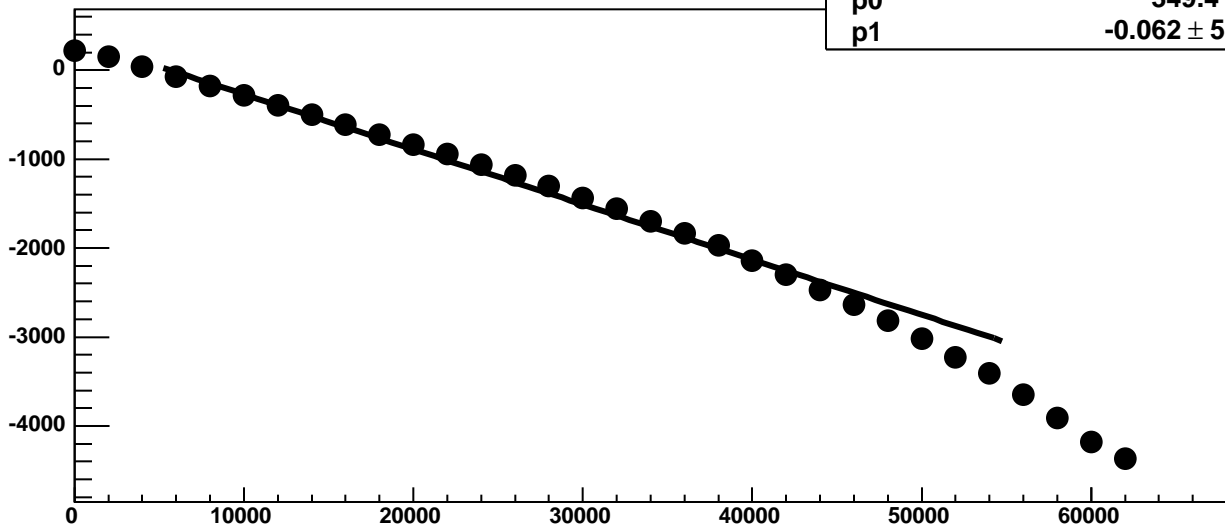
Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

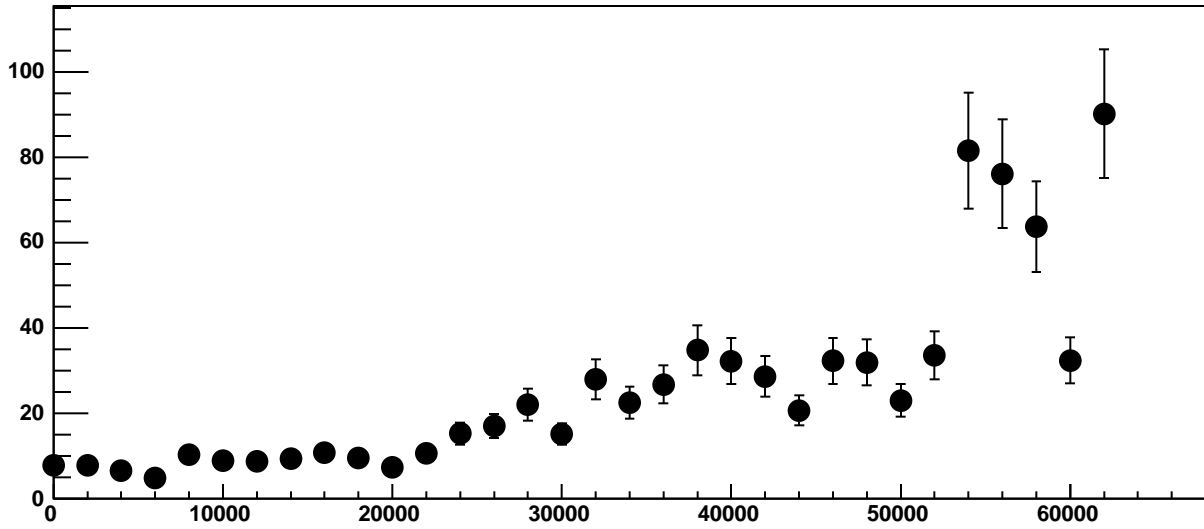


Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

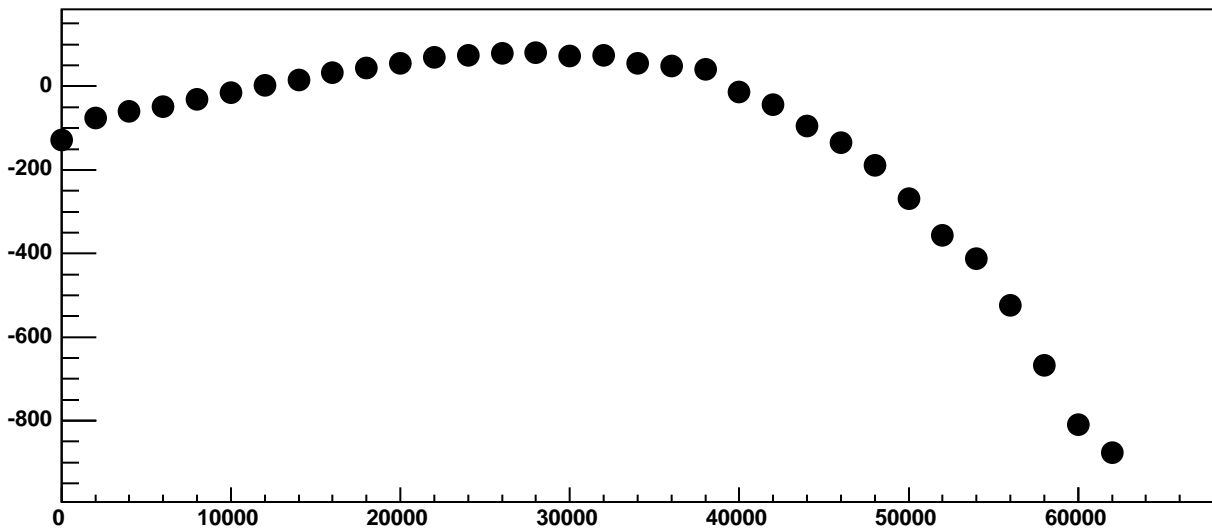


χ^2 / ndf	1.303e+04 / 23
p0	349.4 ± 1.046
p1	-0.062 ± 5.37e-05

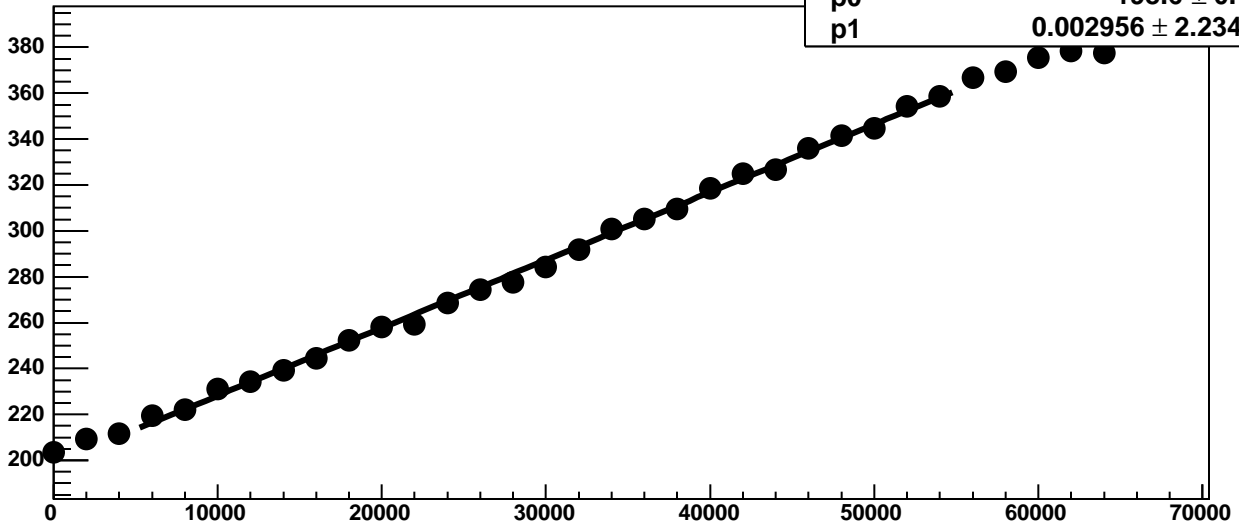
Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.32 / 23

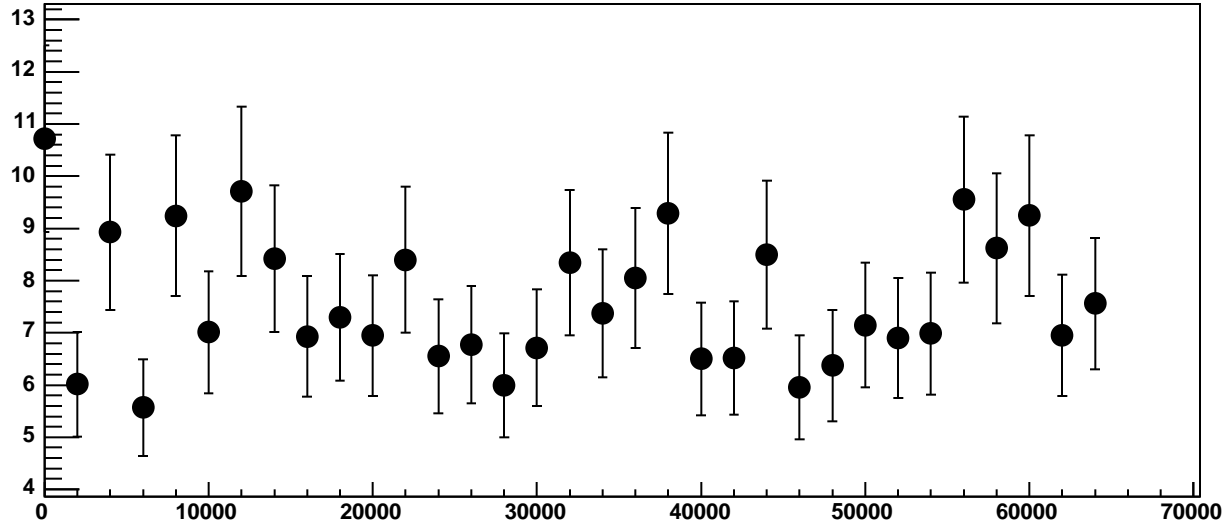
p0

198.6 ± 0.7565

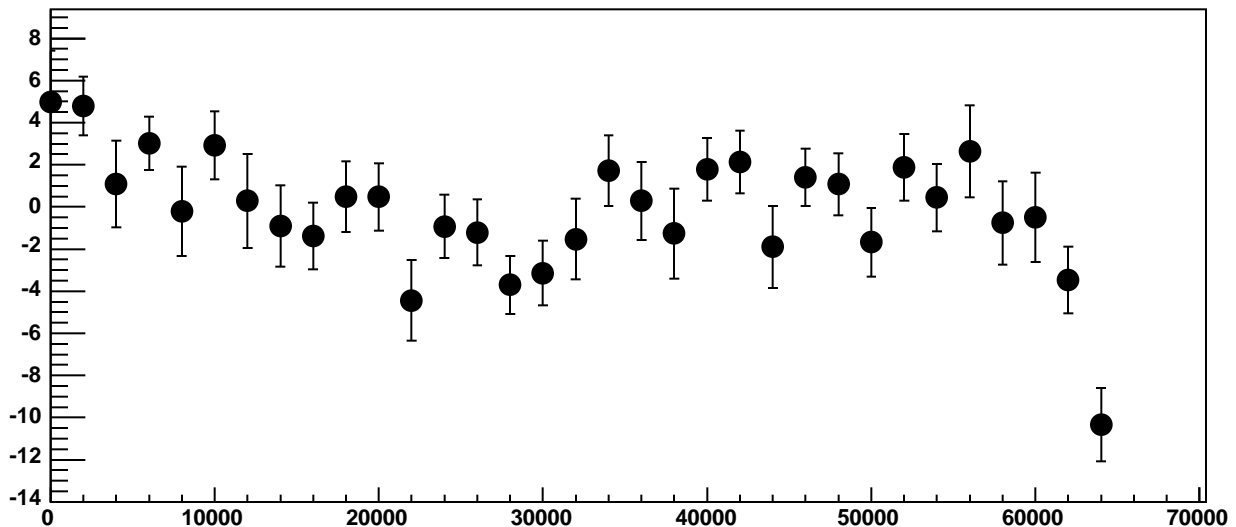
p1

$0.002956 \pm 2.234e-05$

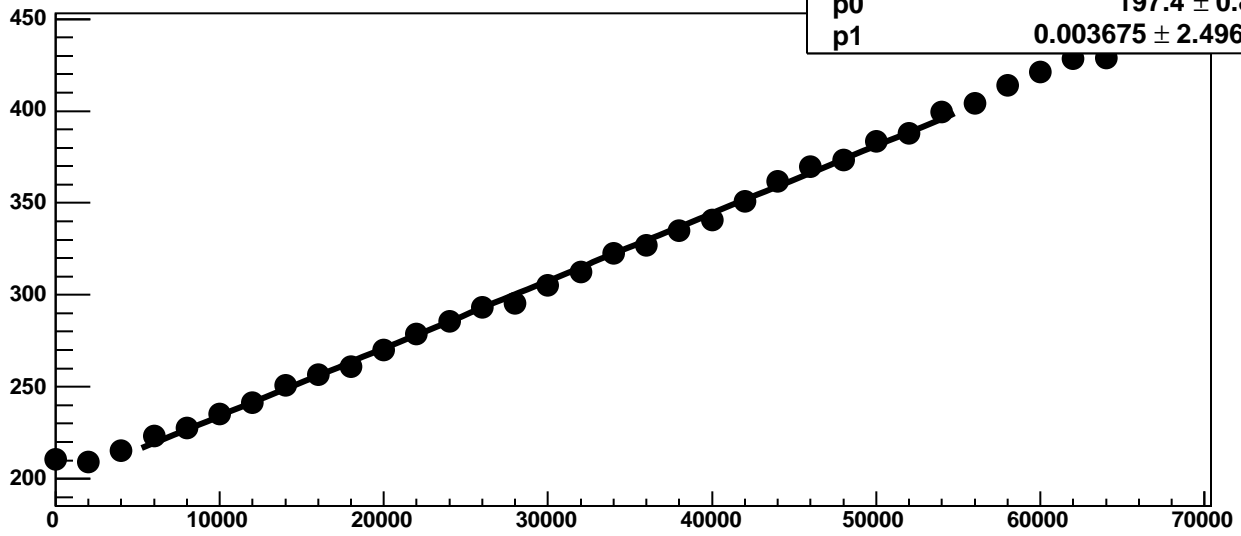
Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.92 / 23

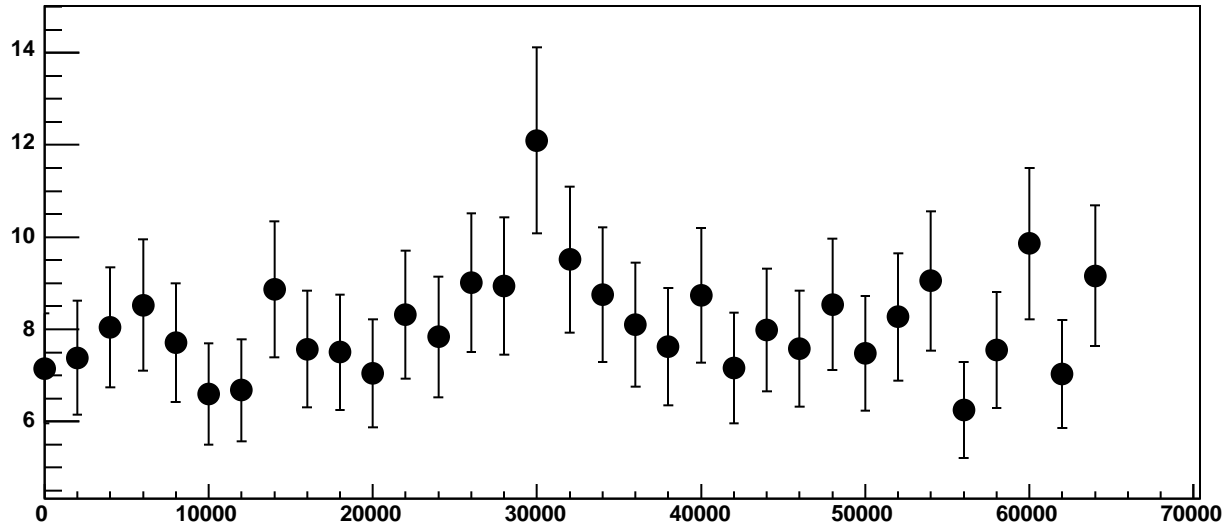
p0

197.4 ± 0.8157

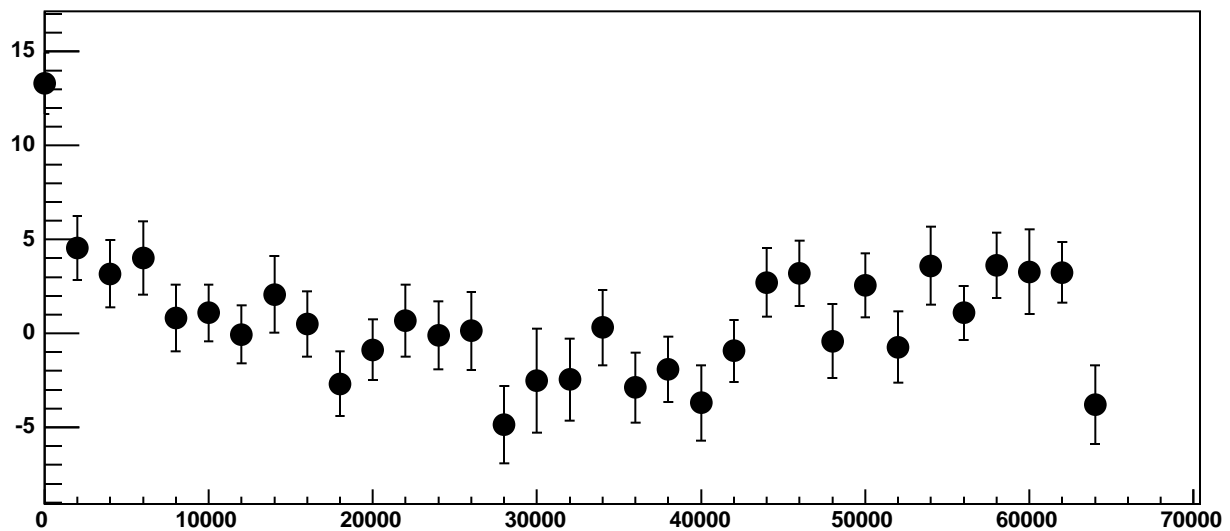
p1

$0.003675 \pm 2.496e-05$

Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

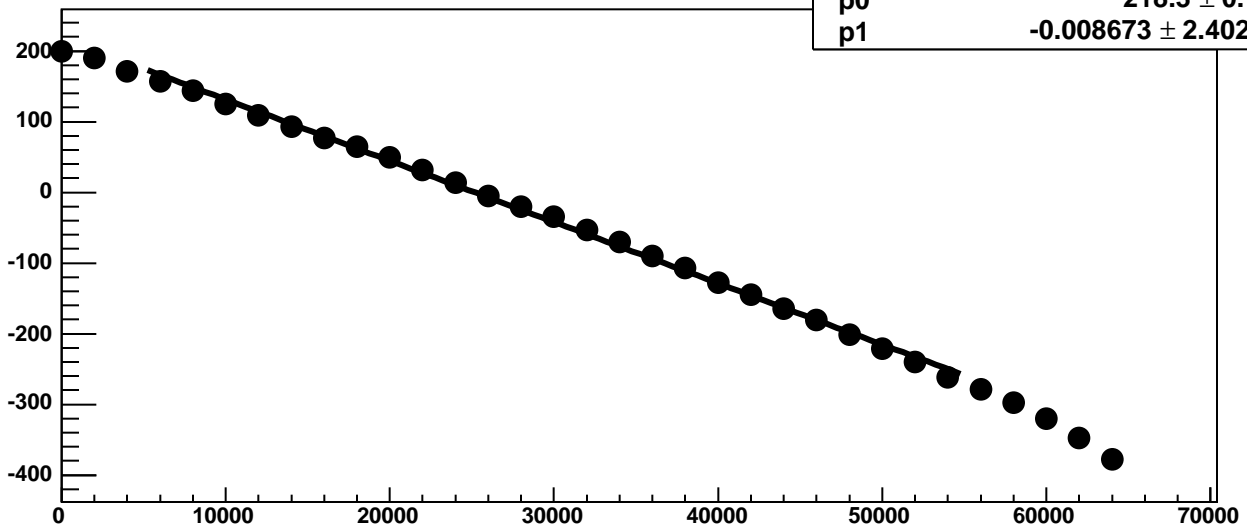


Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

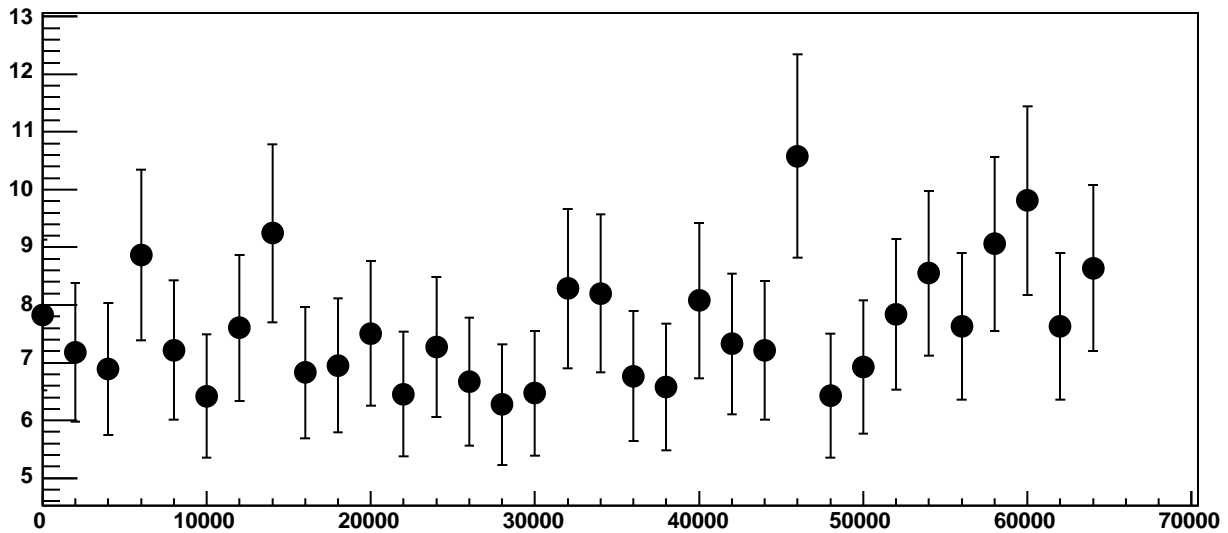


Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

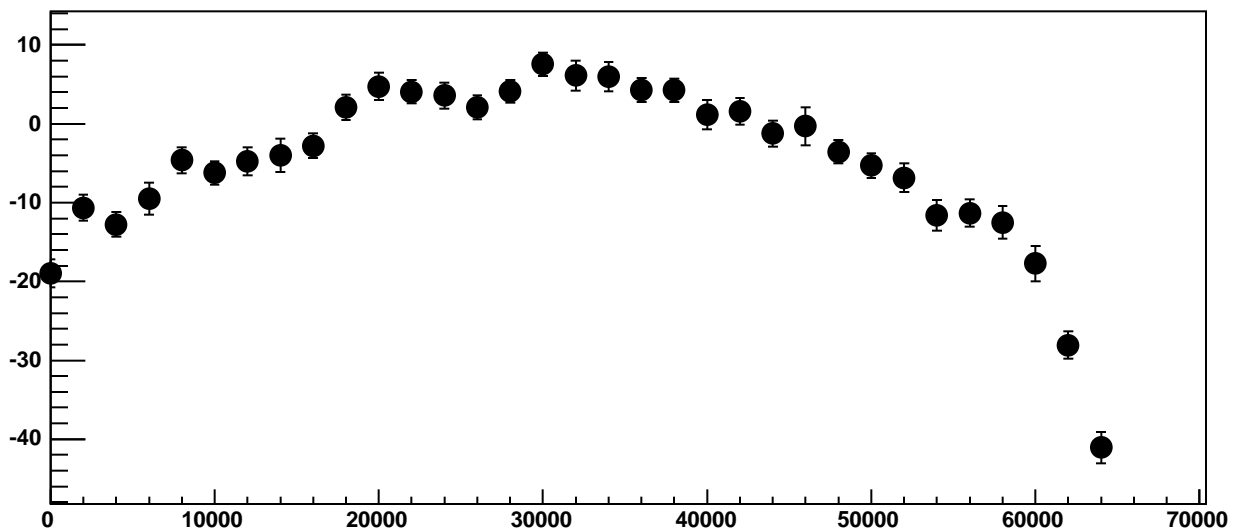
χ^2 / ndf 223.3 / 23
p0 218.3 ± 0.7873
p1 $-0.008673 \pm 2.402e-05$



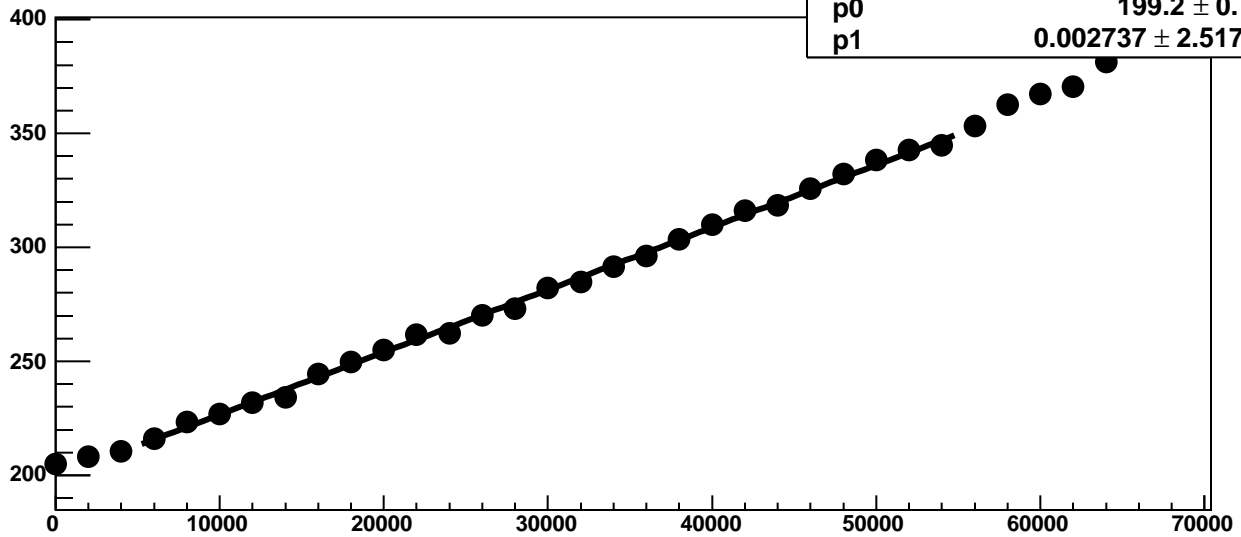
Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

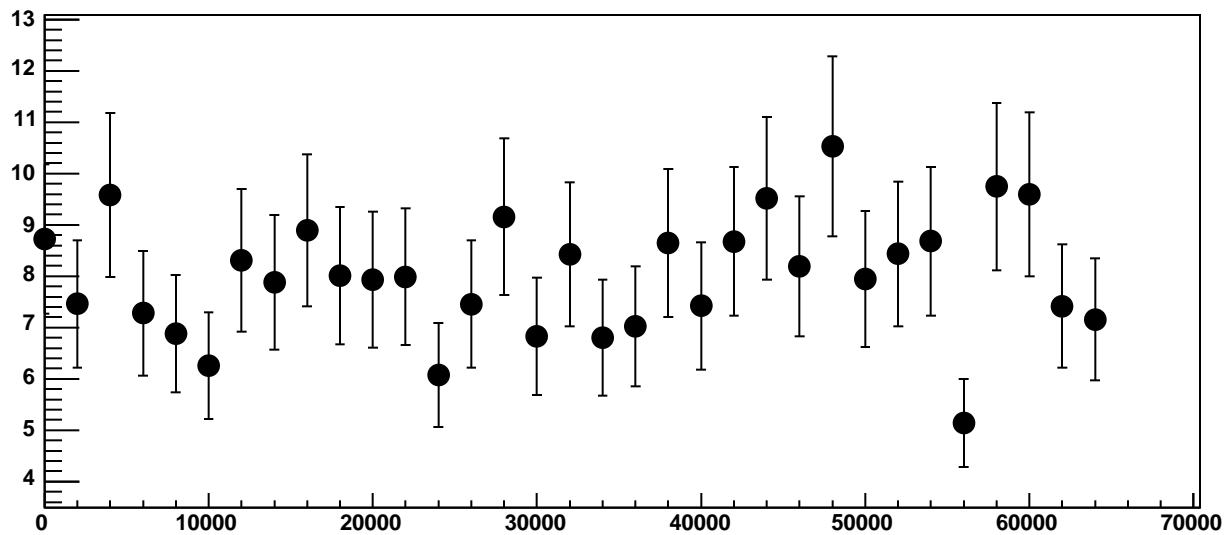


Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

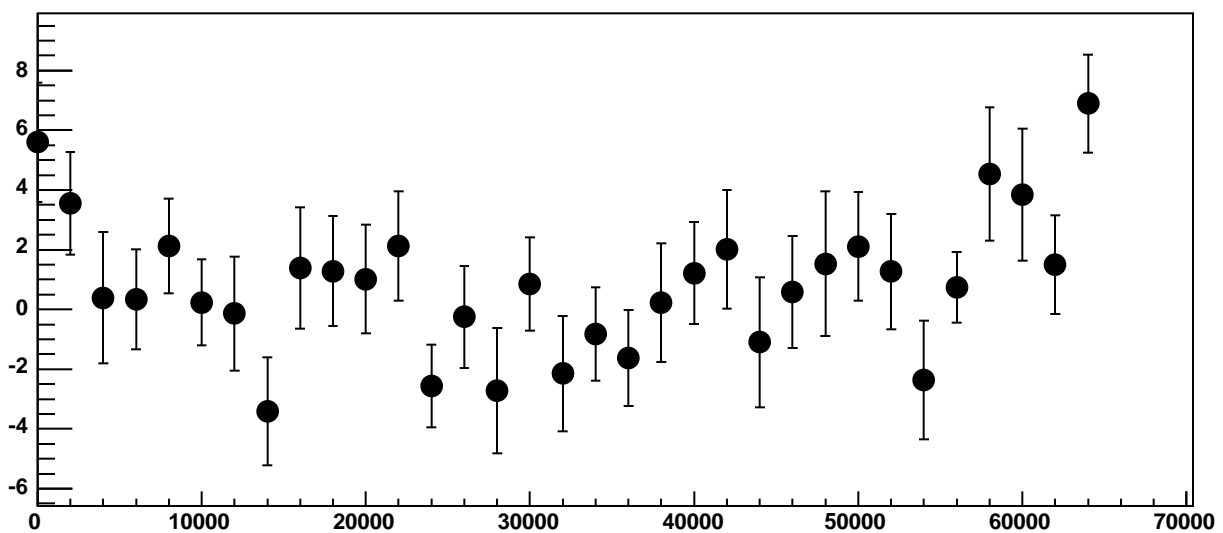


χ^2 / ndf 21.41 / 23
p0 199.2 ± 0.7985
p1 $0.002737 \pm 2.517e-05$

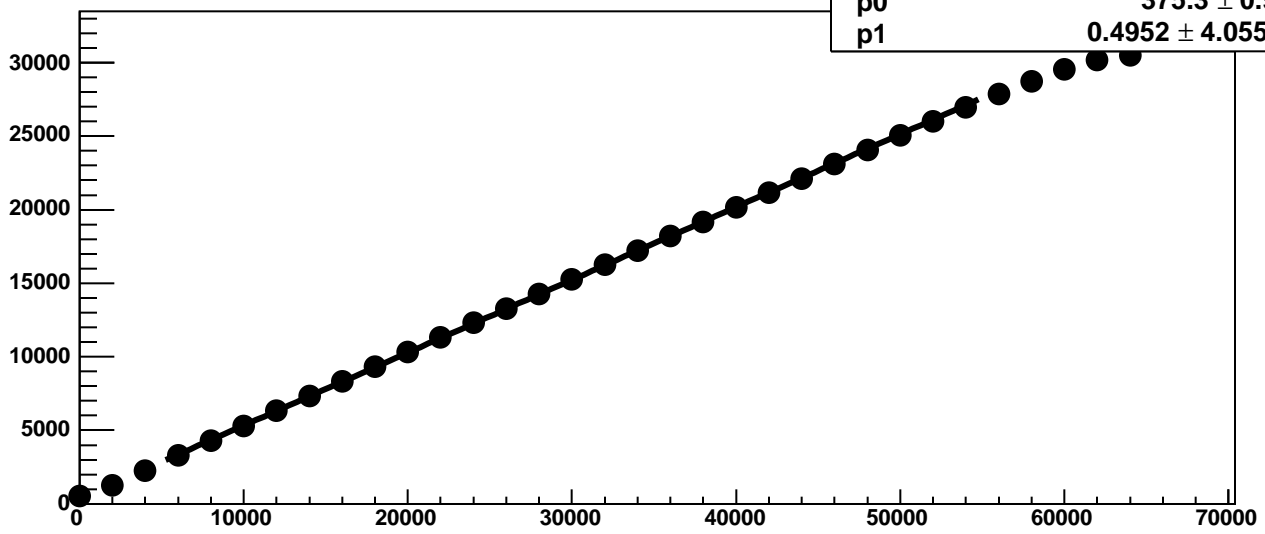
Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



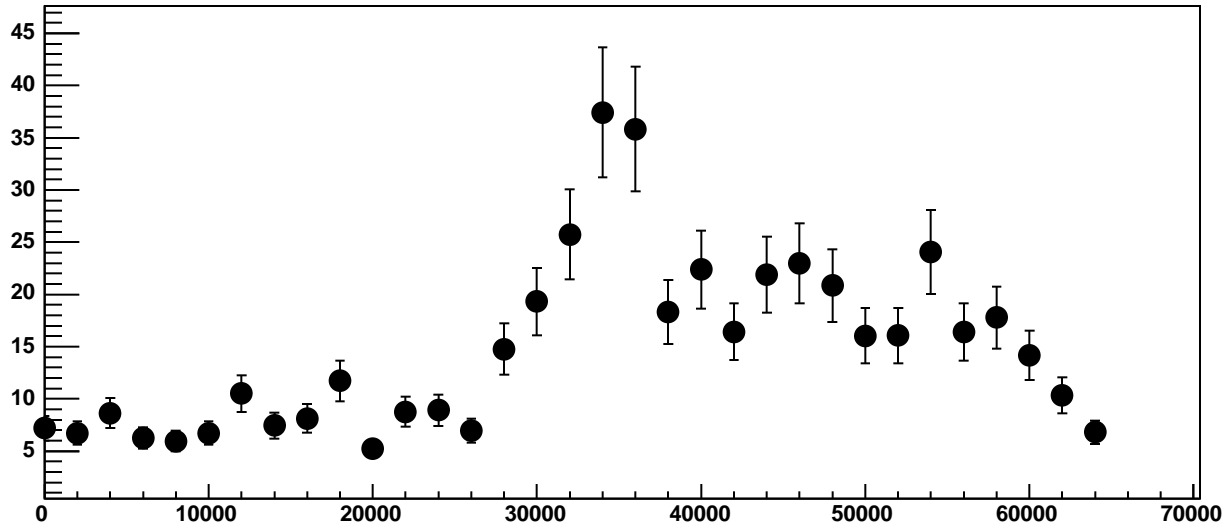
Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



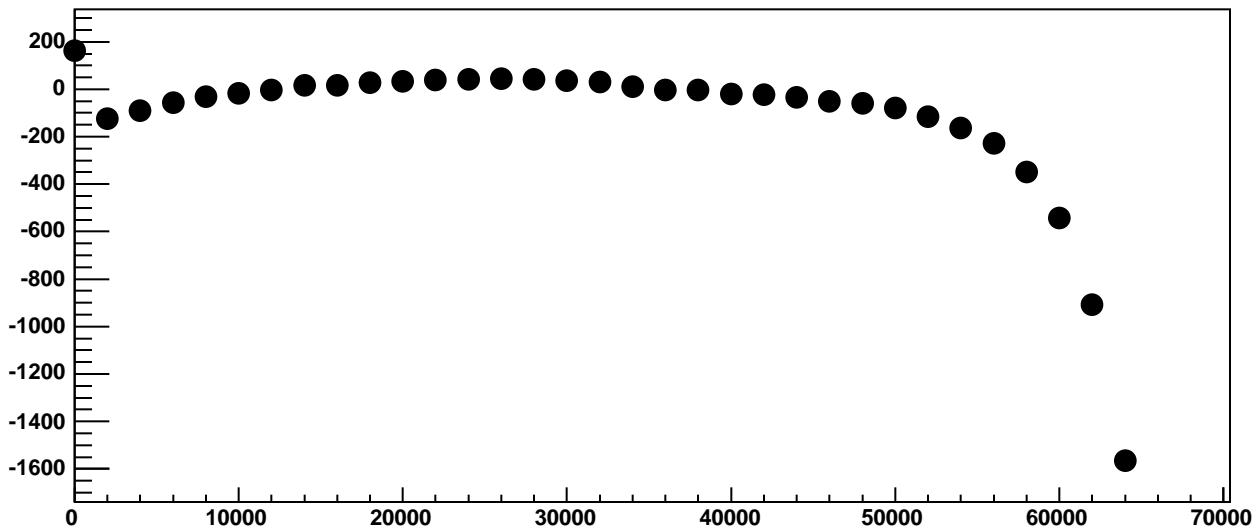
Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC



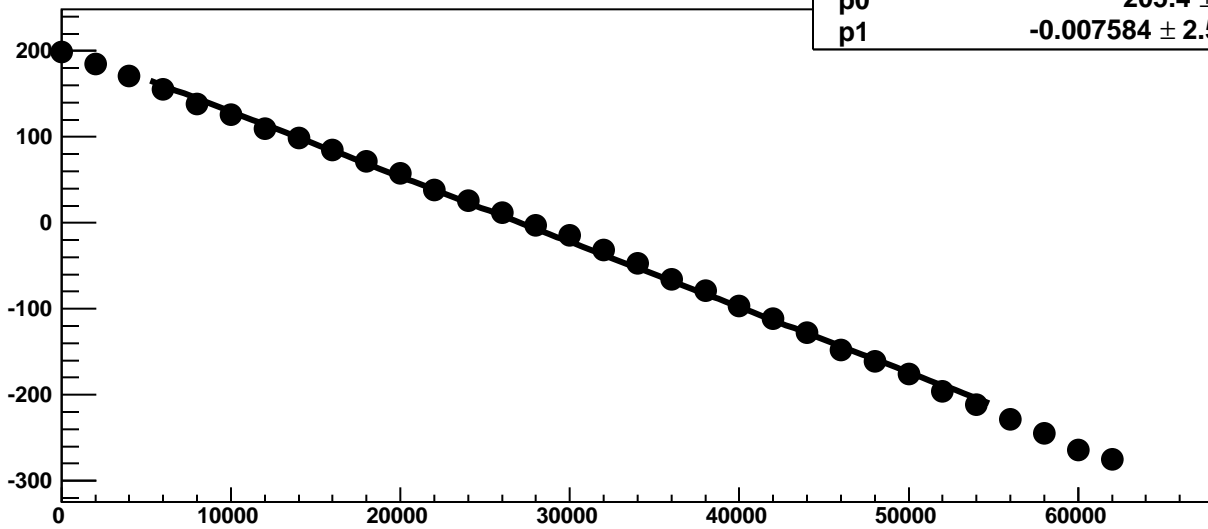
Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

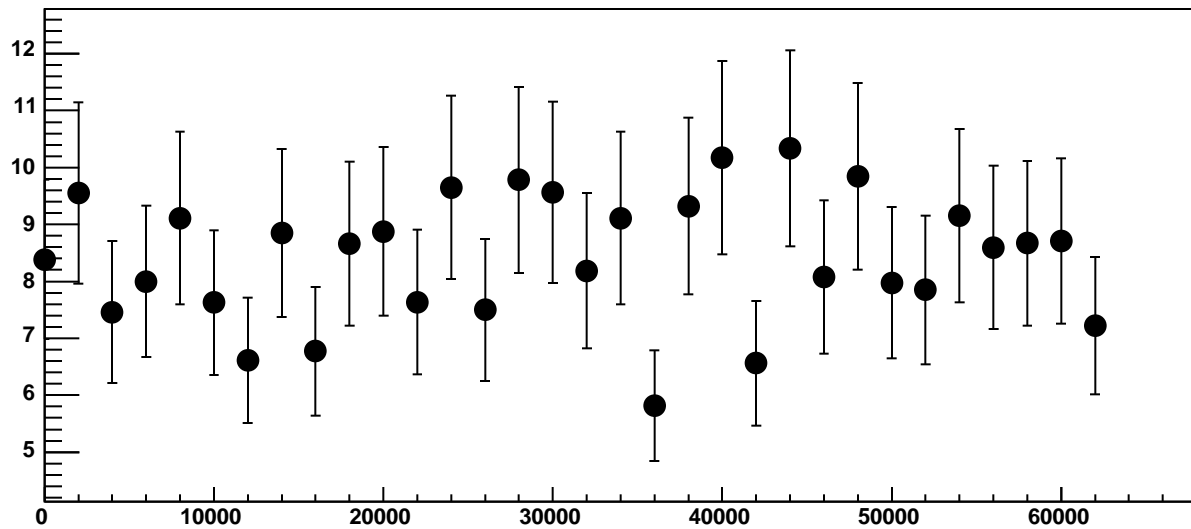


Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

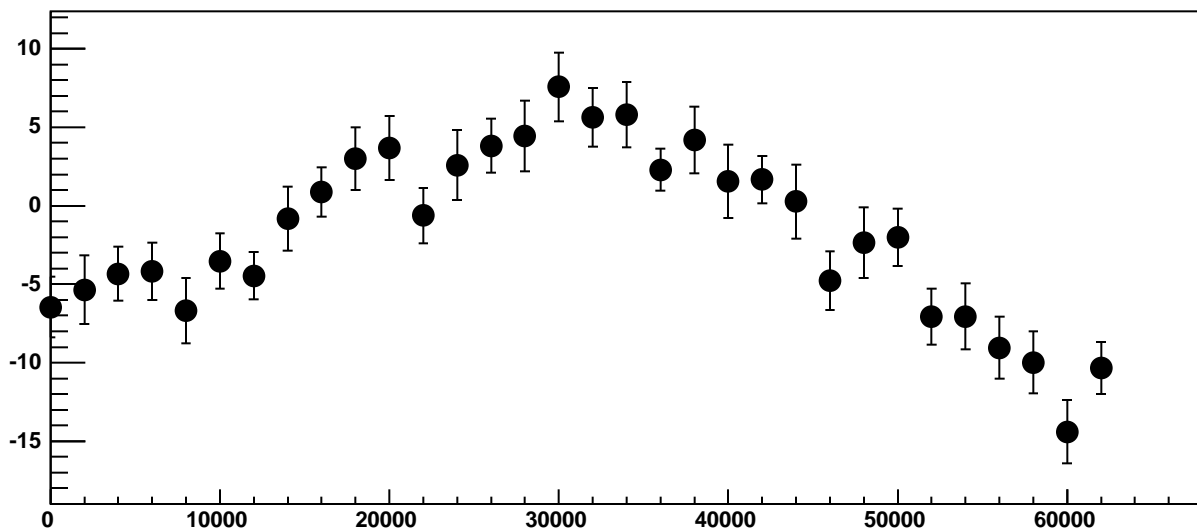


χ^2 / ndf 117.1 / 23
p0 205.4 ± 0.8497
p1 $-0.007584 \pm 2.598e-05$

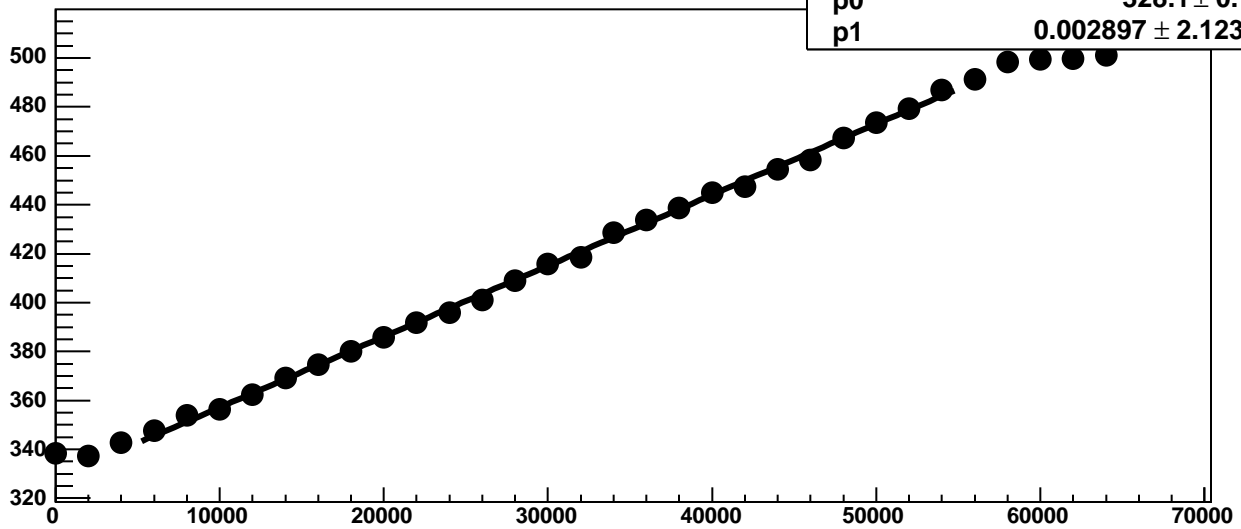
Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

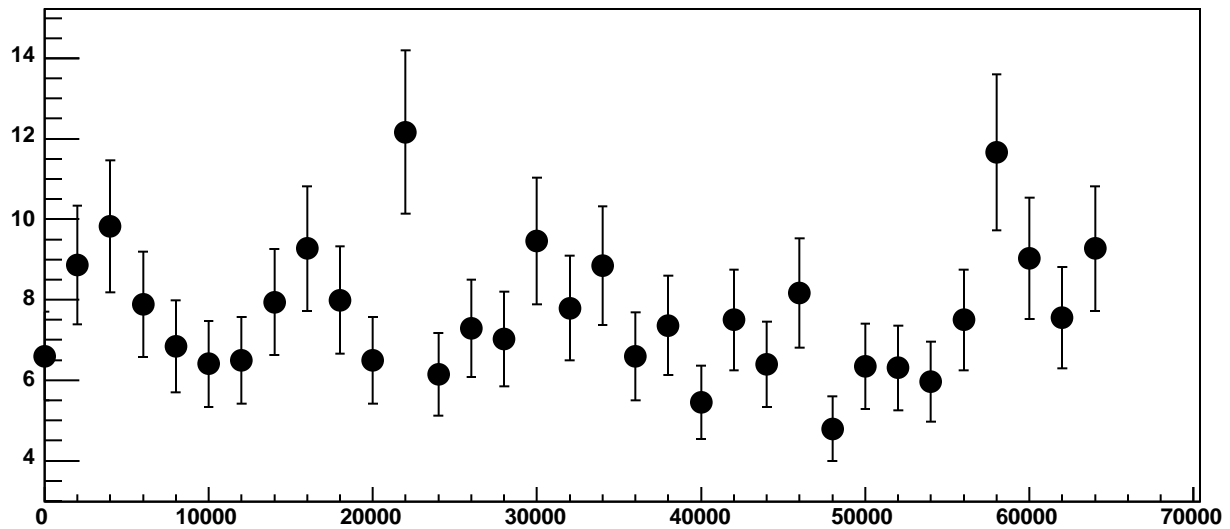


Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

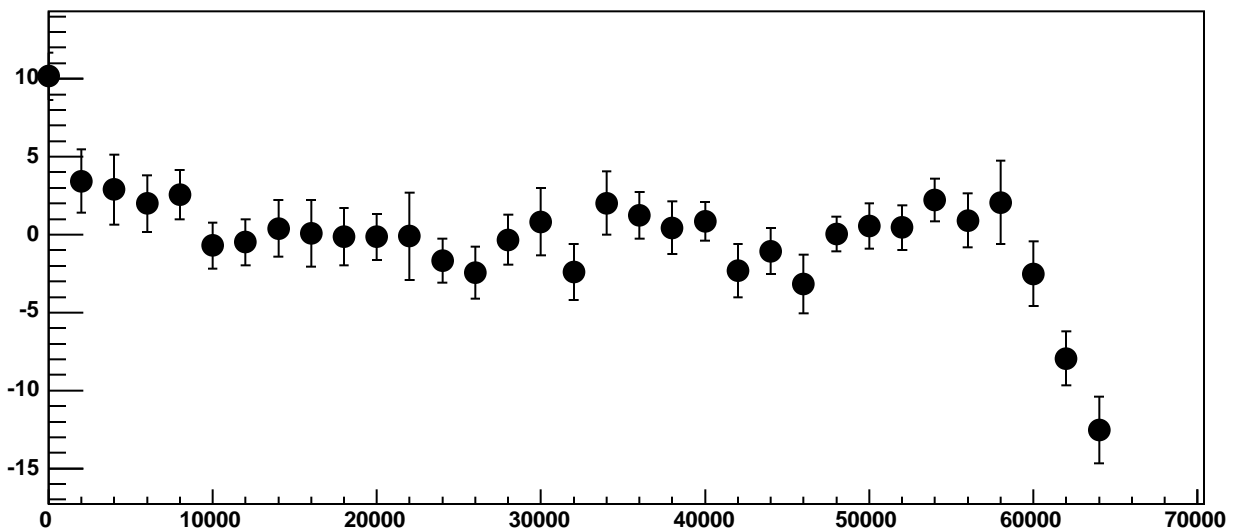


χ^2 / ndf 20.08 / 23
p0 328.1 ± 0.7513
p1 $0.002897 \pm 2.123e-05$

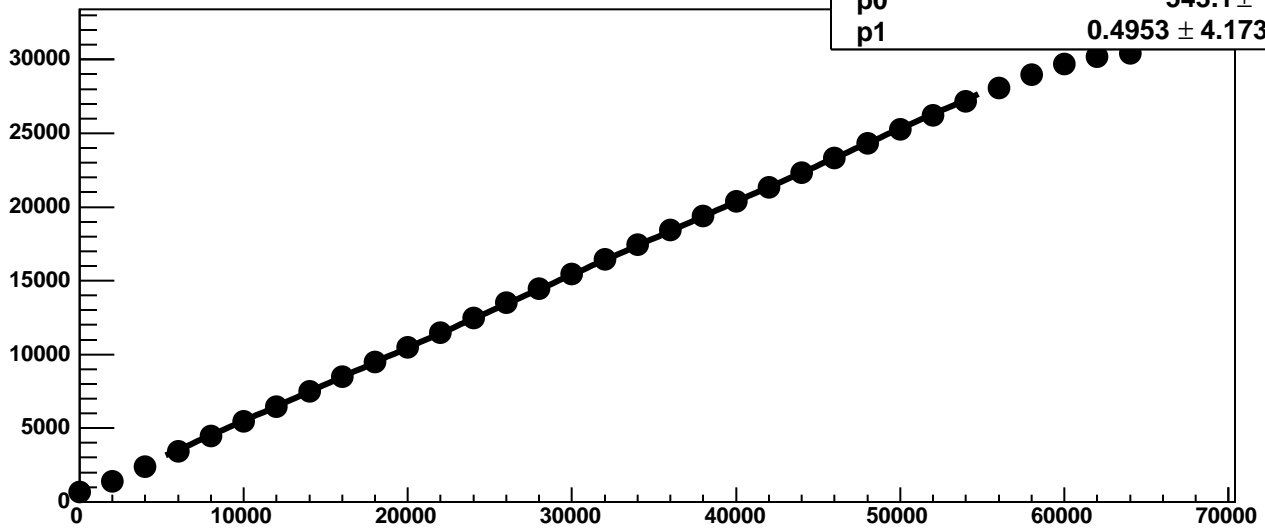
Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



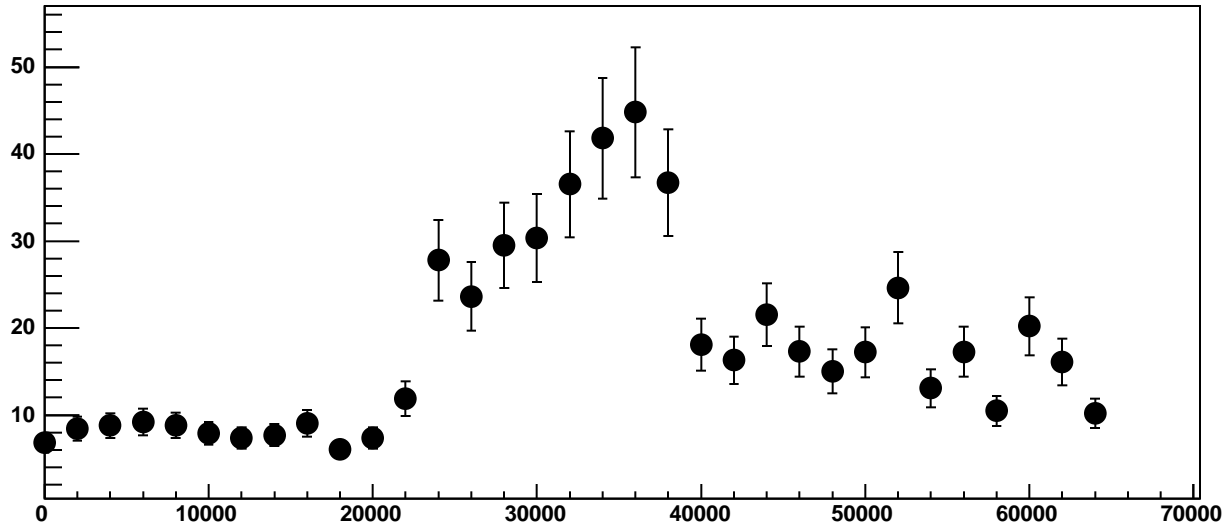
Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



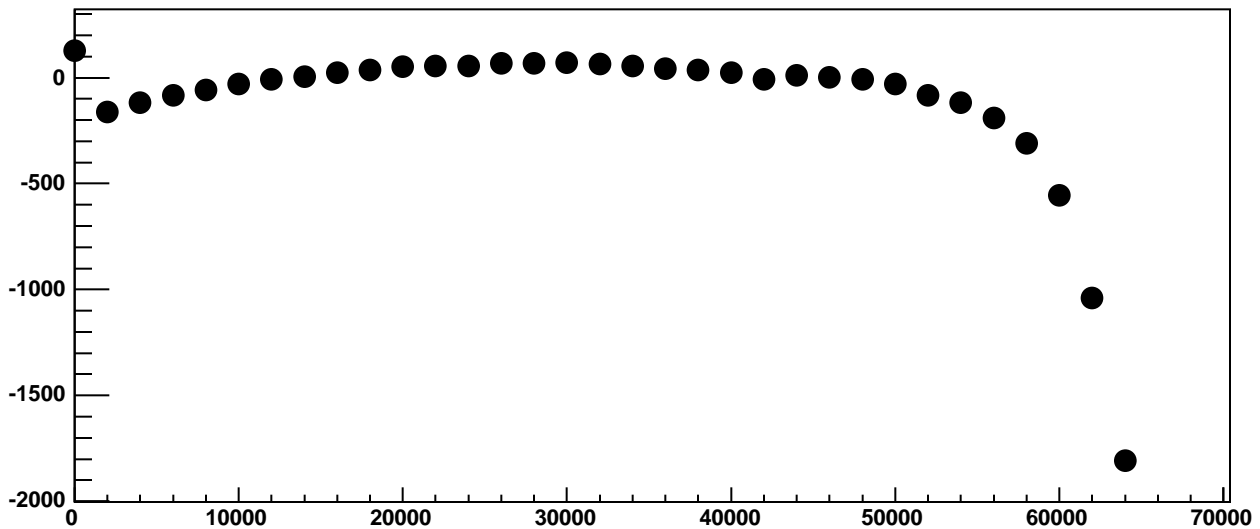
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



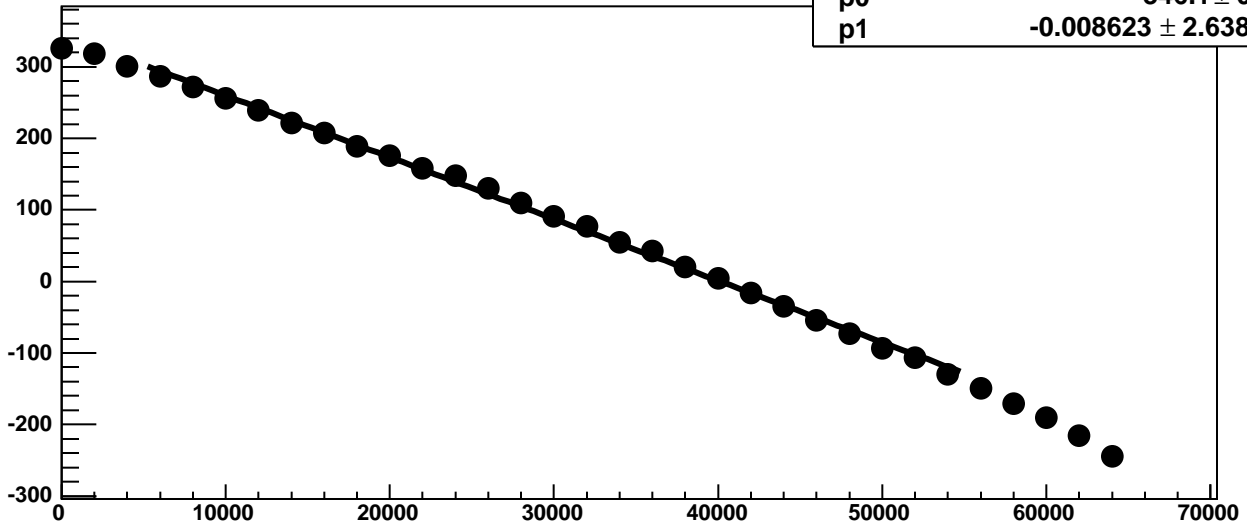
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

182.5 / 23

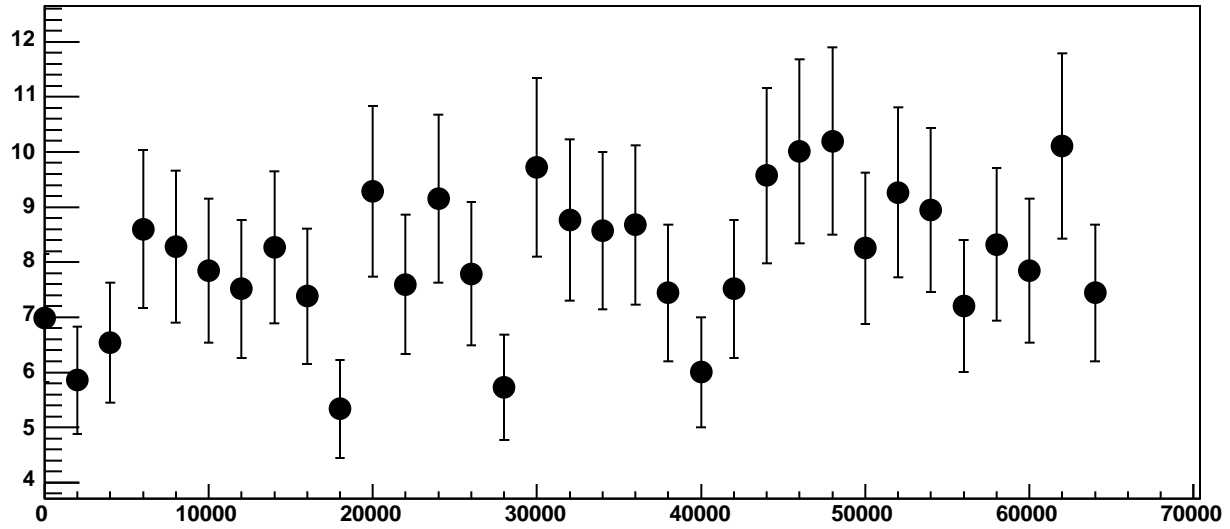
p0

346.1 ± 0.839

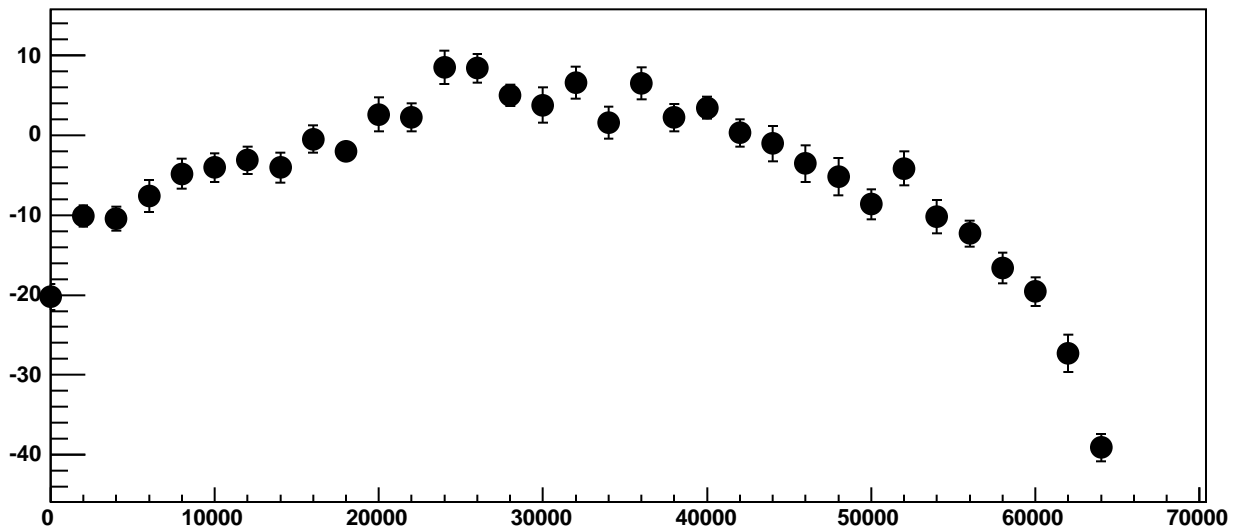
p1

$-0.008623 \pm 2.638e-05$

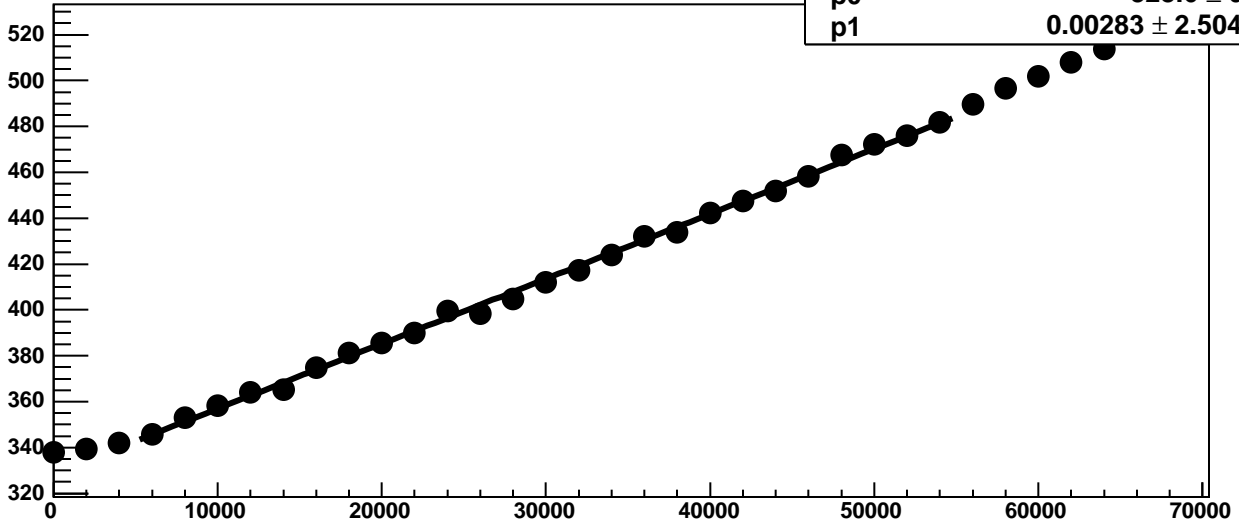
Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

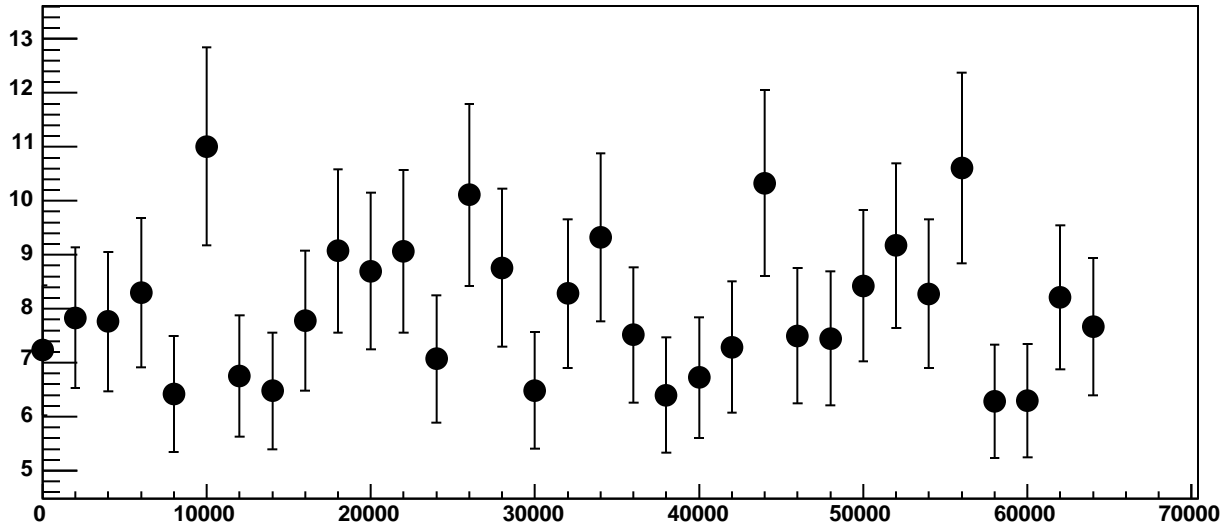


Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

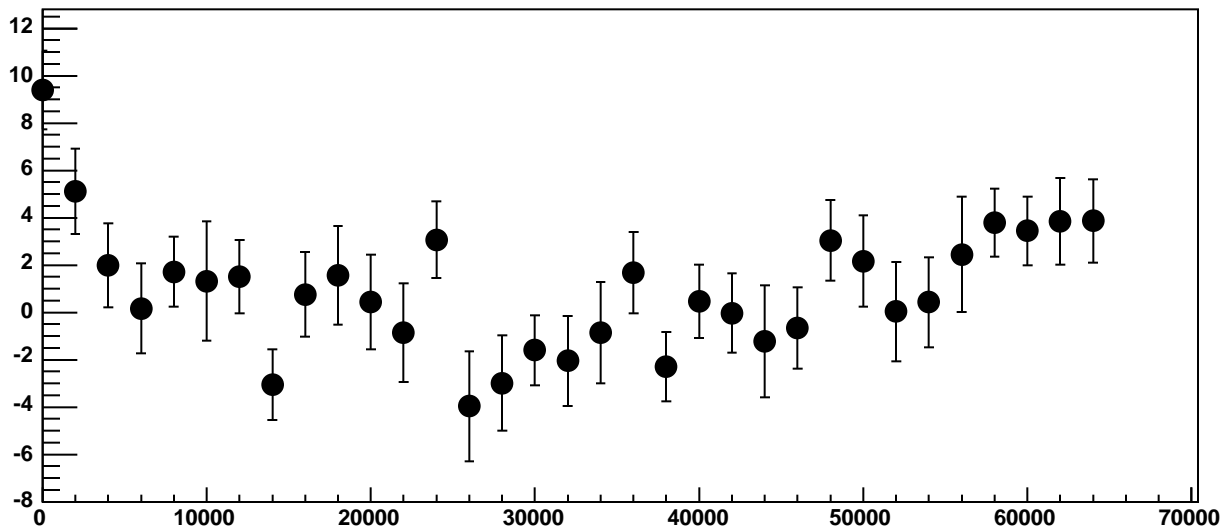


χ^2 / ndf 27.32 / 23
p0 328.6 ± 0.824
p1 $0.00283 \pm 2.504e-05$

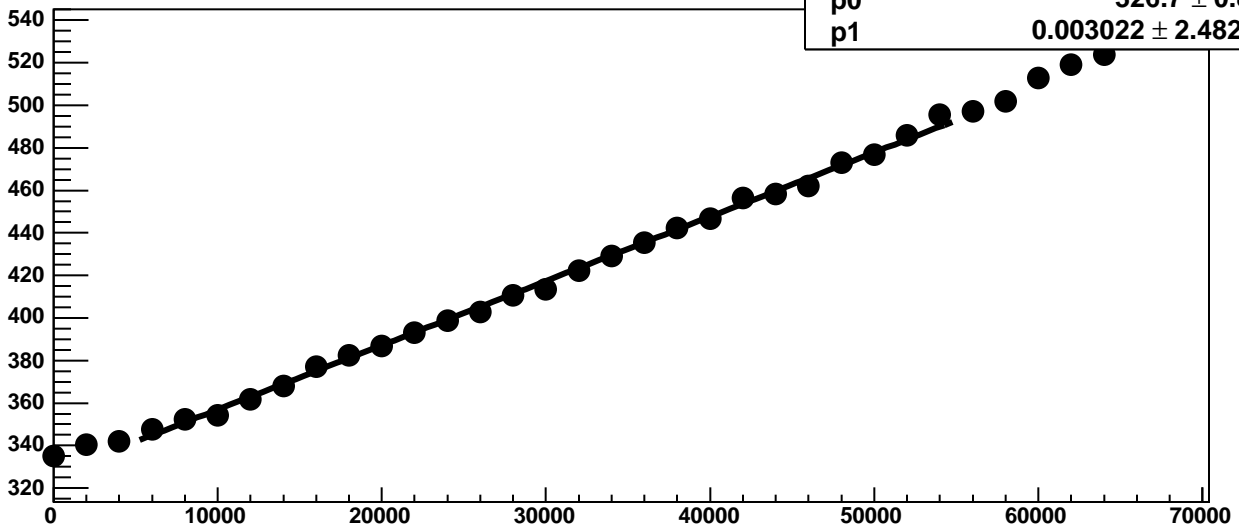
Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

34.08 / 23

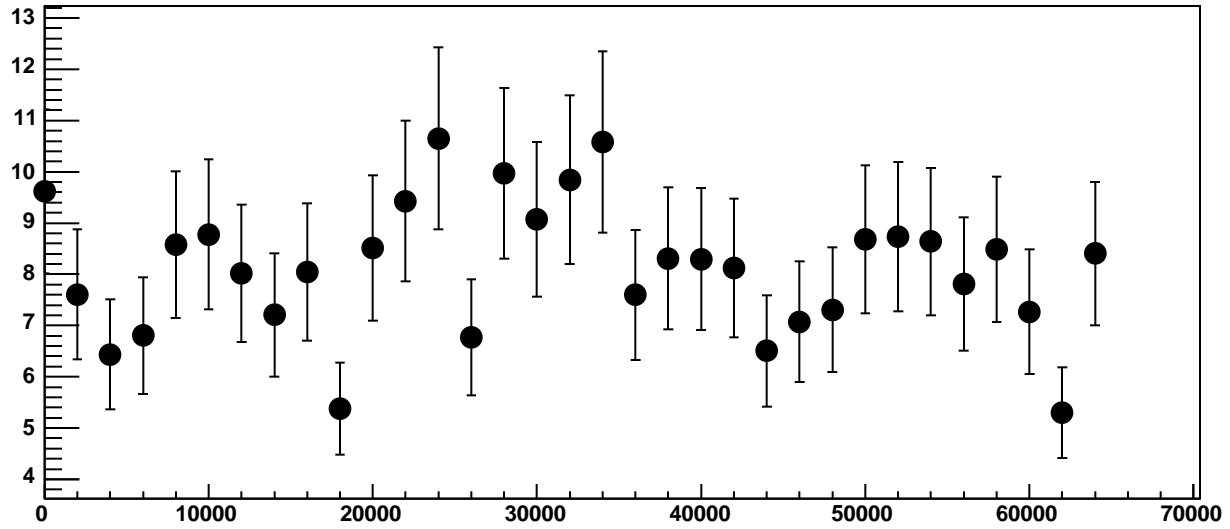
p0

326.7 ± 0.8172

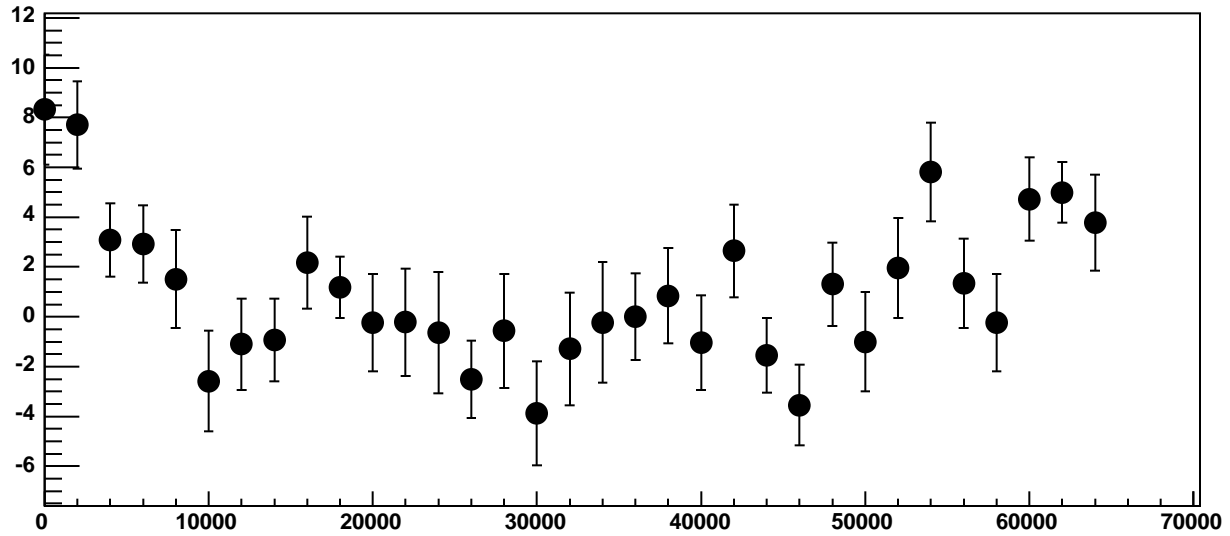
p1

$0.003022 \pm 2.482e-05$

Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

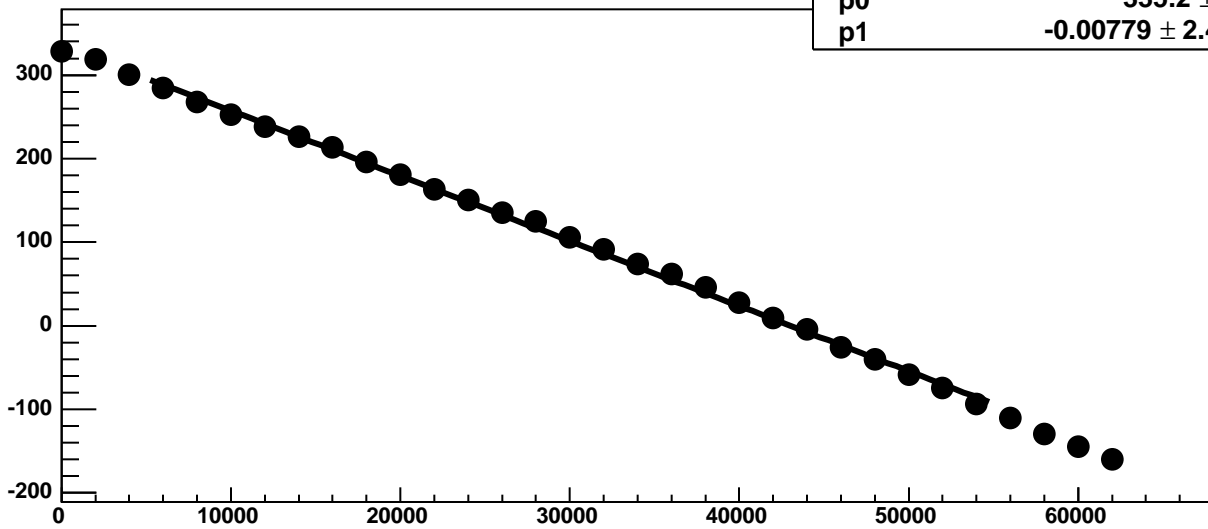


Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

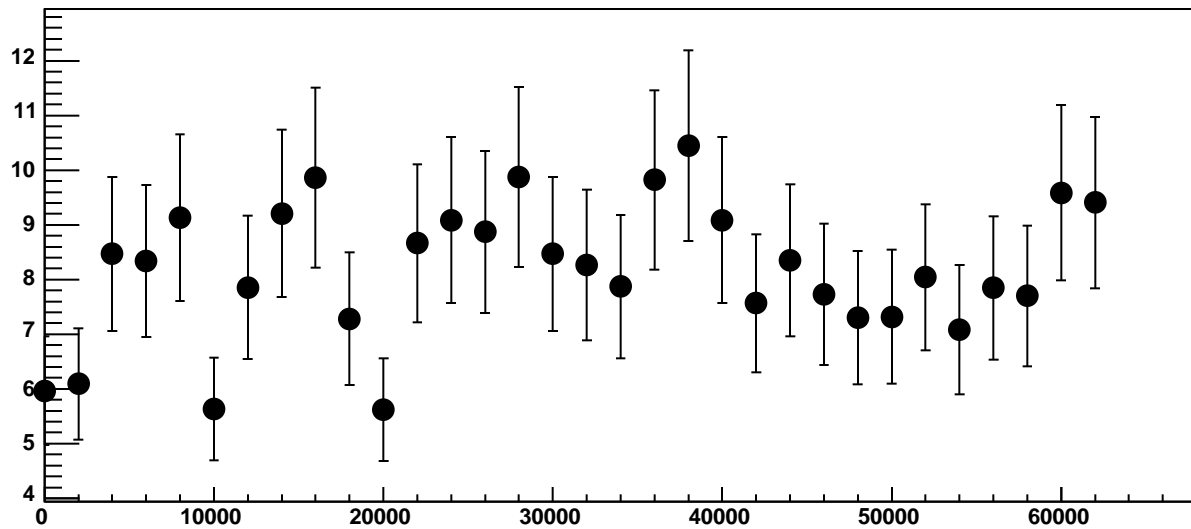


Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

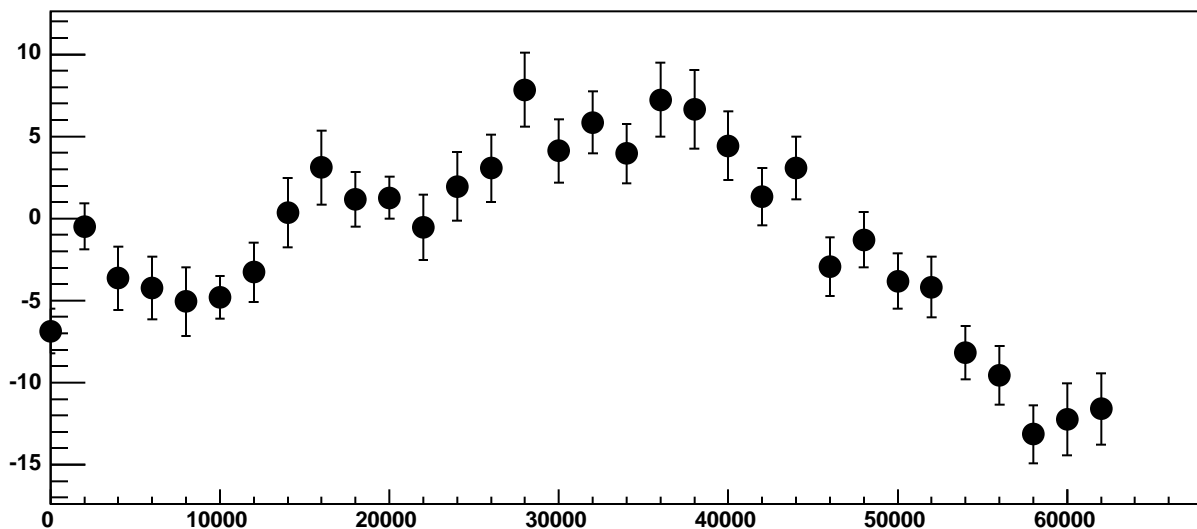
χ^2 / ndf 129.8 / 23
p0 335.2 ± 0.8109
p1 $-0.00779 \pm 2.432e-05$



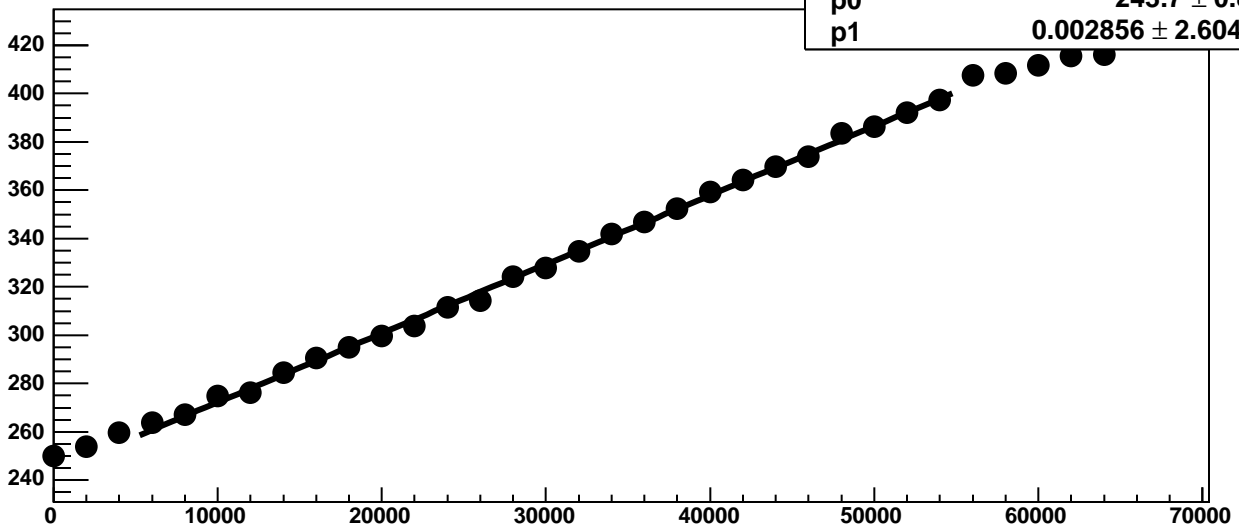
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

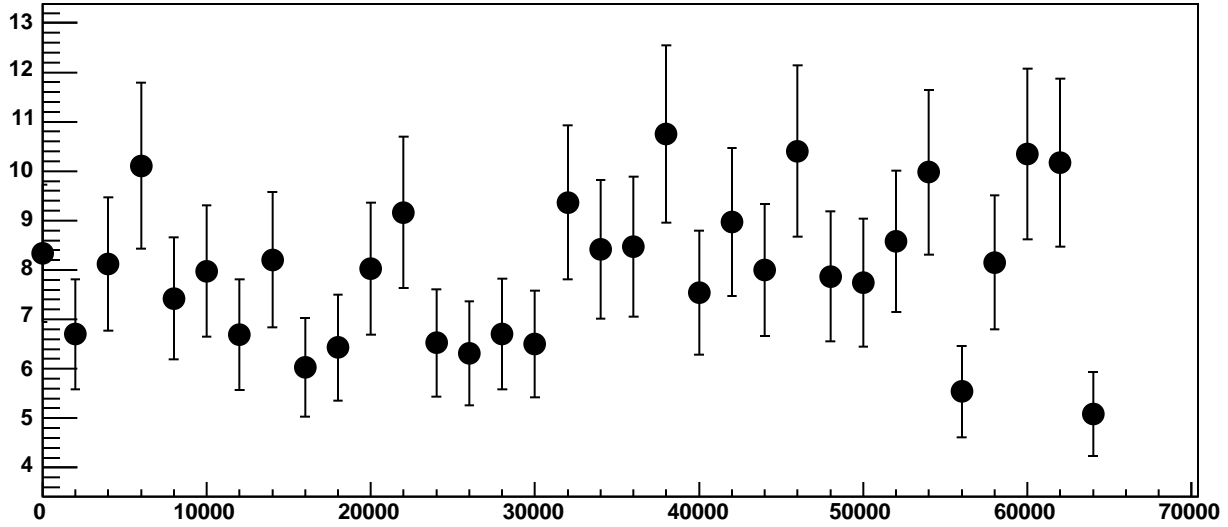


Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

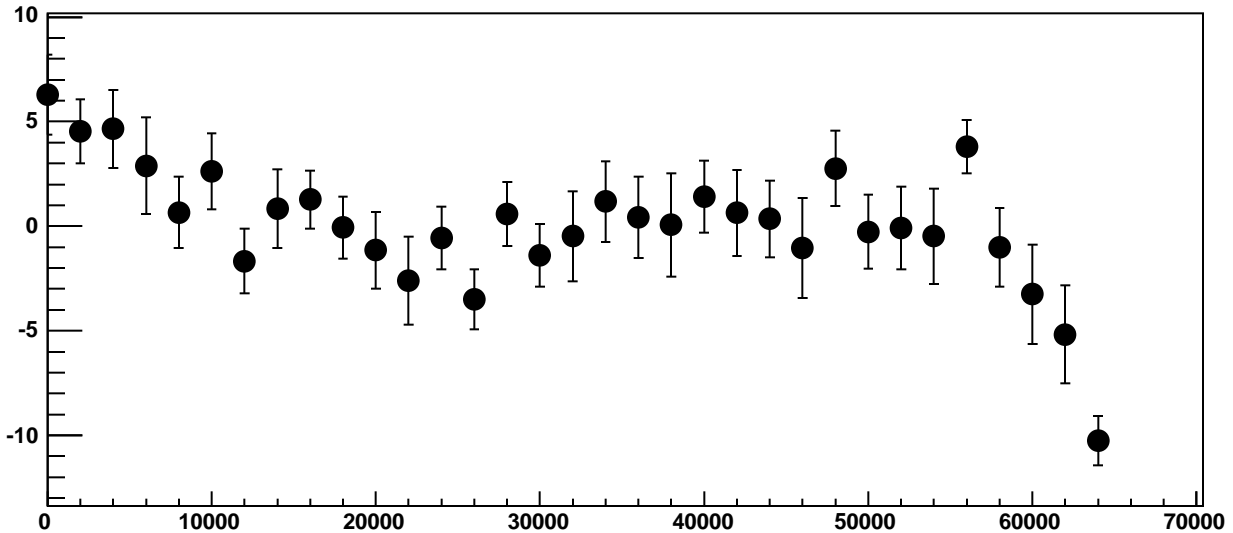


χ^2 / ndf 18.78 / 23
p0 243.7 ± 0.8182
p1 $0.002856 \pm 2.604e-05$

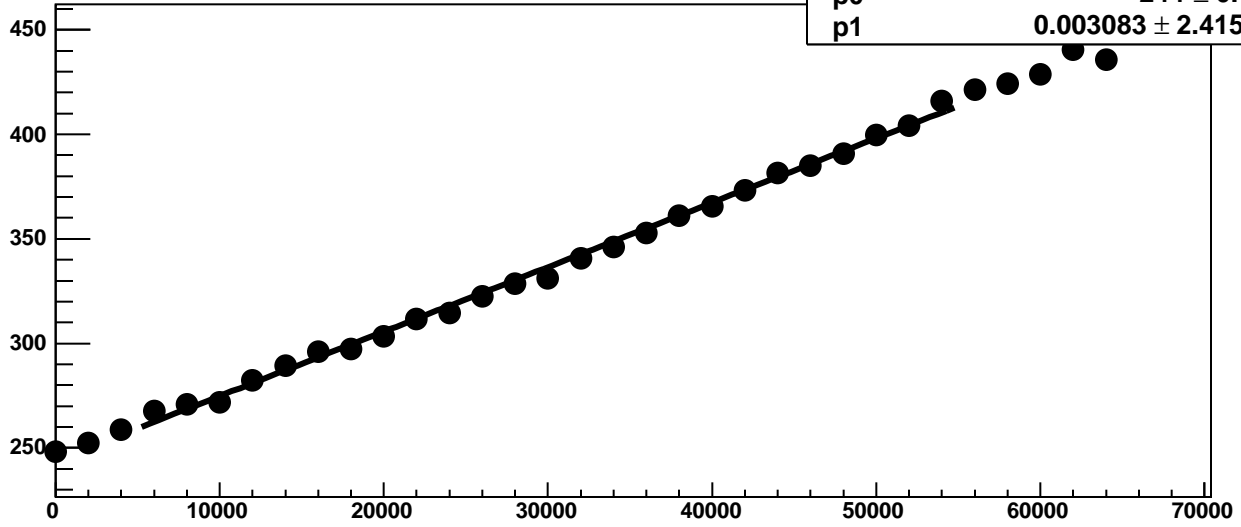
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

49.78 / 23

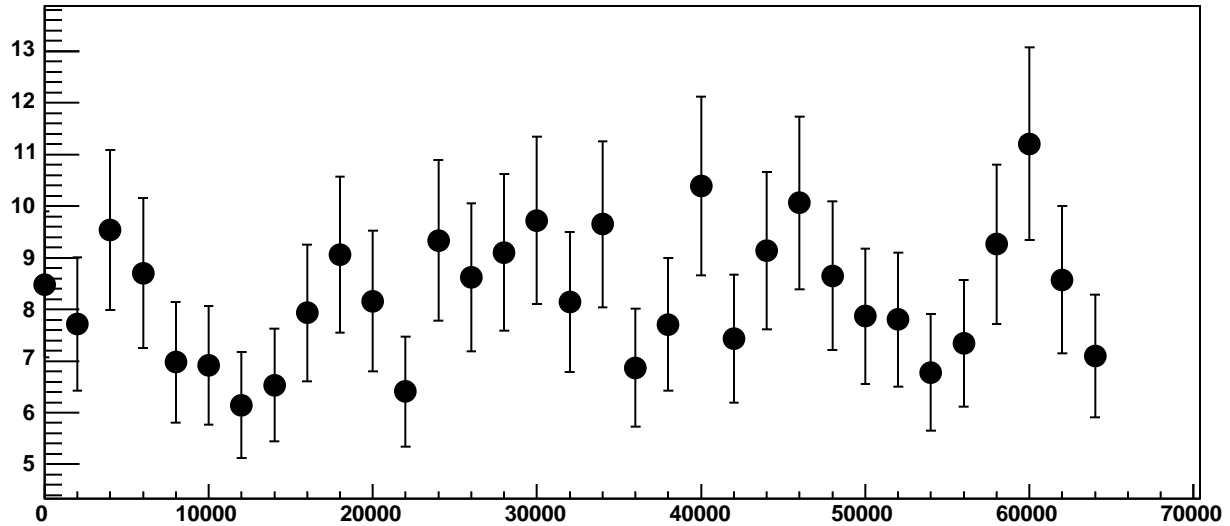
p0

244 ± 0.7842

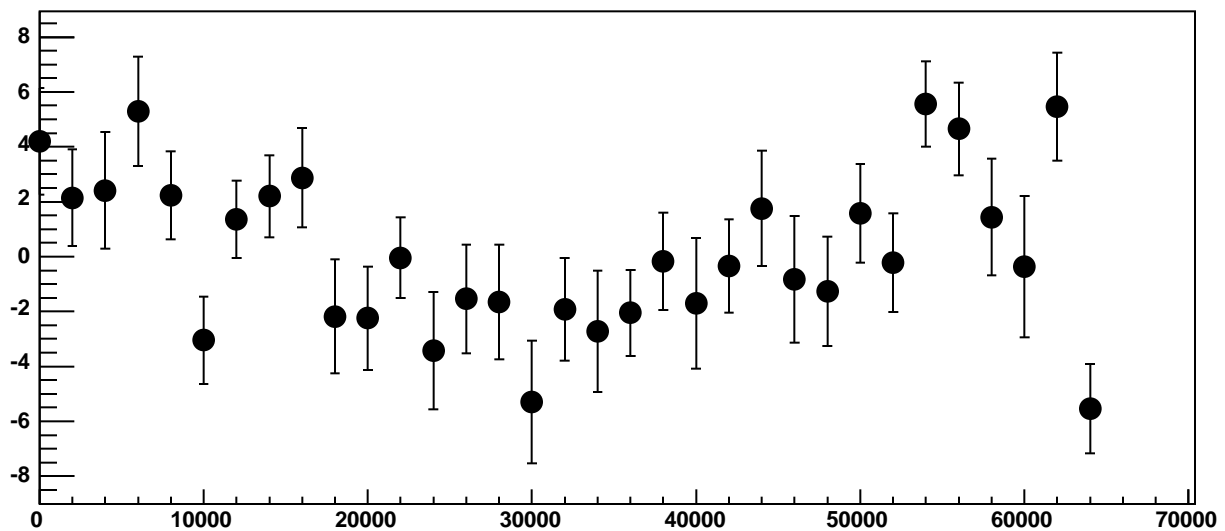
p1

$0.003083 \pm 2.415e-05$

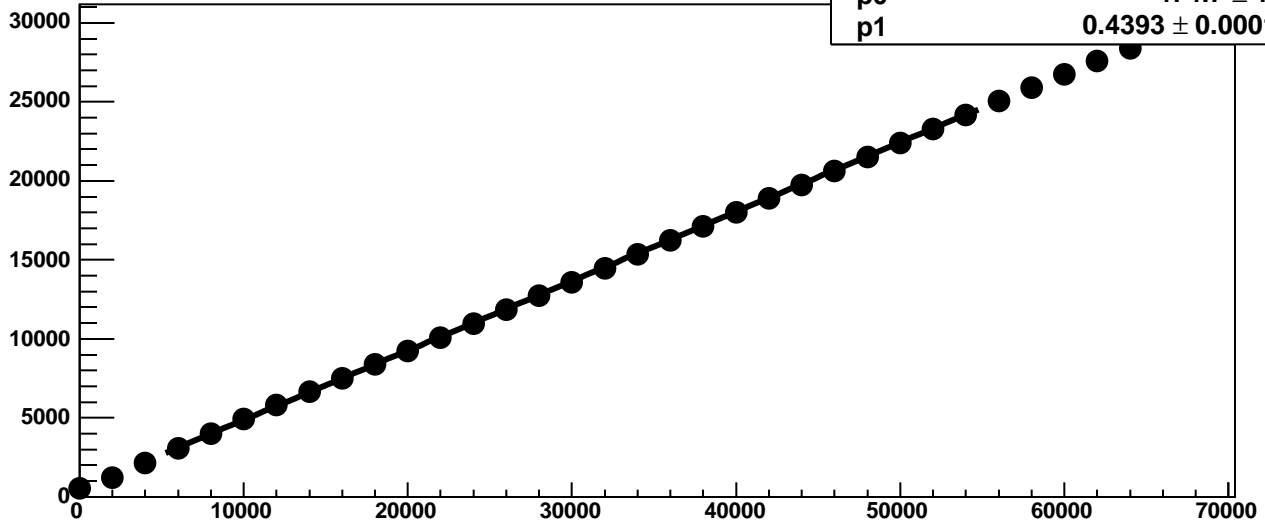
Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



χ^2 / ndf

884.9 / 23

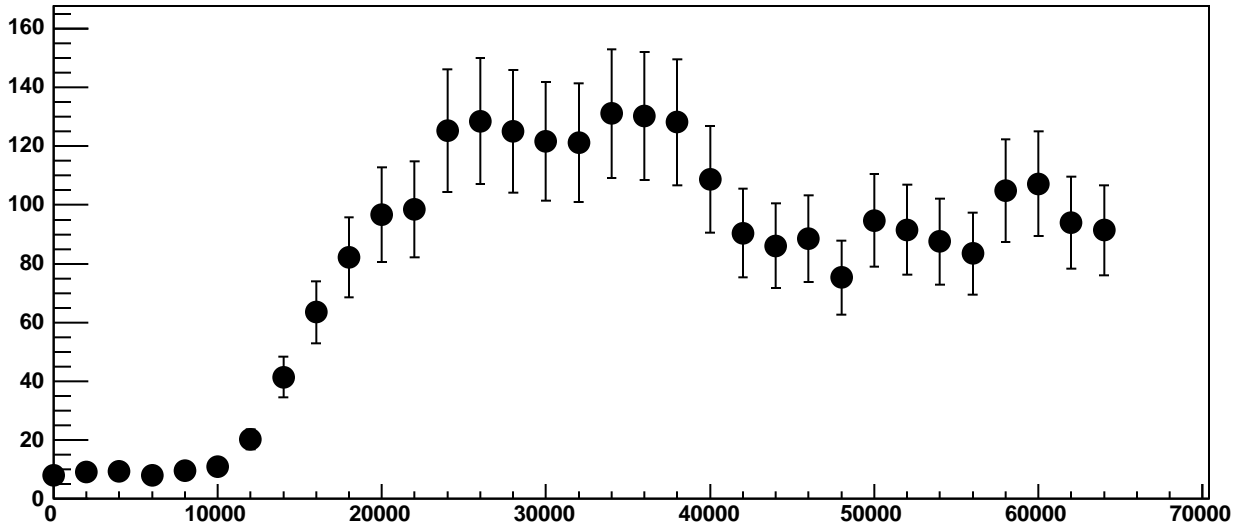
p0

474.7 ± 1.926

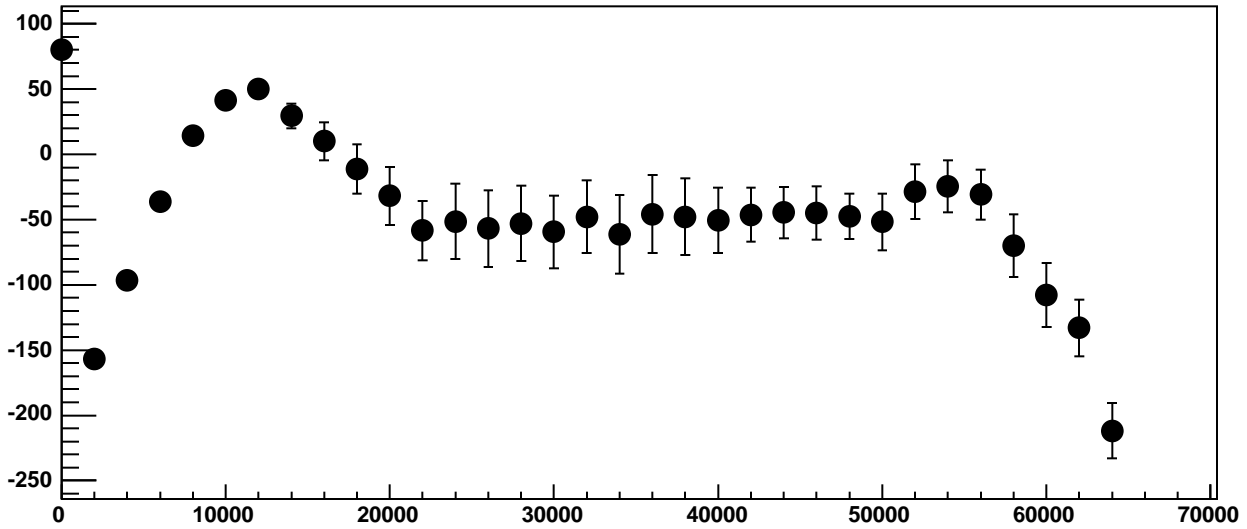
p1

0.4393 ± 0.0001628

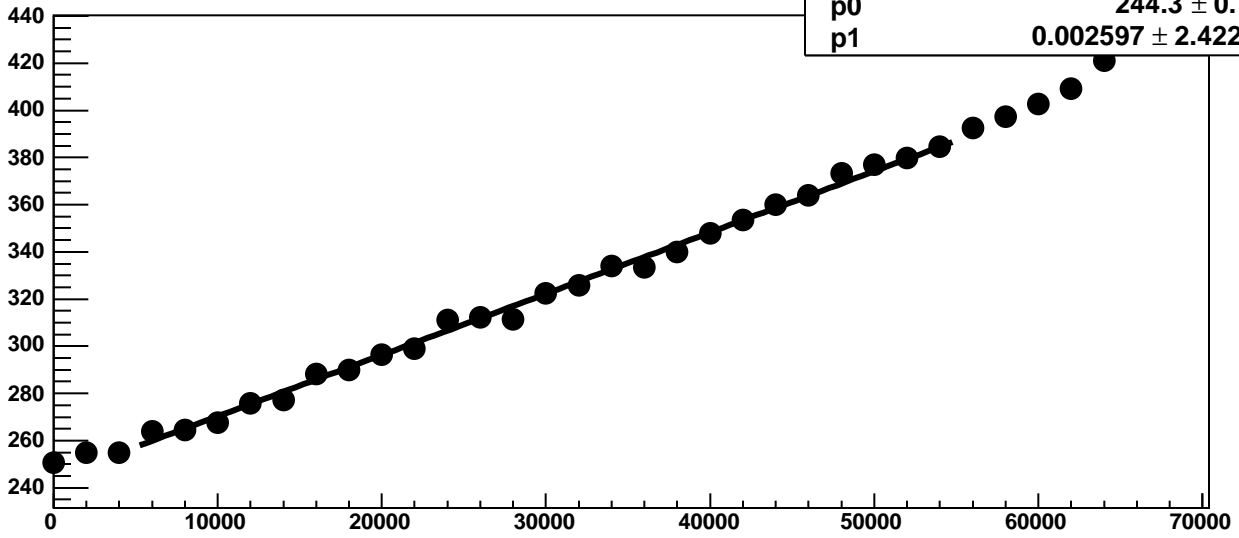
Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

57.42 / 23

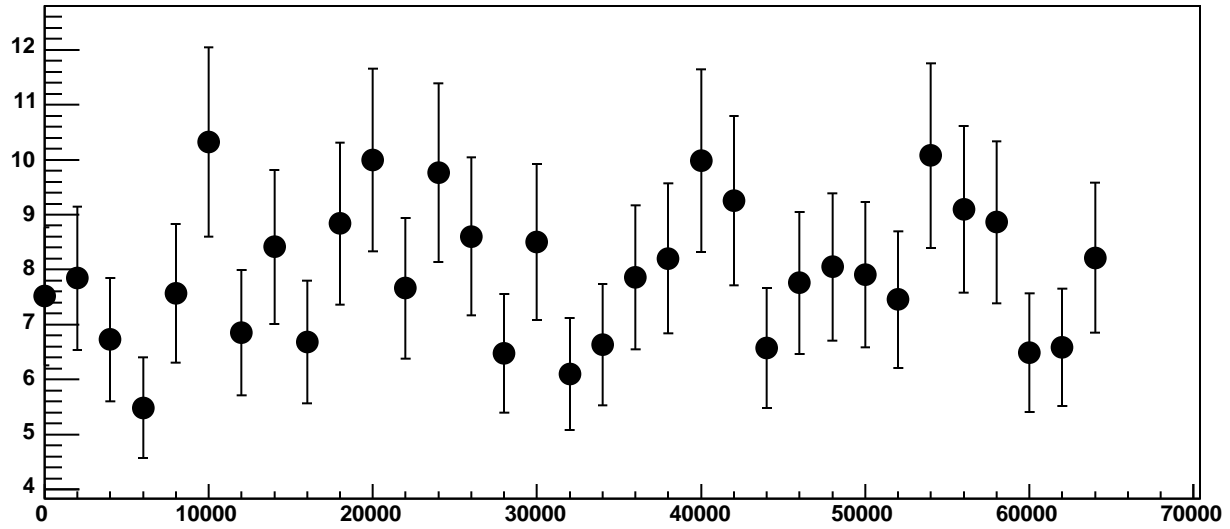
p0

244.3 ± 0.7863

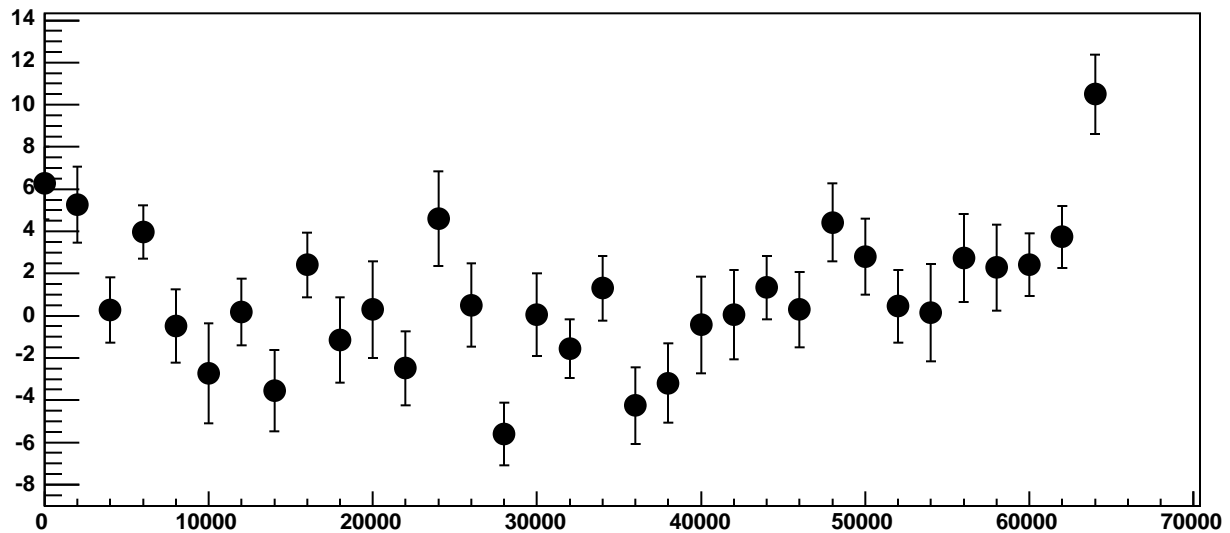
p1

$0.002597 \pm 2.422e-05$

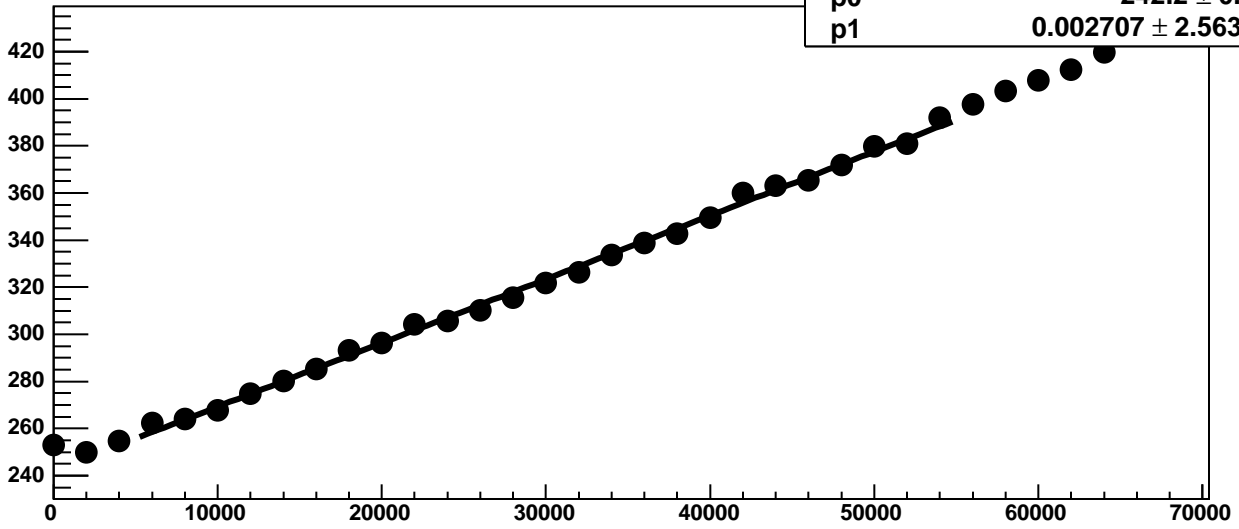
Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.13 / 23

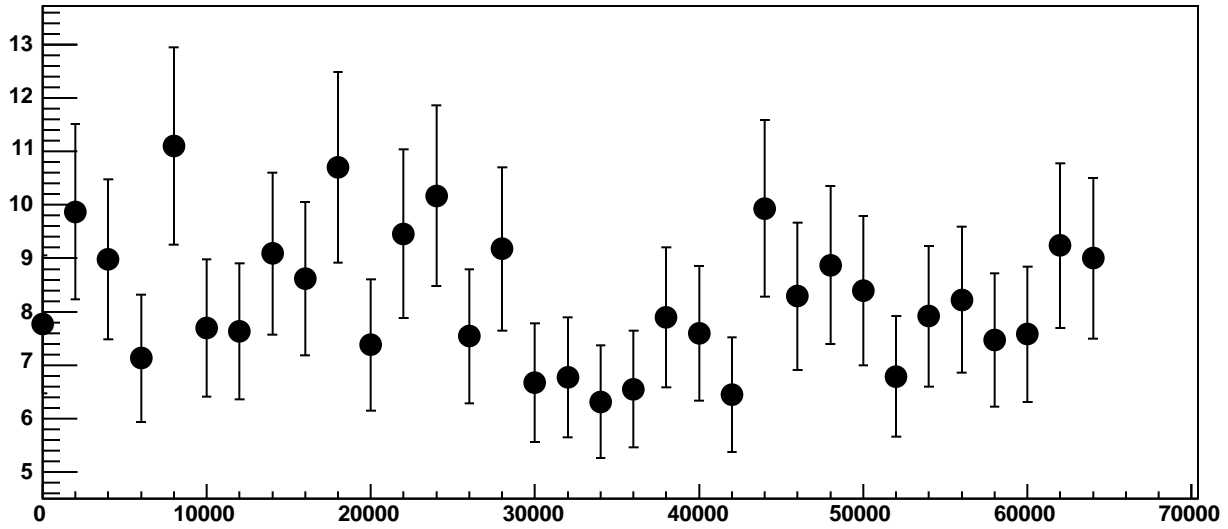
p0

242.2 ± 0.8731

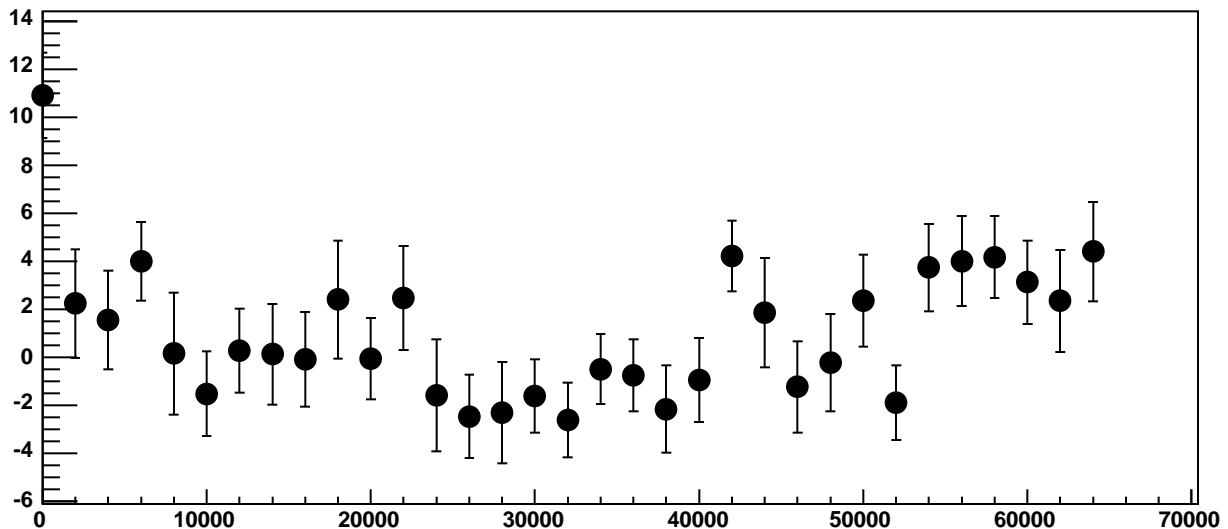
p1

$0.002707 \pm 2.563e-05$

Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

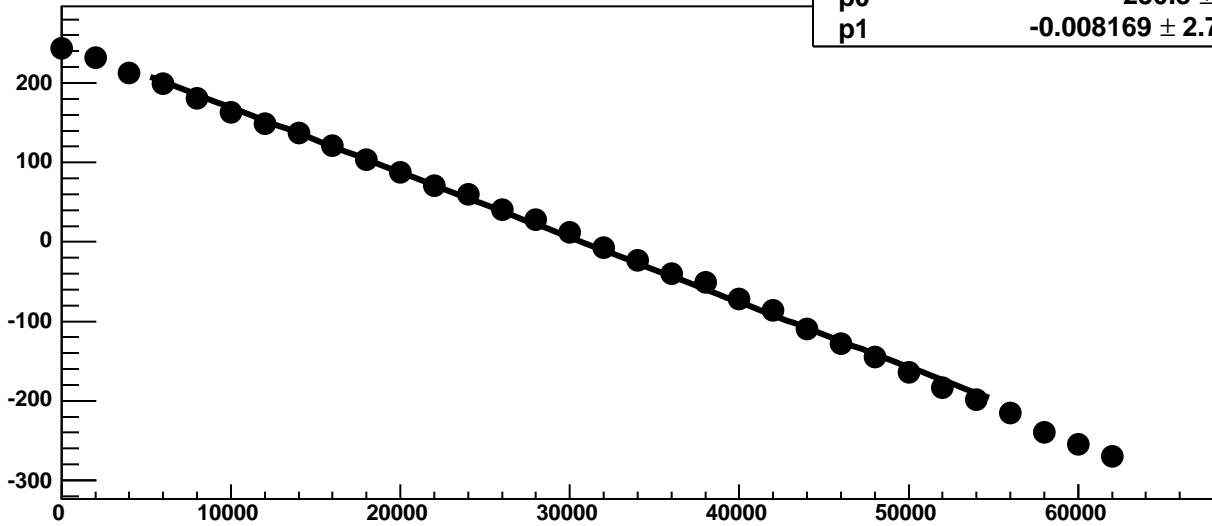


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

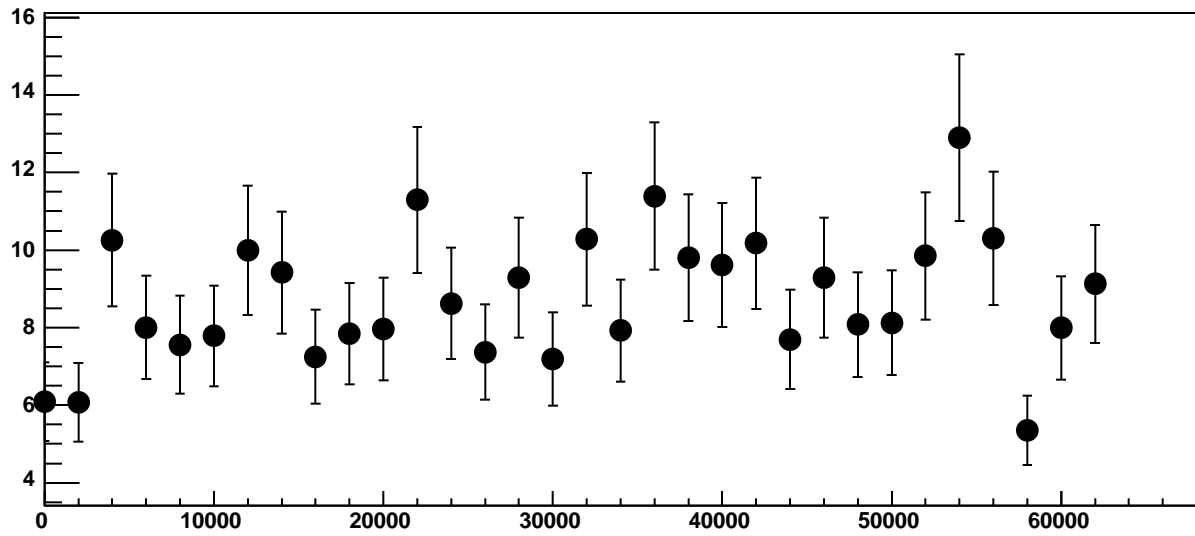


Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

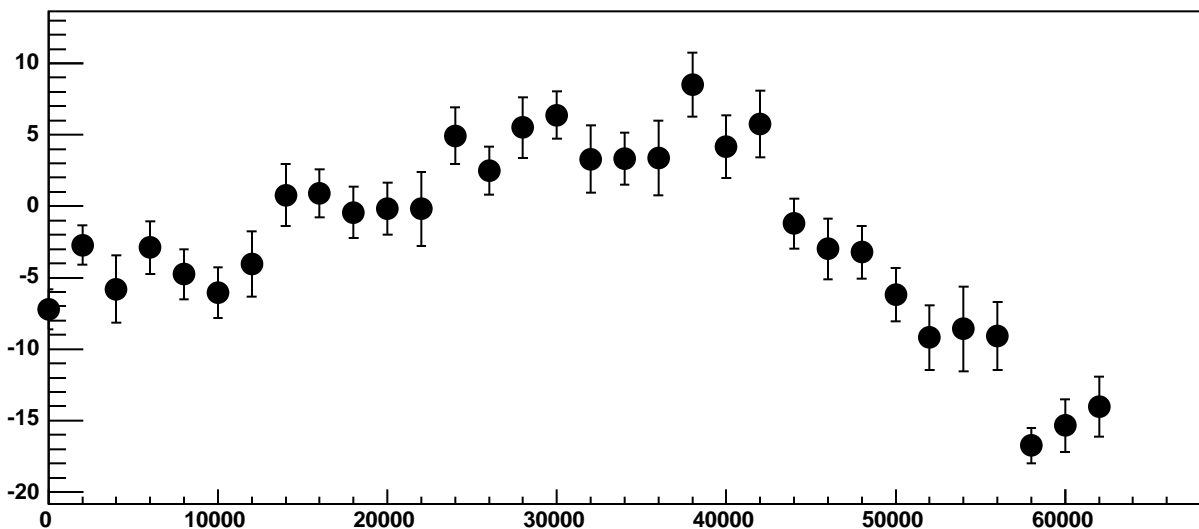
χ^2 / ndf 127.4 / 23
p0 250.8 ± 0.8877
p1 -0.008169 ± 2.785e-05



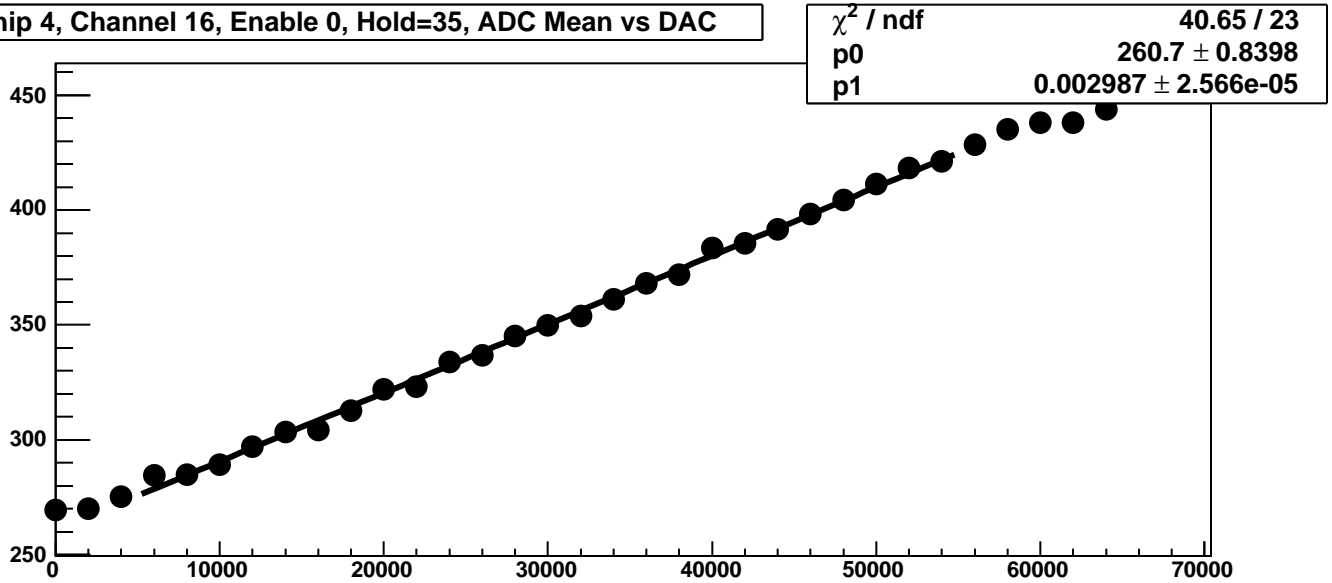
Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



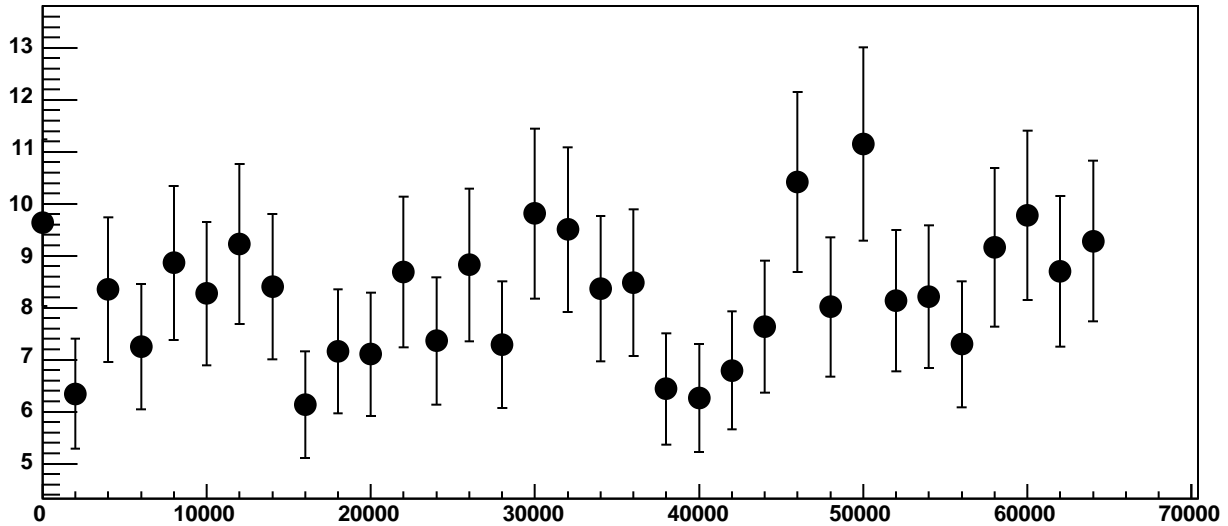
Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



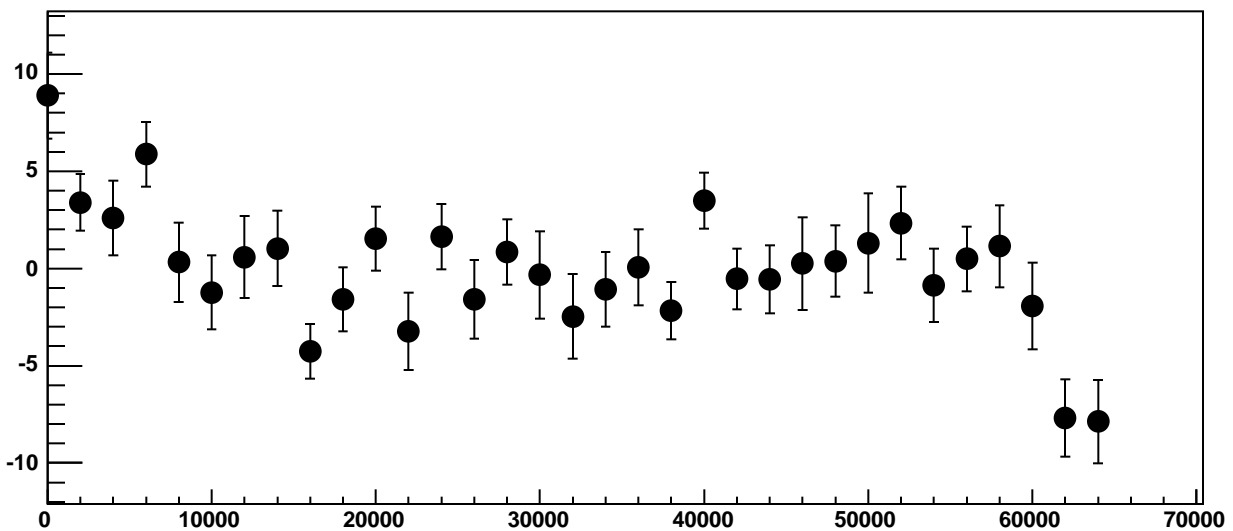
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



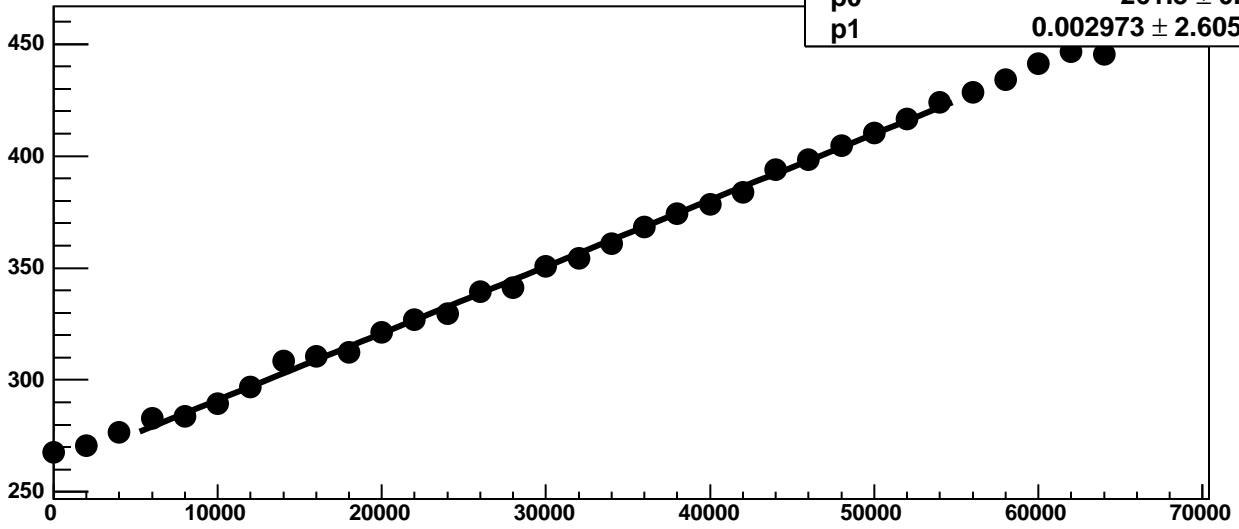
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

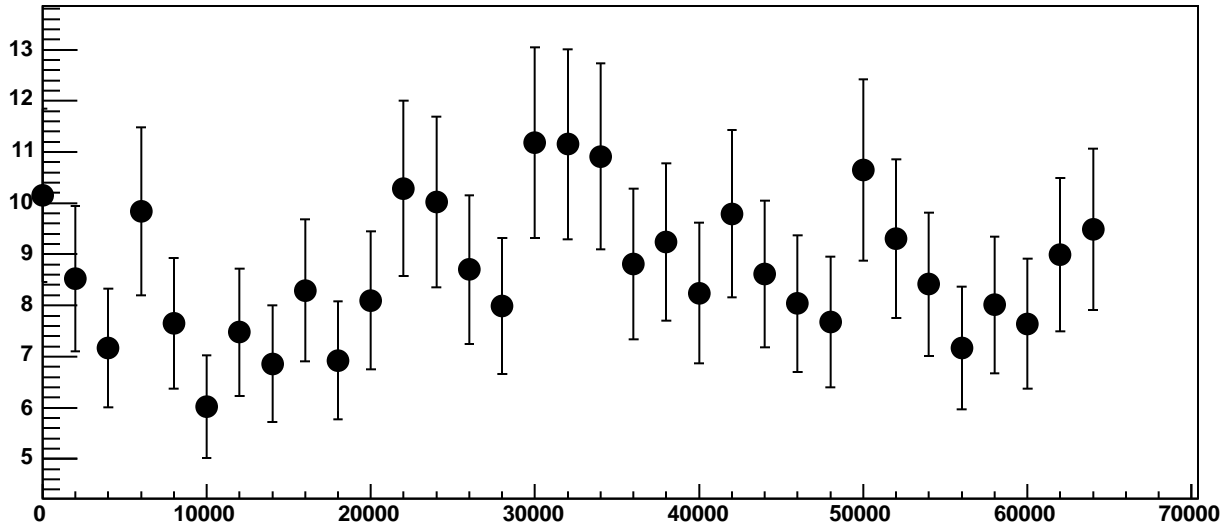


Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

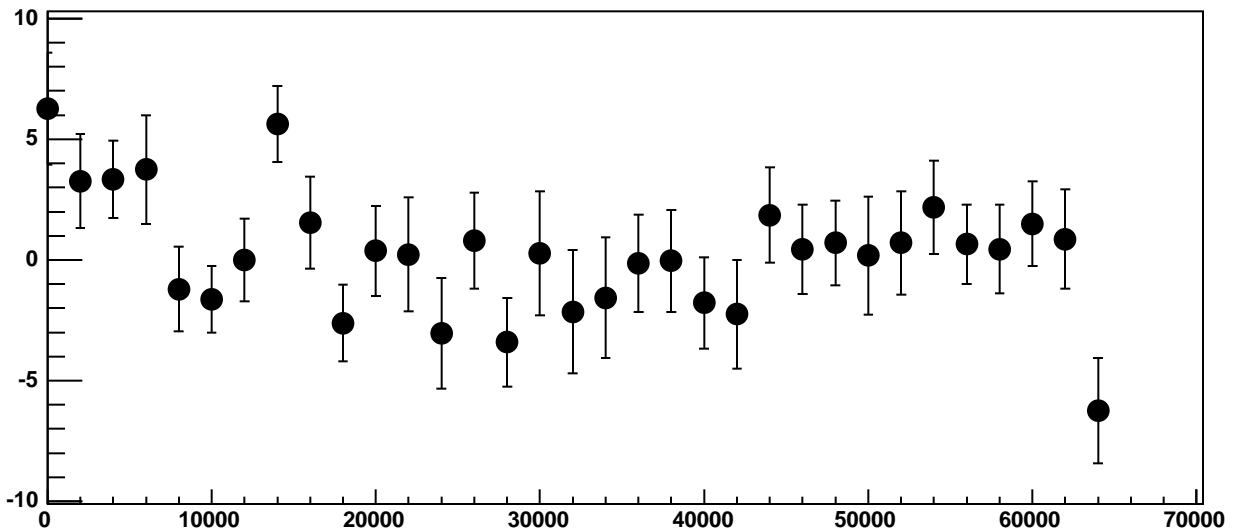


χ^2 / ndf 31.72 / 23
p0 261.3 ± 0.8291
p1 0.002973 ± 2.605e-05

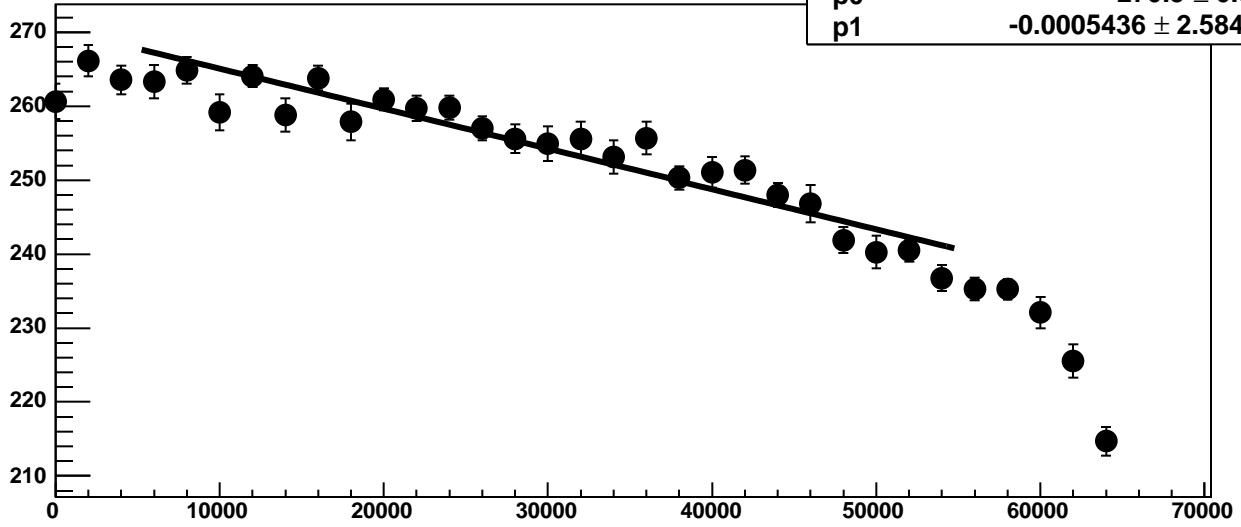
Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



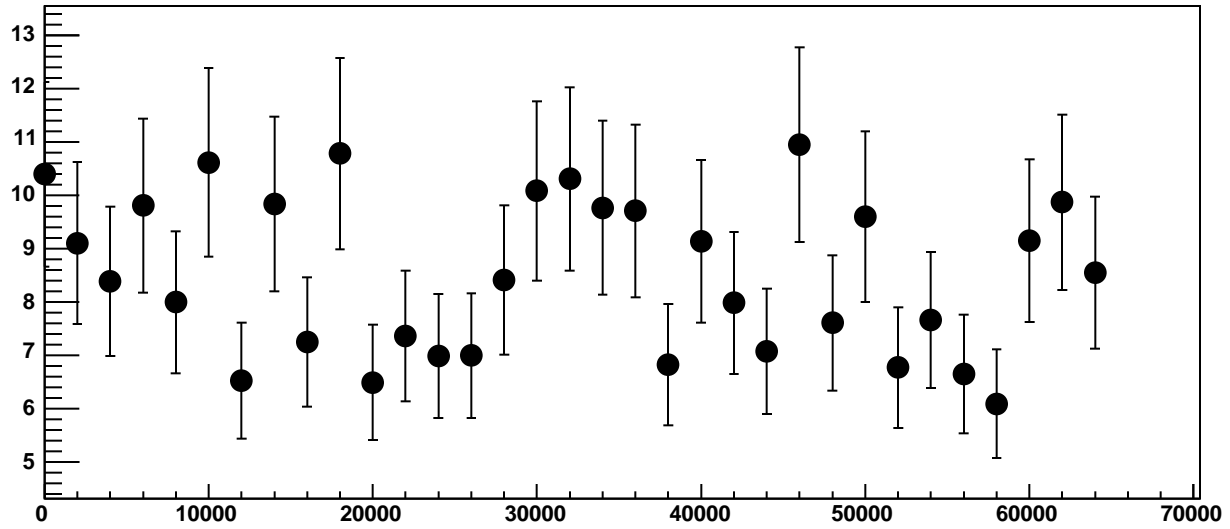
Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



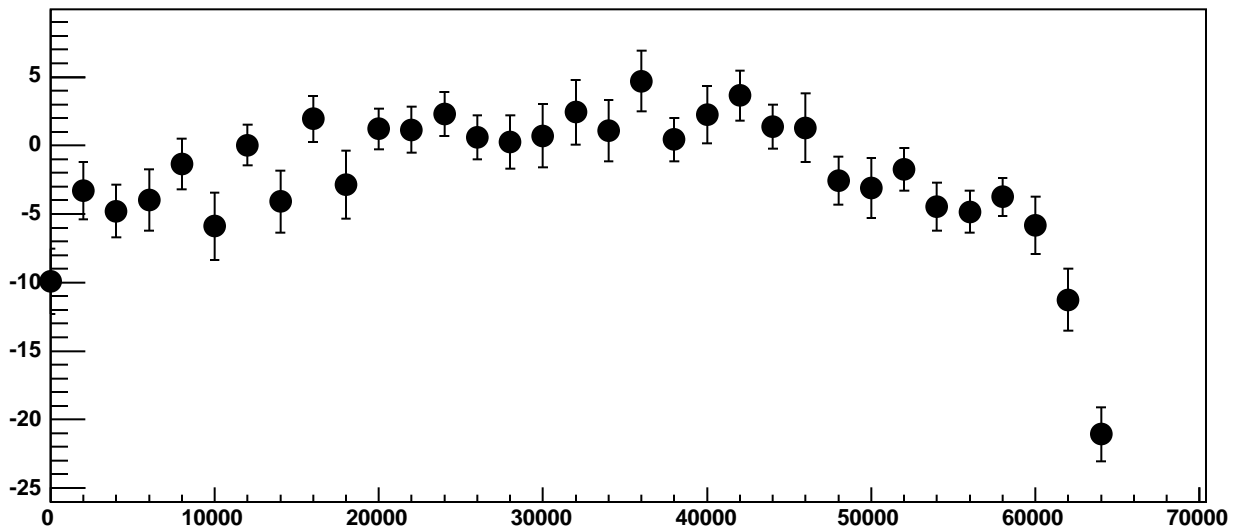
Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



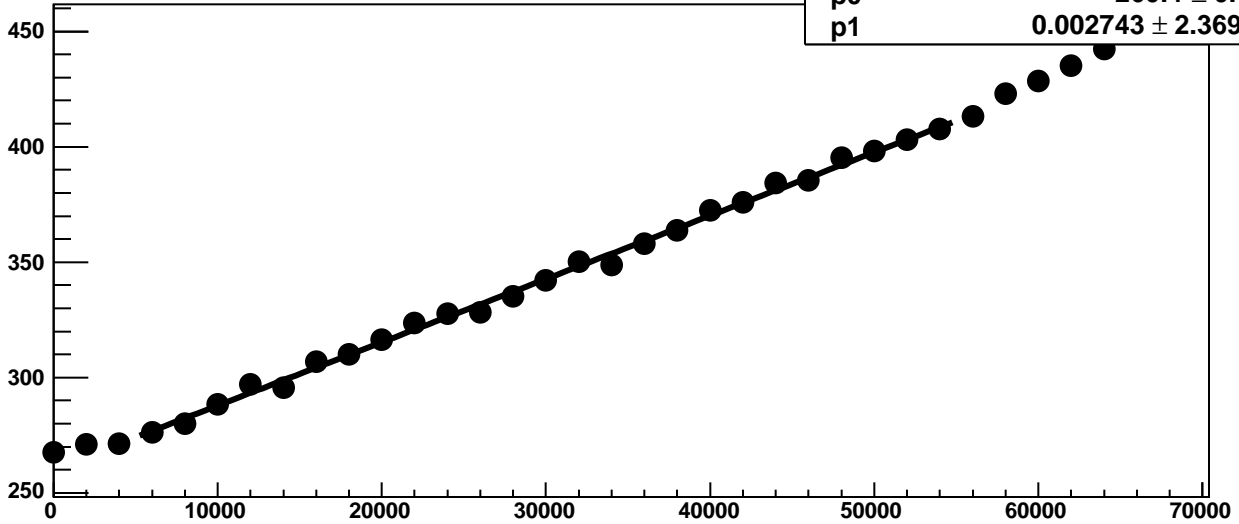
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



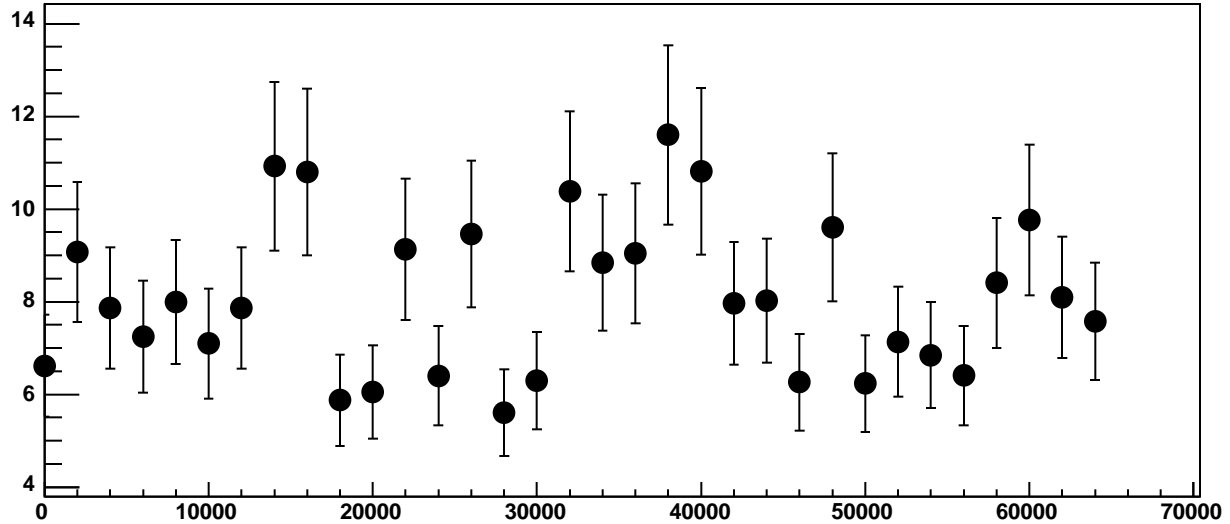
Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



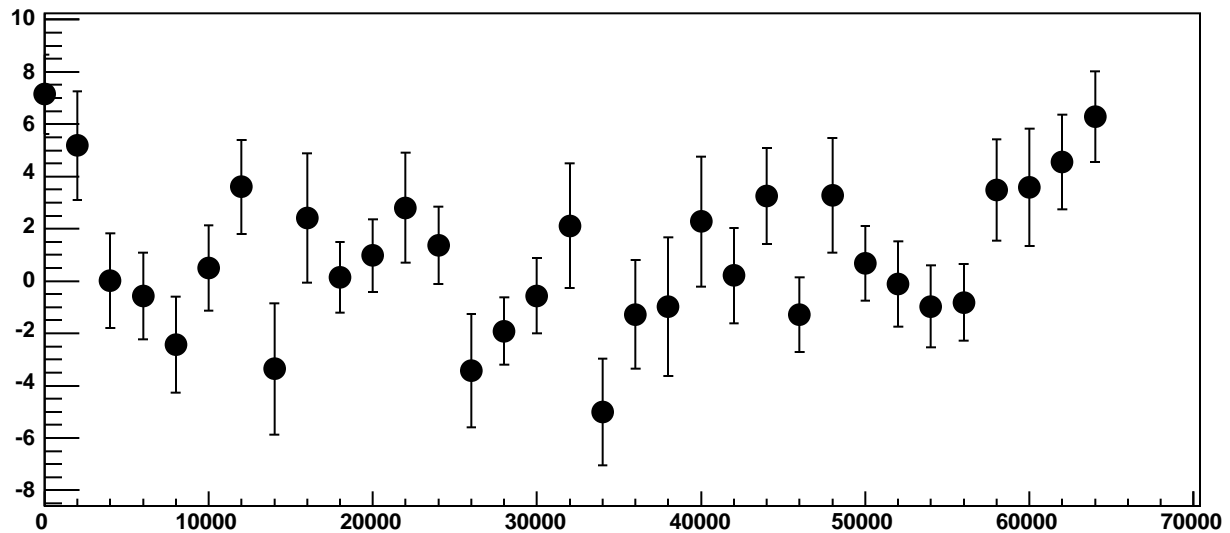
Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



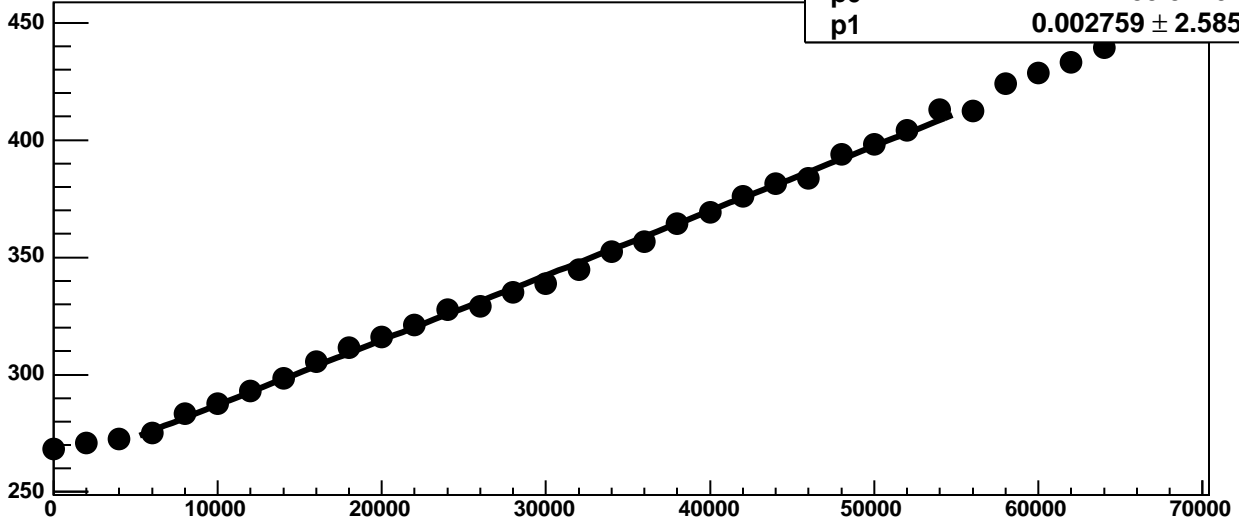
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

26.17 / 23

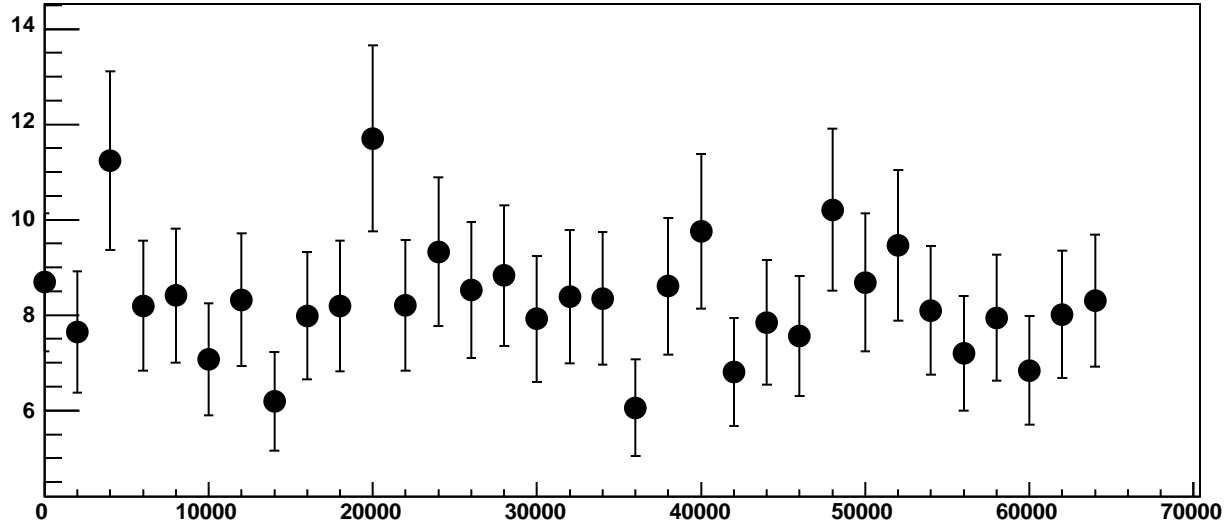
p0

259.5 ± 0.8473

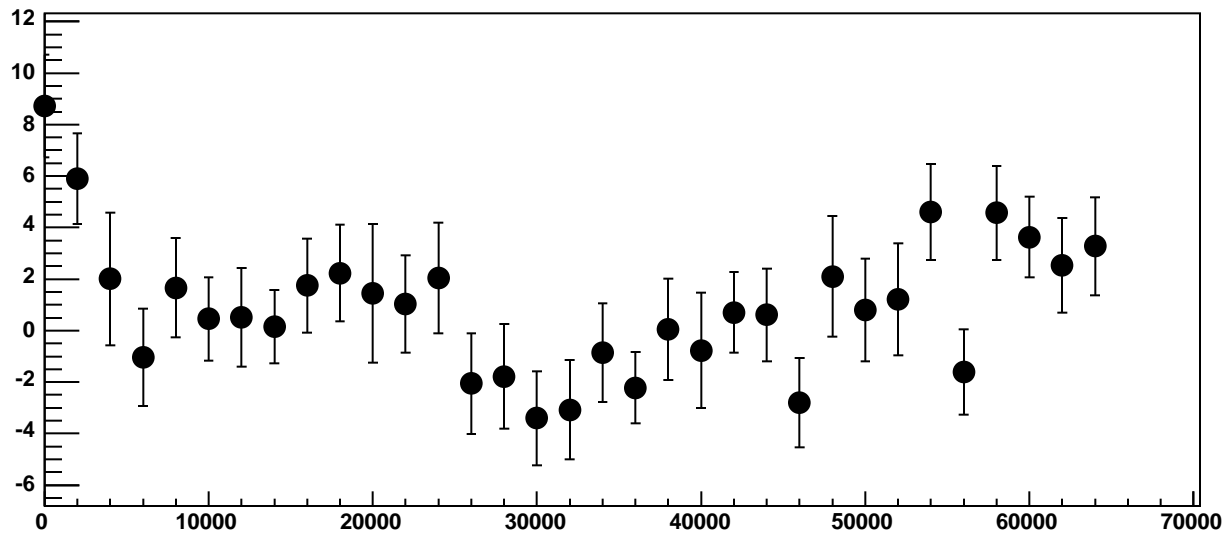
p1

$0.002759 \pm 2.585e-05$

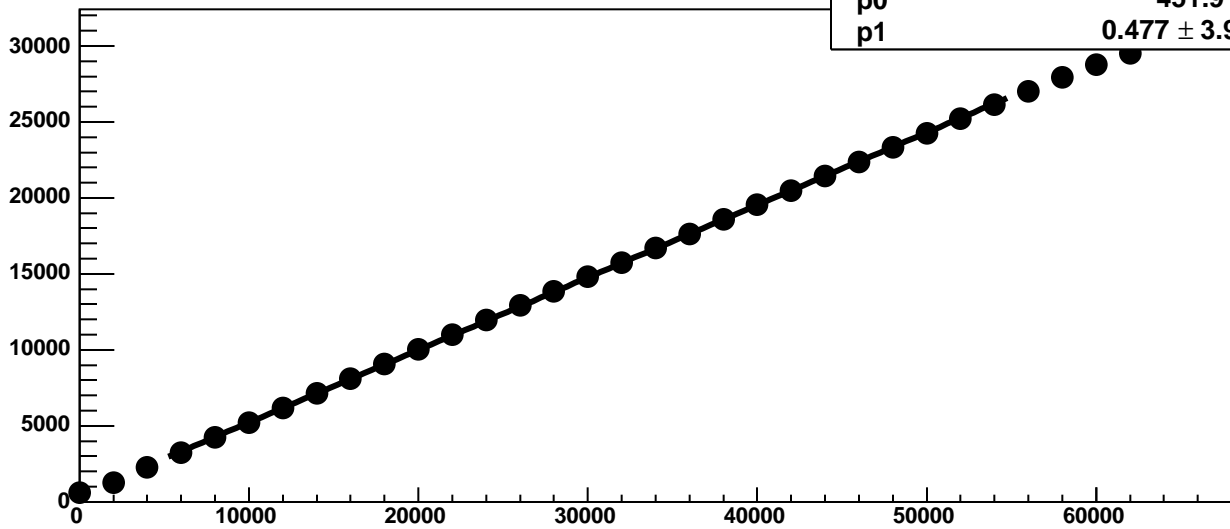
Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



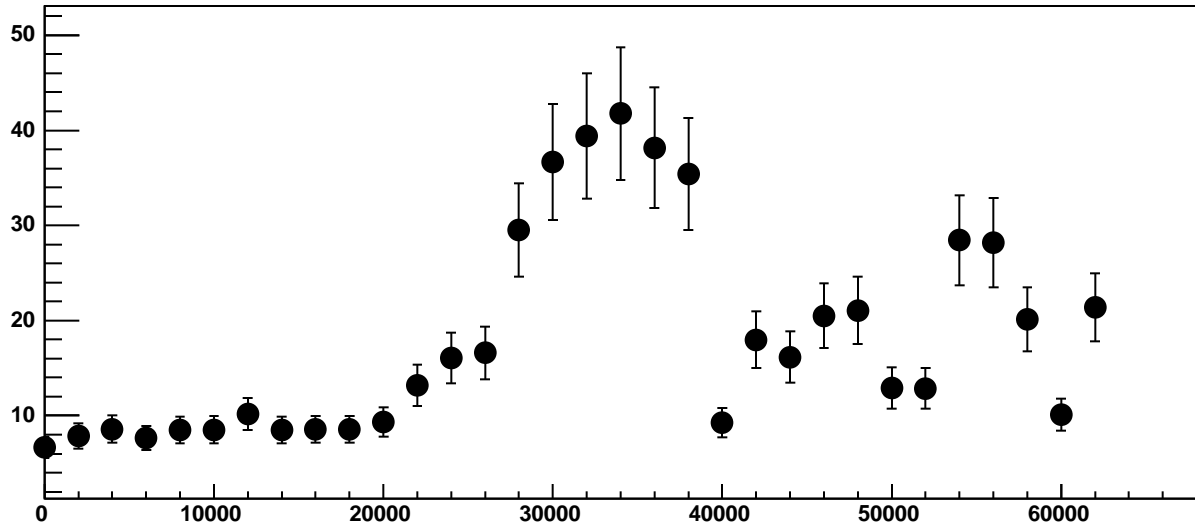
Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



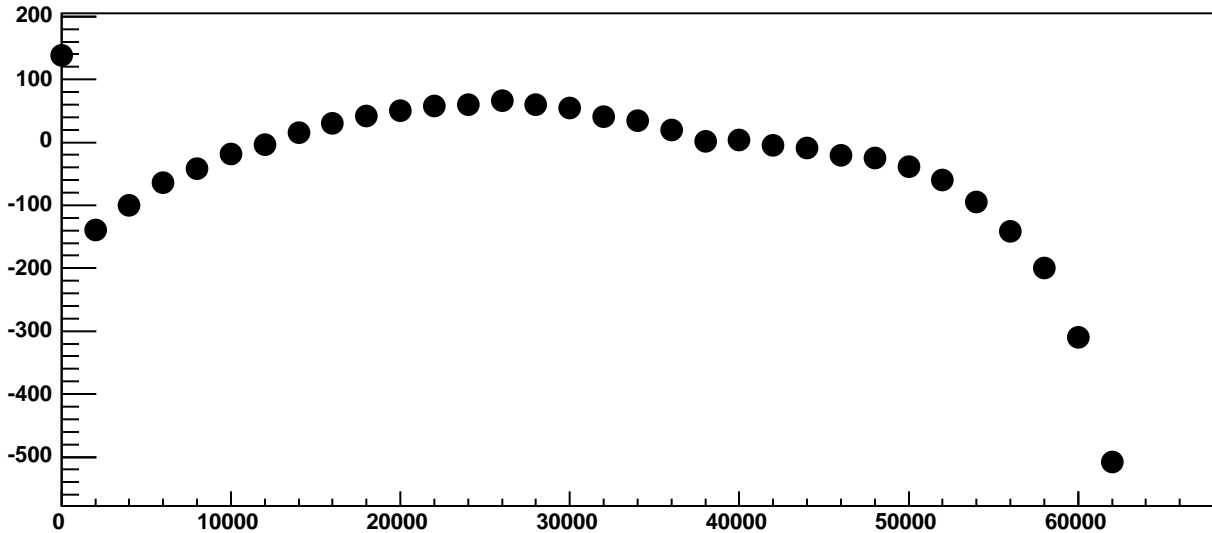
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



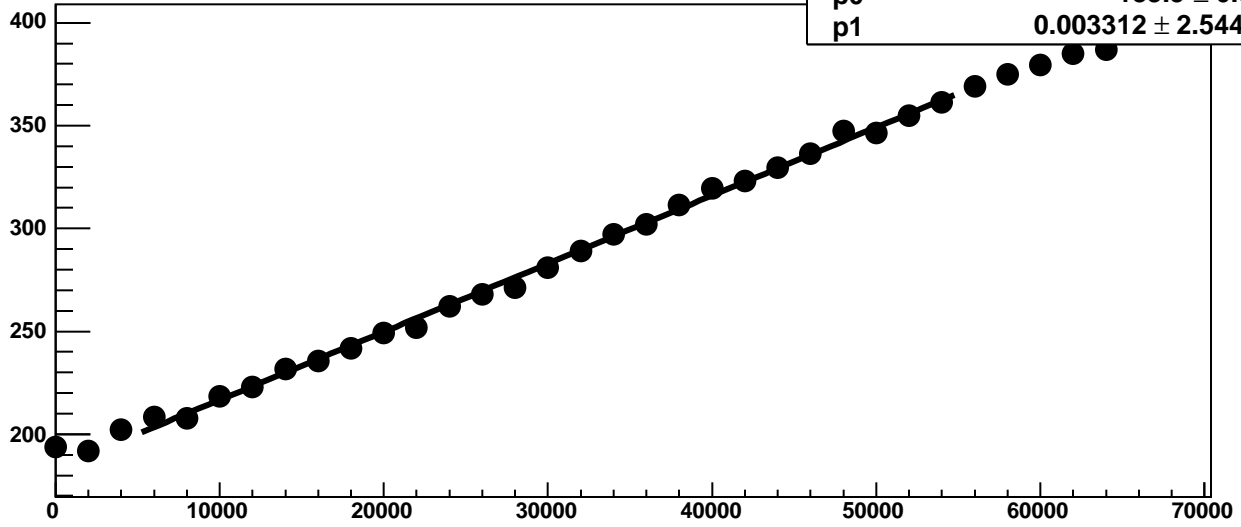
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

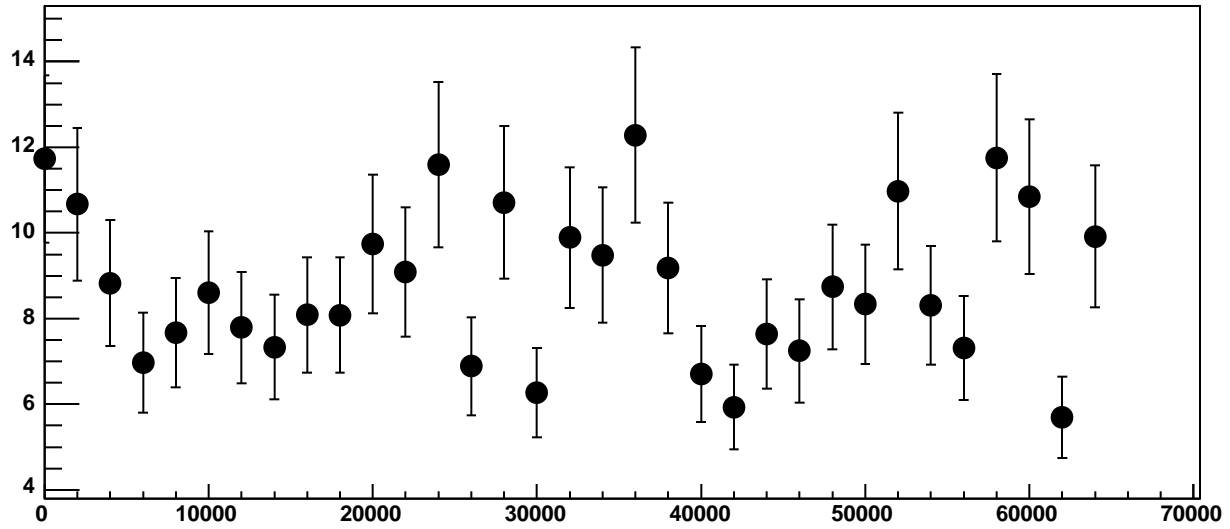


Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

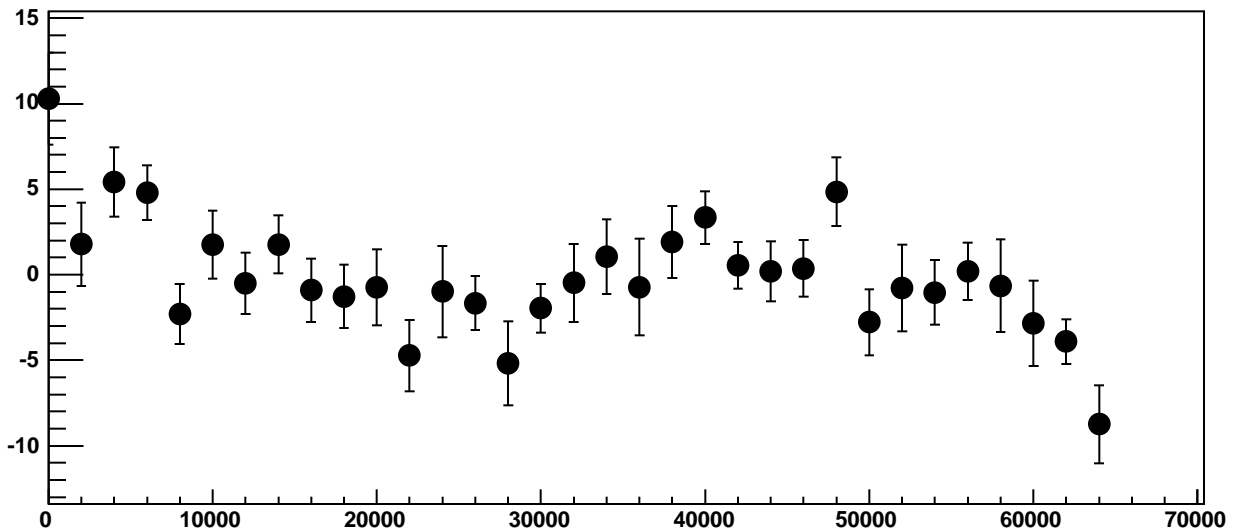


χ^2 / ndf 40.6 / 23
p0 183.5 ± 0.8423
p1 $0.003312 \pm 2.544e-05$

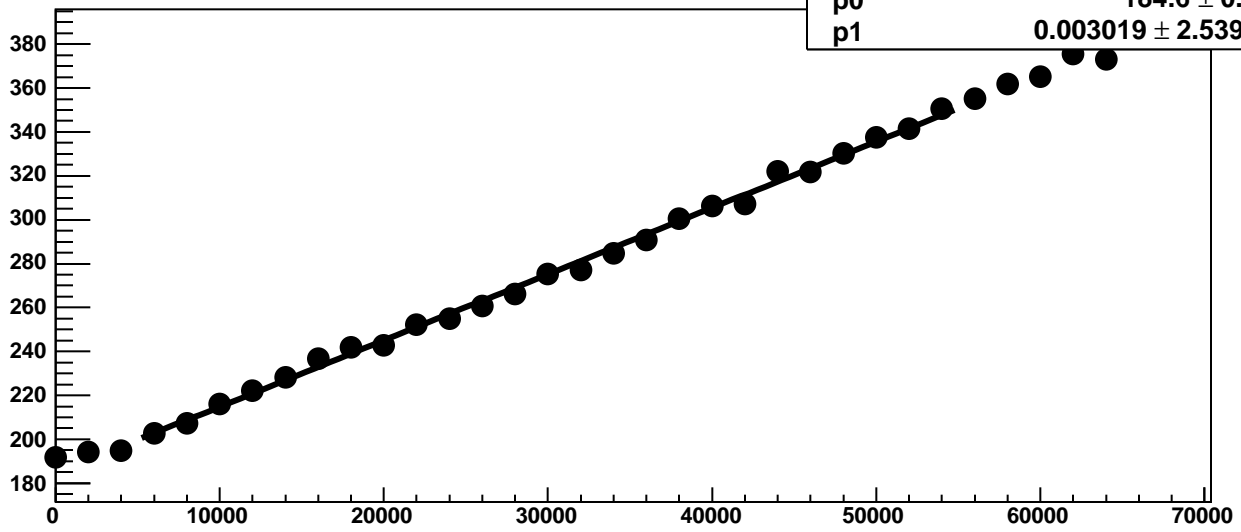
Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

35.44 / 23

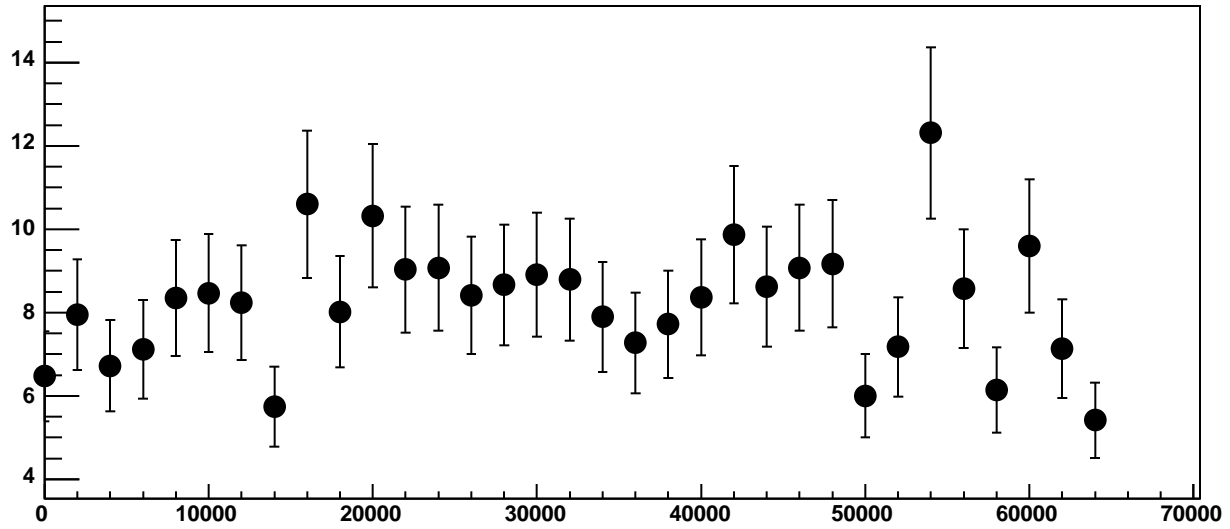
p0

184.6 ± 0.8411

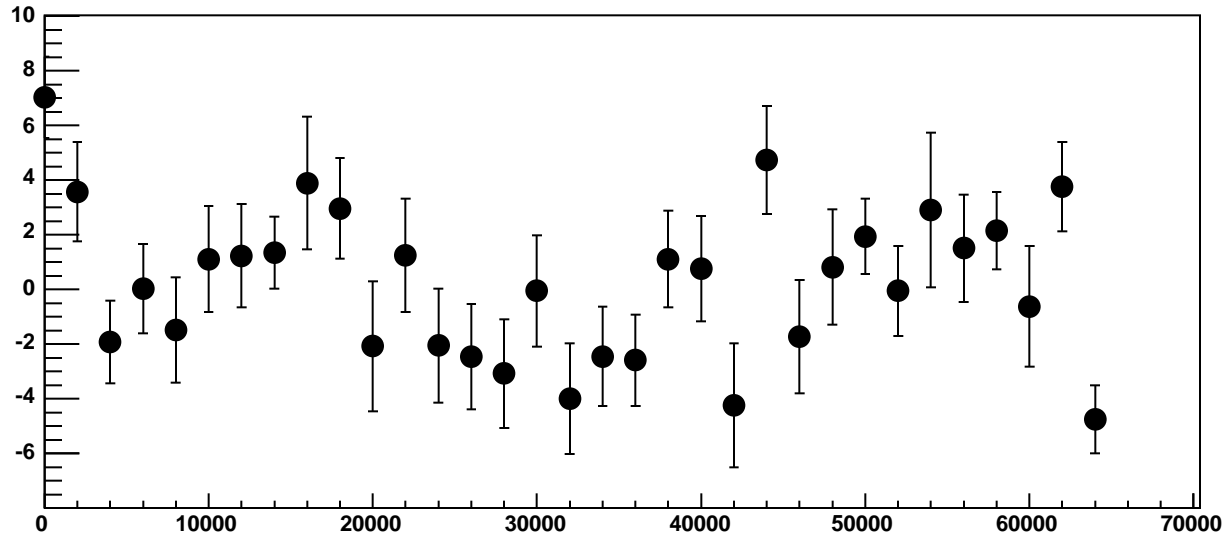
p1

$0.003019 \pm 2.539e-05$

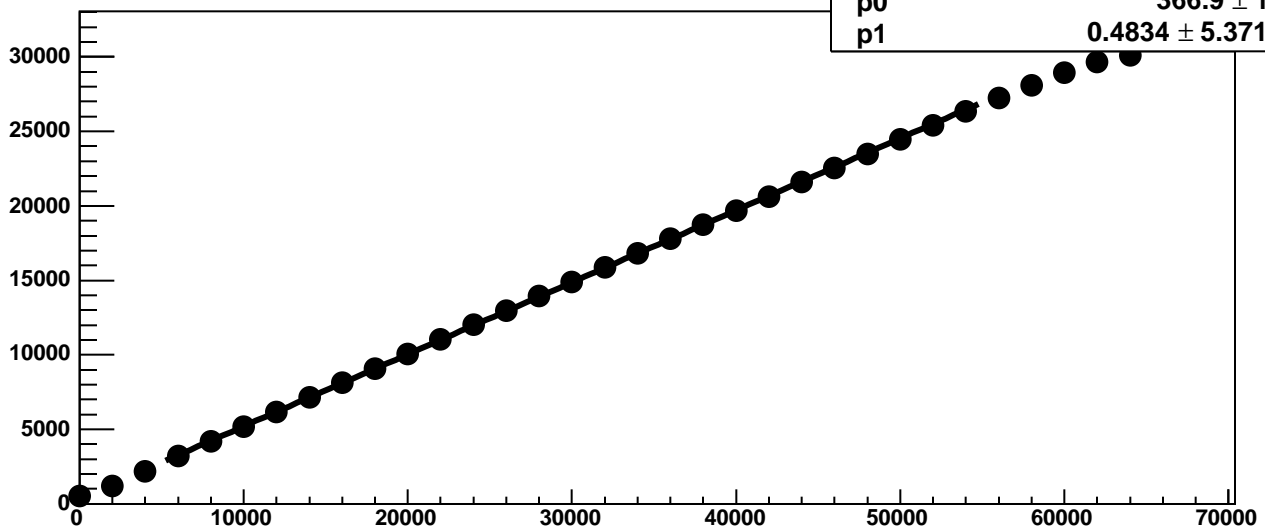
Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



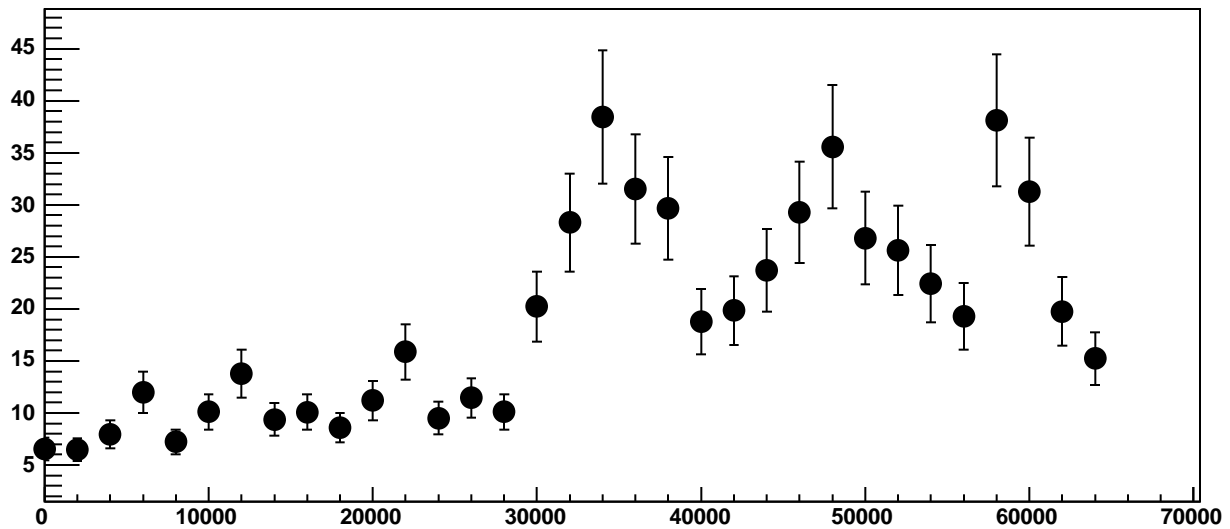
Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



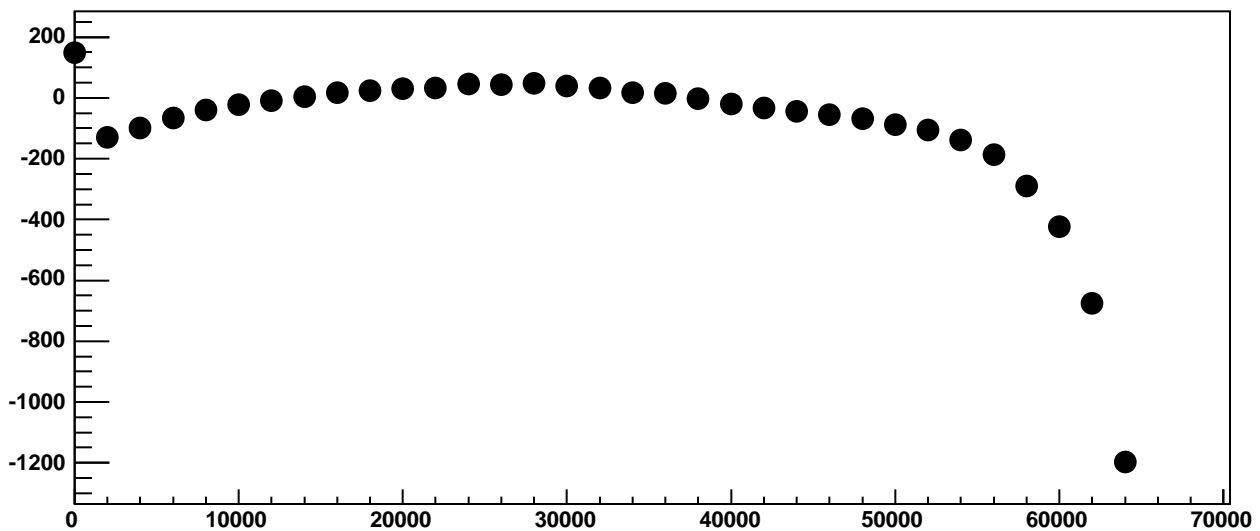
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



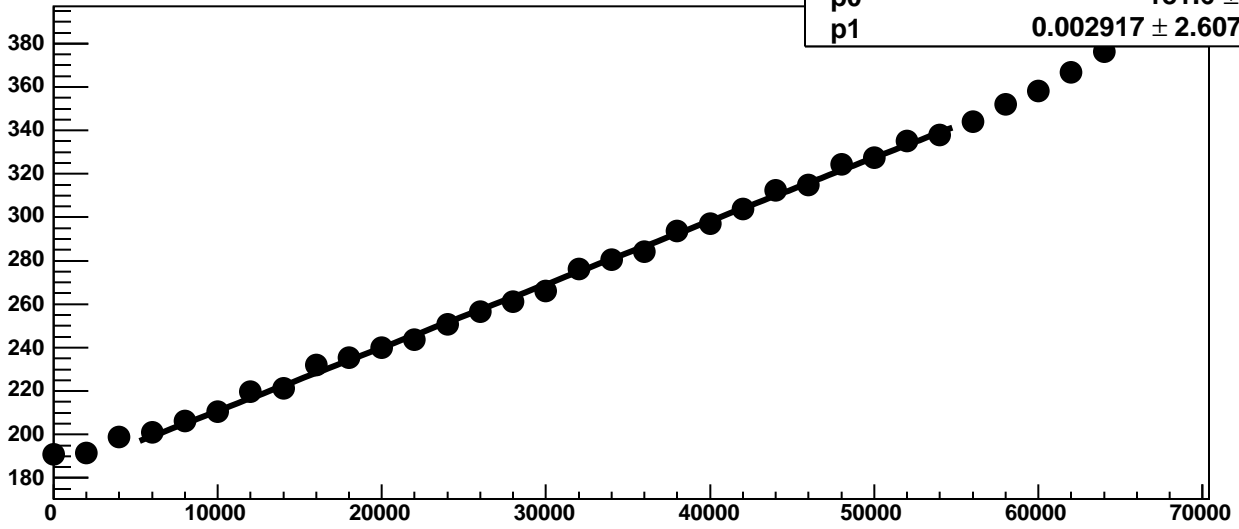
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

23.47 / 23

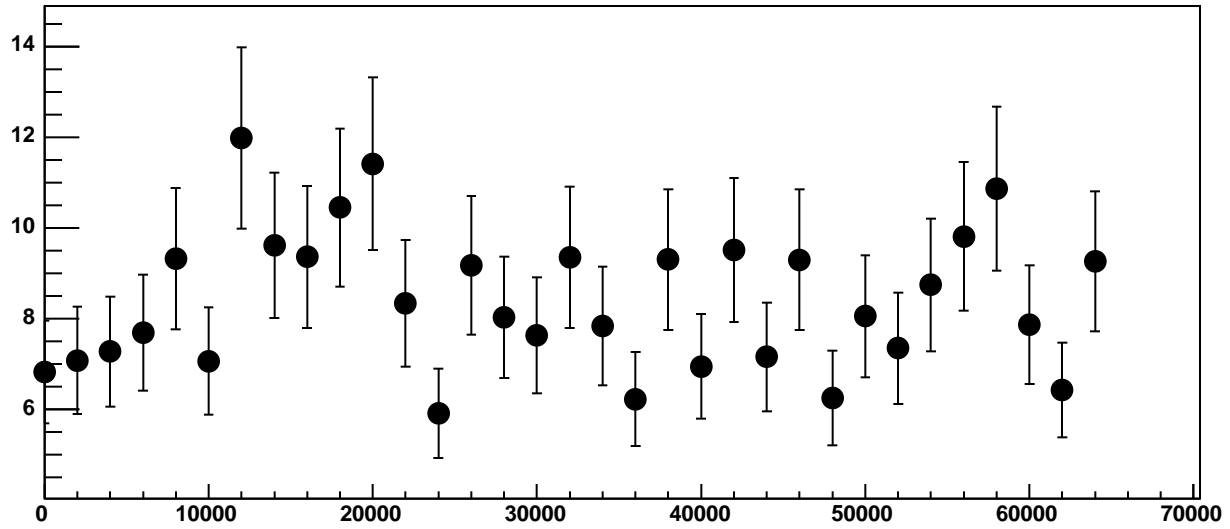
p0

181.6 ± 0.9

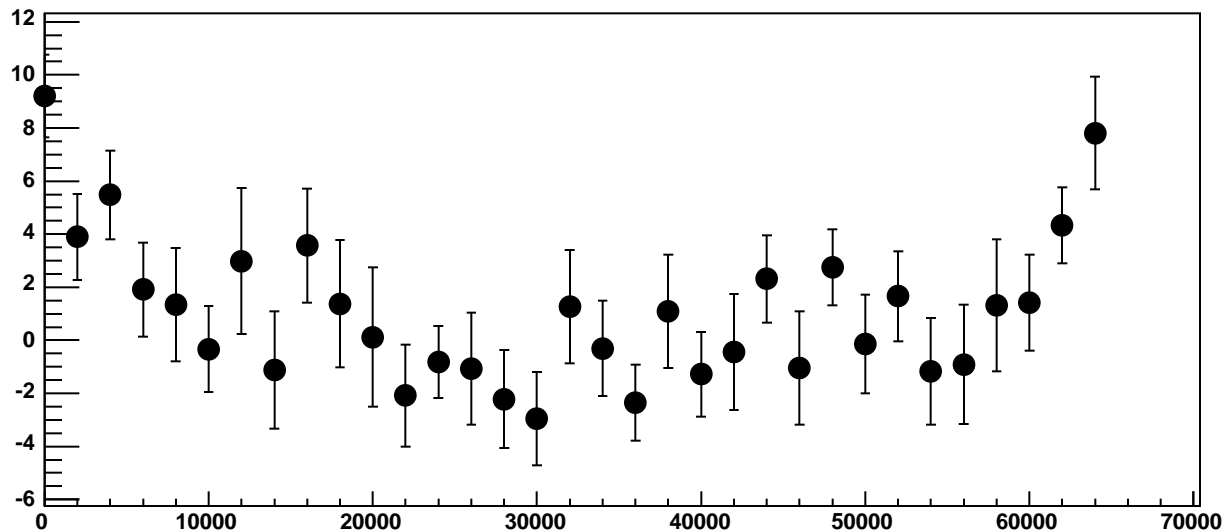
p1

$0.002917 \pm 2.607\text{e-}05$

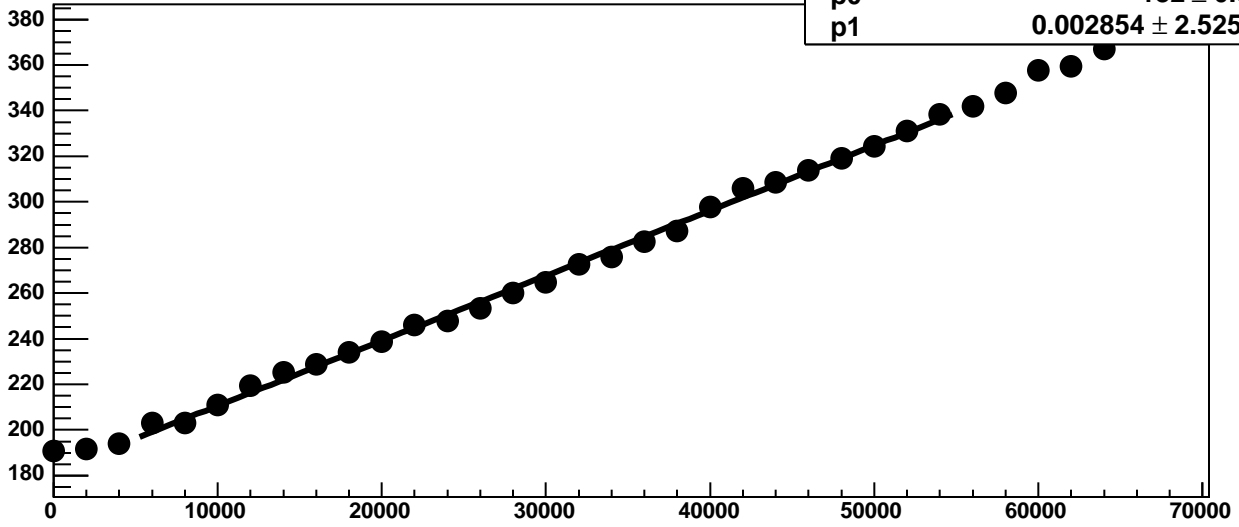
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

38.71 / 23

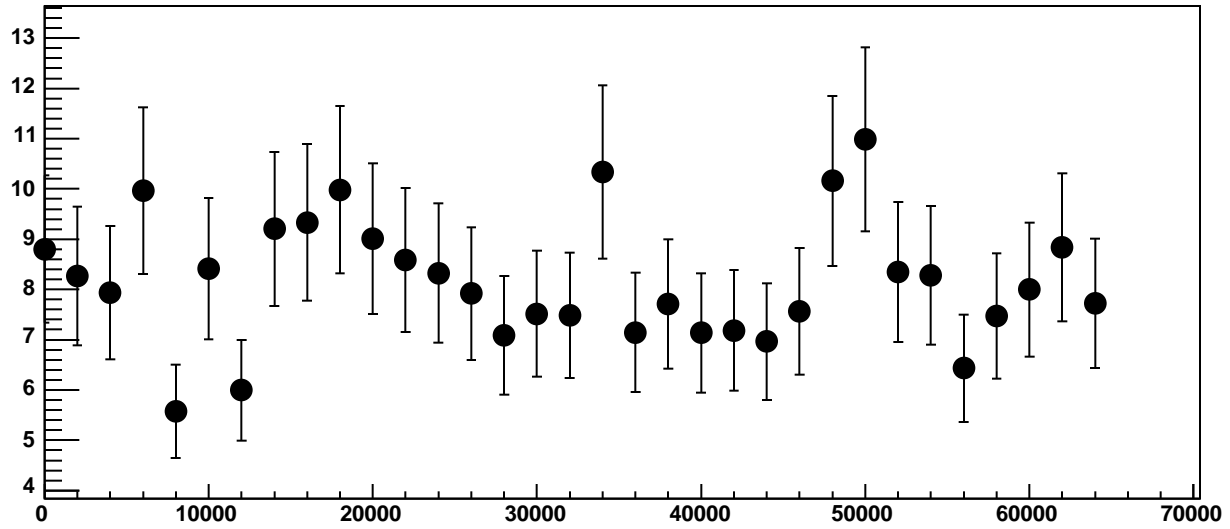
p0

182 ± 0.8232

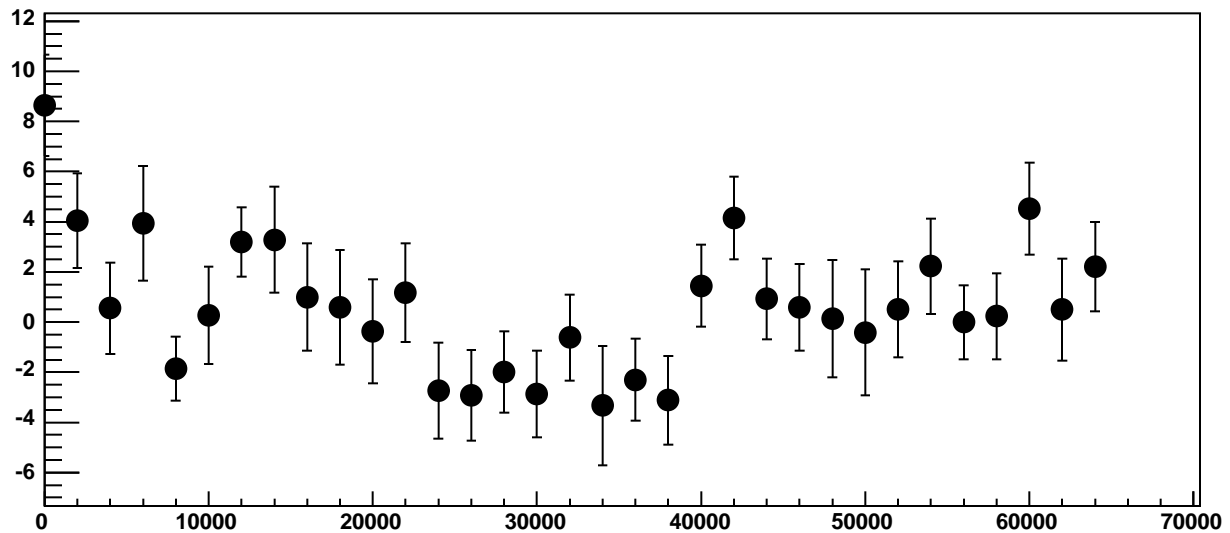
p1

$0.002854 \pm 2.525e-05$

Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

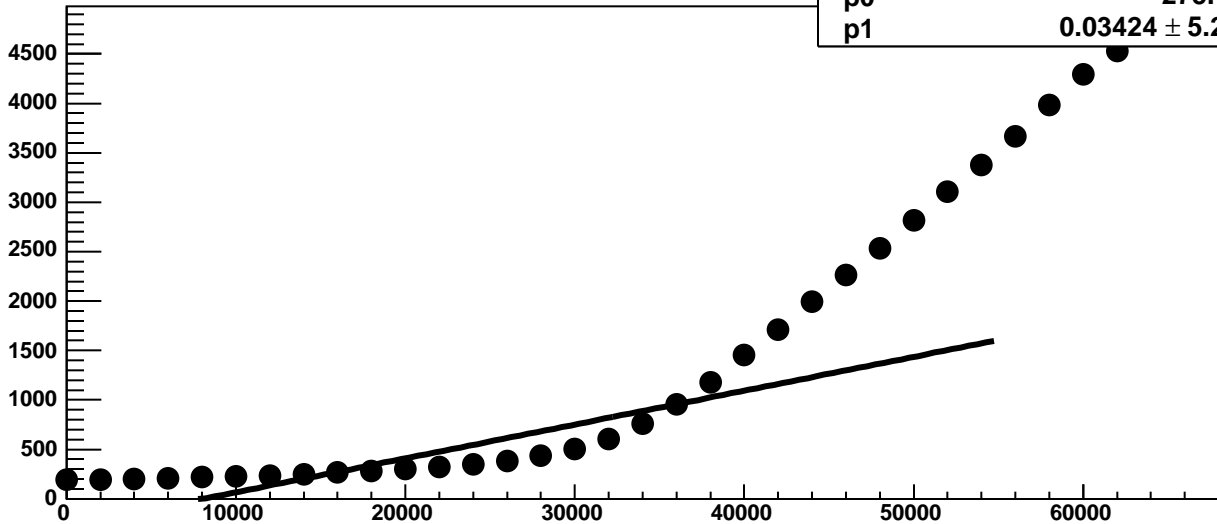


Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

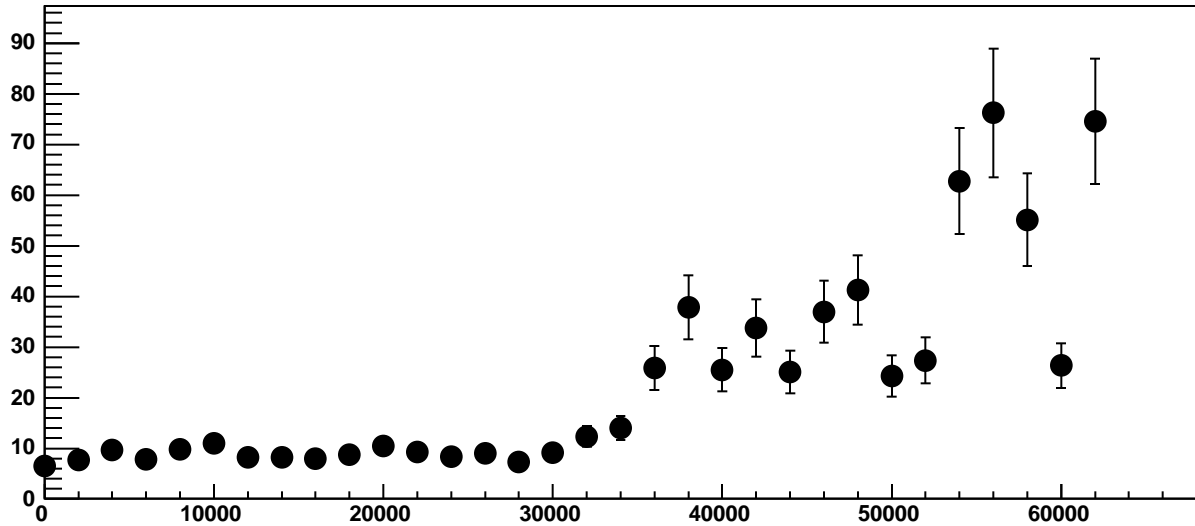


Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

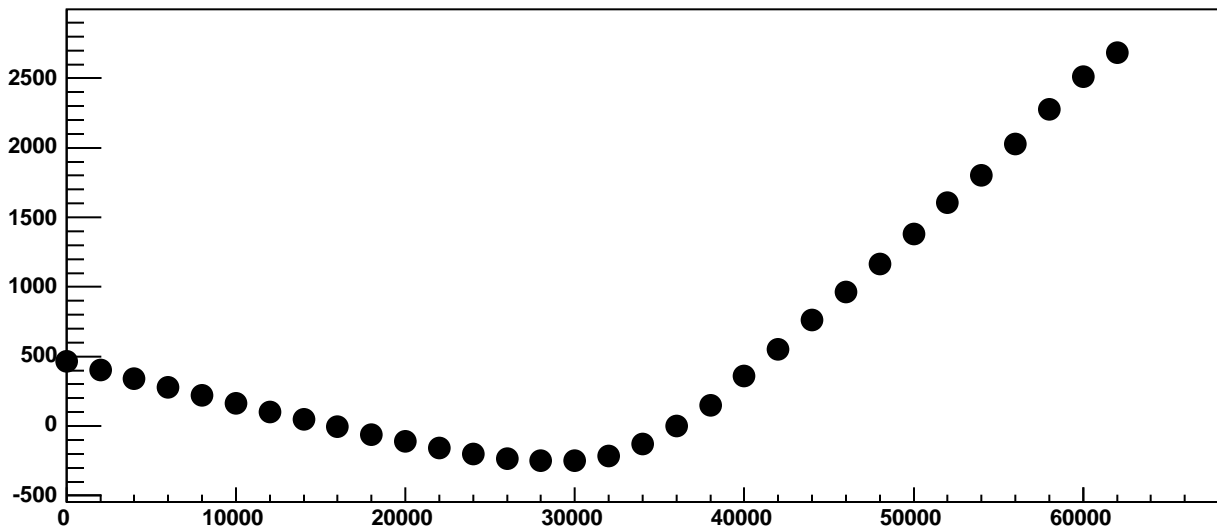
χ^2 / ndf 3.133e+05 / 23
p0 -275.7 ± 1.2
p1 0.03424 ± 5.265e-05



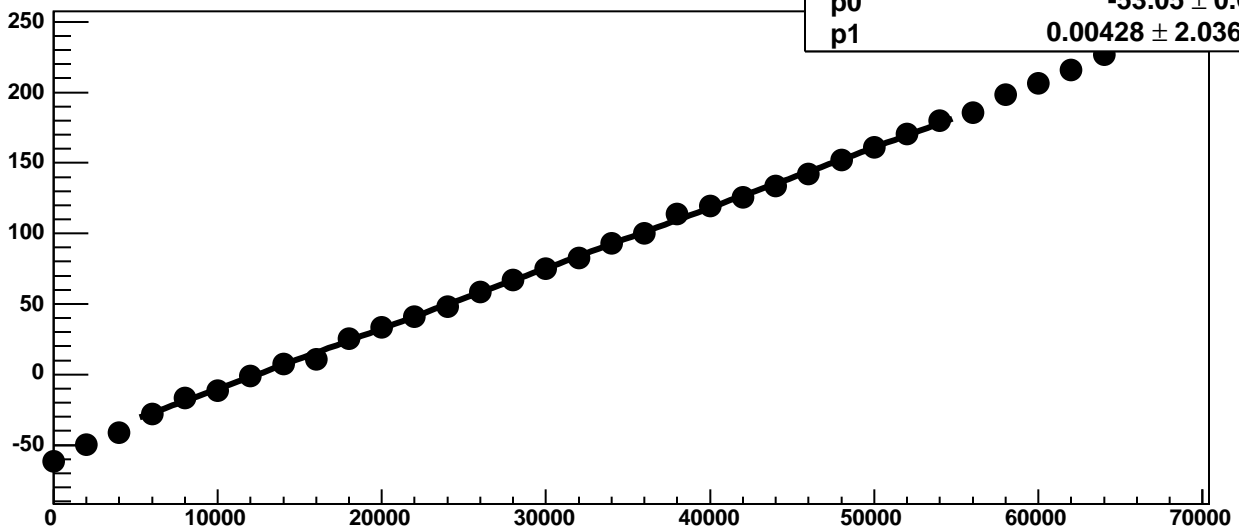
Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

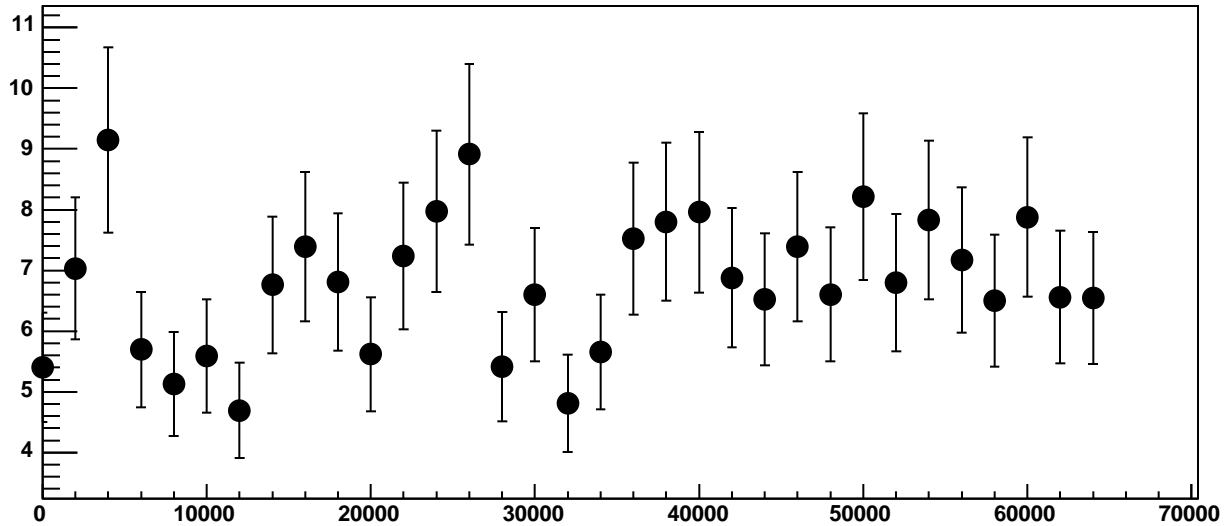


Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

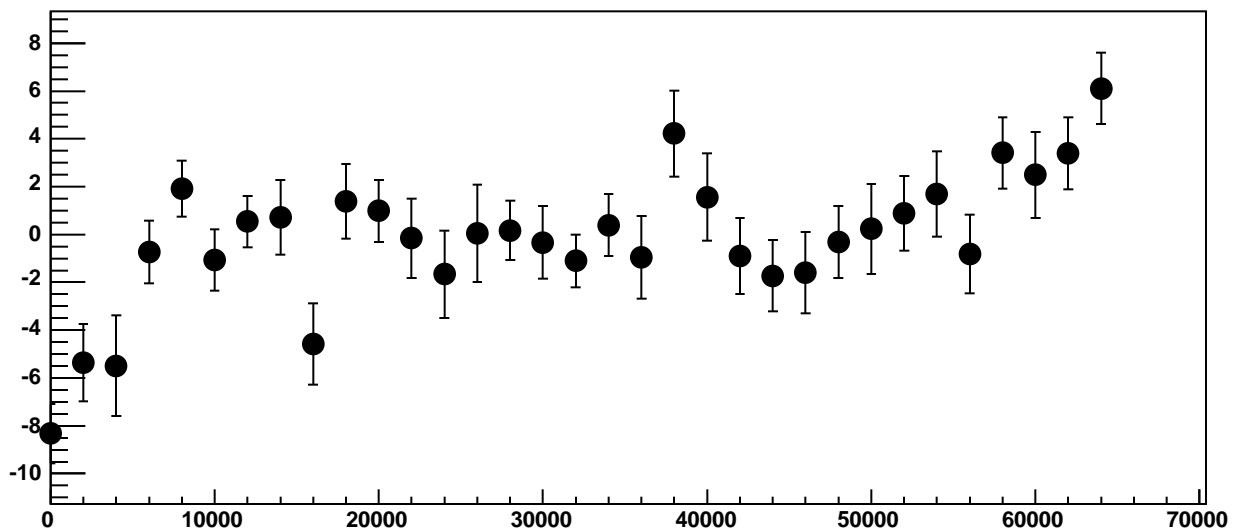


χ^2 / ndf 25.18 / 23
p0 -53.05 ± 0.6325
p1 $0.00428 \pm 2.036e-05$

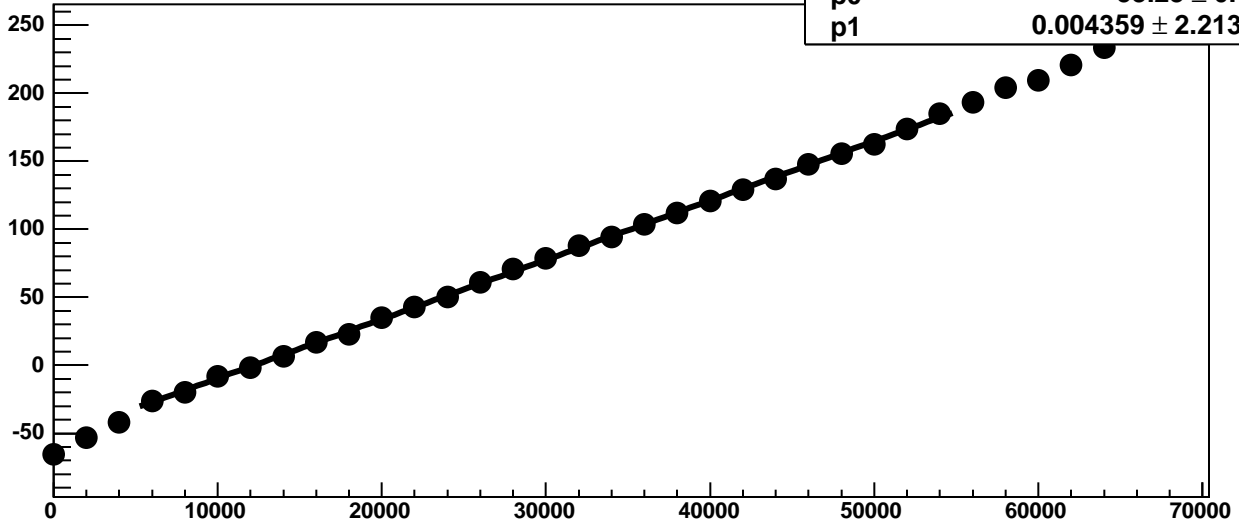
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



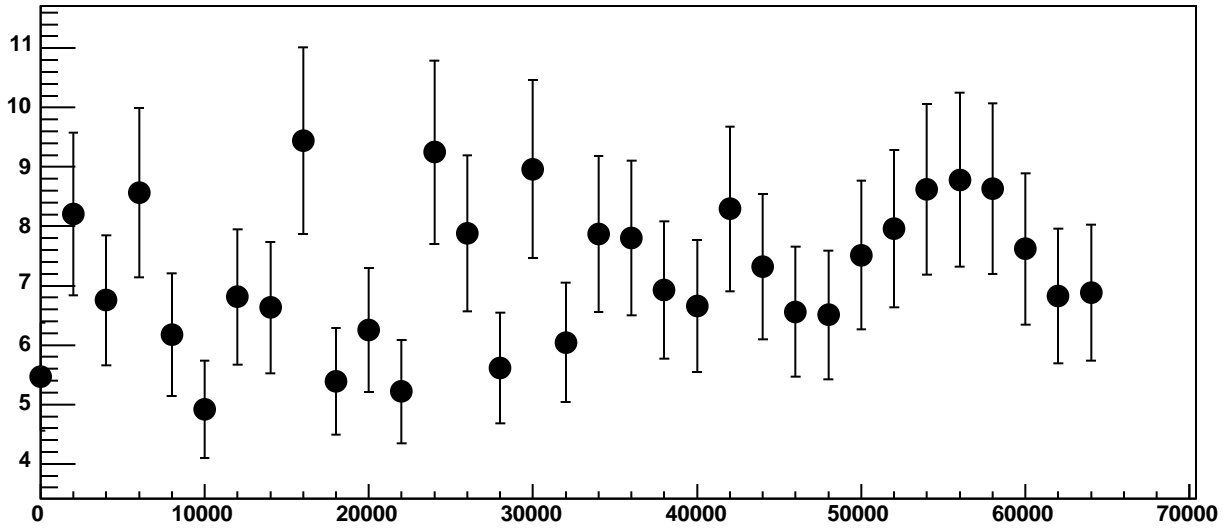
Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



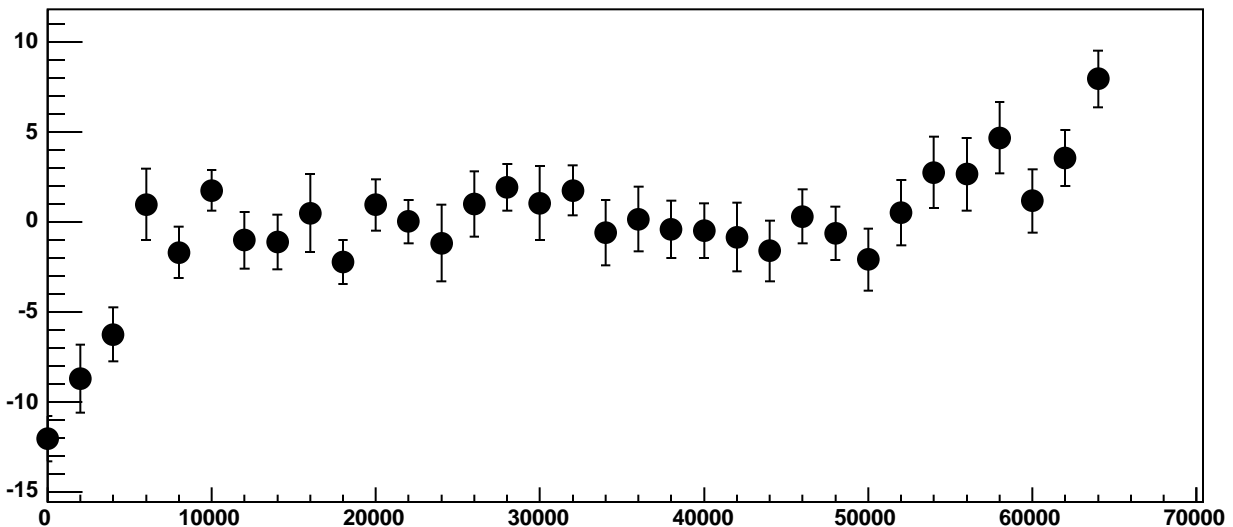
Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



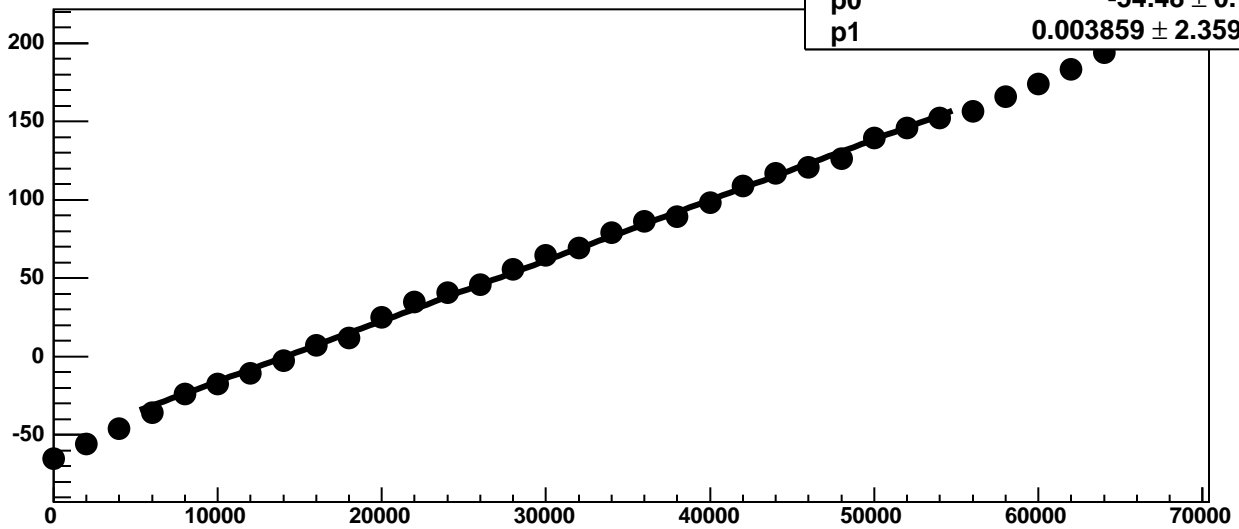
Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

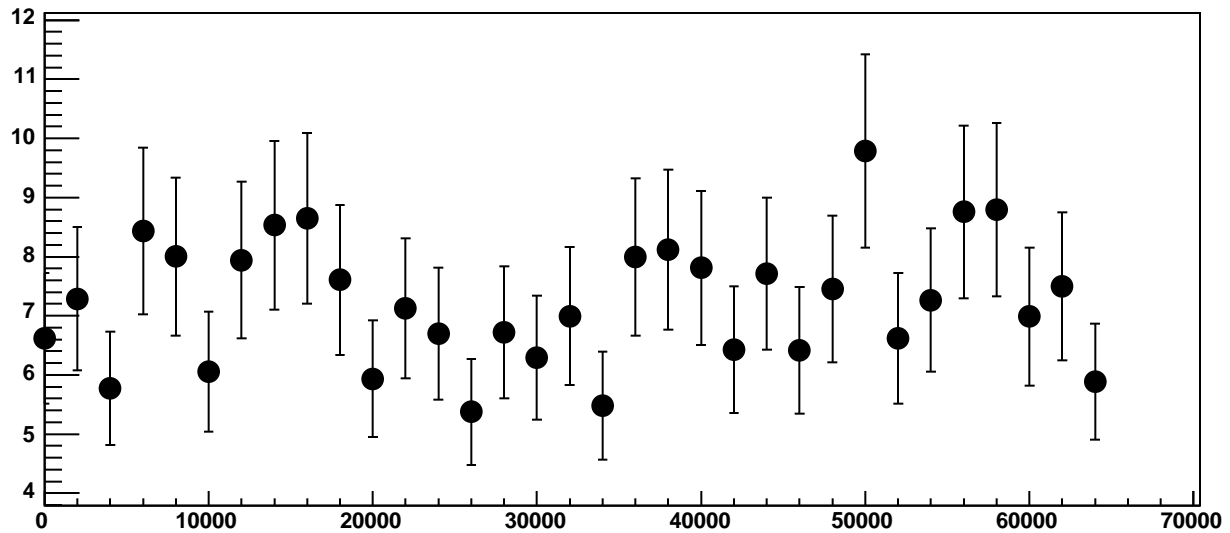


Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

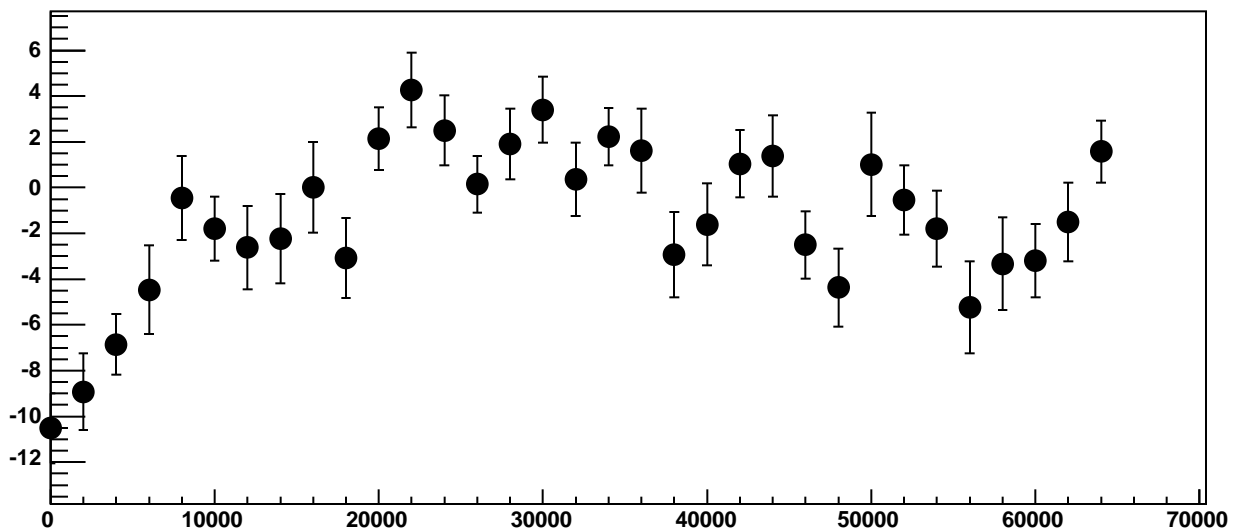


χ^2 / ndf 51.85 / 23
p0 -54.48 ± 0.7808
p1 0.003859 ± 2.359e-05

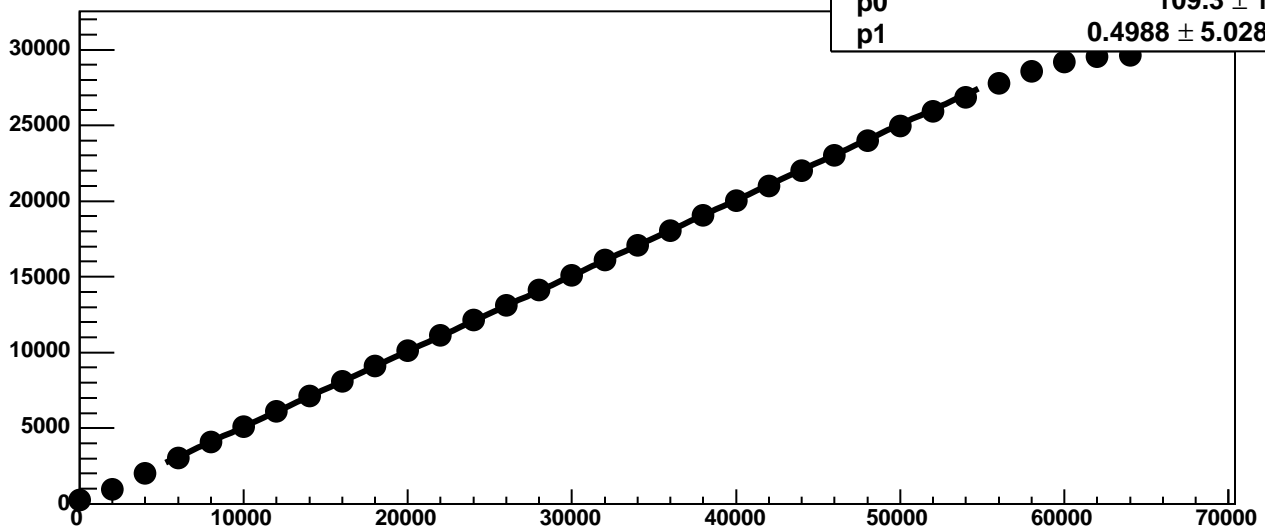
Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



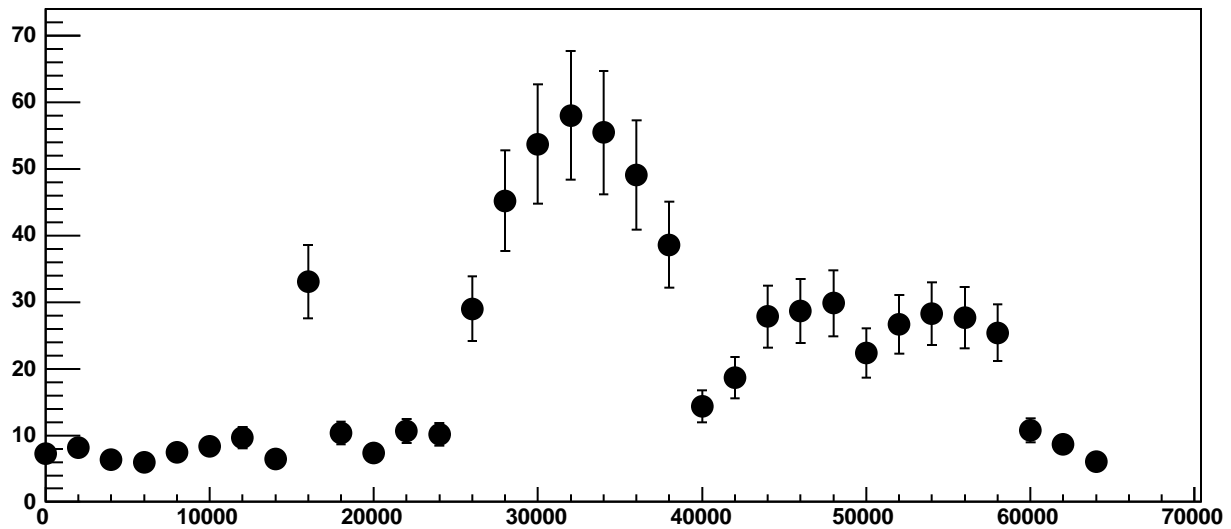
Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



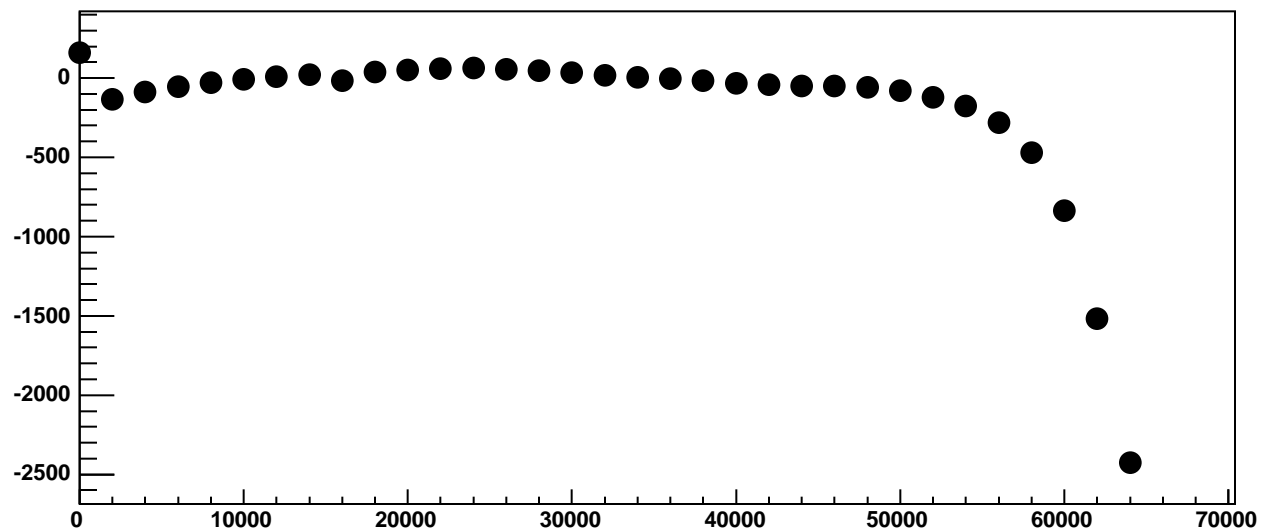
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



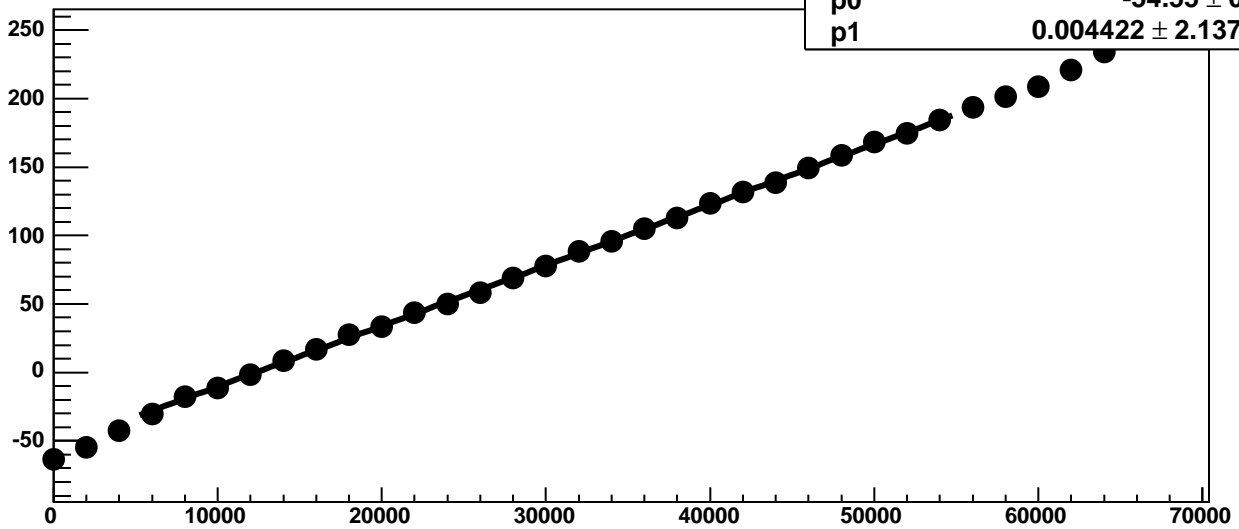
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

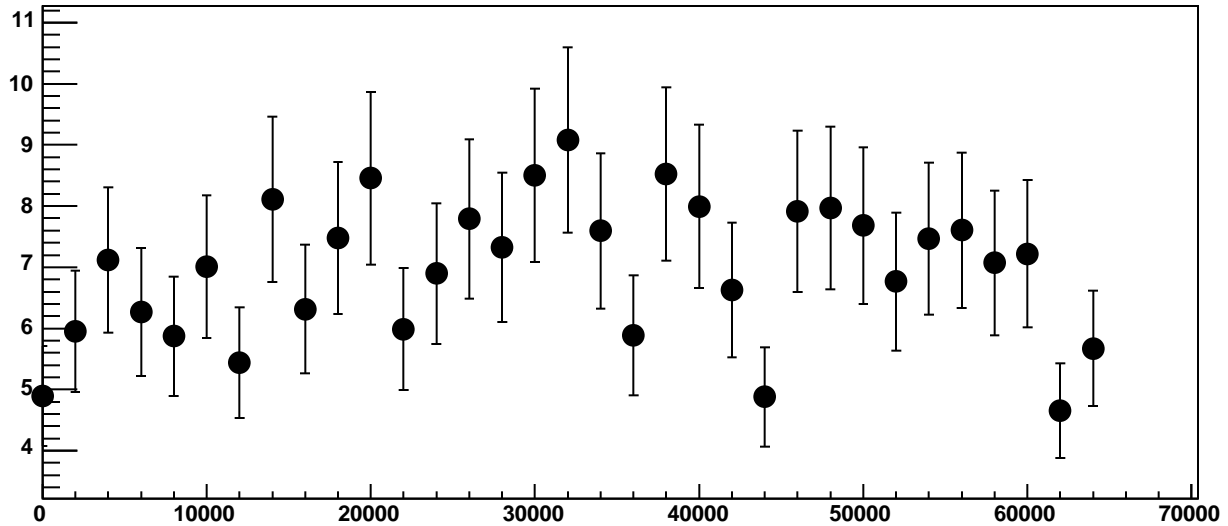


Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

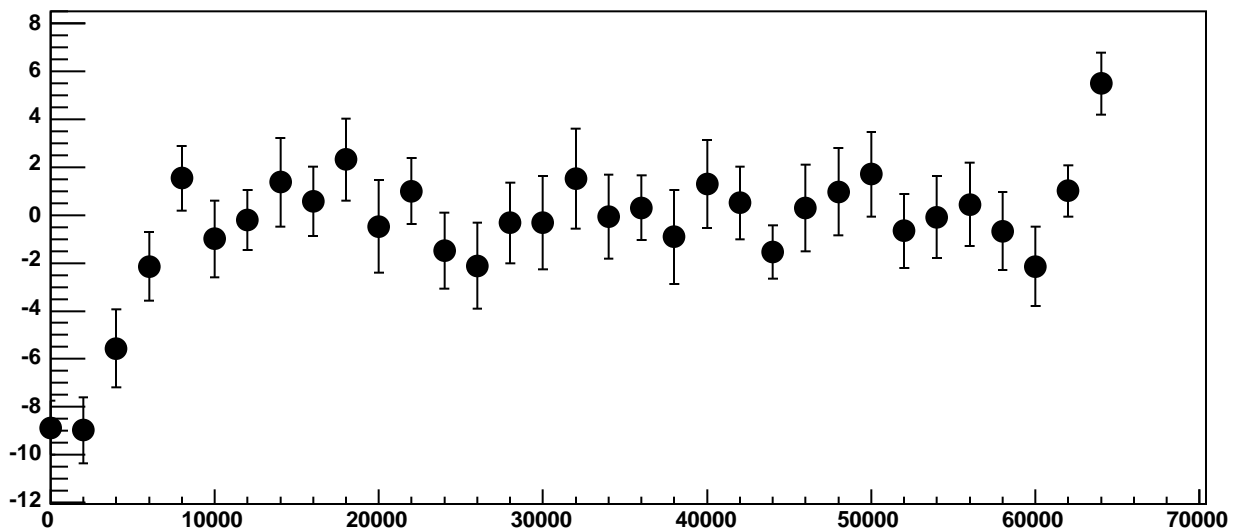


χ^2 / ndf 14.14 / 23
p0 -54.55 ± 0.698
p1 $0.004422 \pm 2.137\text{e-}05$

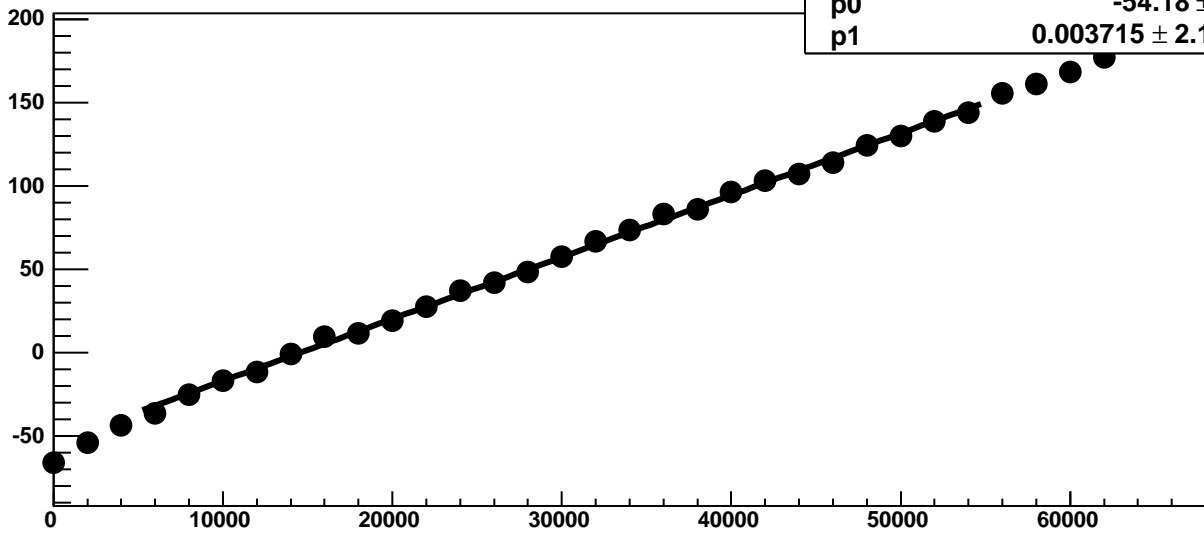
Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

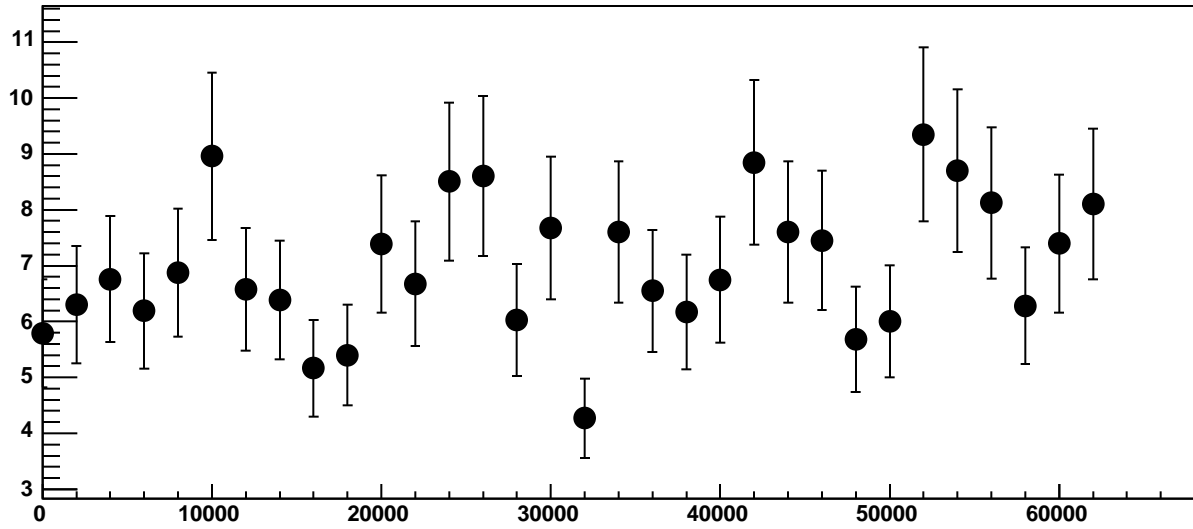


Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

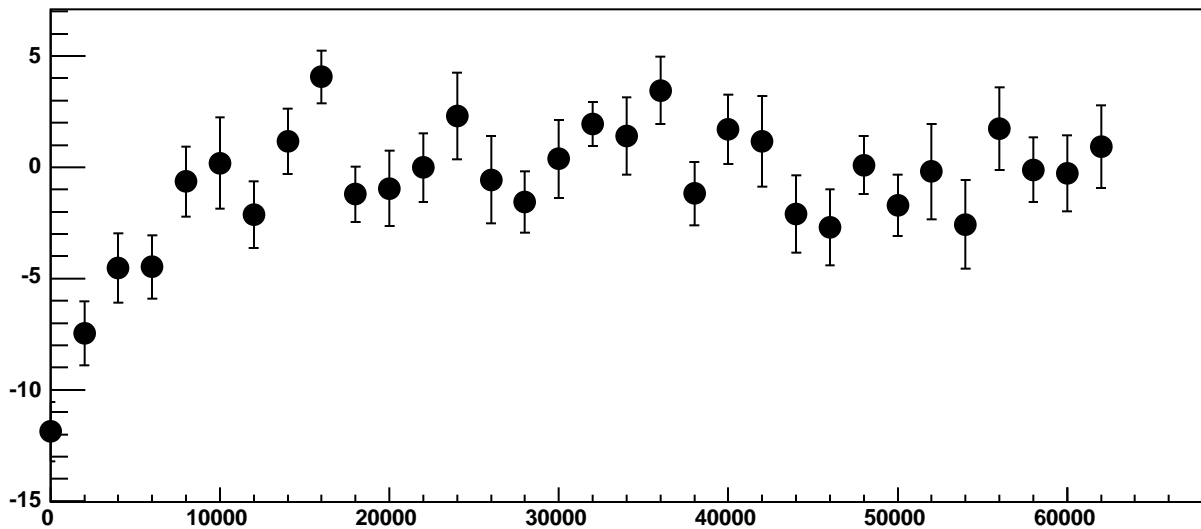


χ^2 / ndf 47.8 / 23
p0 -54.18 ± 0.7071
p1 $0.003715 \pm 2.195e-05$

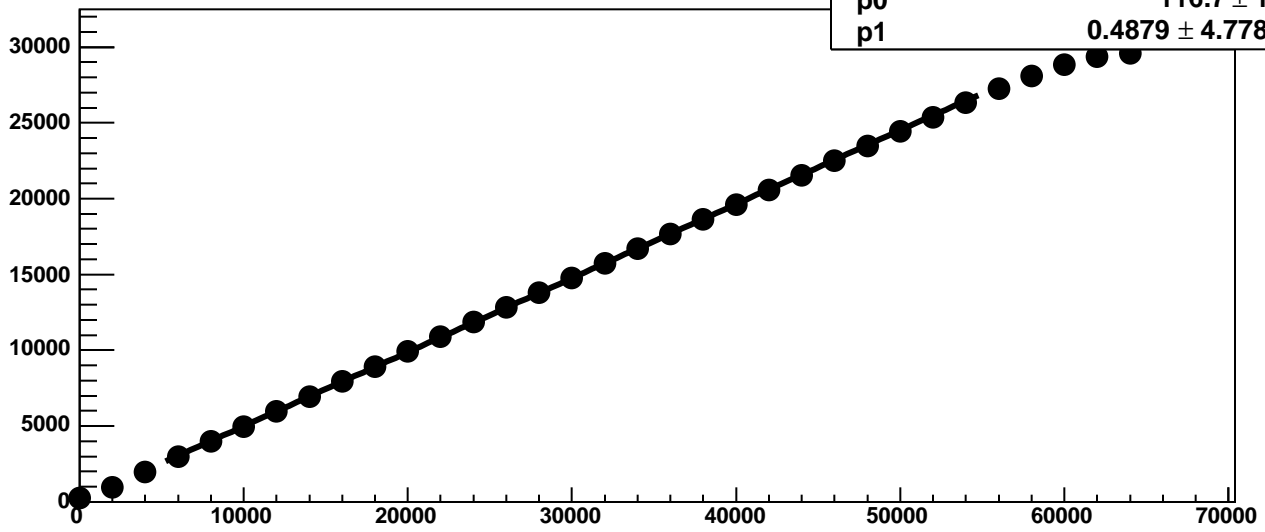
Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



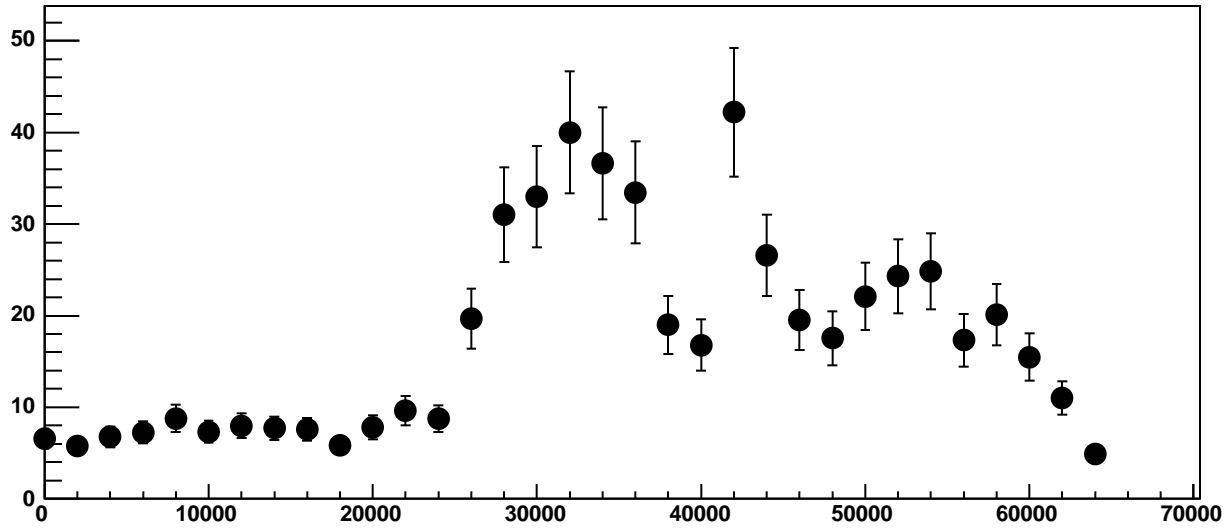
Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



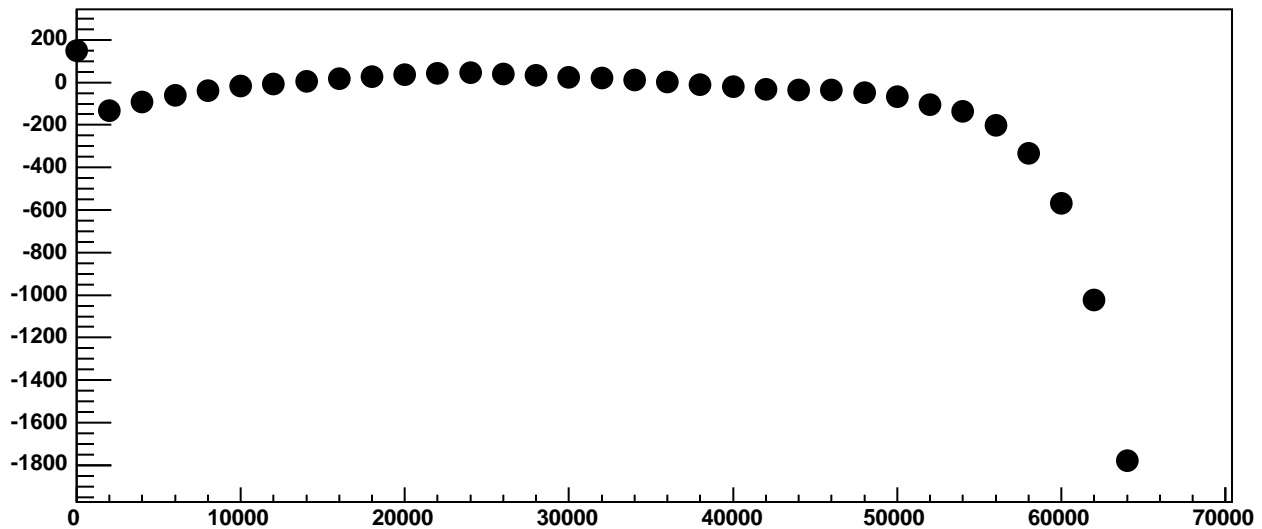
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



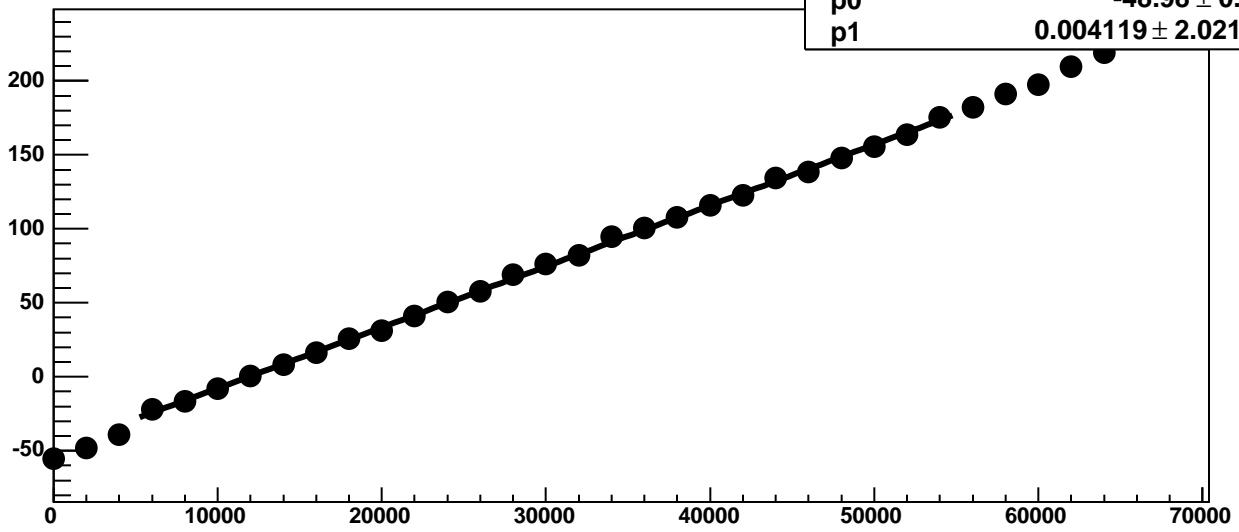
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



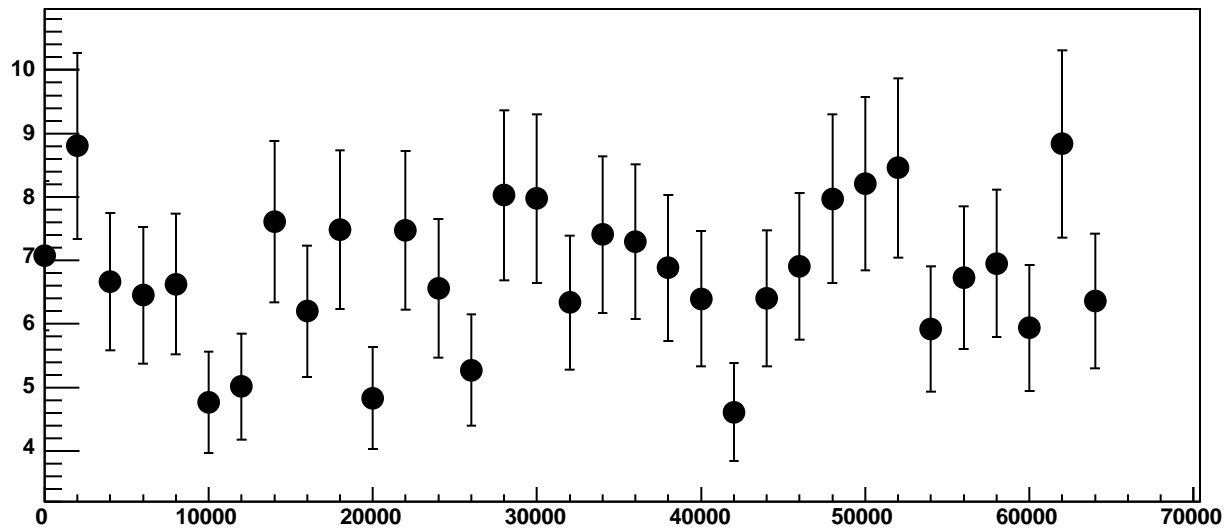
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC



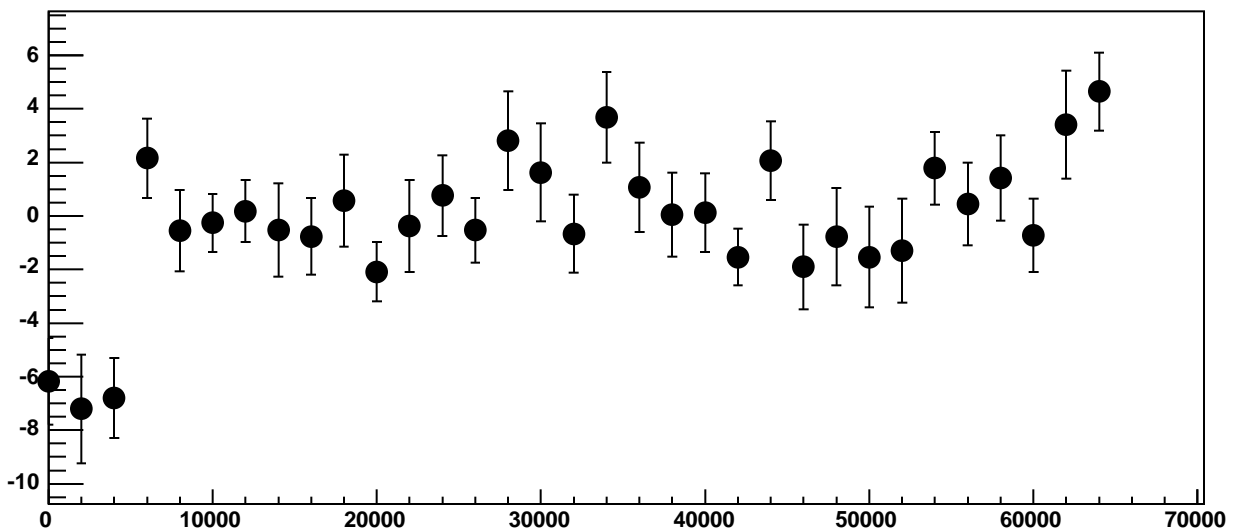
Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



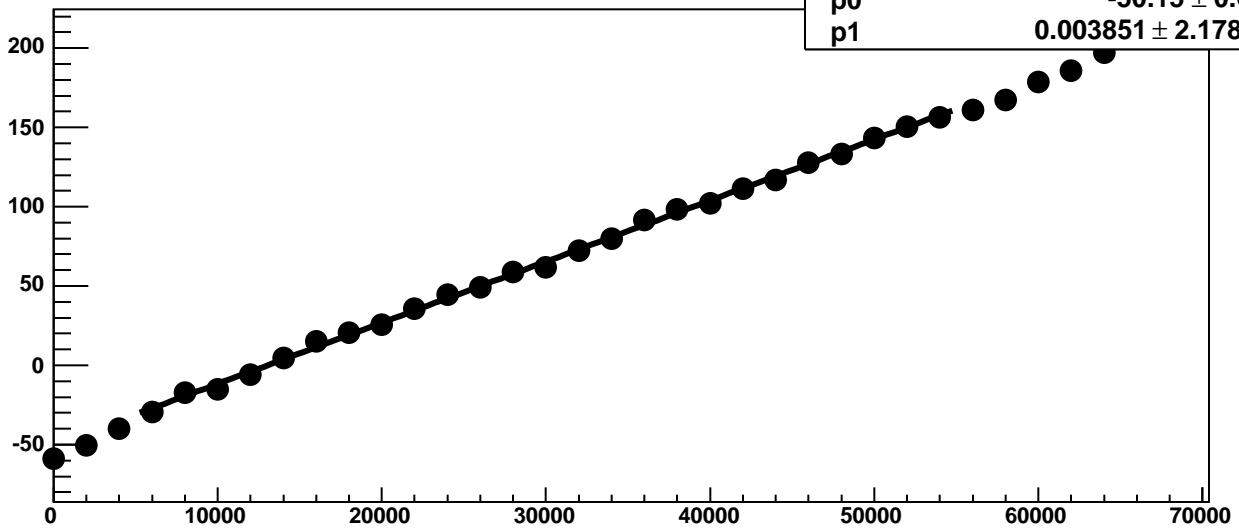
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



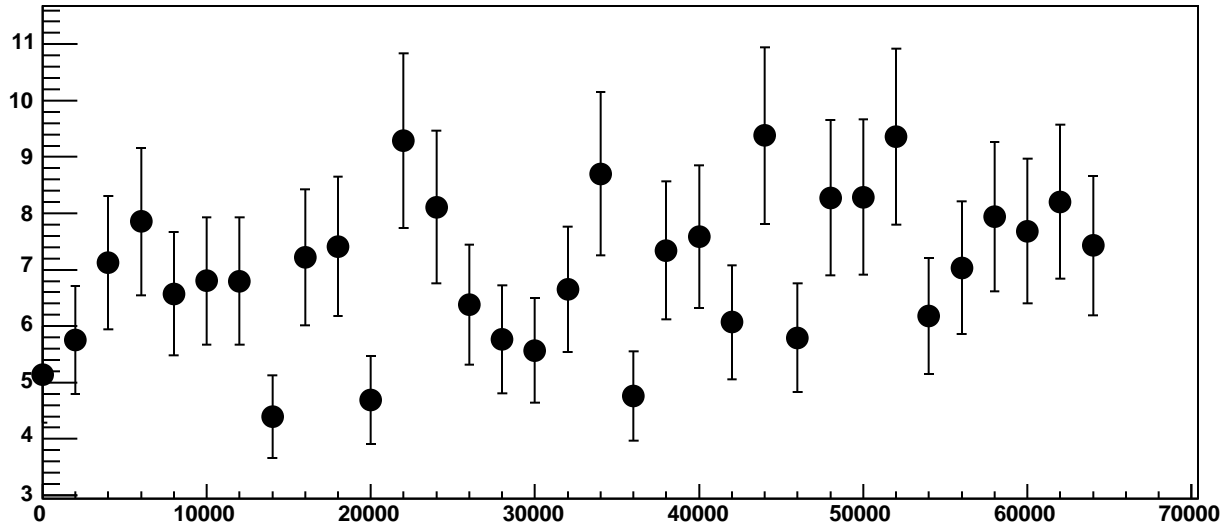
Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



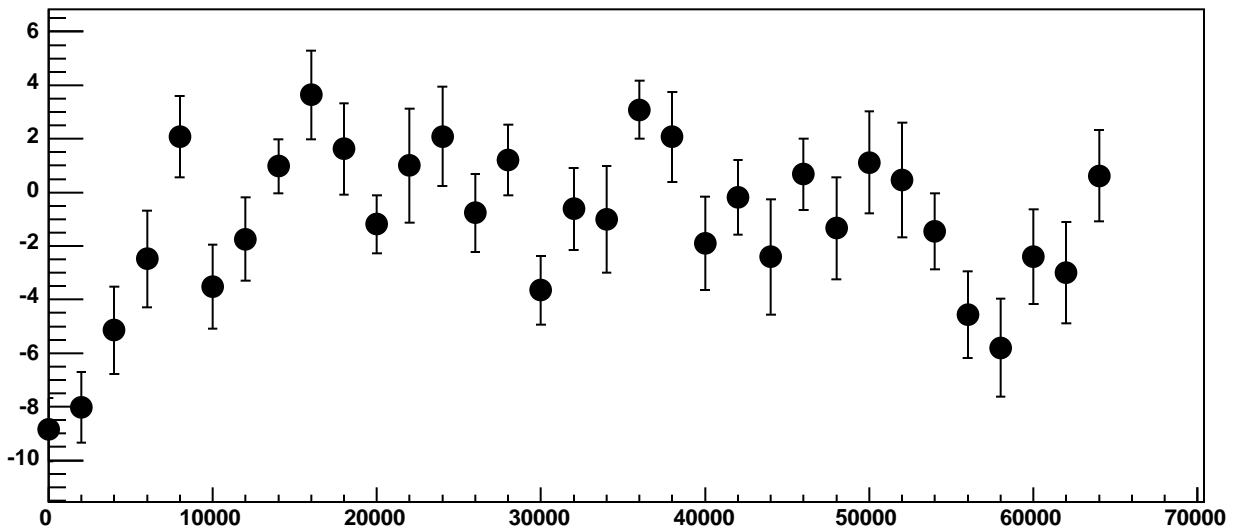
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



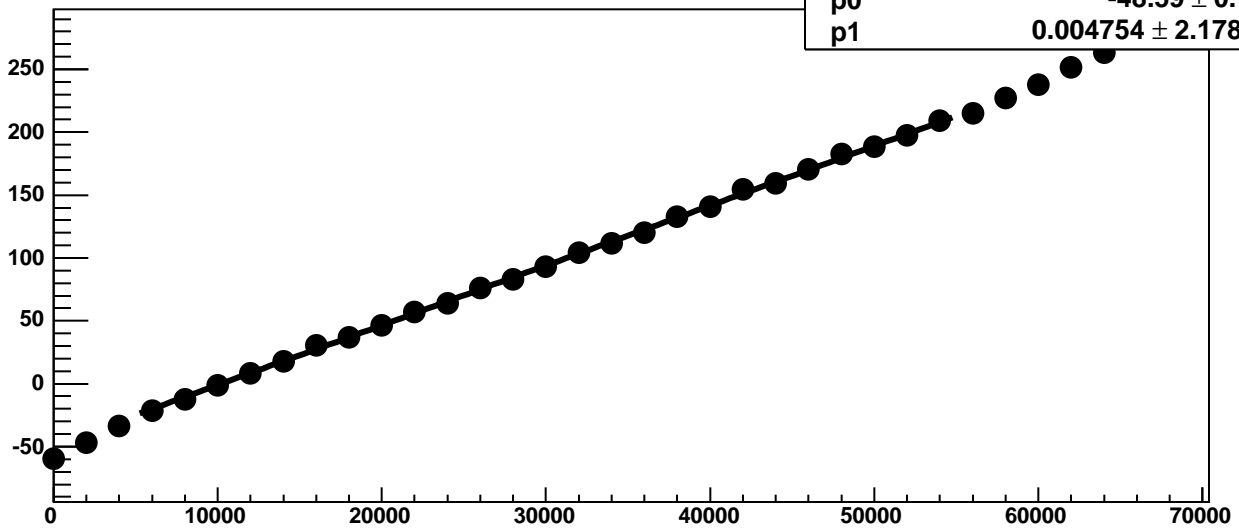
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



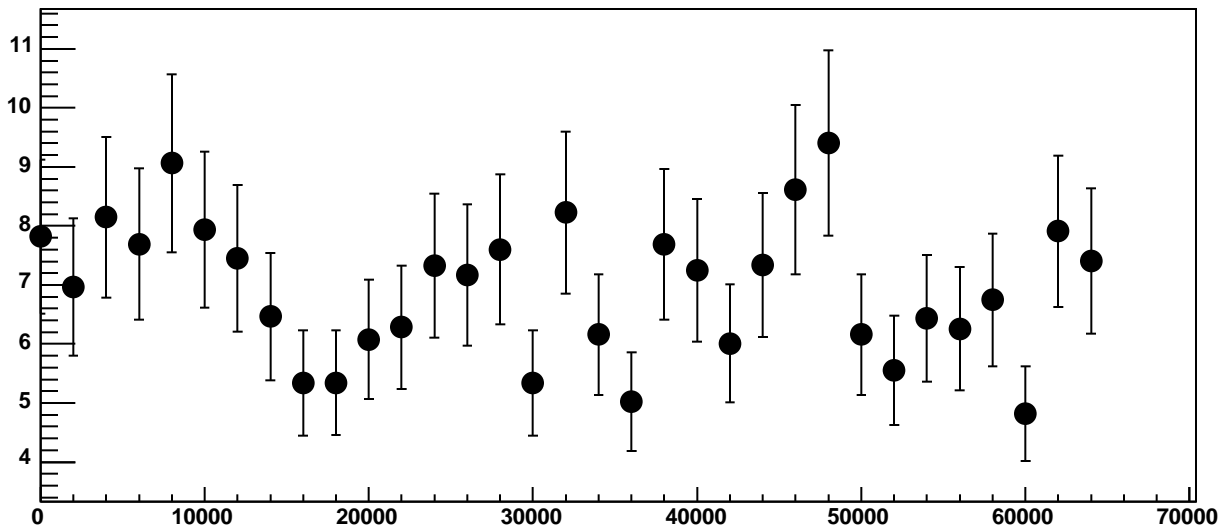
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



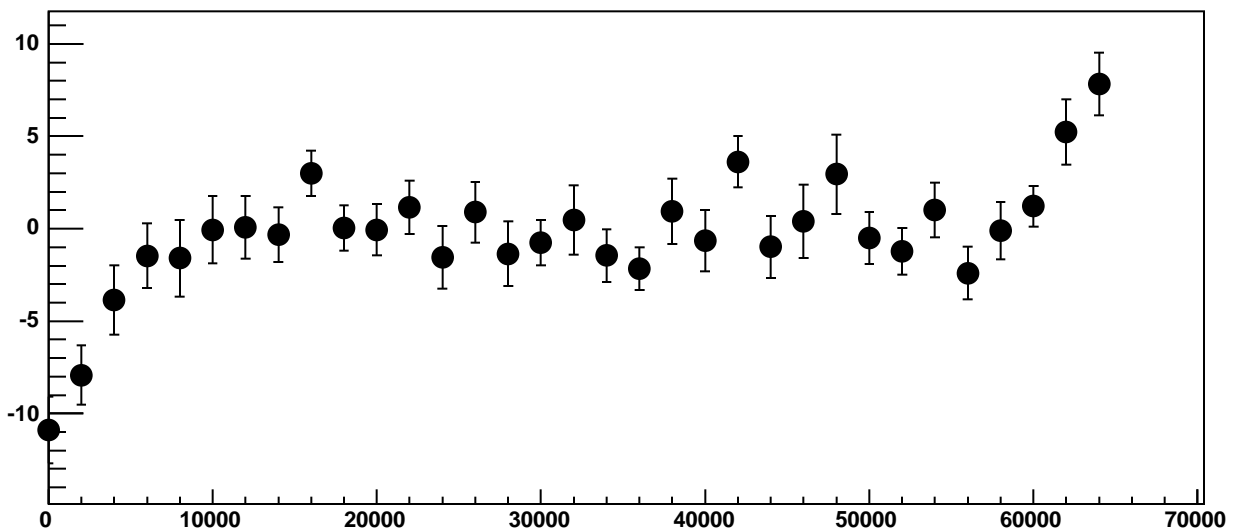
Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



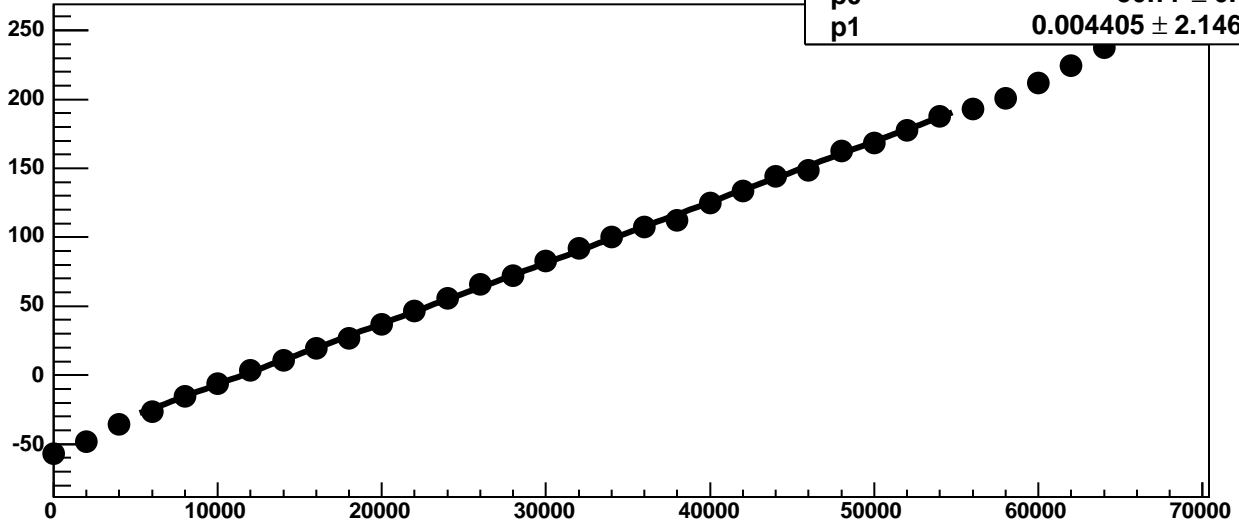
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



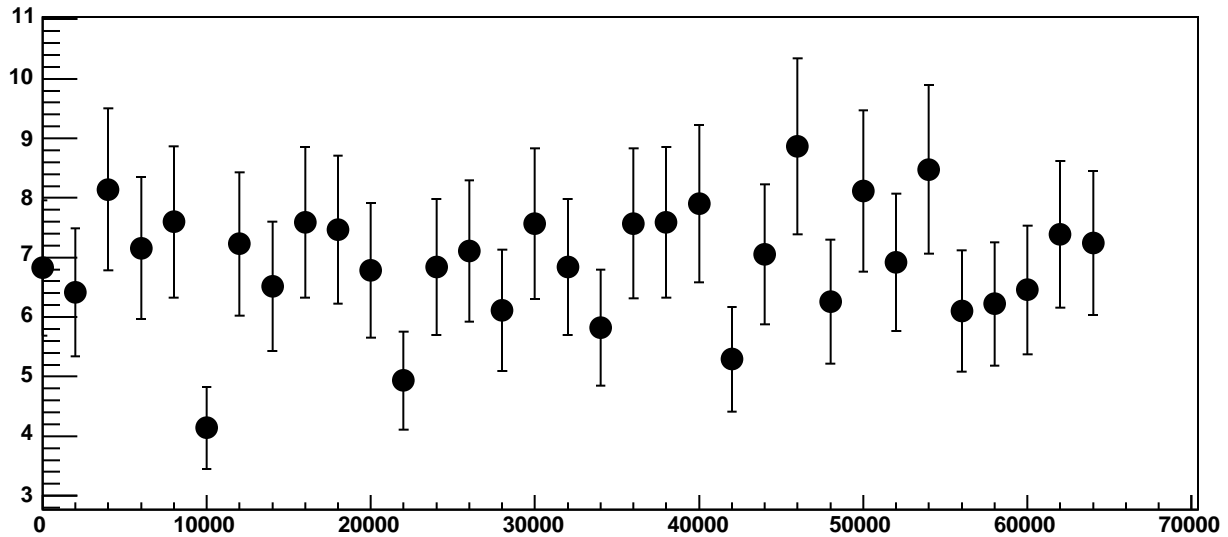
Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



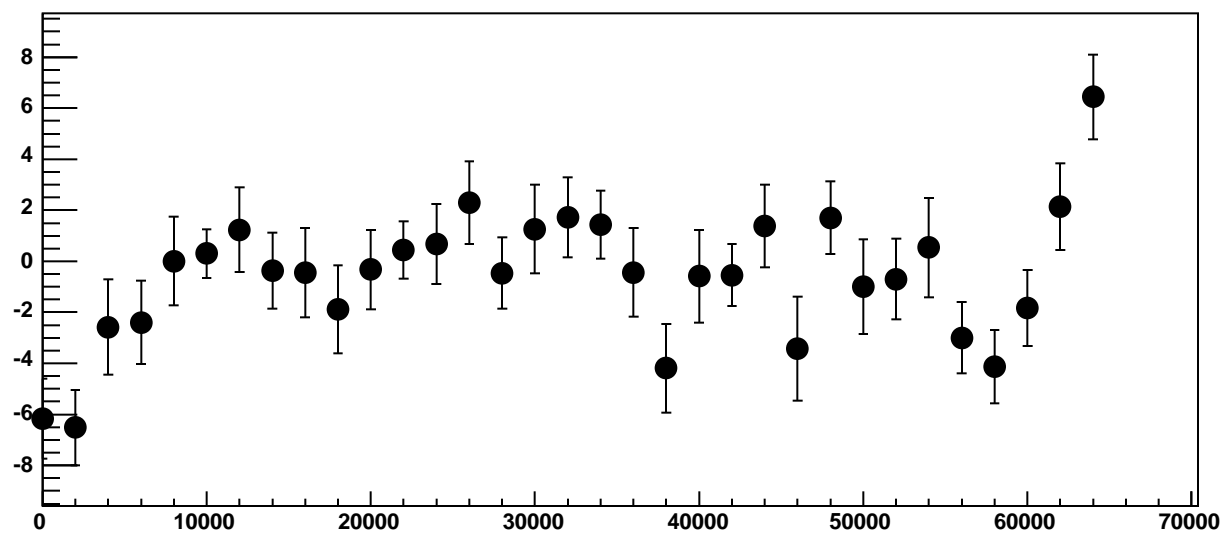
Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



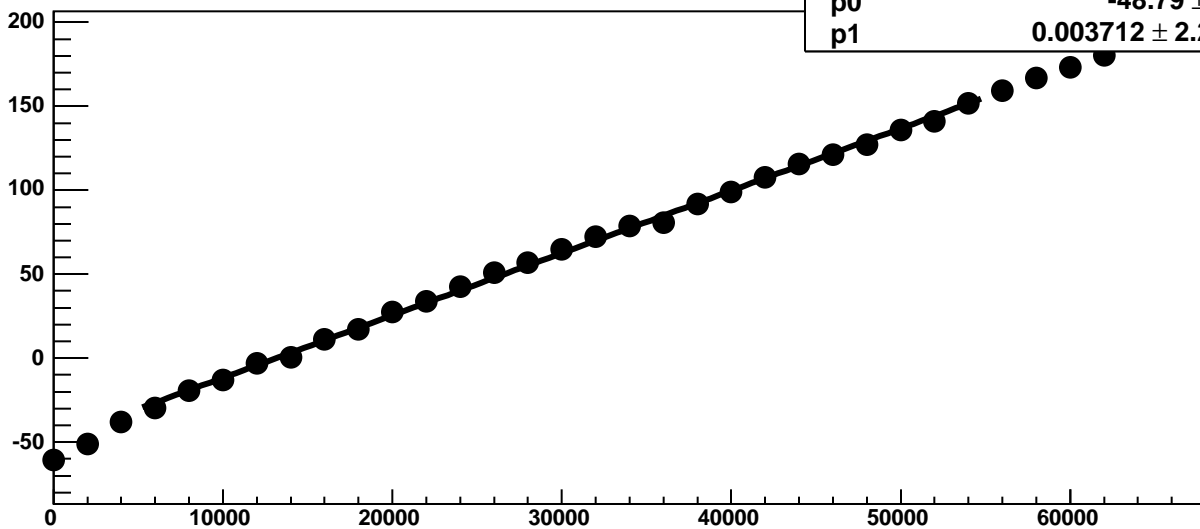
Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

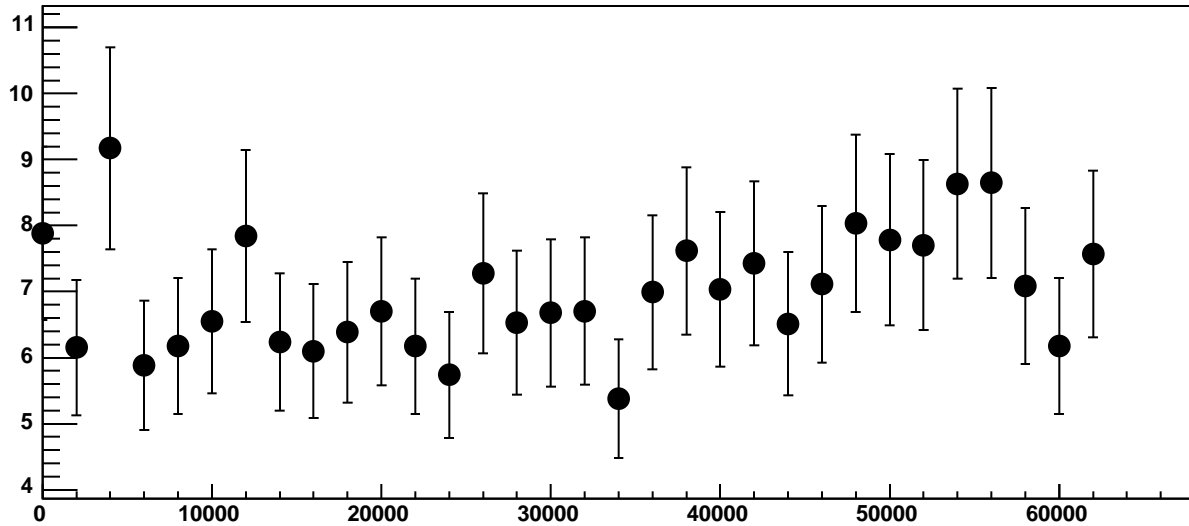


Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

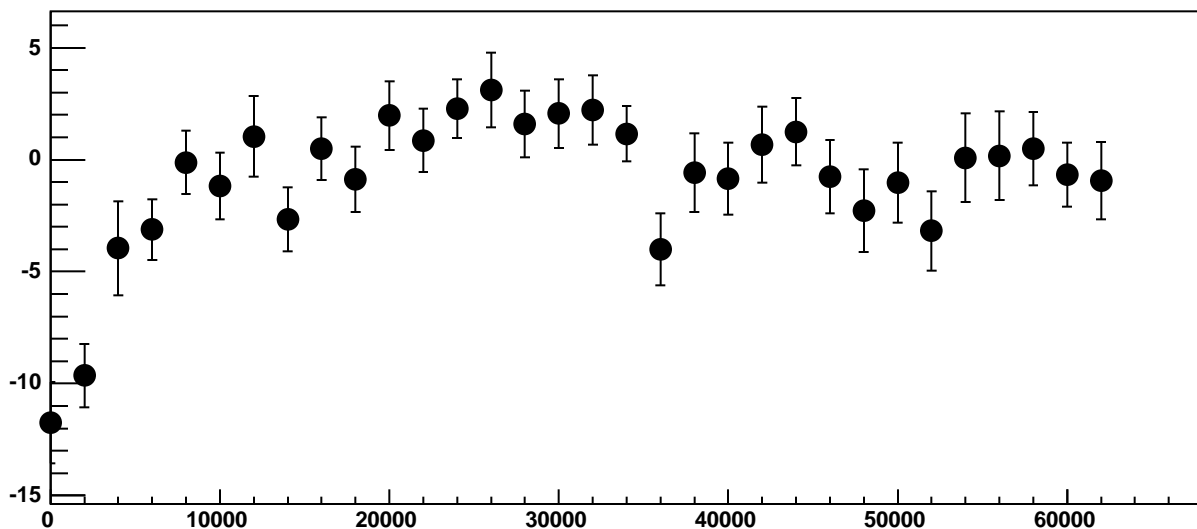


χ^2 / ndf 37.37 / 23
p0 -48.79 ± 0.6957
p1 $0.003712 \pm 2.217\text{e-}05$

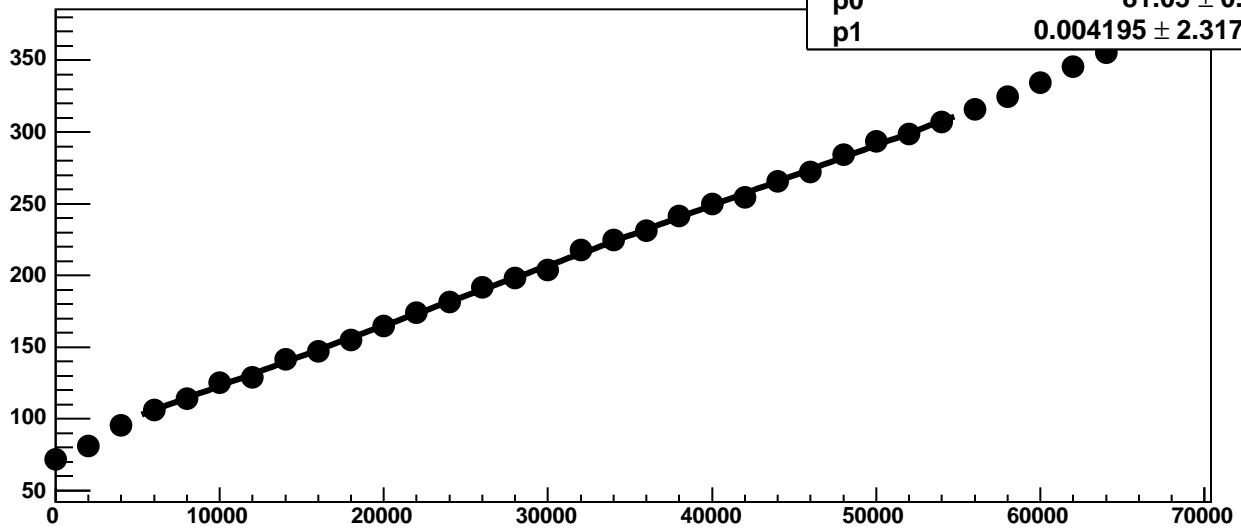
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

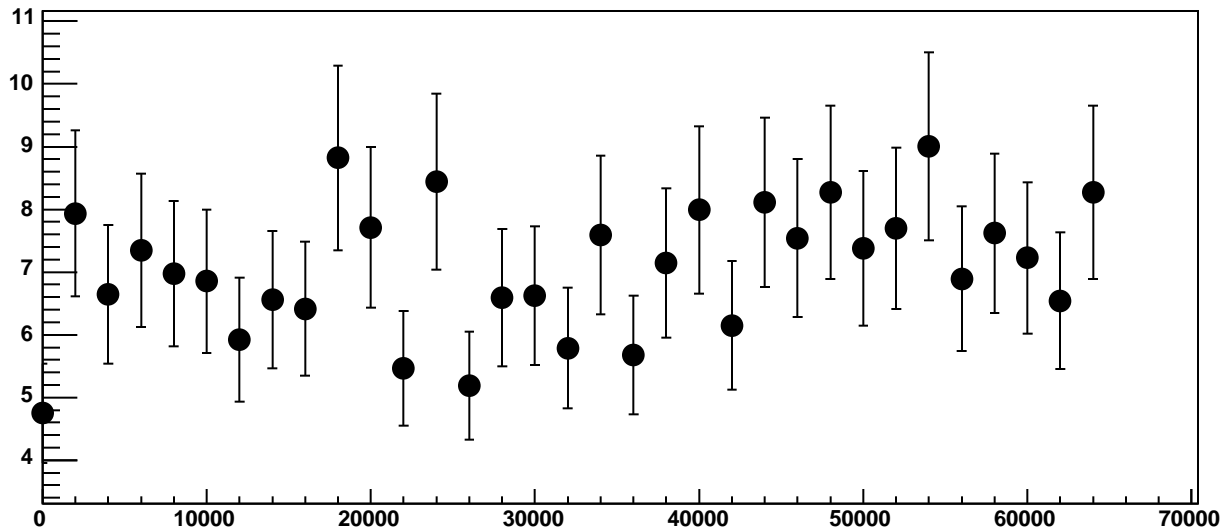


Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

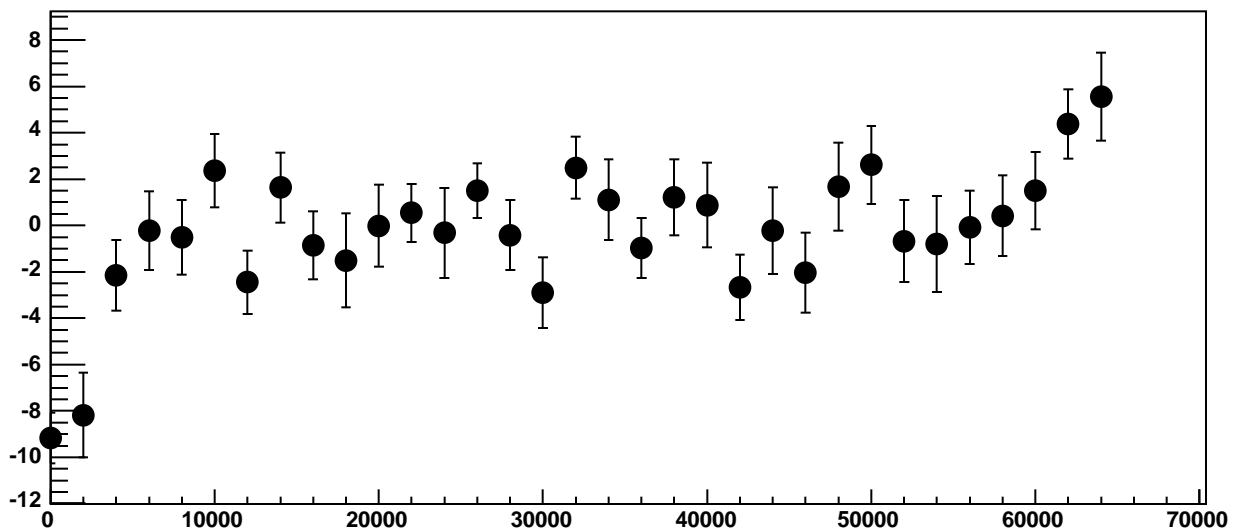


χ^2 / ndf 26.95 / 23
p0 81.05 ± 0.7361
p1 0.004195 ± 2.317e-05

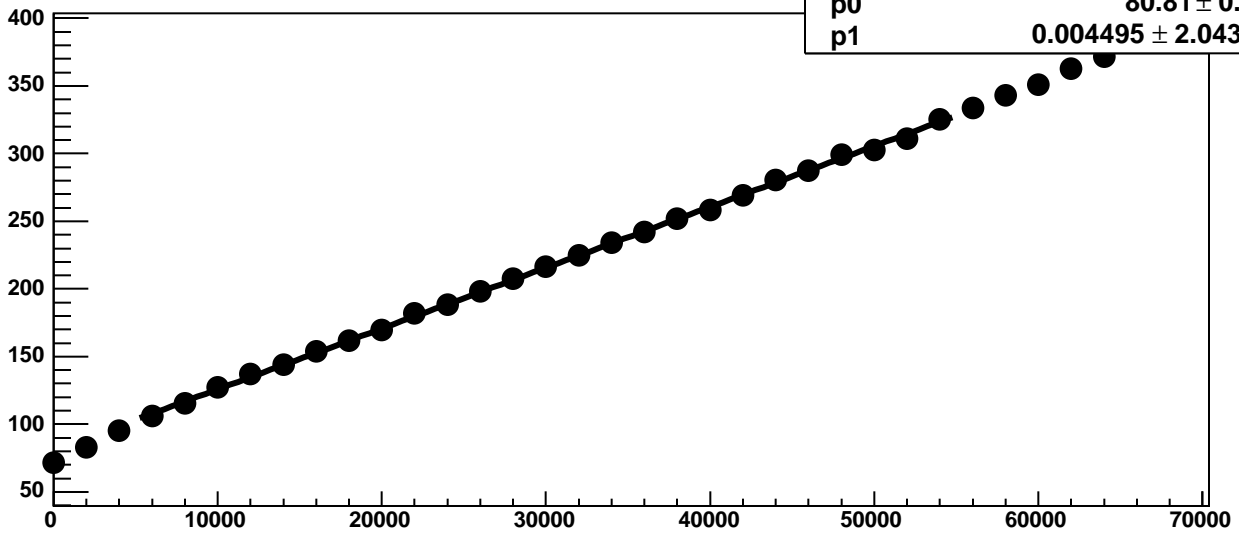
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

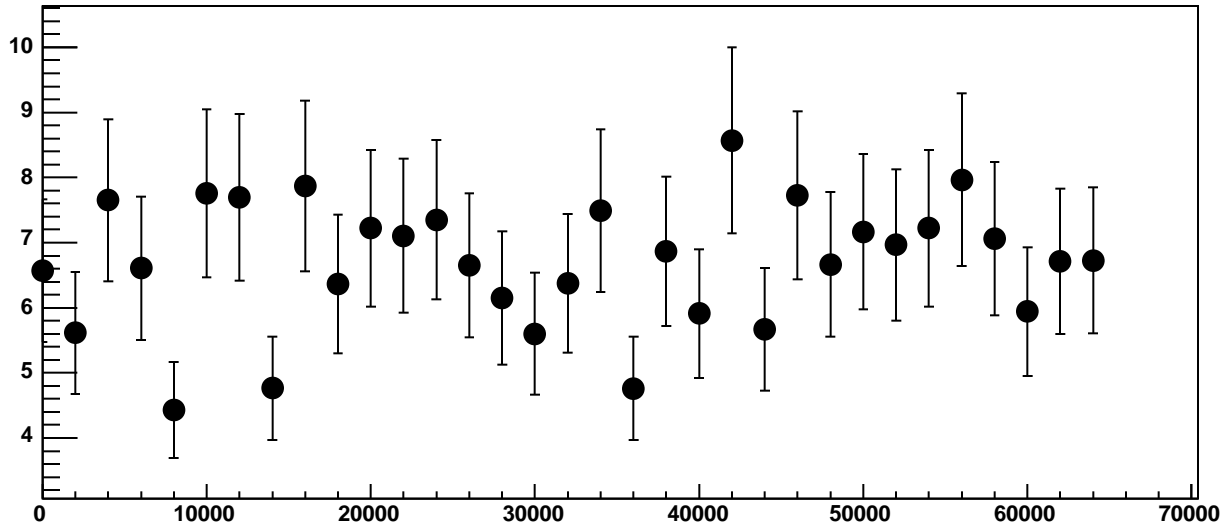


Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

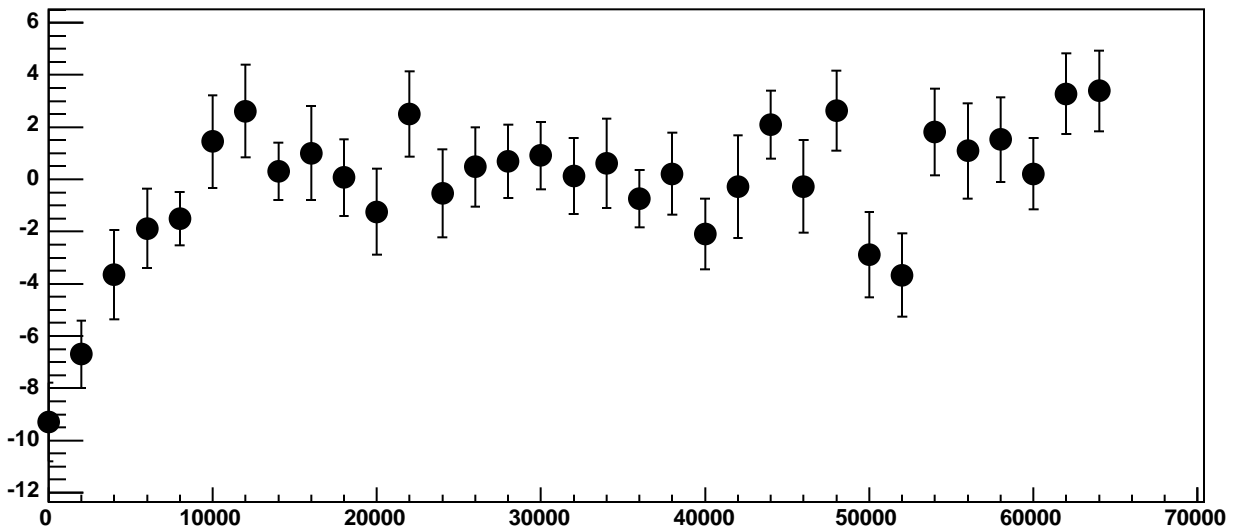


χ^2 / ndf 28.99 / 23
p0 80.81 ± 0.6581
p1 $0.004495 \pm 2.043e-05$

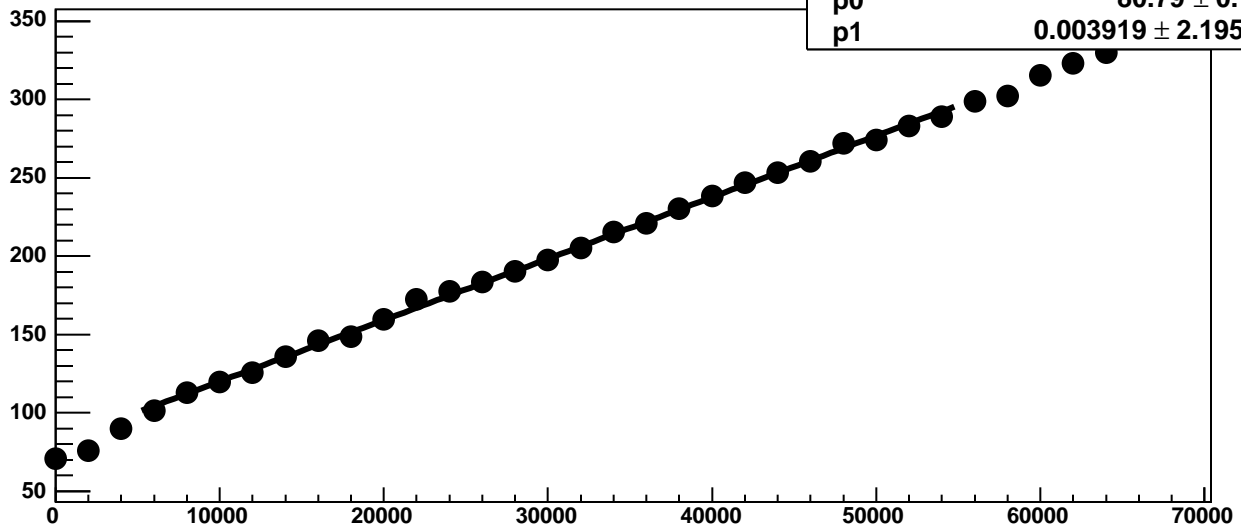
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



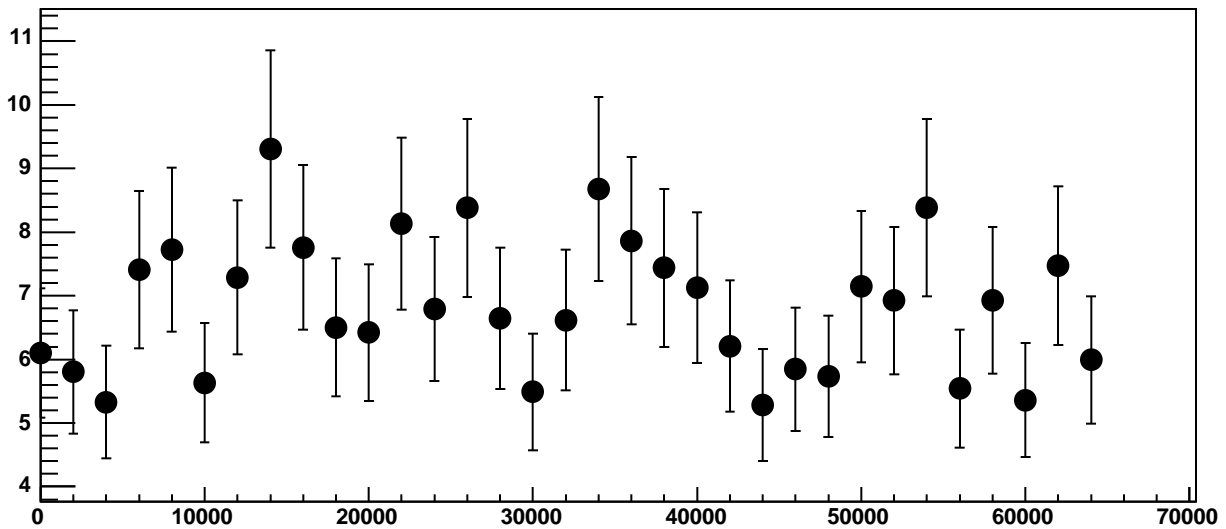
Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



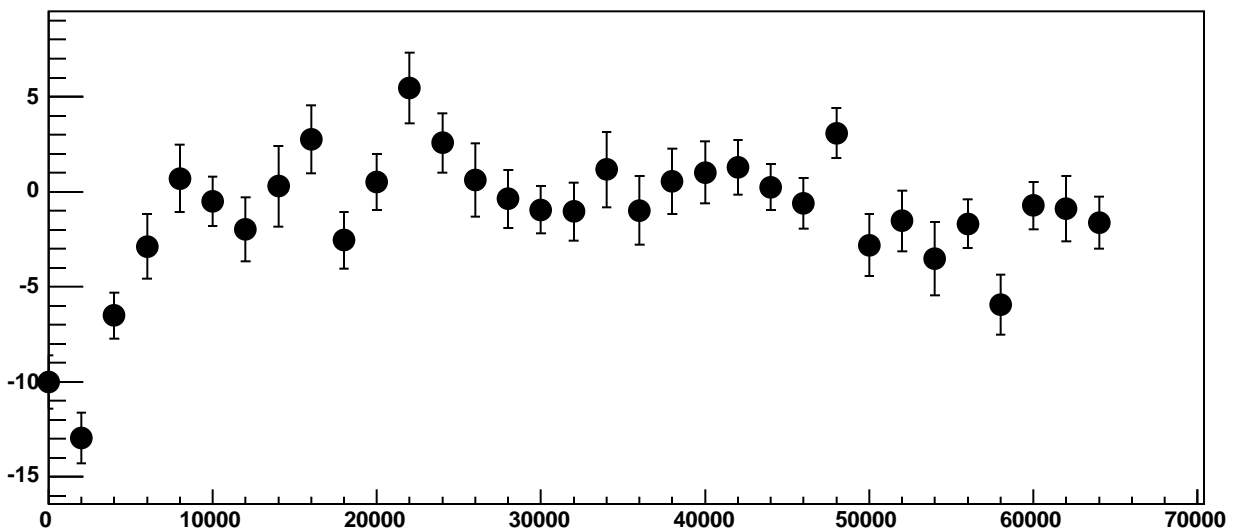
Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



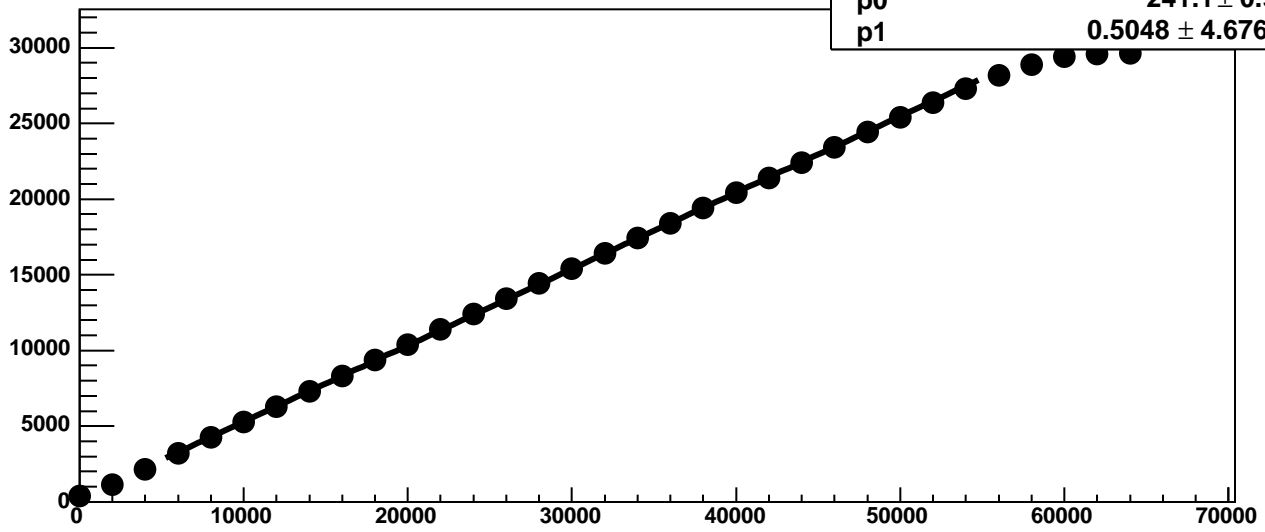
Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



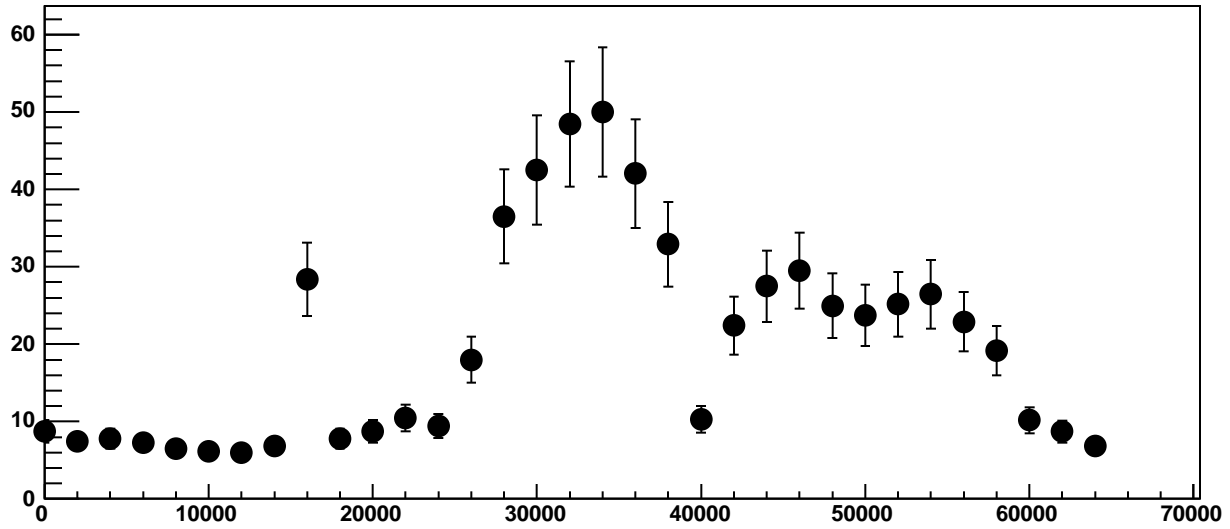
Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



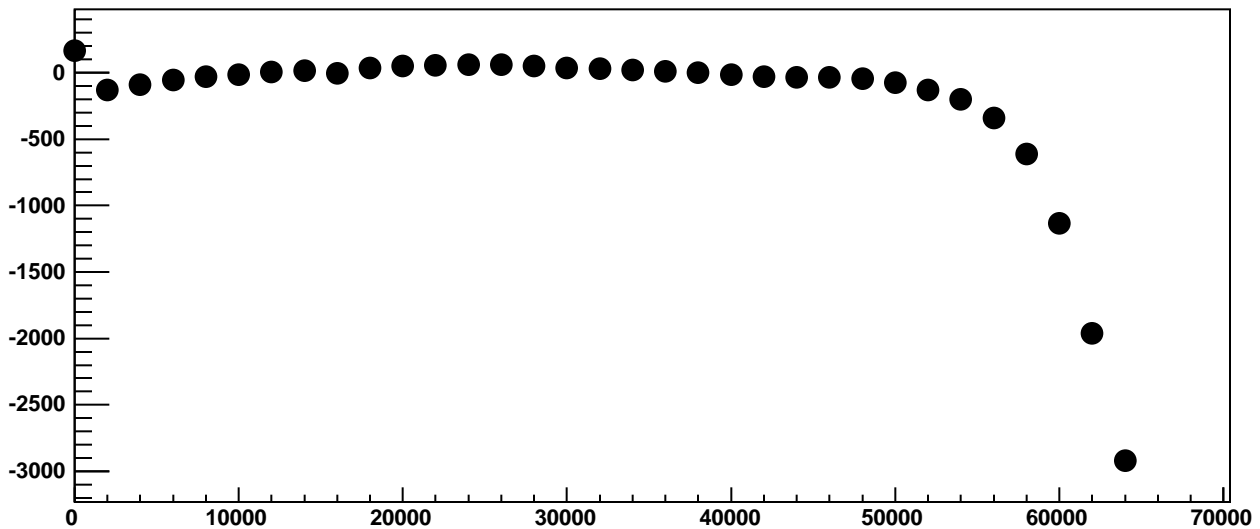
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



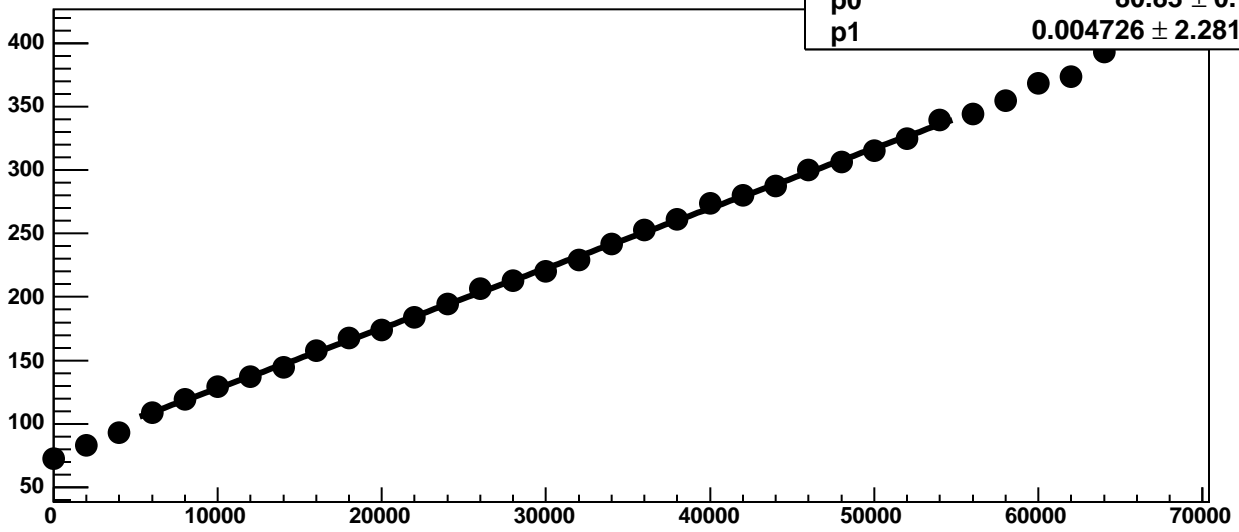
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

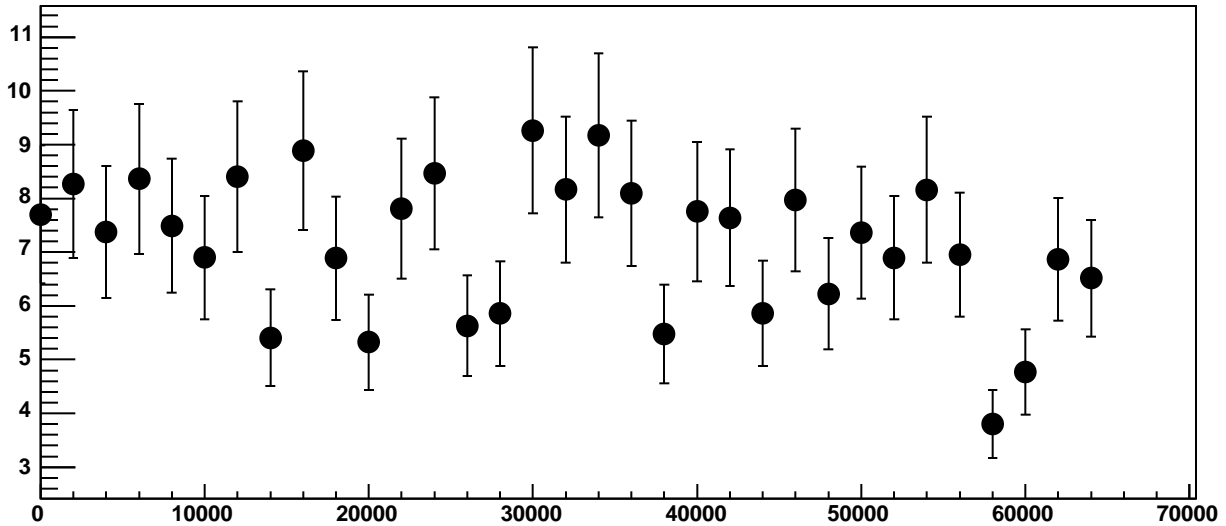


Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

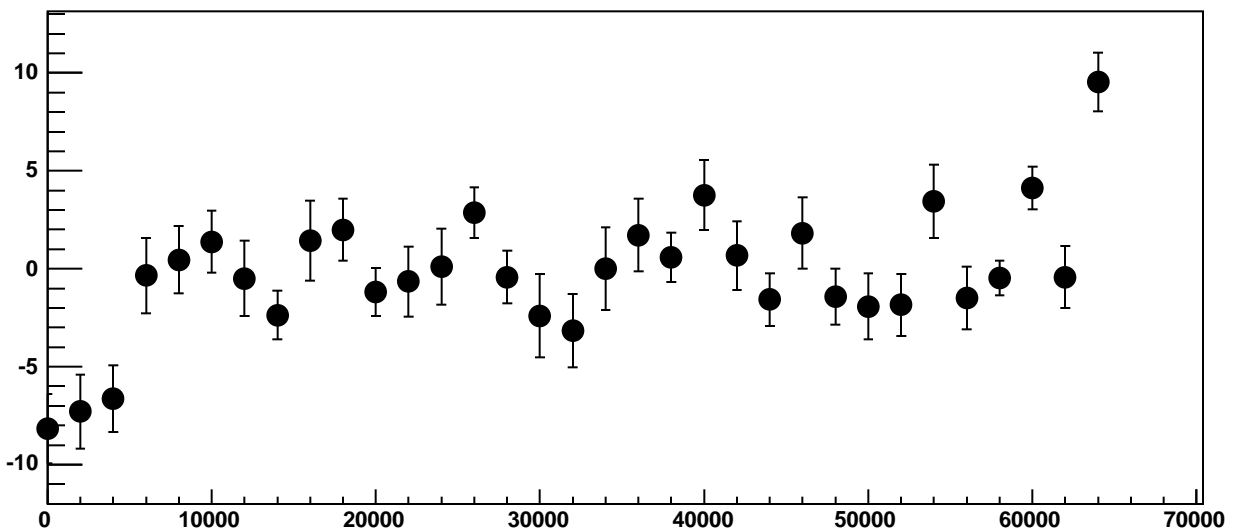


χ^2 / ndf 31.97 / 23
p0 80.83 ± 0.7556
p1 $0.004726 \pm 2.281e-05$

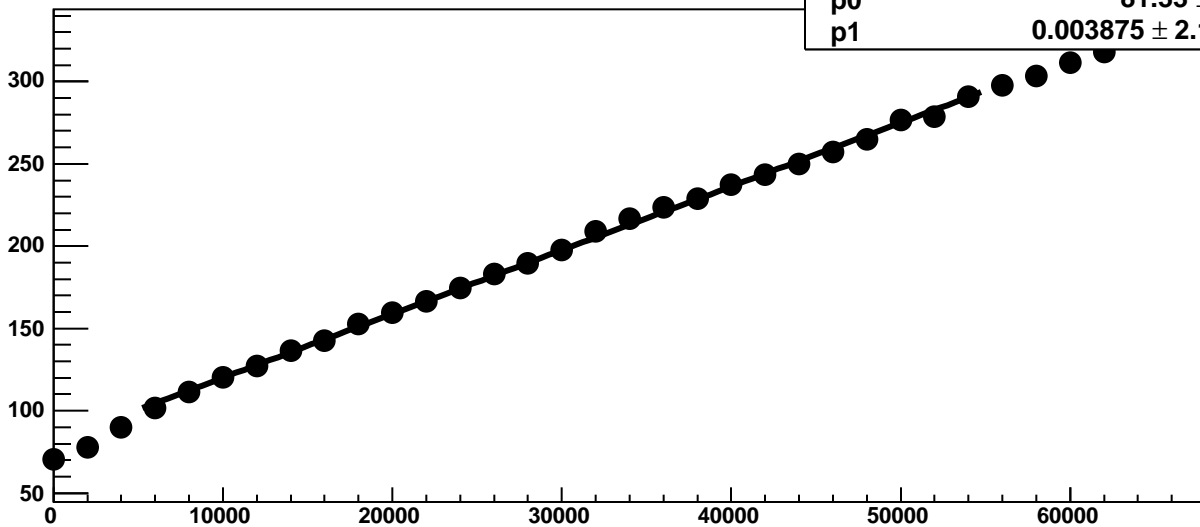
Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

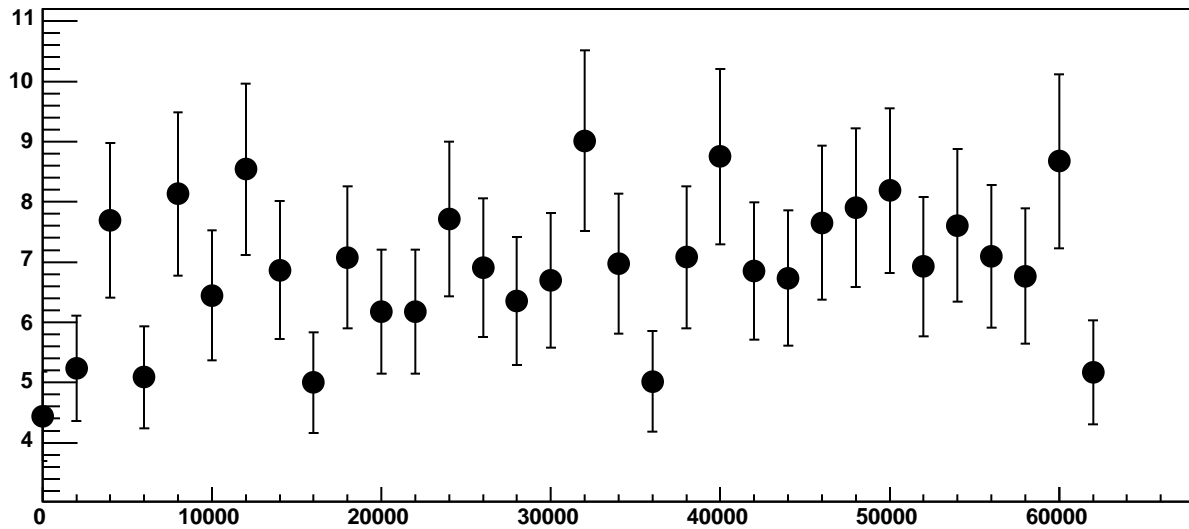


Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

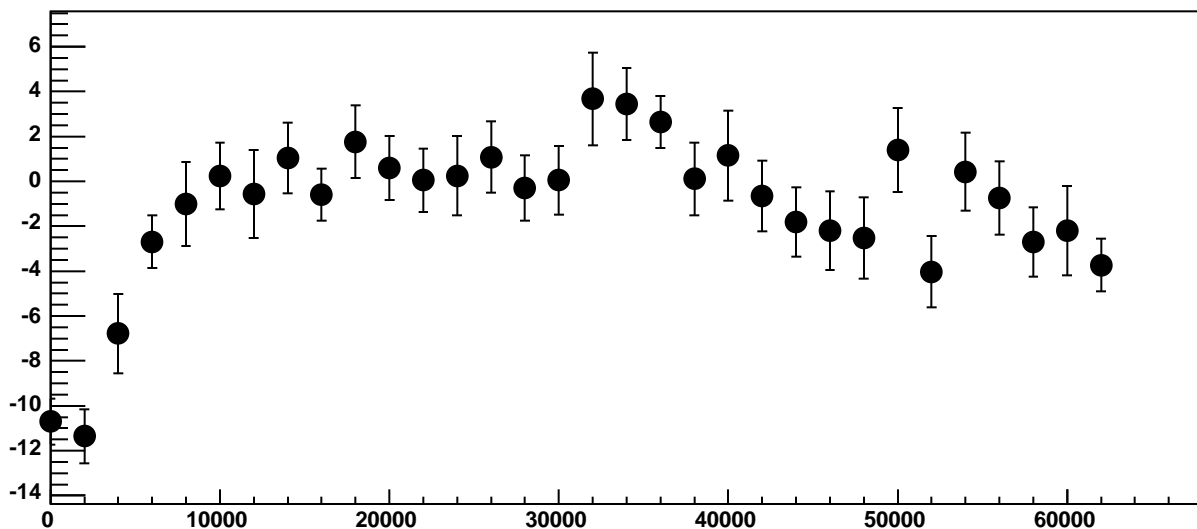


χ^2 / ndf 33.9 / 23
p0 81.33 ± 0.6911
p1 $0.003875 \pm 2.177e-05$

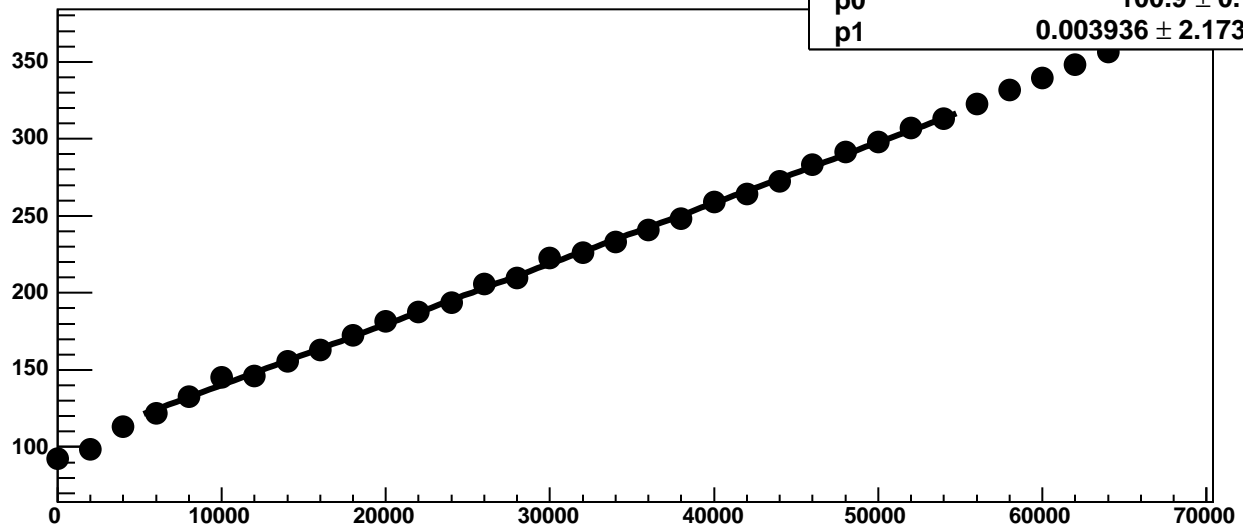
Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

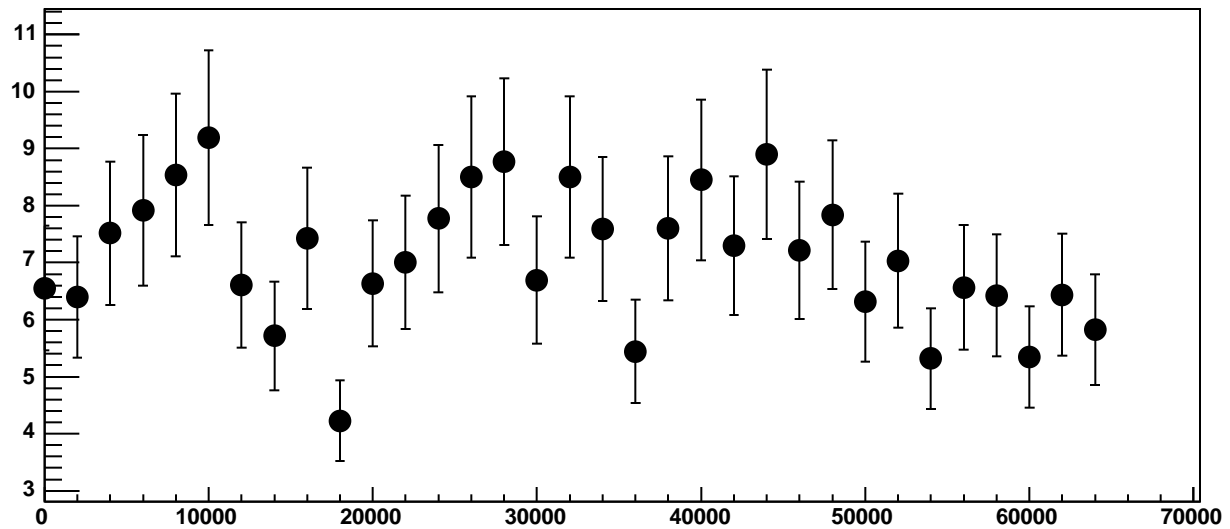


Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

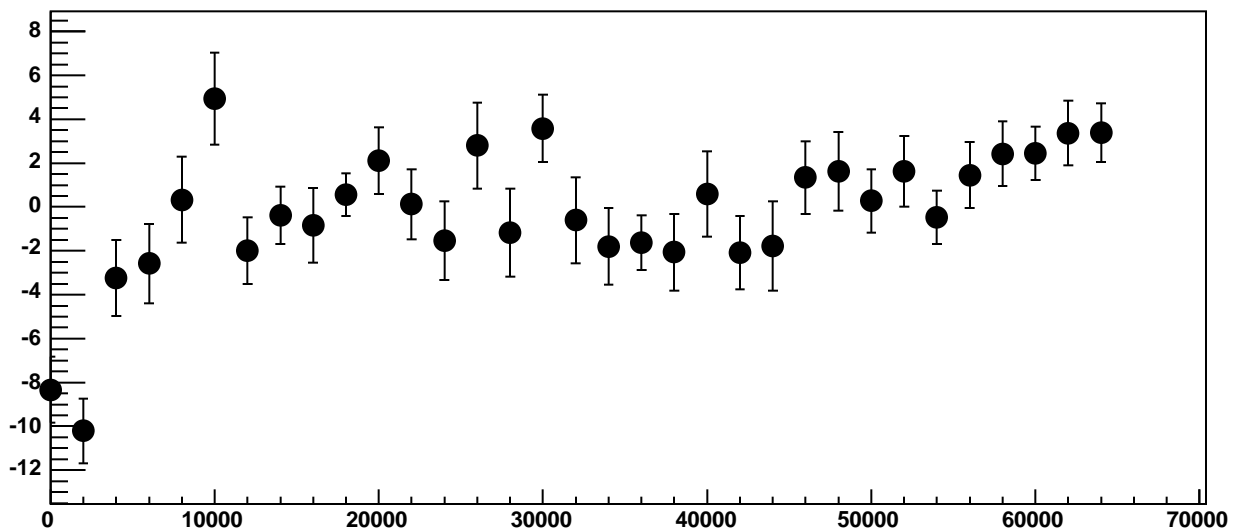


χ^2 / ndf 29.69 / 23
p0 100.9 ± 0.7272
p1 $0.003936 \pm 2.173e-05$

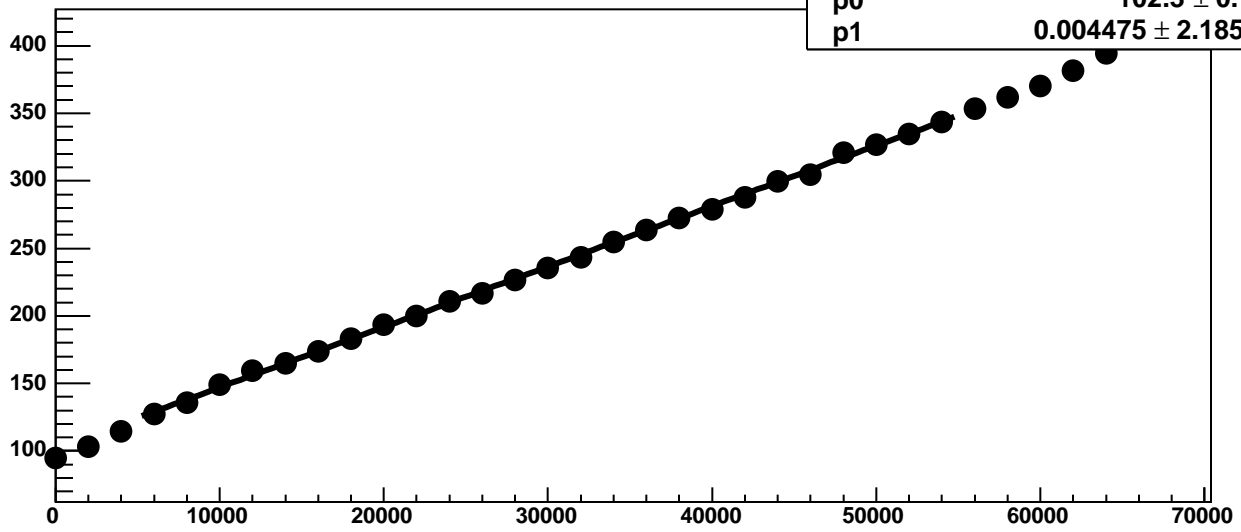
Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

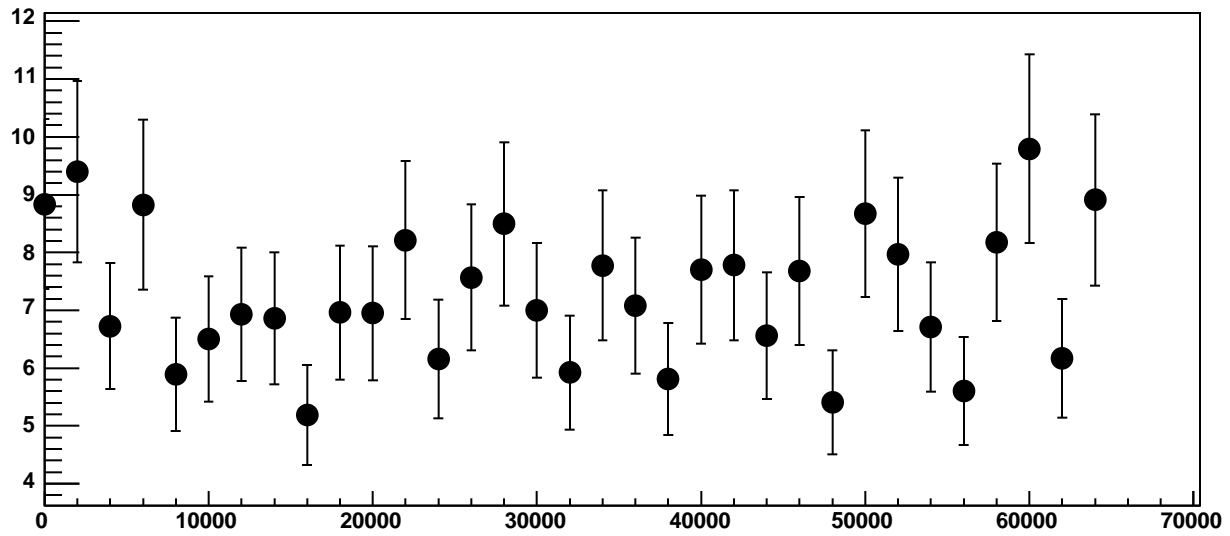


Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

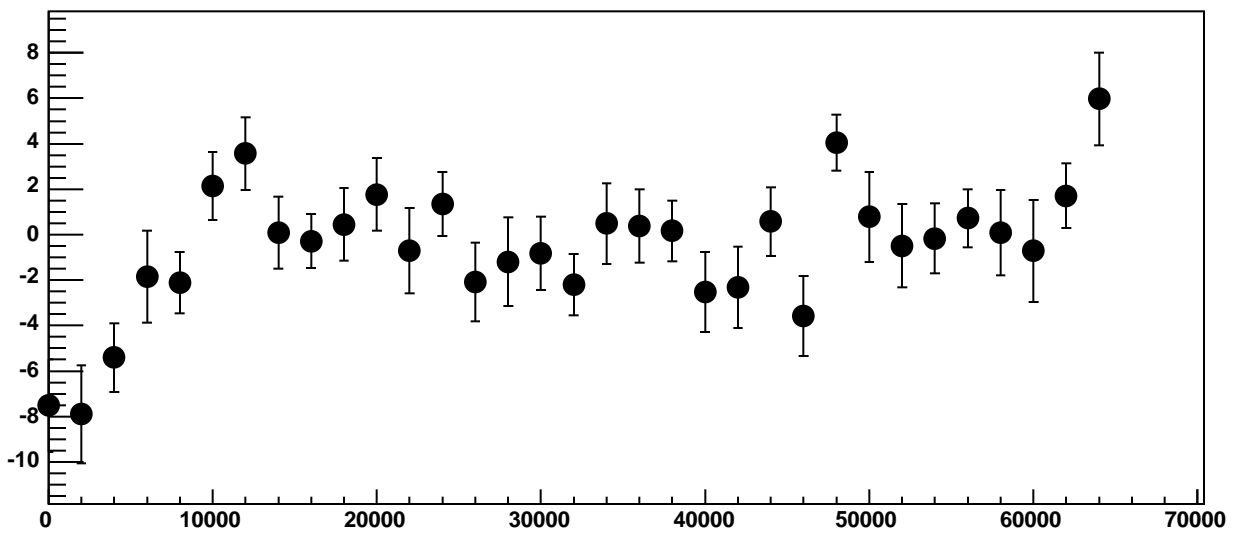


χ^2 / ndf 36.51 / 23
p0 102.3 ± 0.7169
p1 0.004475 ± 2.185e-05

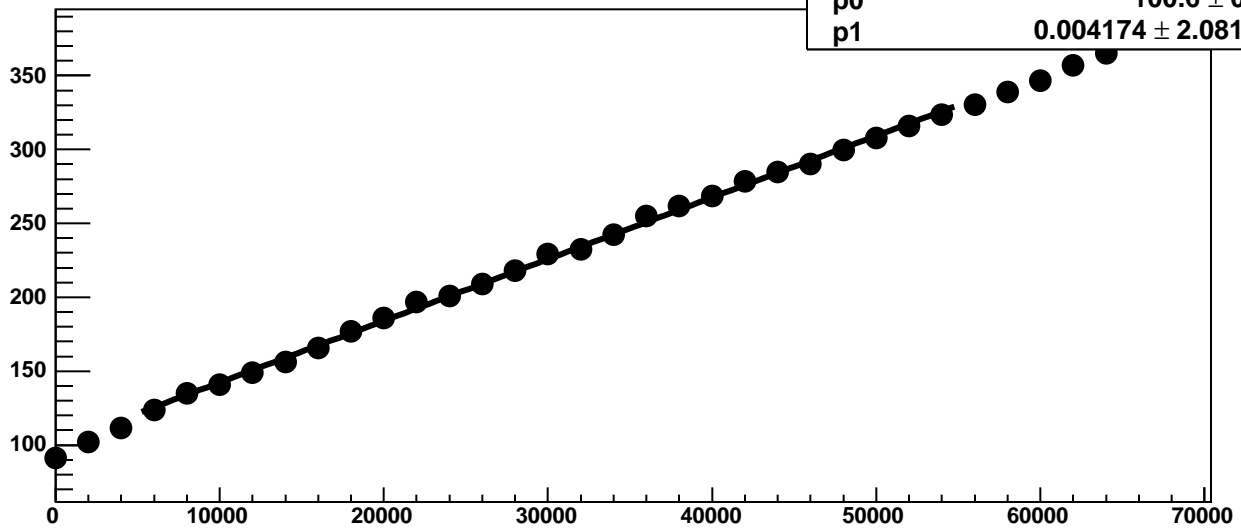
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

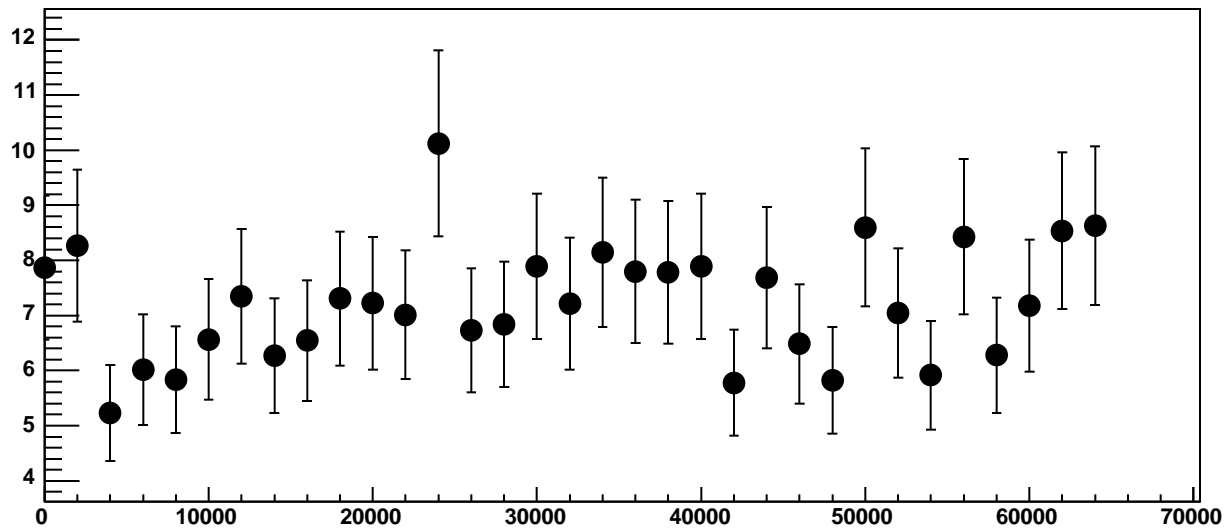


Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

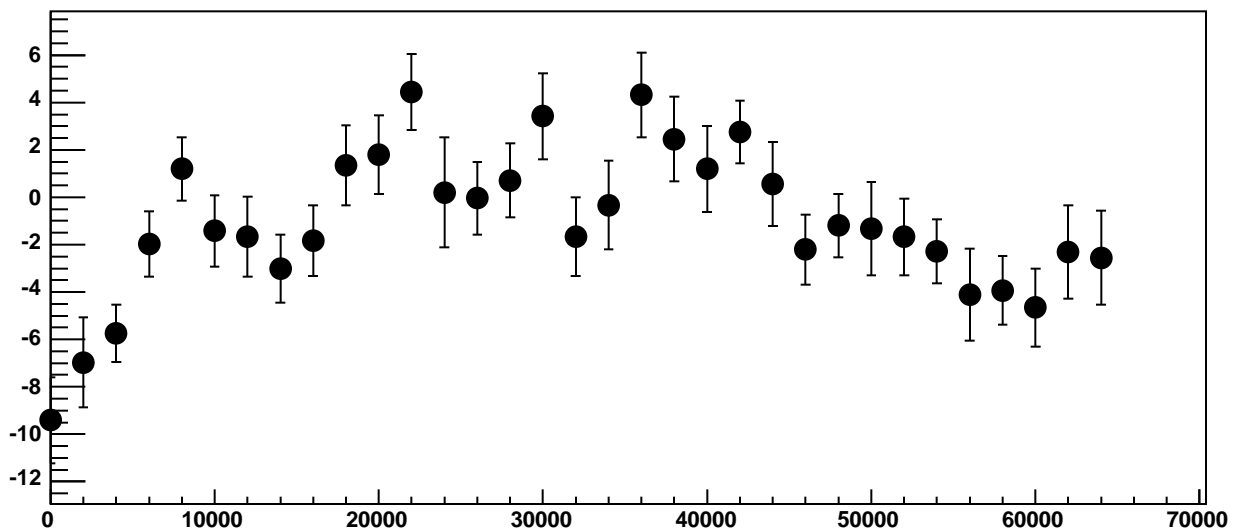


χ^2 / ndf 44.76 / 23
p0 100.6 ± 0.693
p1 0.004174 ± 2.081e-05

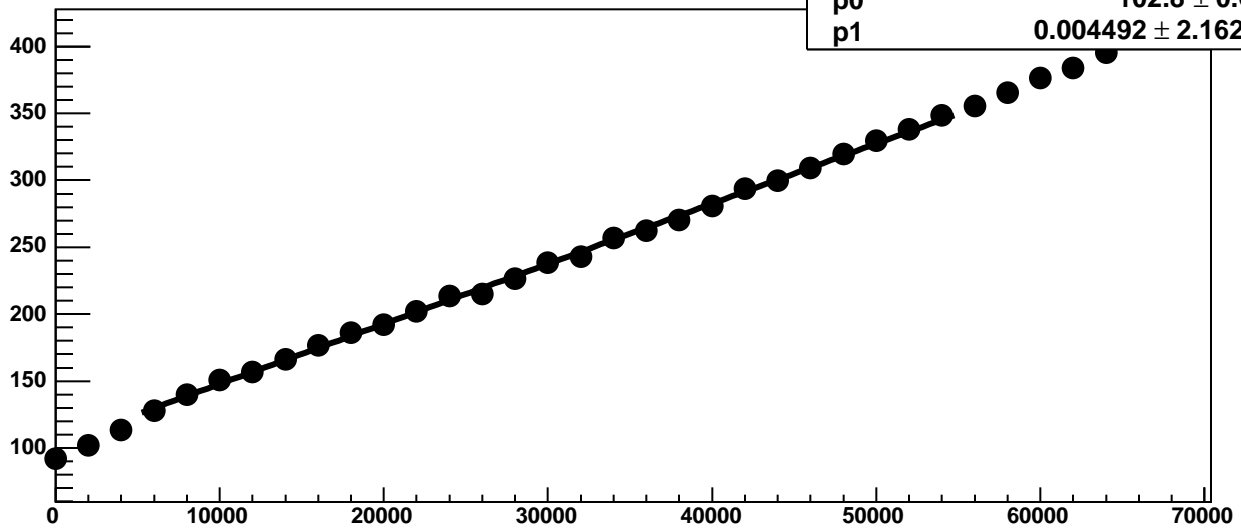
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

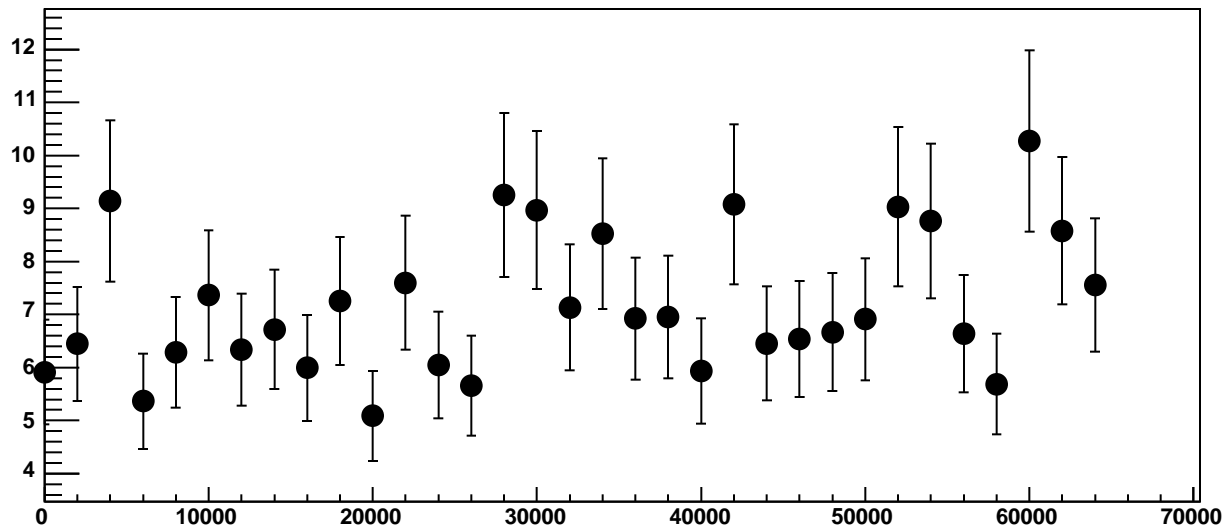


Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

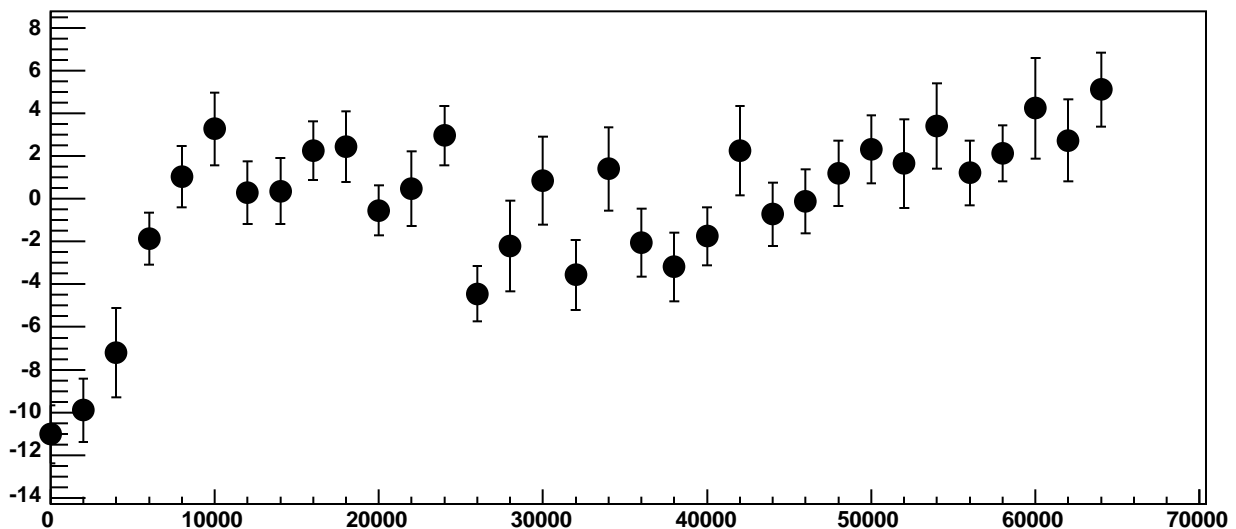


χ^2 / ndf 49.62 / 23
p0 102.8 ± 0.6806
p1 $0.004492 \pm 2.162e-05$

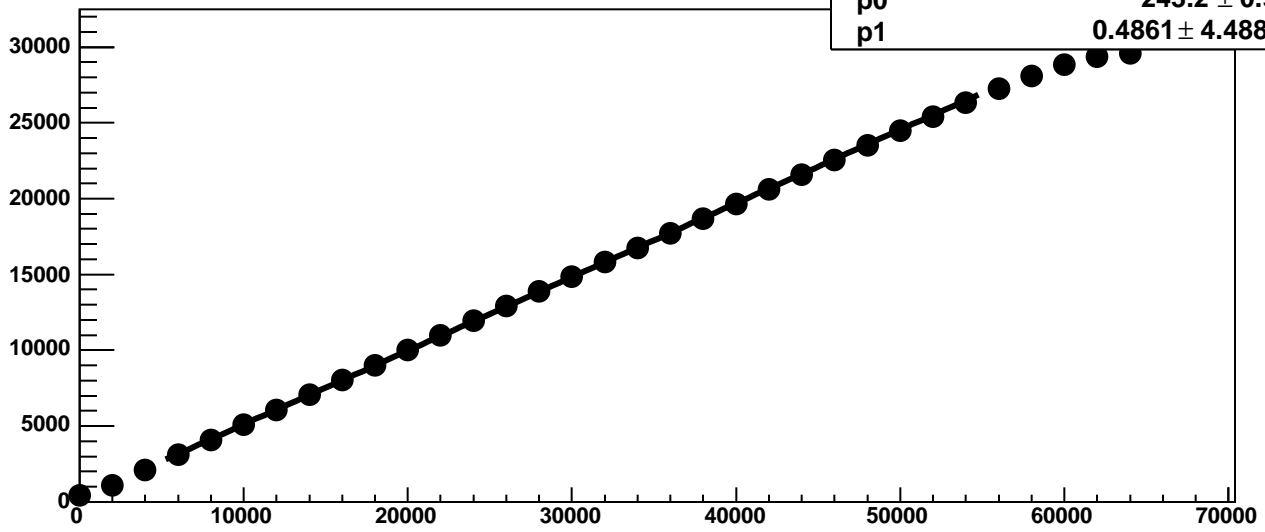
Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



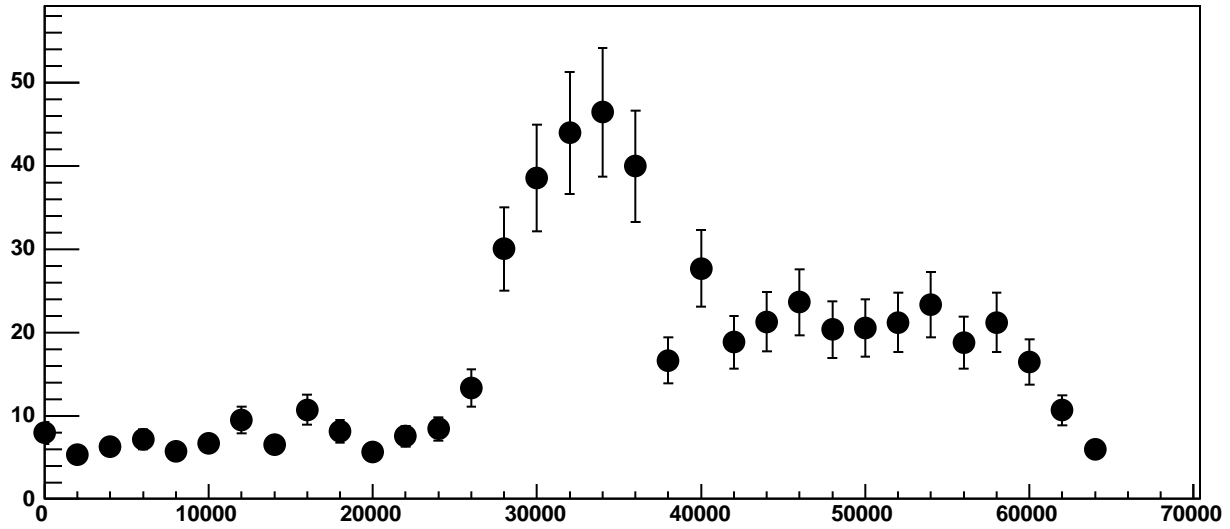
Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



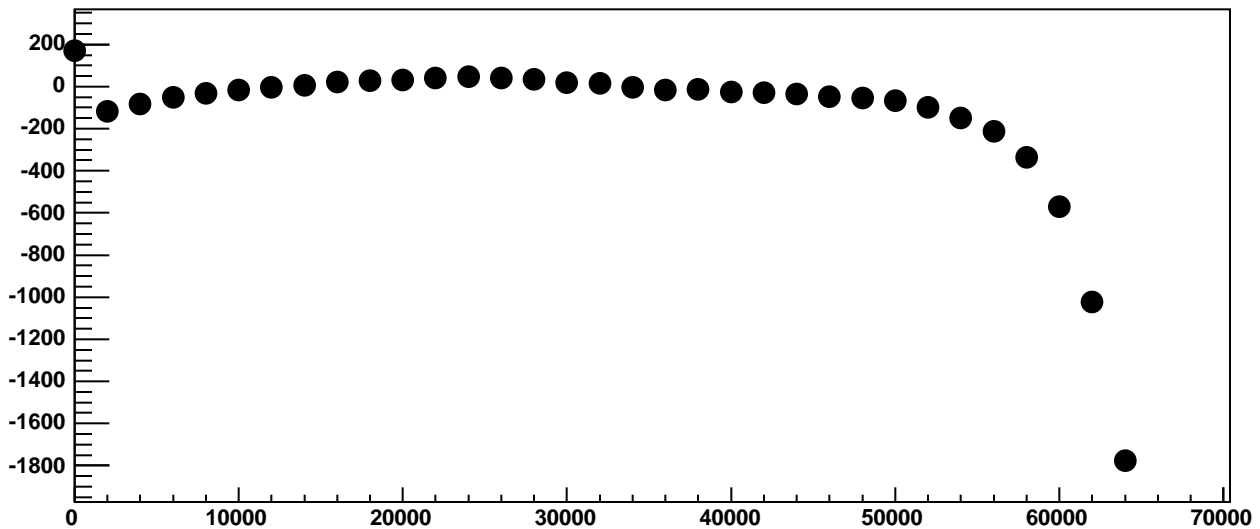
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



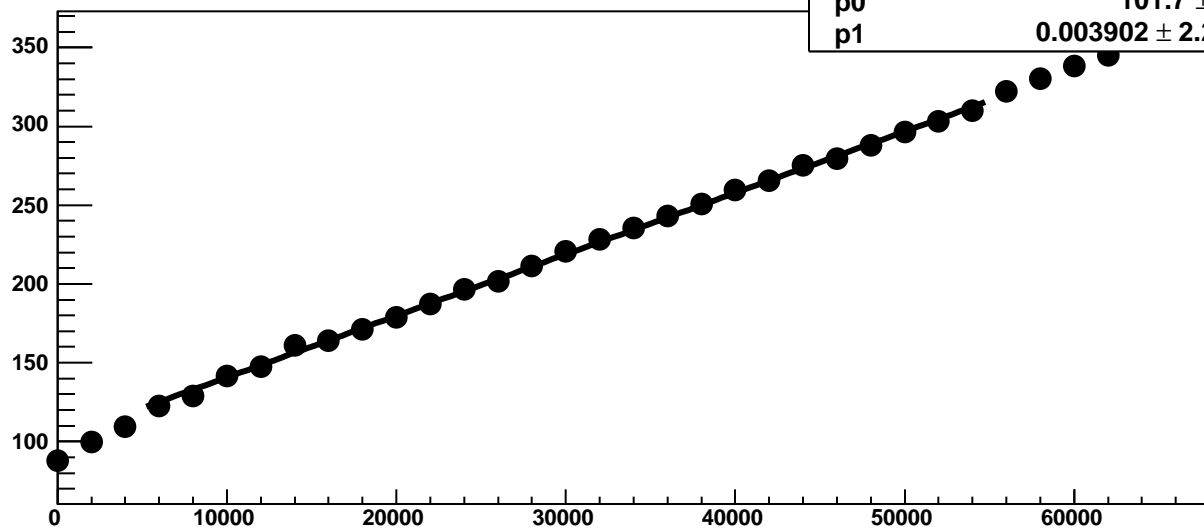
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

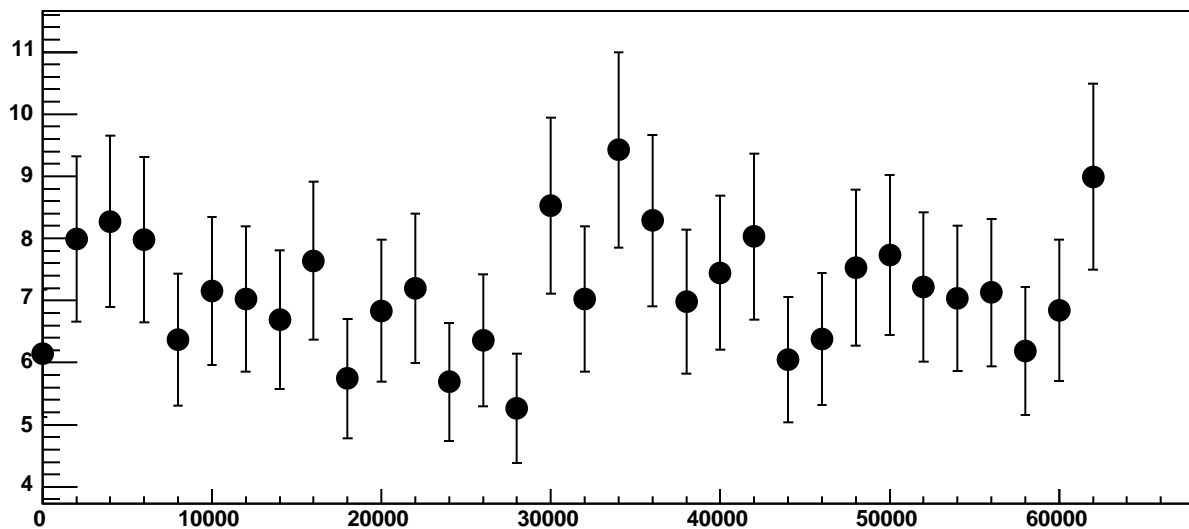


Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

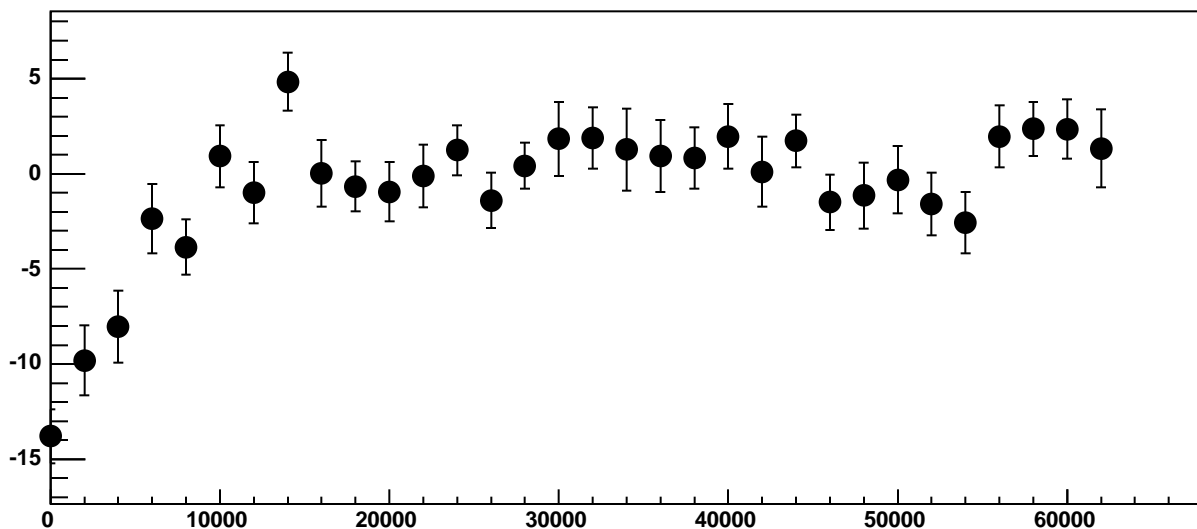


χ^2 / ndf 32.72 / 23
p0 101.7 ± 0.7312
p1 0.003902 ± 2.242e-05

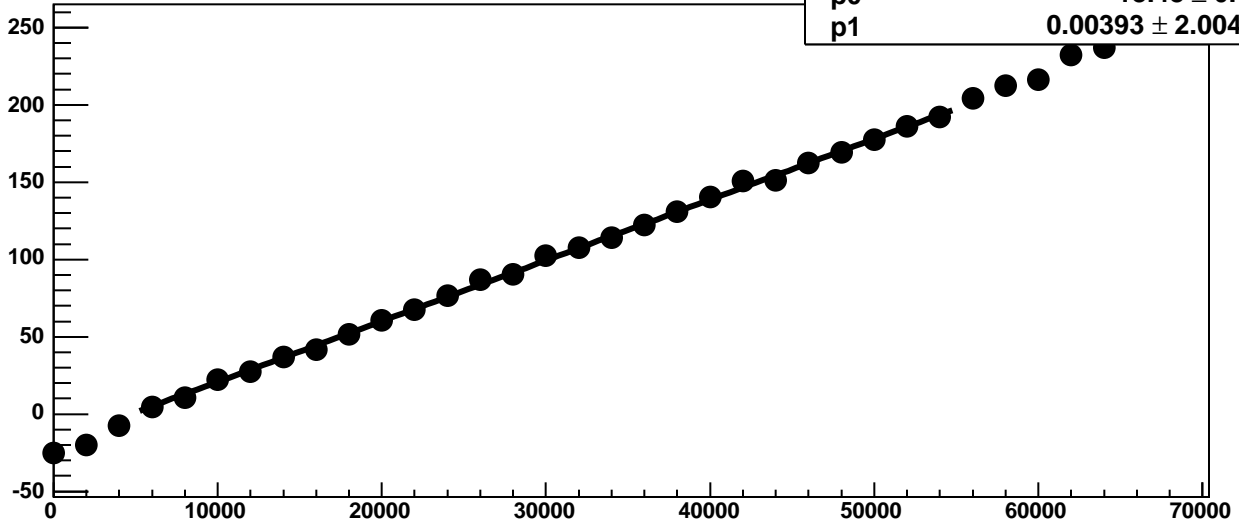
Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

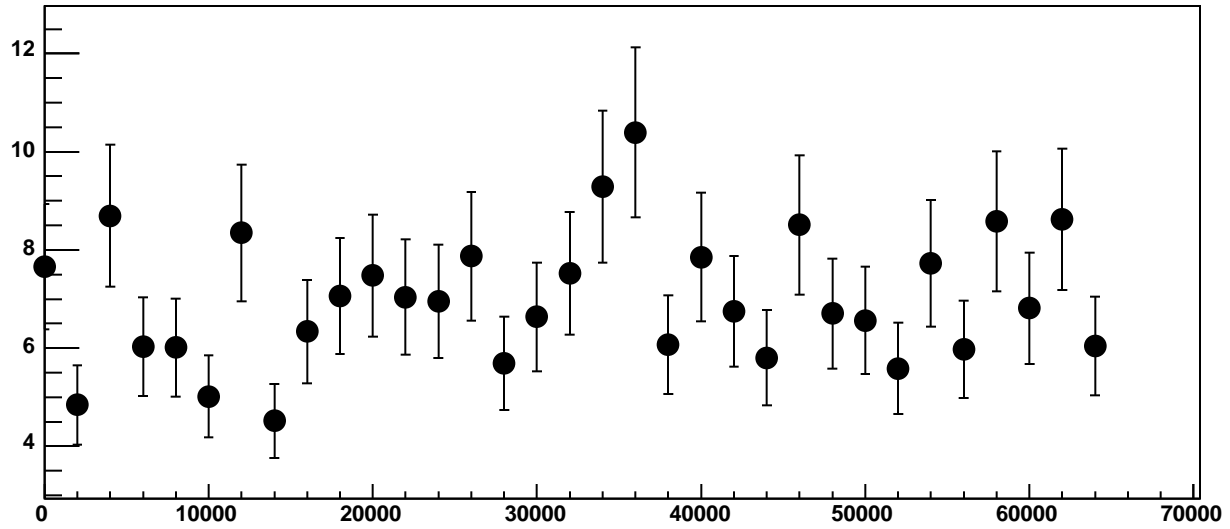


Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

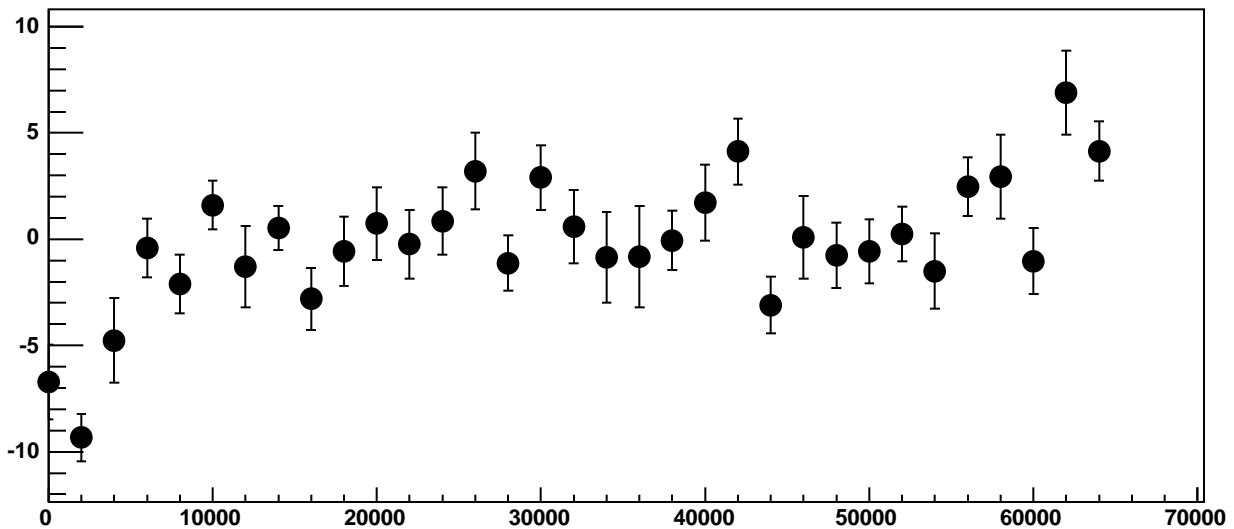


χ^2 / ndf 32.01 / 23
p0 -18.48 ± 0.6453
p1 0.00393 ± 2.004e-05

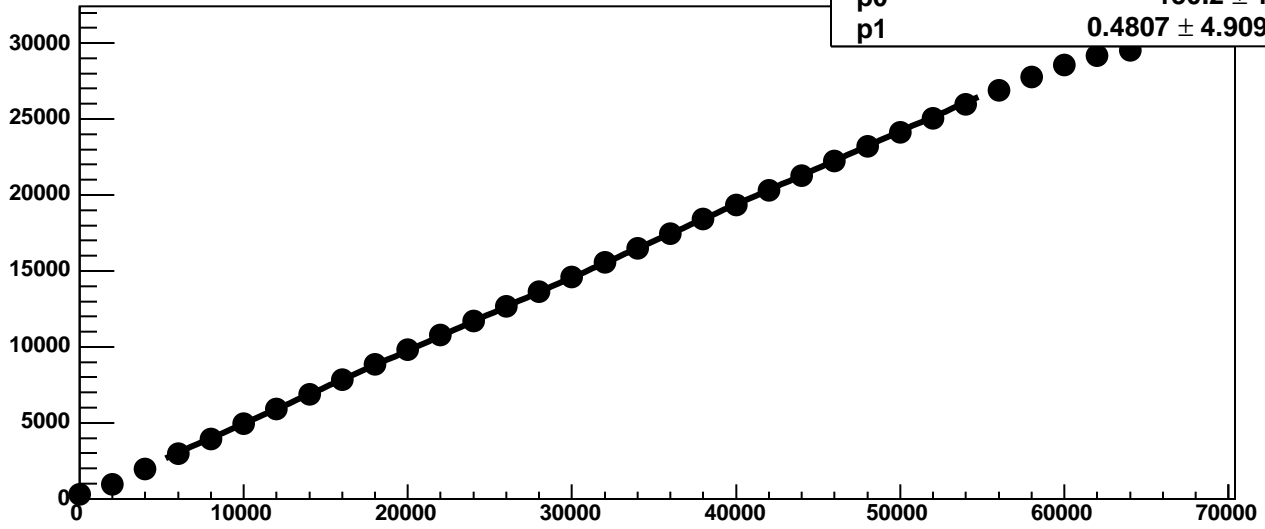
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



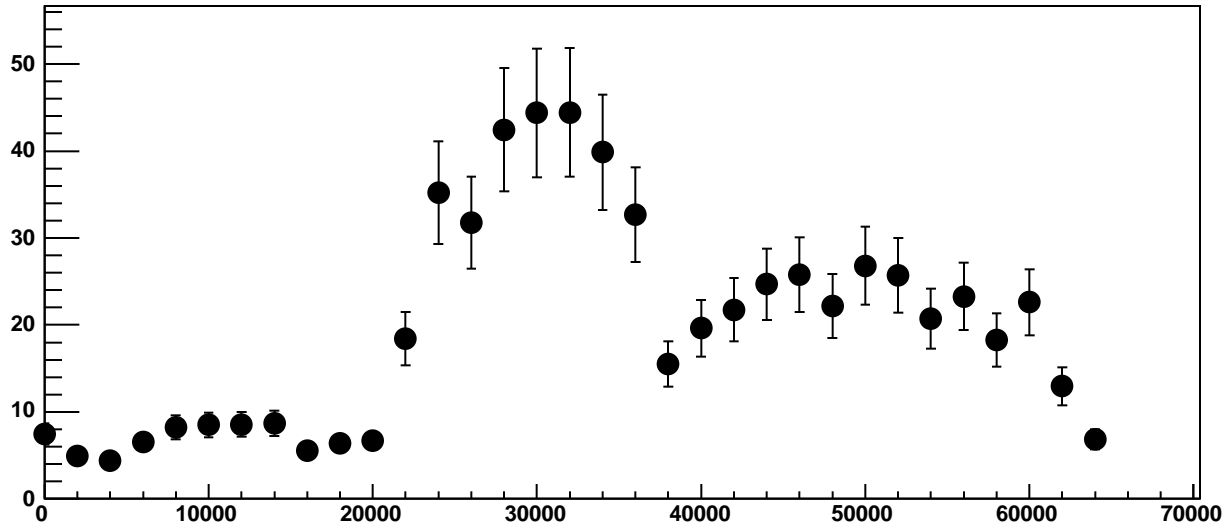
Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



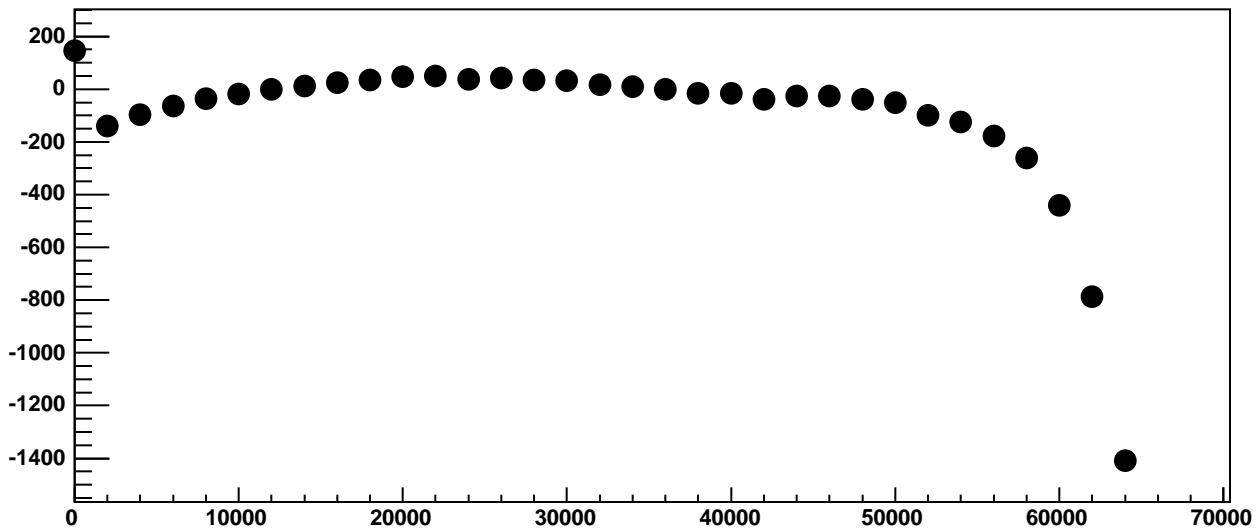
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



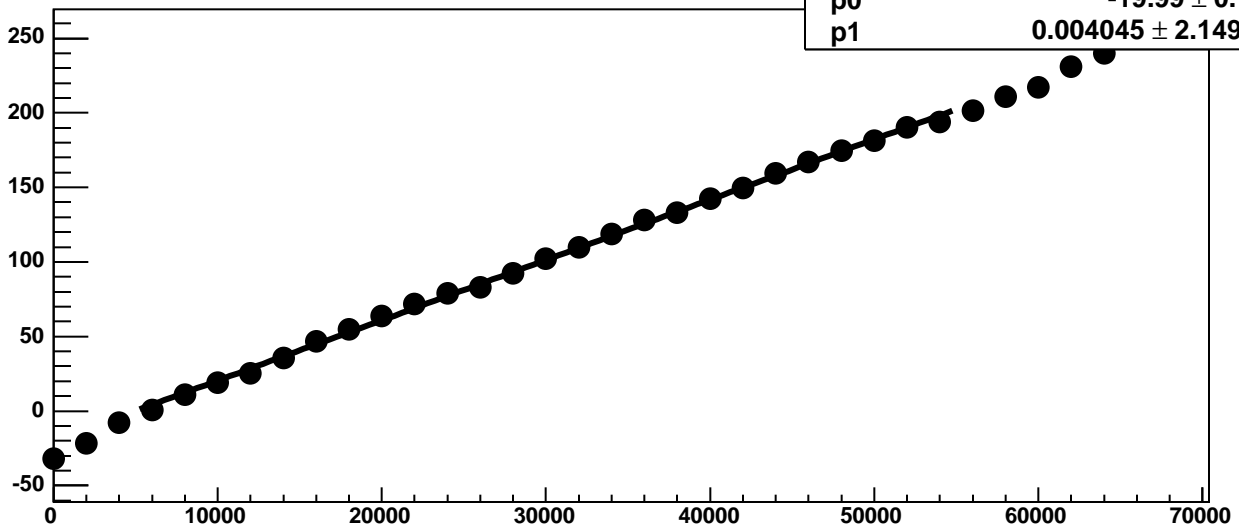
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

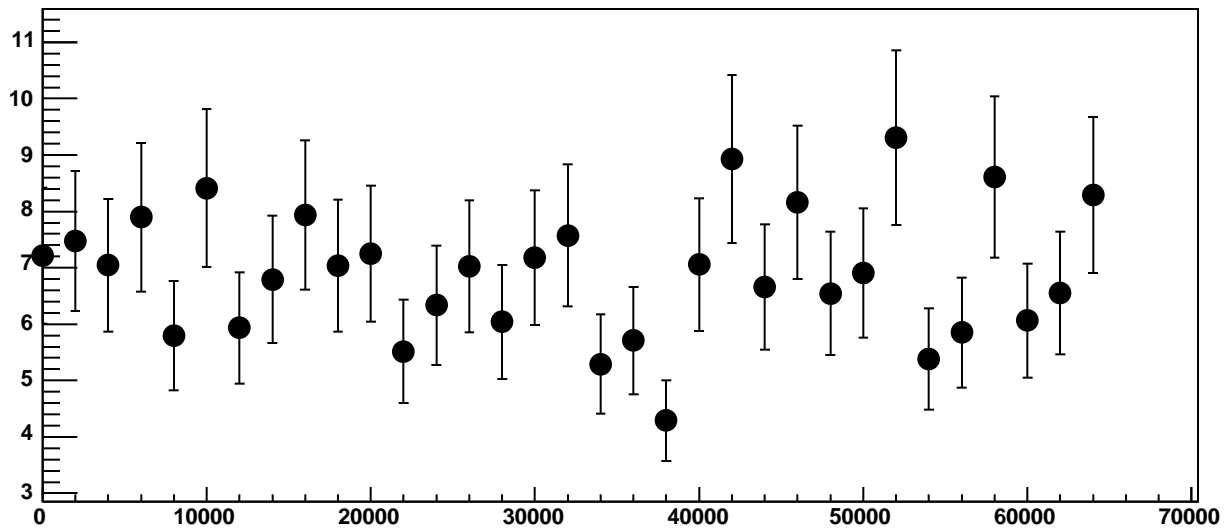


Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

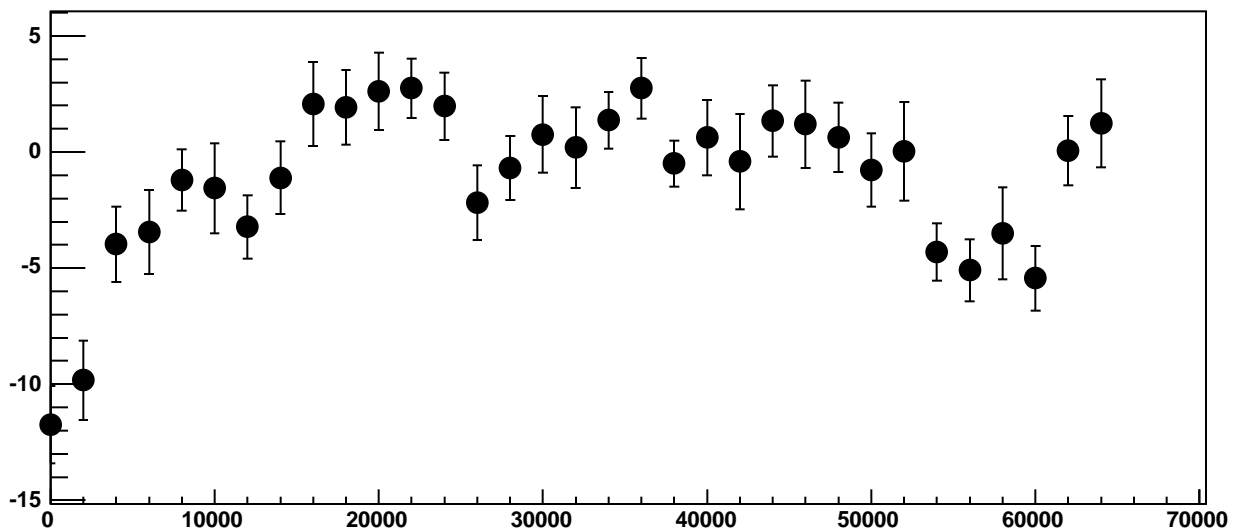


χ^2 / ndf 45.15 / 23
p0 -19.99 ± 0.7186
p1 $0.004045 \pm 2.149\text{e-}05$

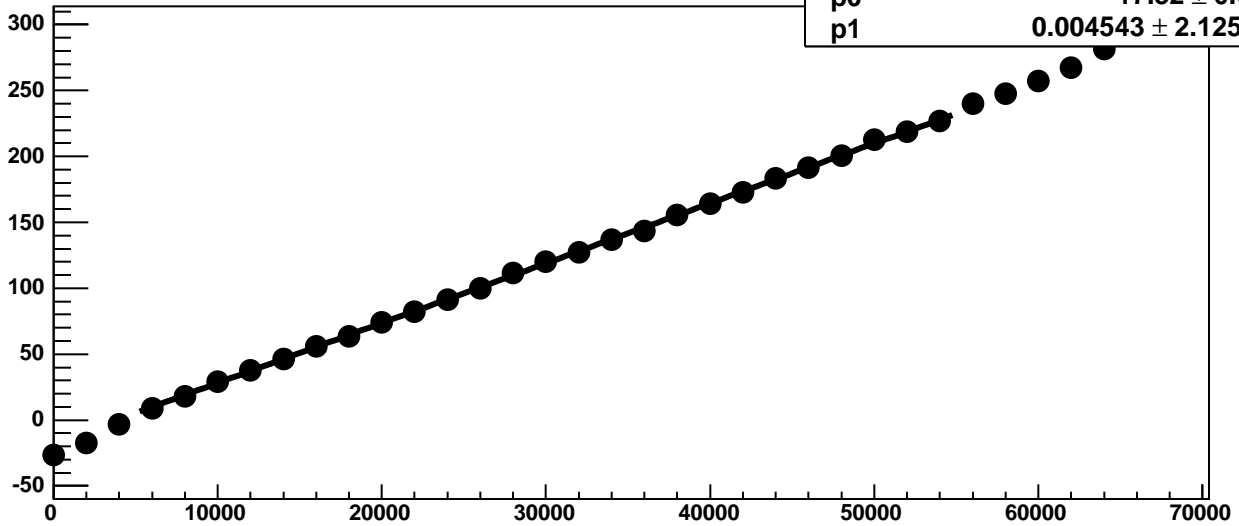
Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



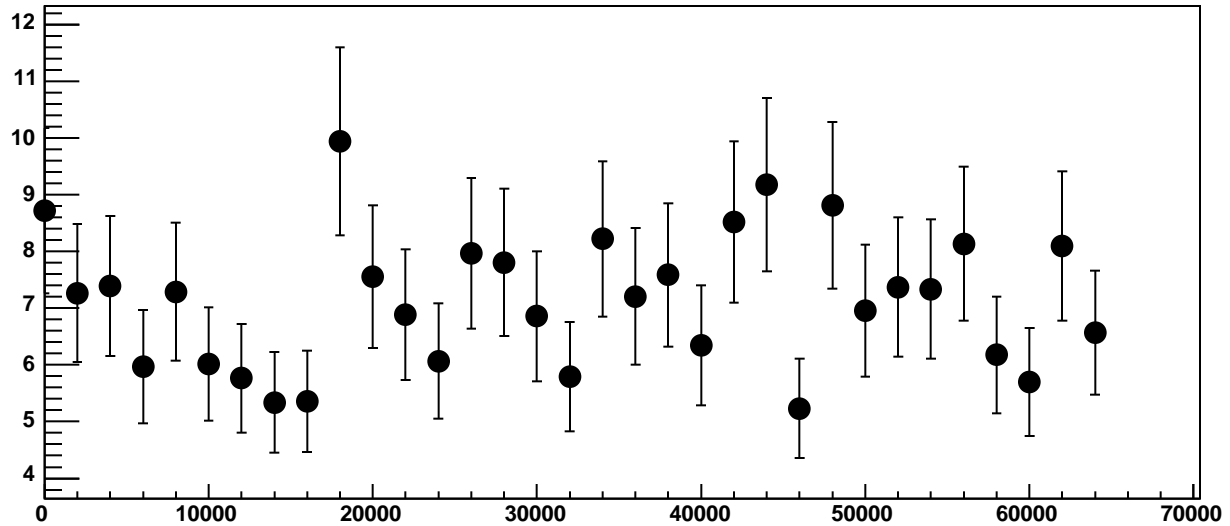
Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



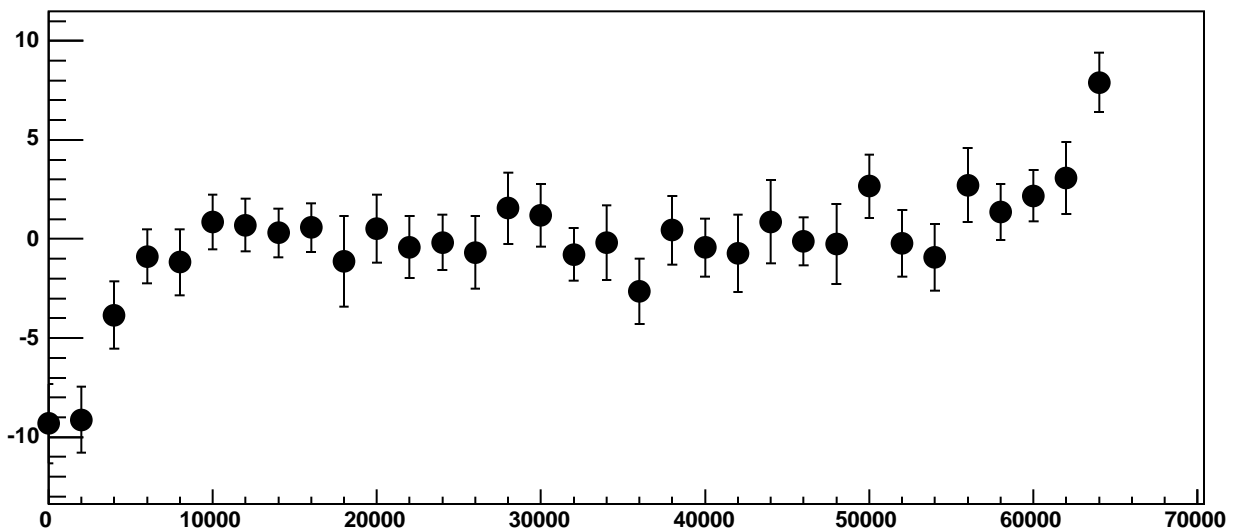
Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



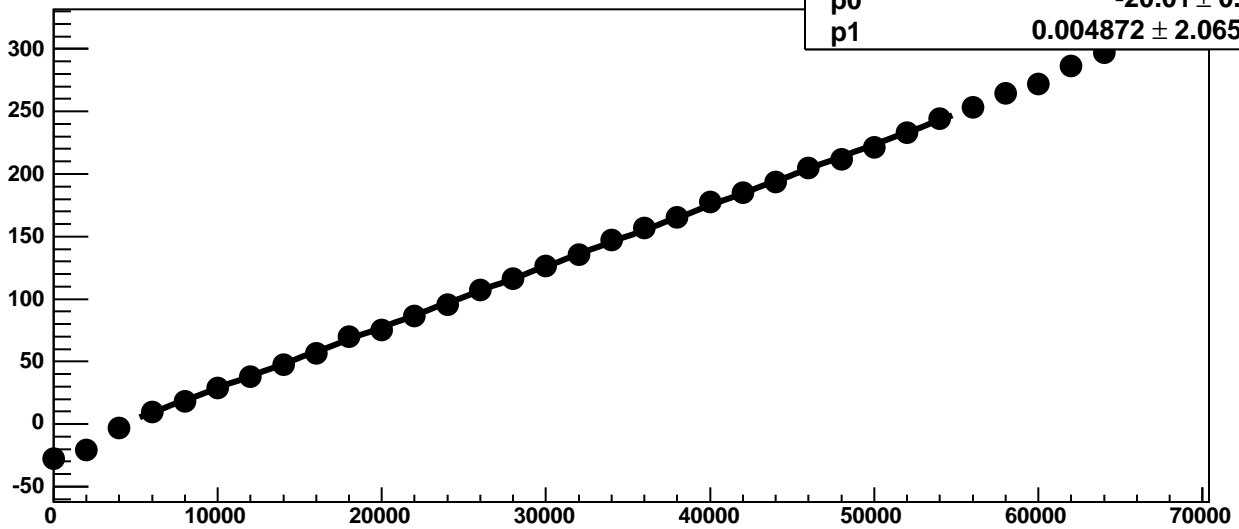
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



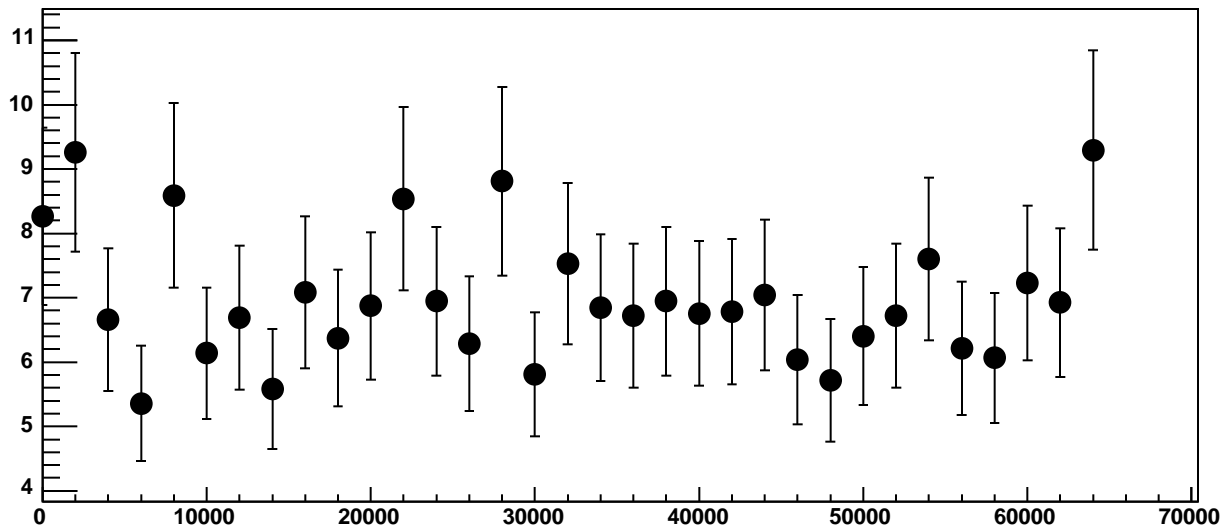
Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



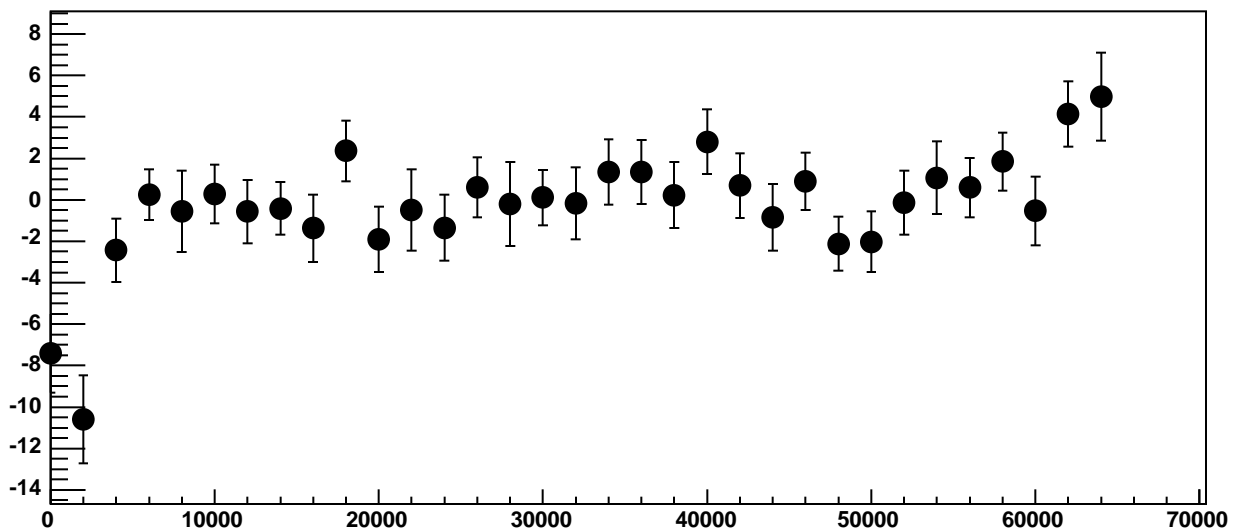
Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



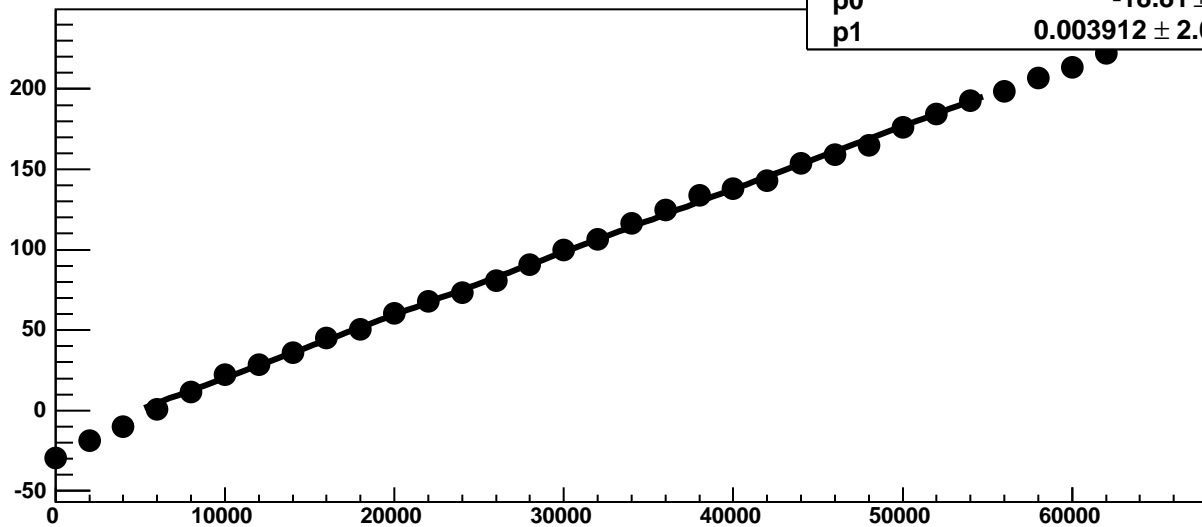
Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

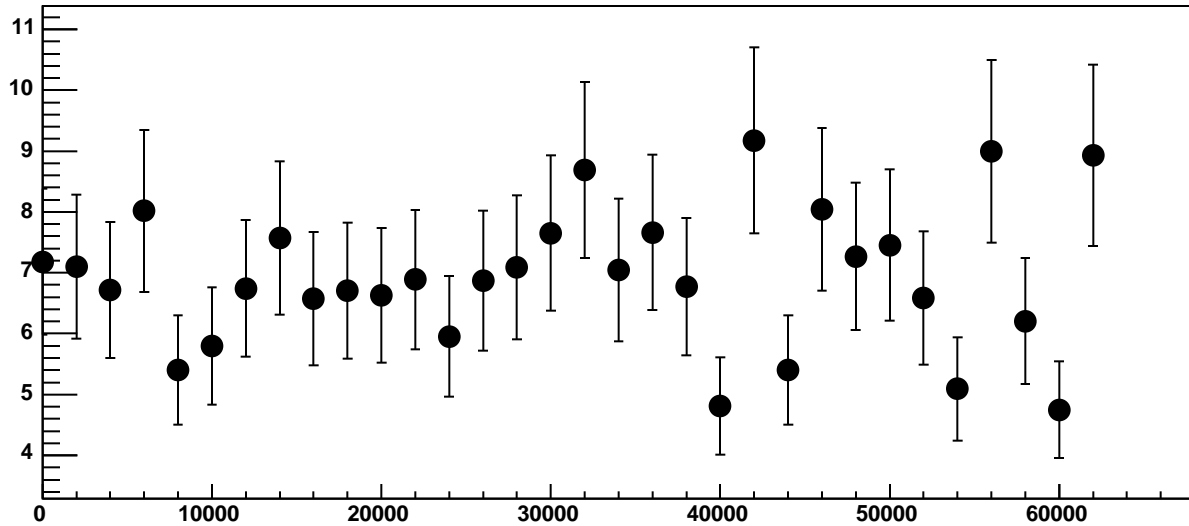


Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

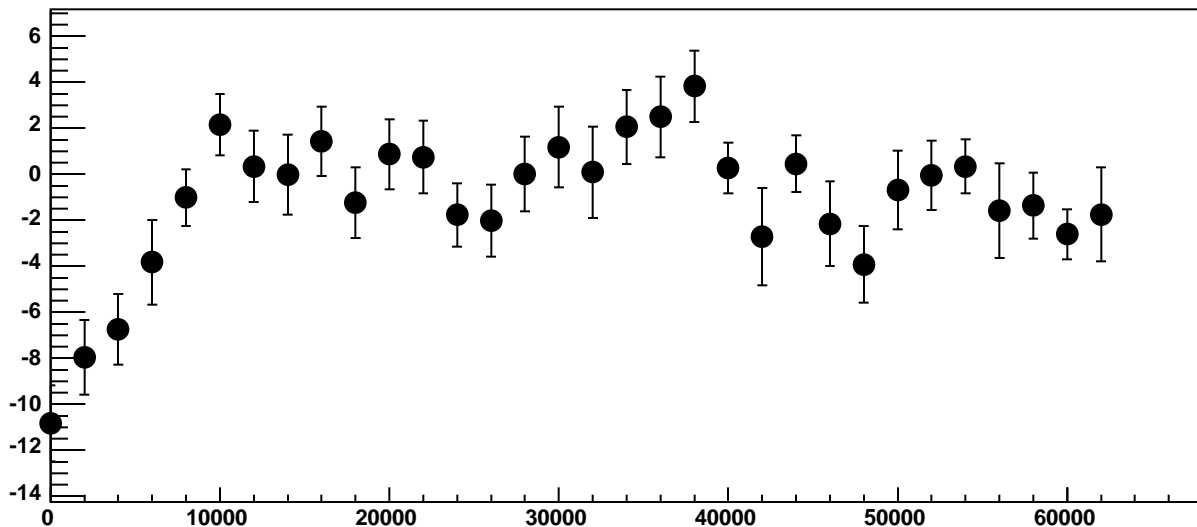


χ^2 / ndf 32.26 / 23
p0 -18.81 ± 0.6853
p1 $0.003912 \pm 2.031e-05$

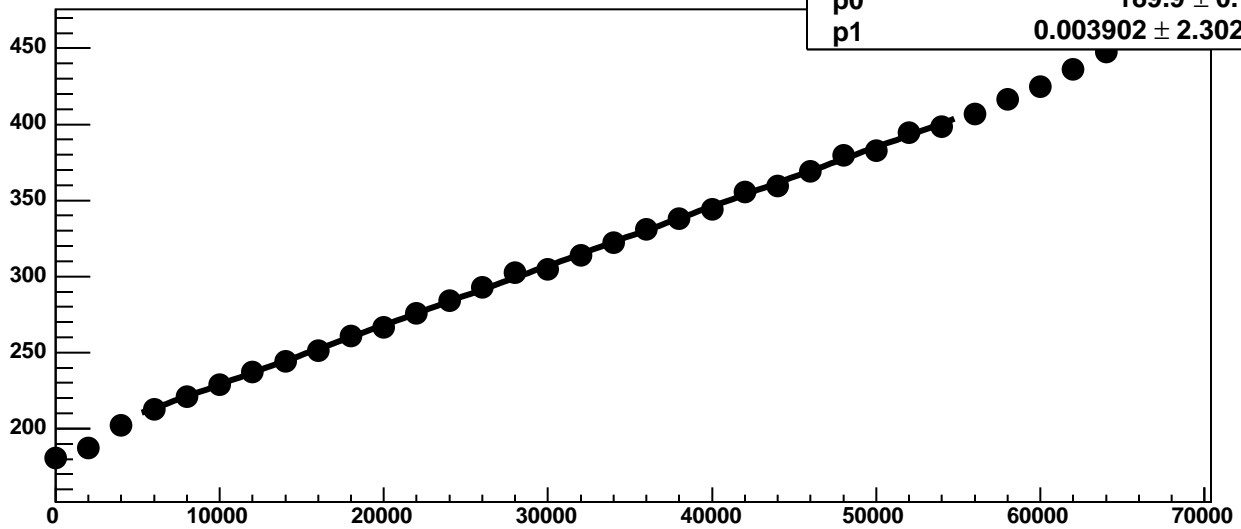
Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



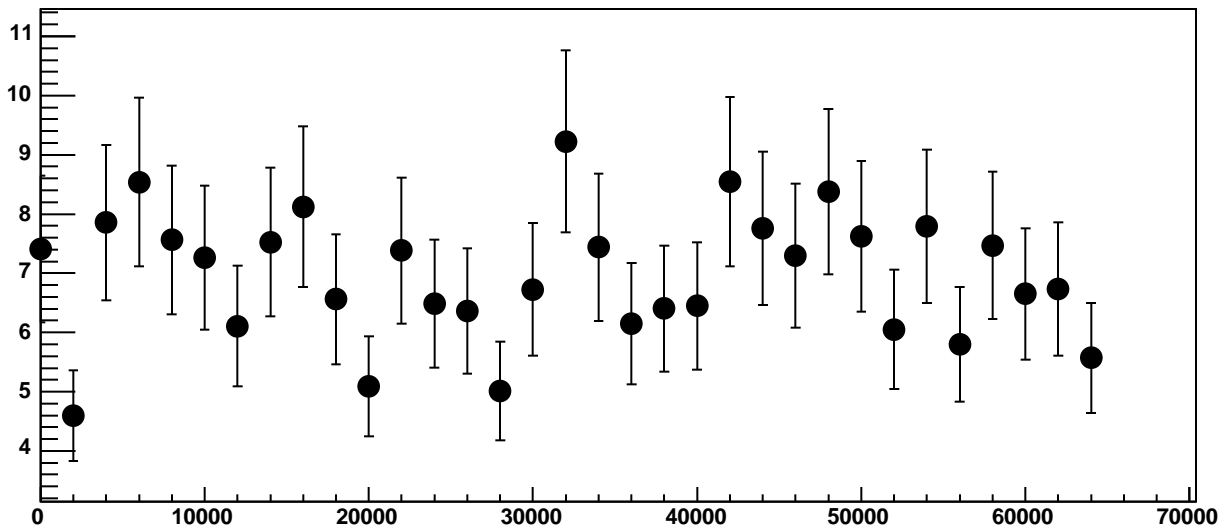
Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



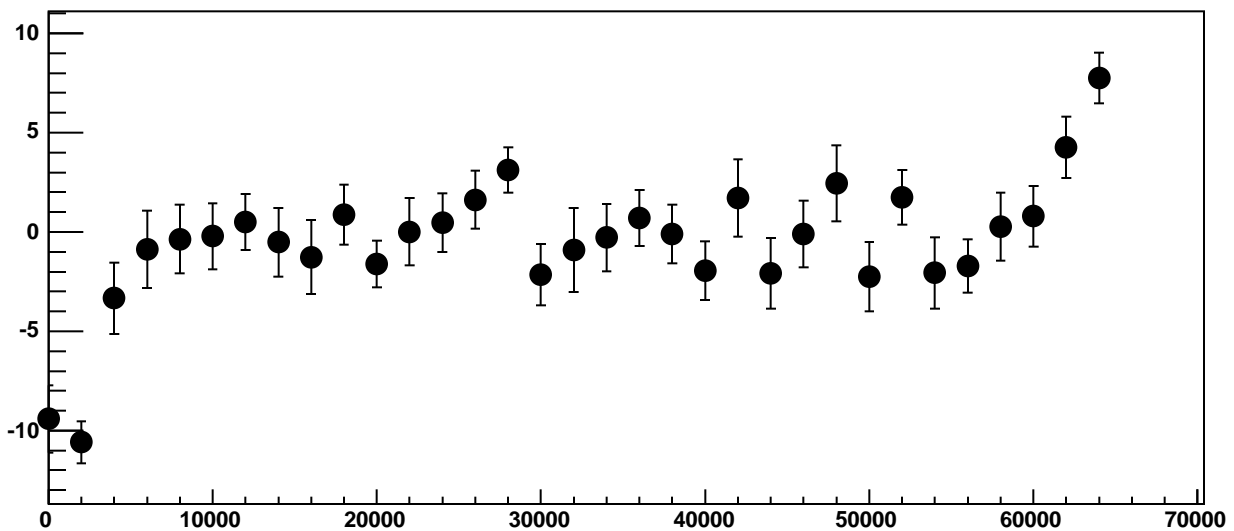
Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



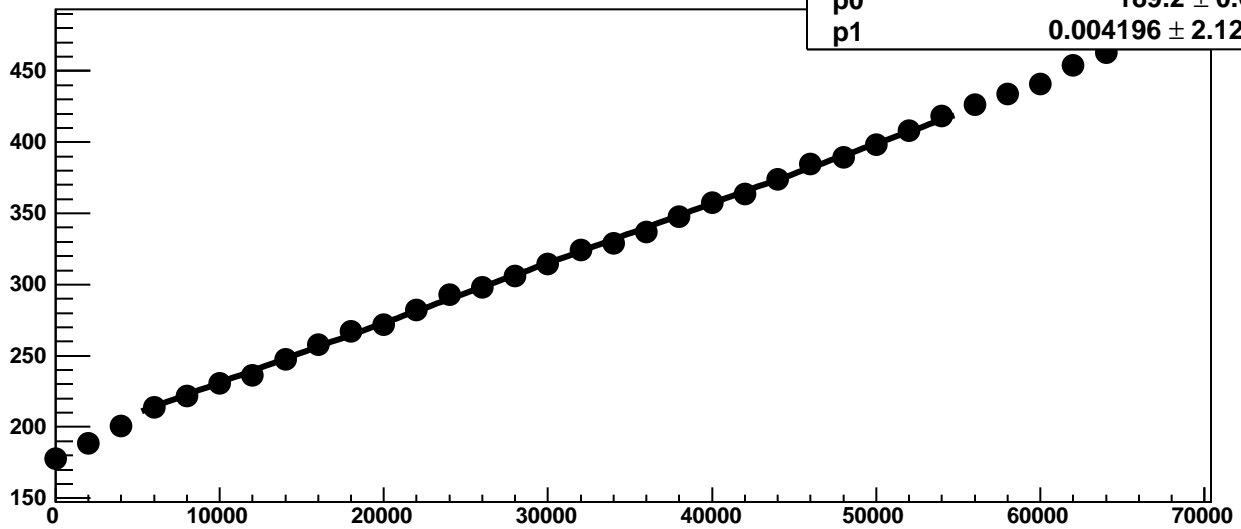
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



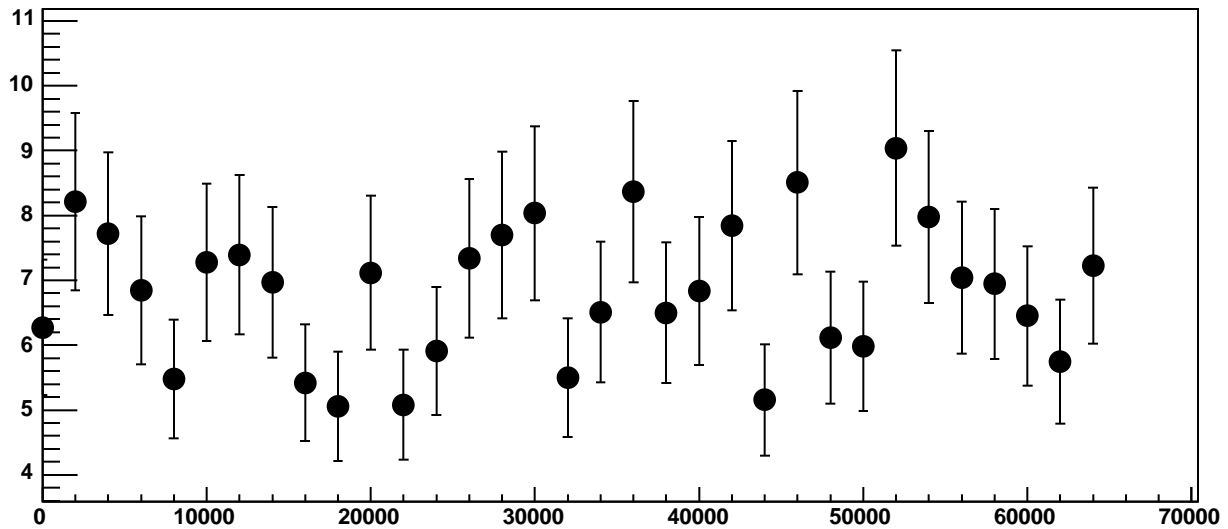
Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



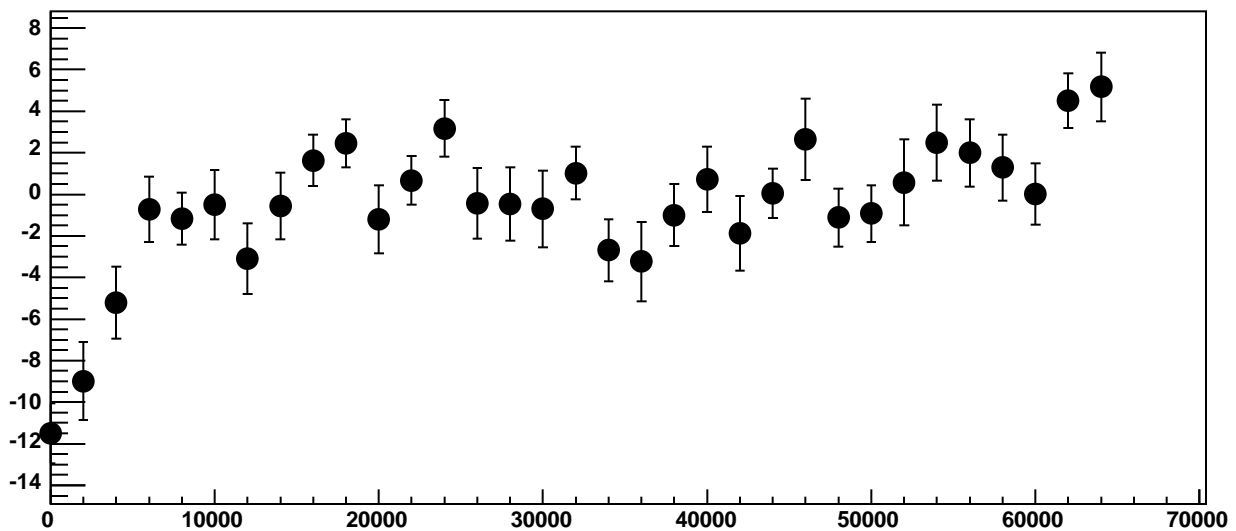
Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



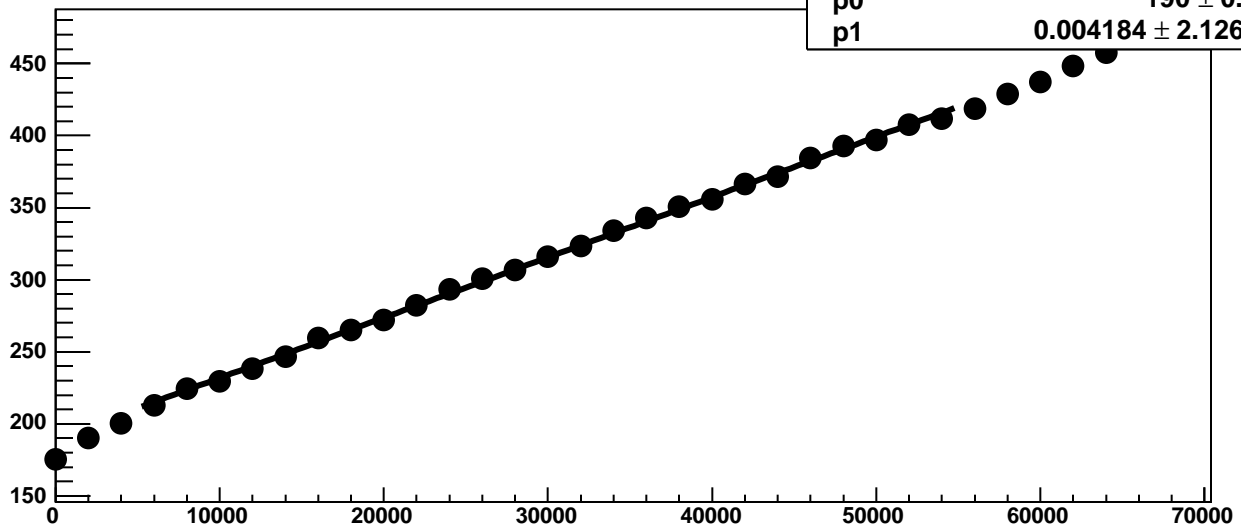
Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

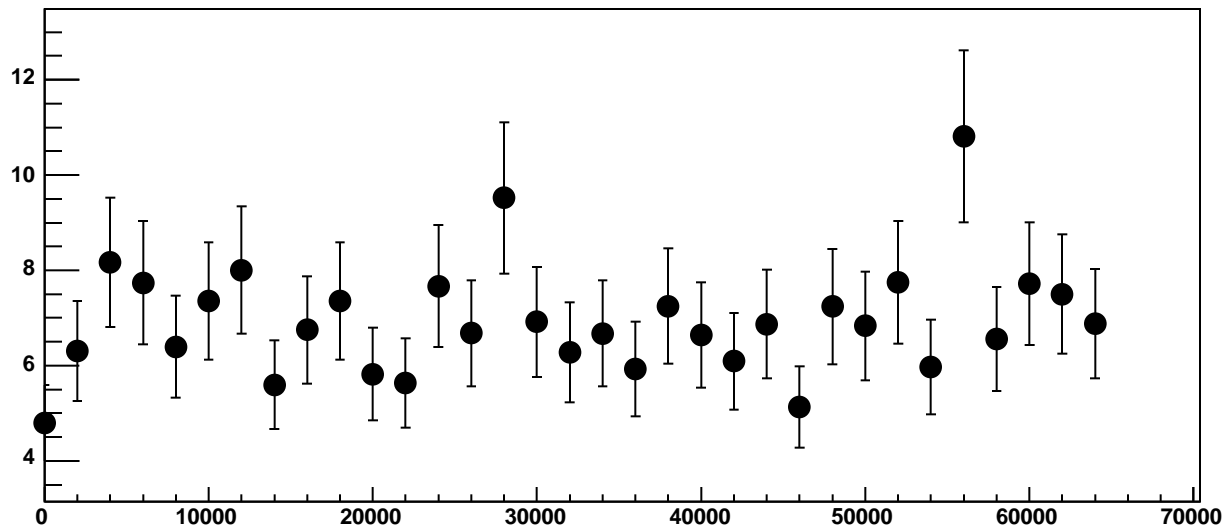


Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

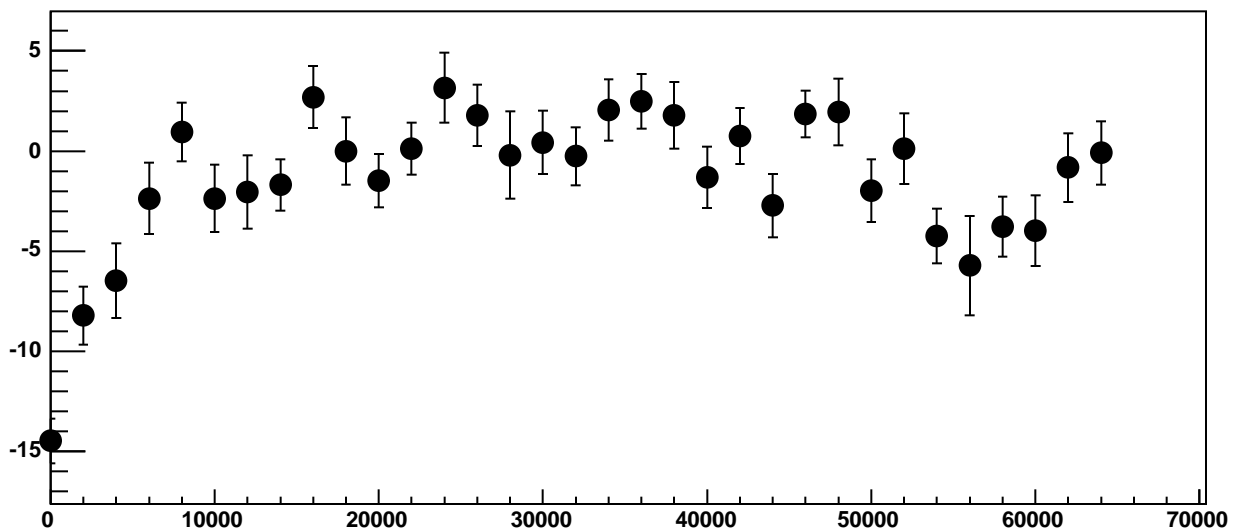


χ^2 / ndf 41.48 / 23
p0 190 ± 0.7181
p1 $0.004184 \pm 2.126e-05$

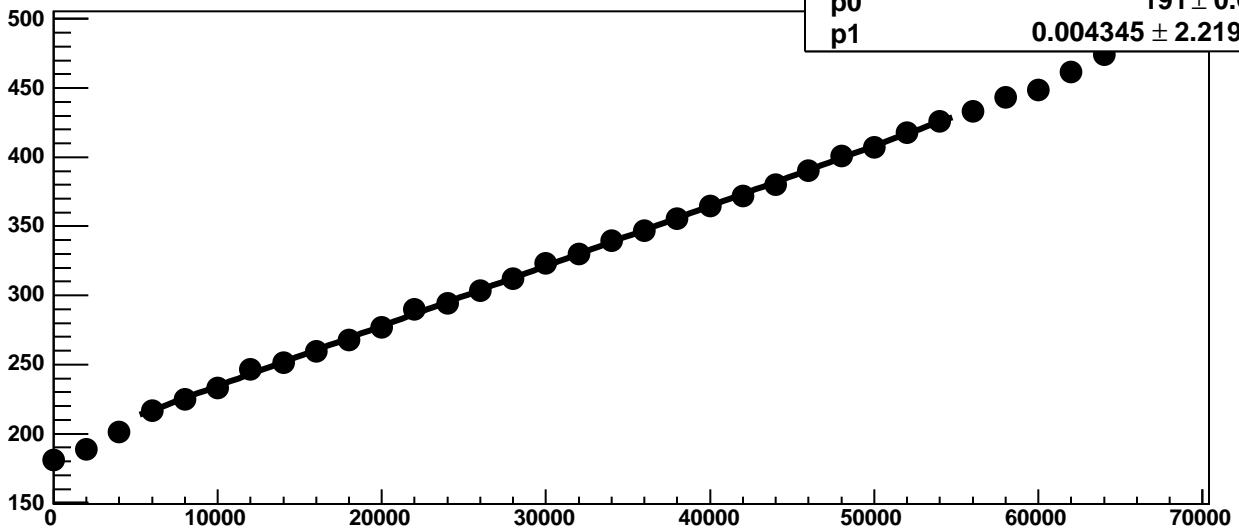
Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

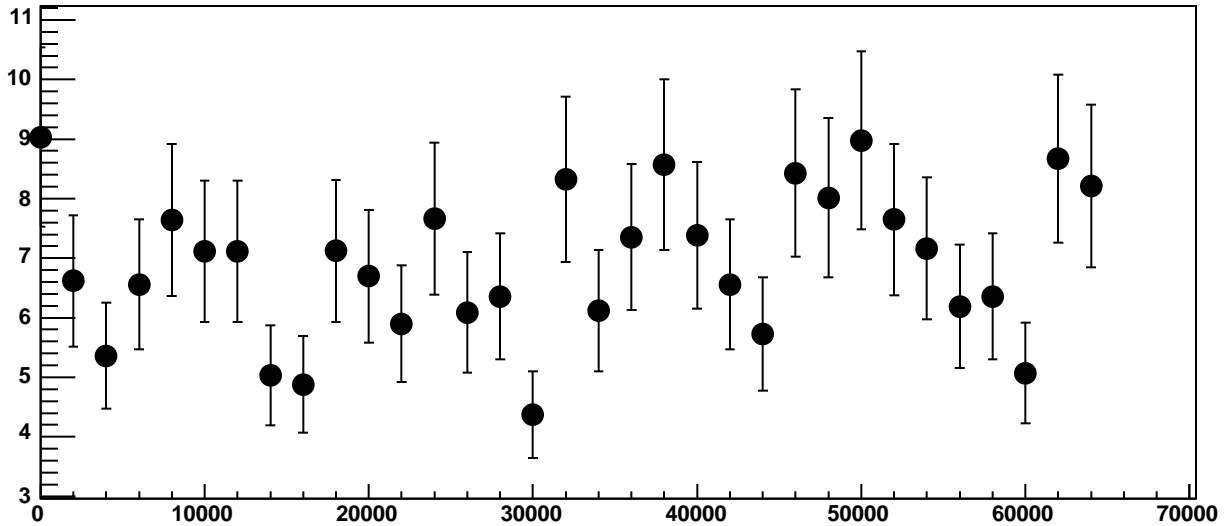


Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

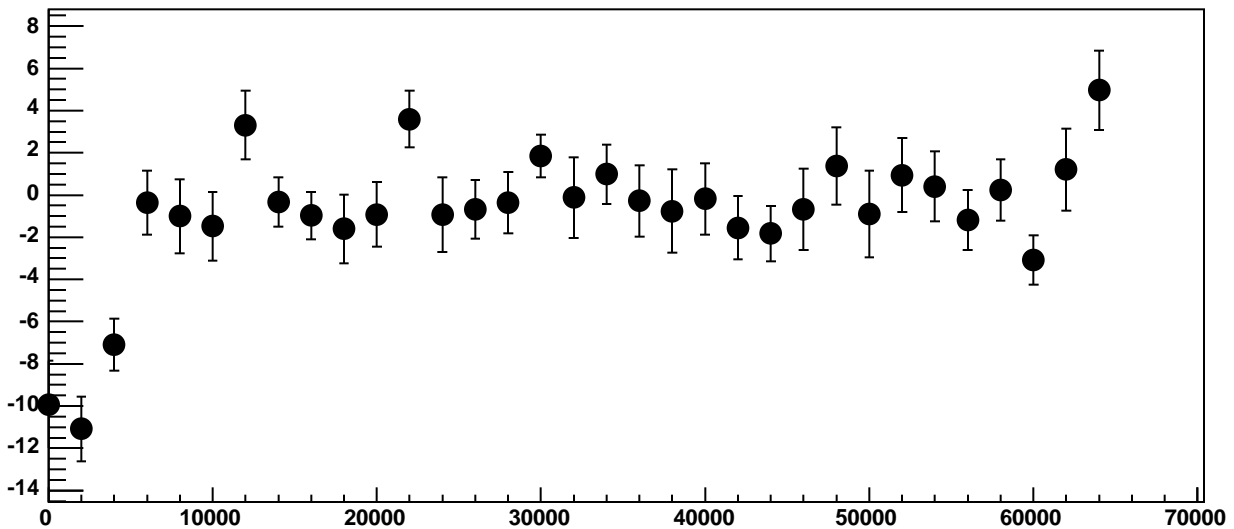


χ^2 / ndf 23.38 / 23
p0 191 ± 0.6958
p1 $0.004345 \pm 2.219e-05$

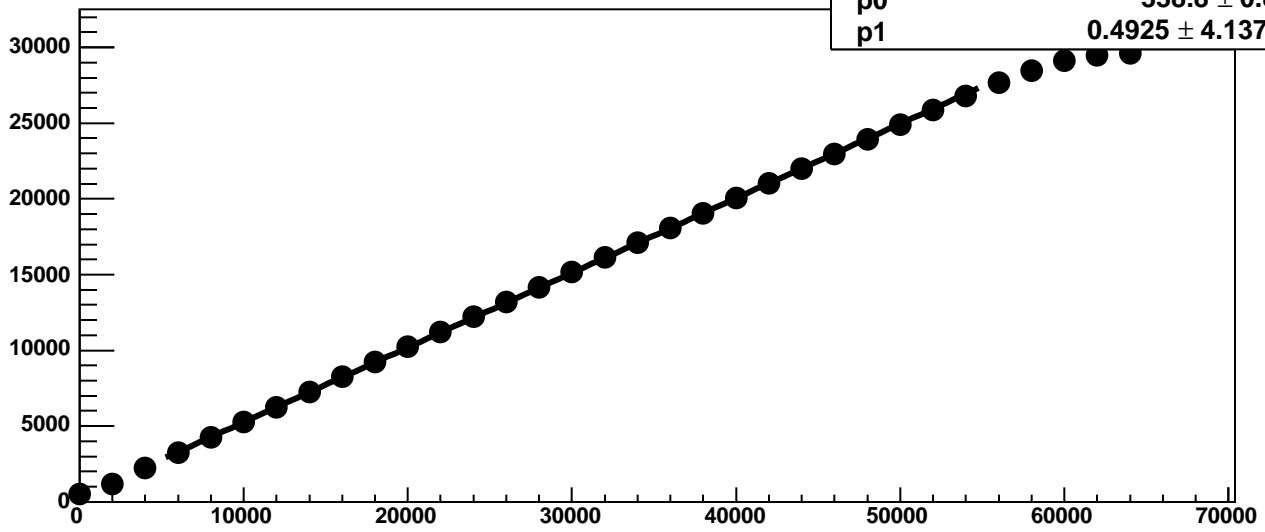
Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



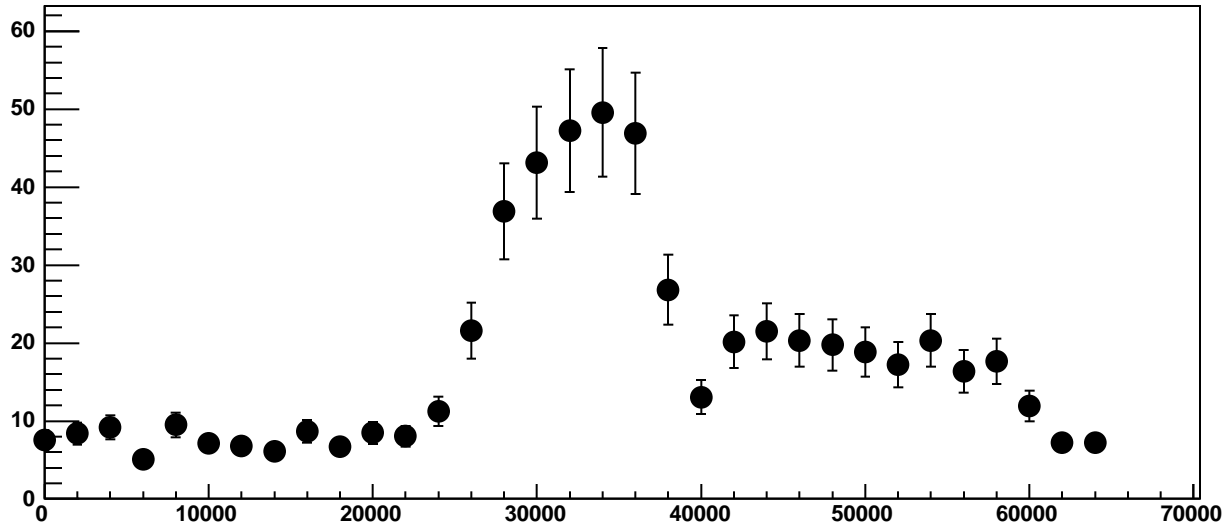
Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



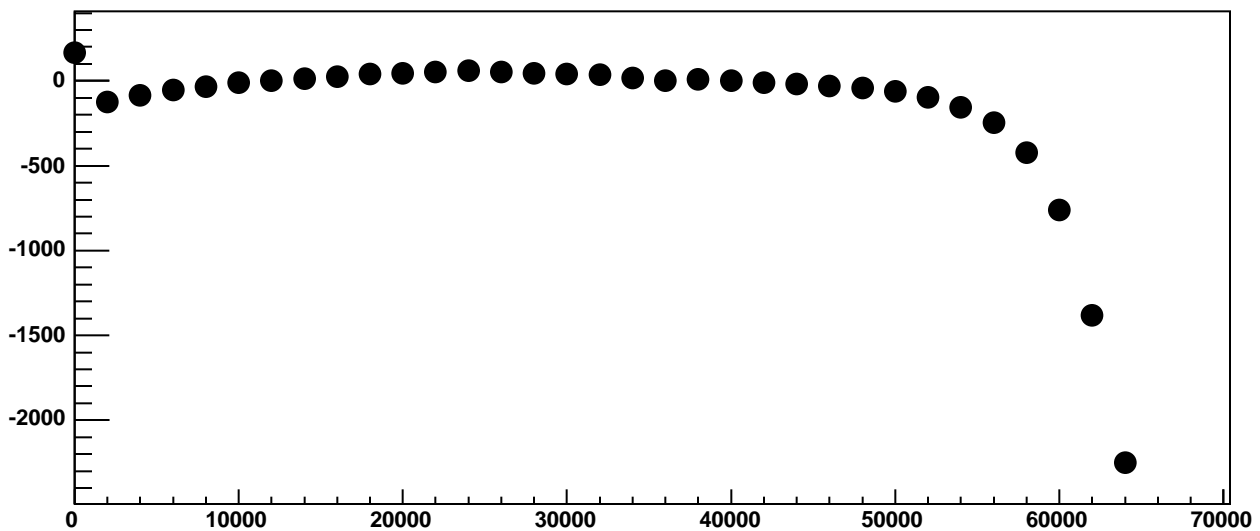
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



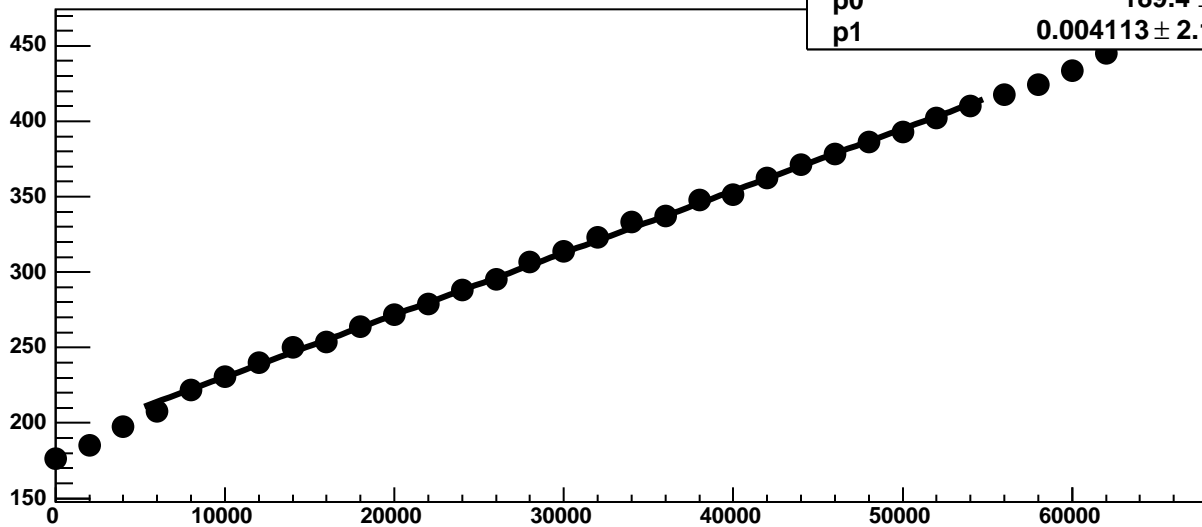
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

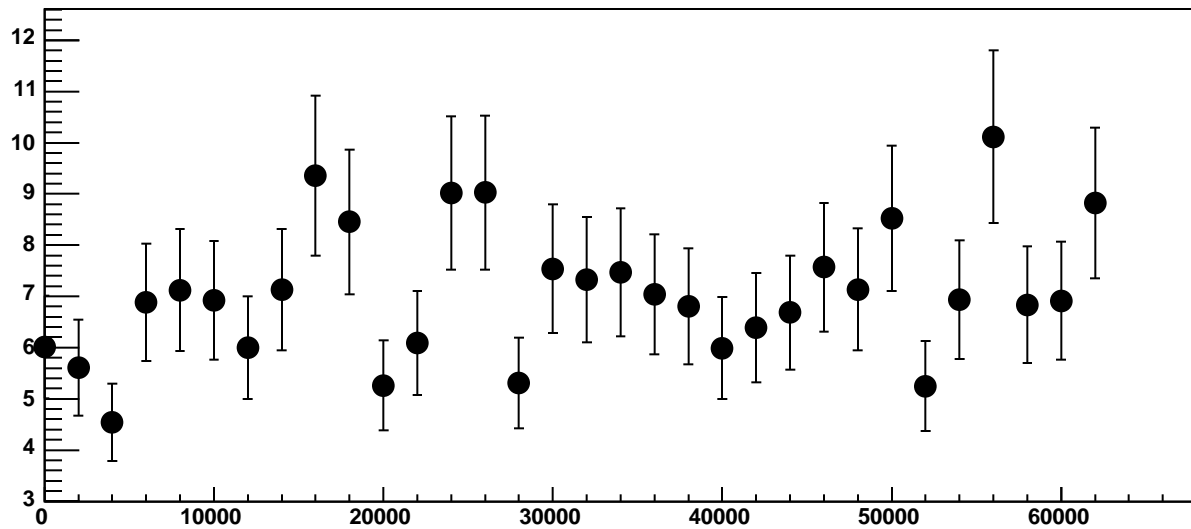


Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

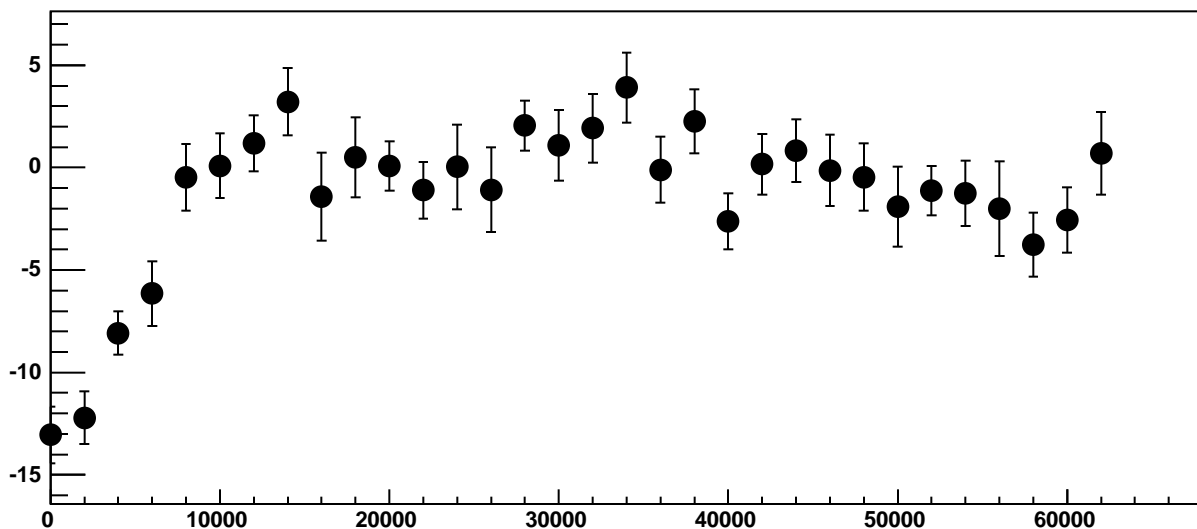


χ^2 / ndf 39.54 / 23
p0 189.4 ± 0.7261
p1 0.004113 ± 2.154e-05

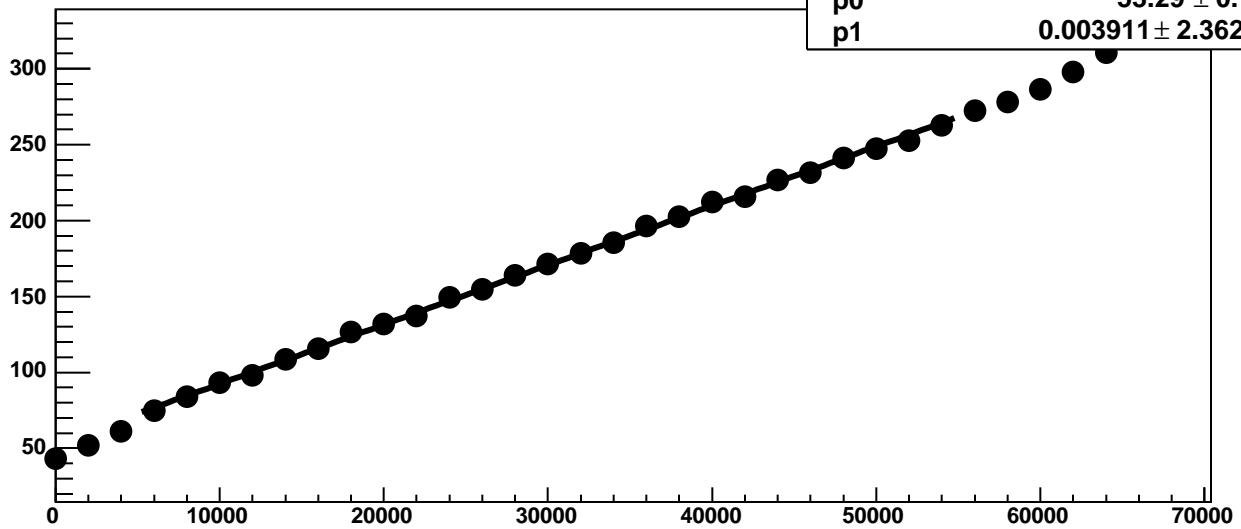
Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



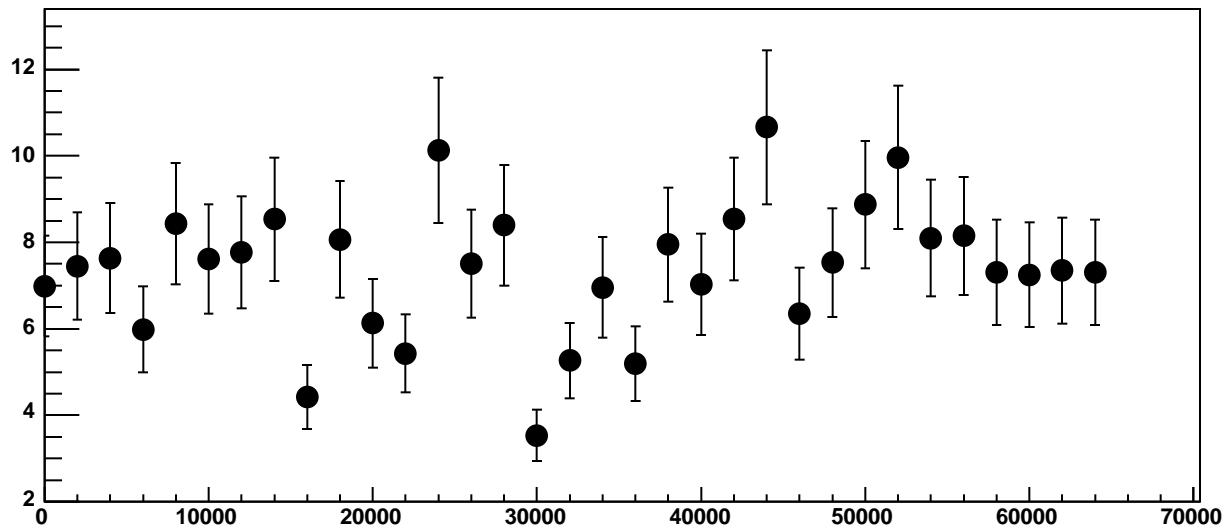
Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



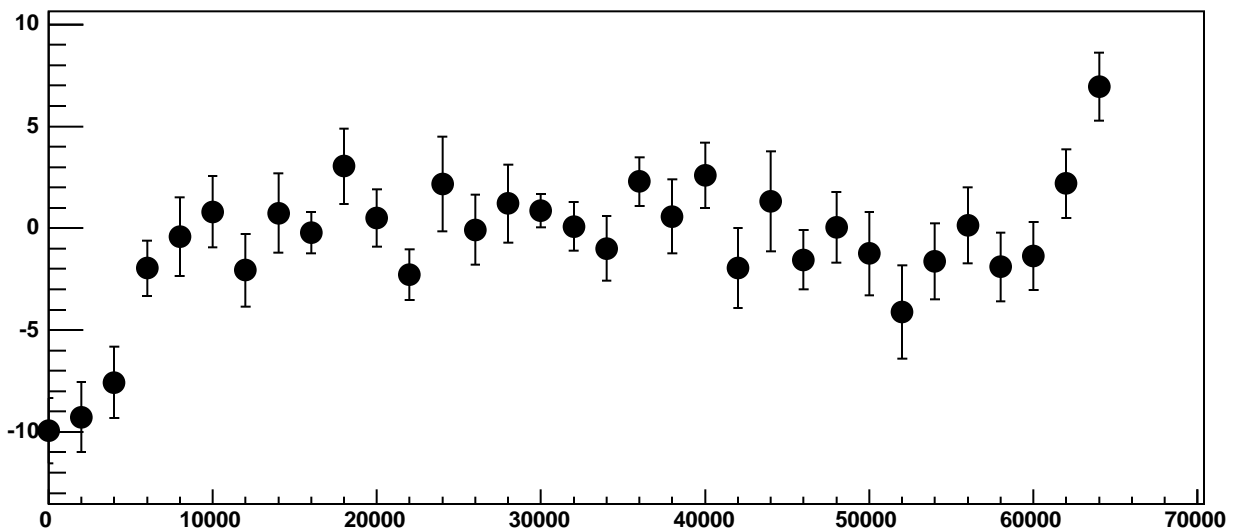
Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



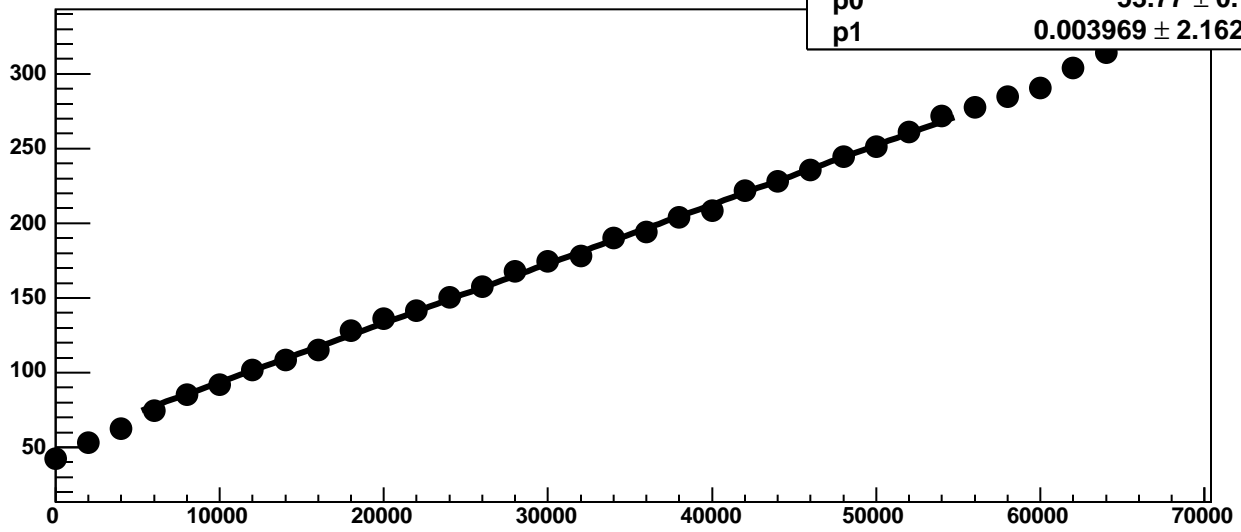
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

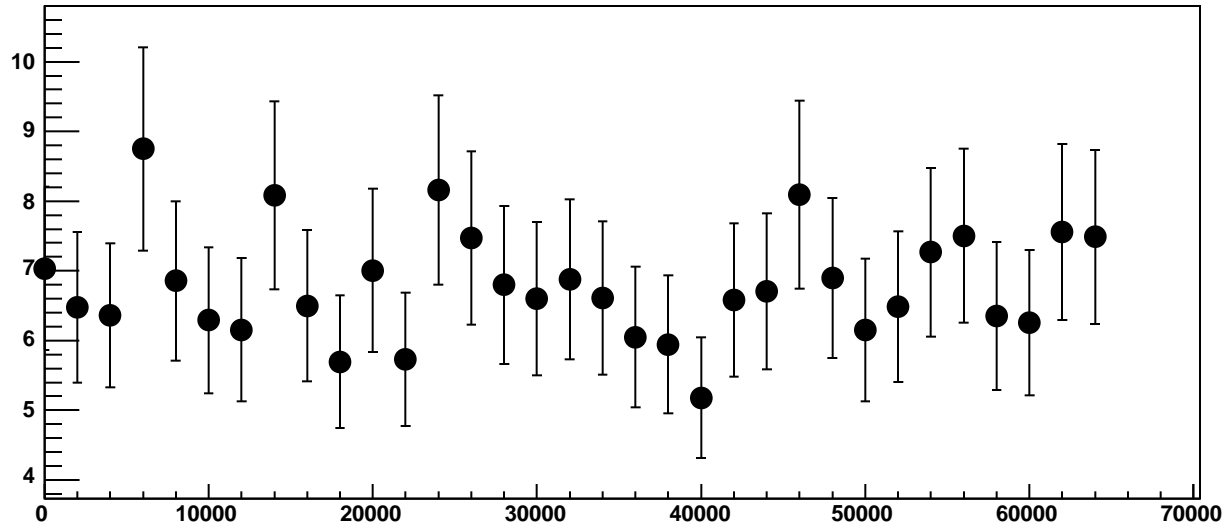


Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

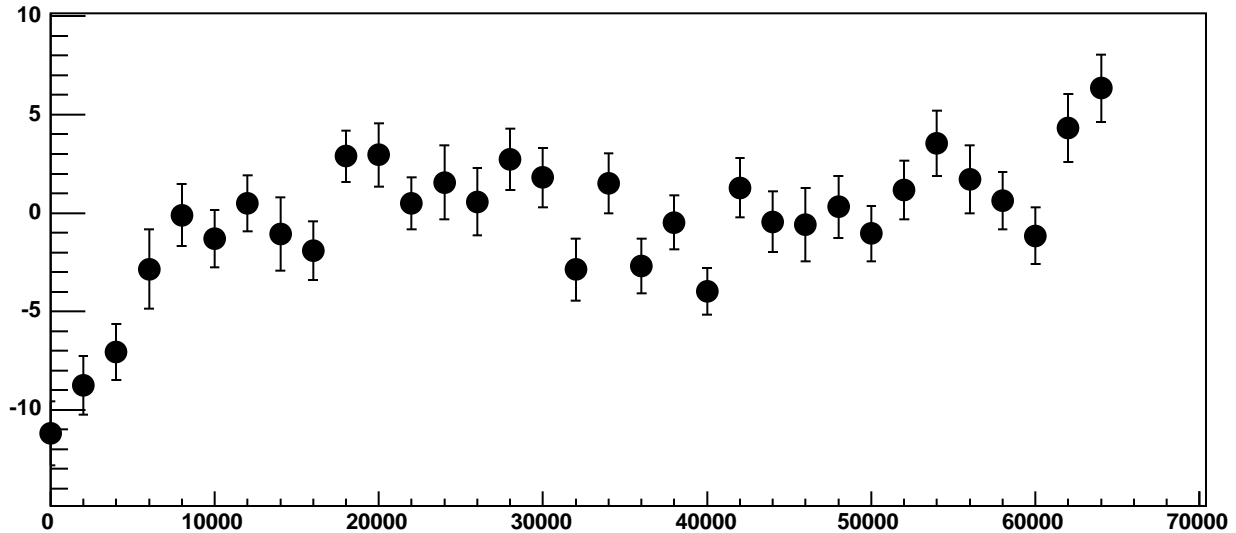


χ^2 / ndf 44.8 / 23
p0 53.77 ± 0.7247
p1 $0.003969 \pm 2.162e-05$

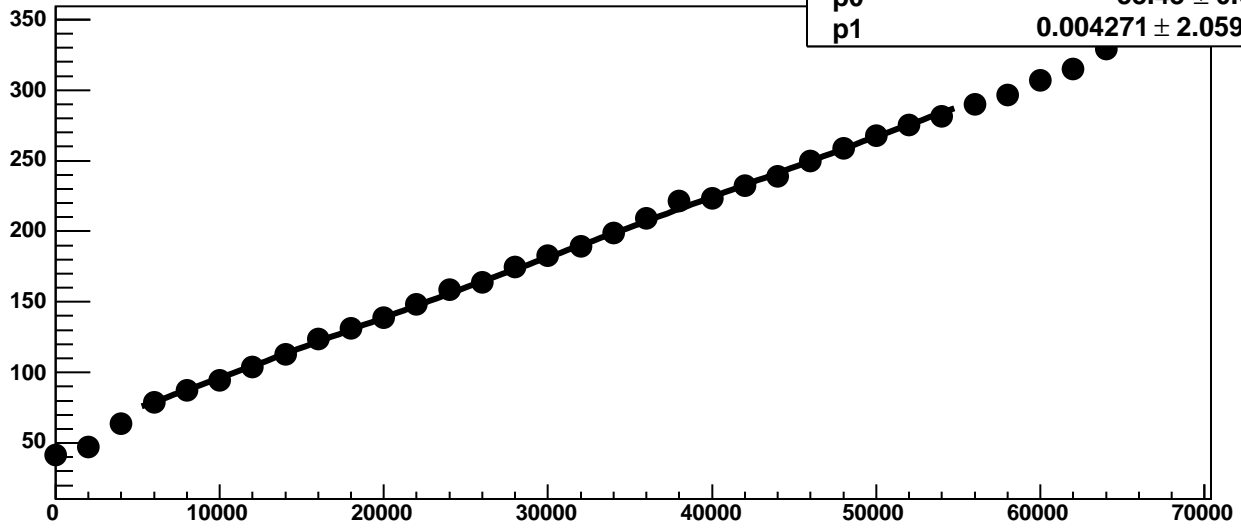
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



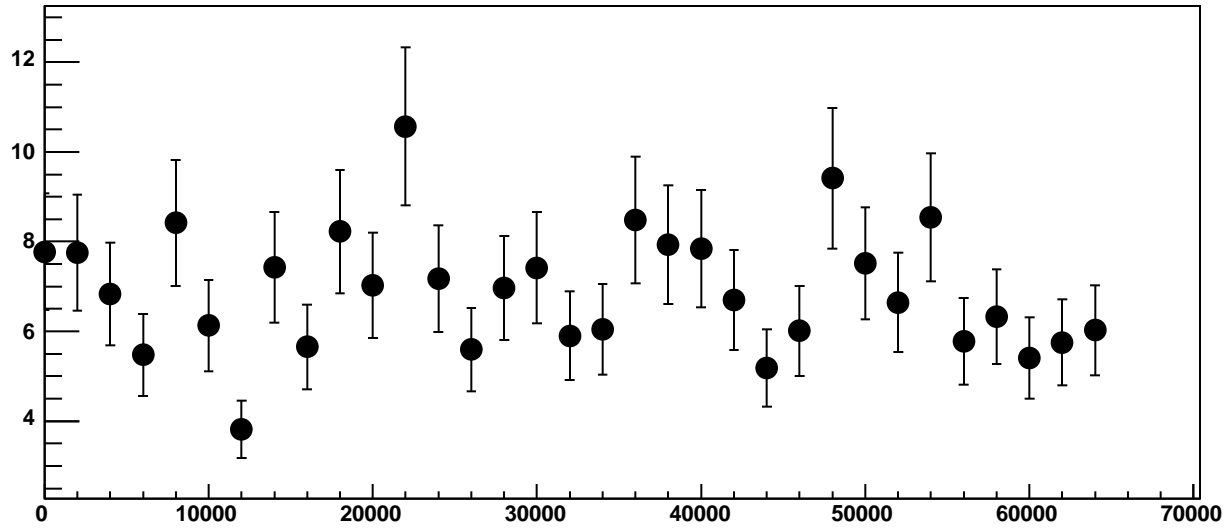
Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



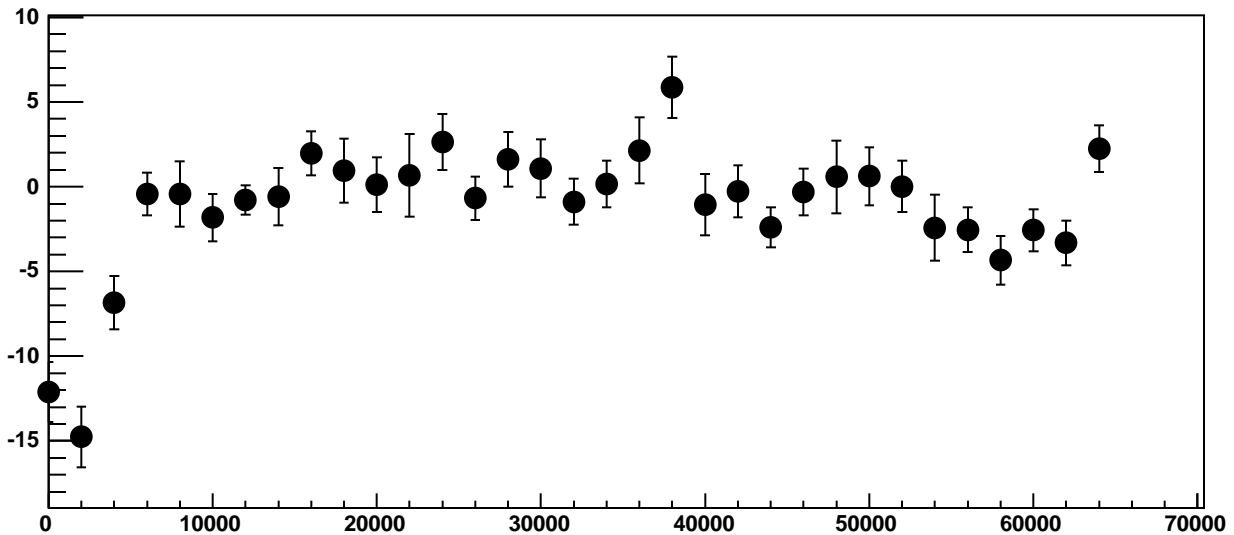
Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



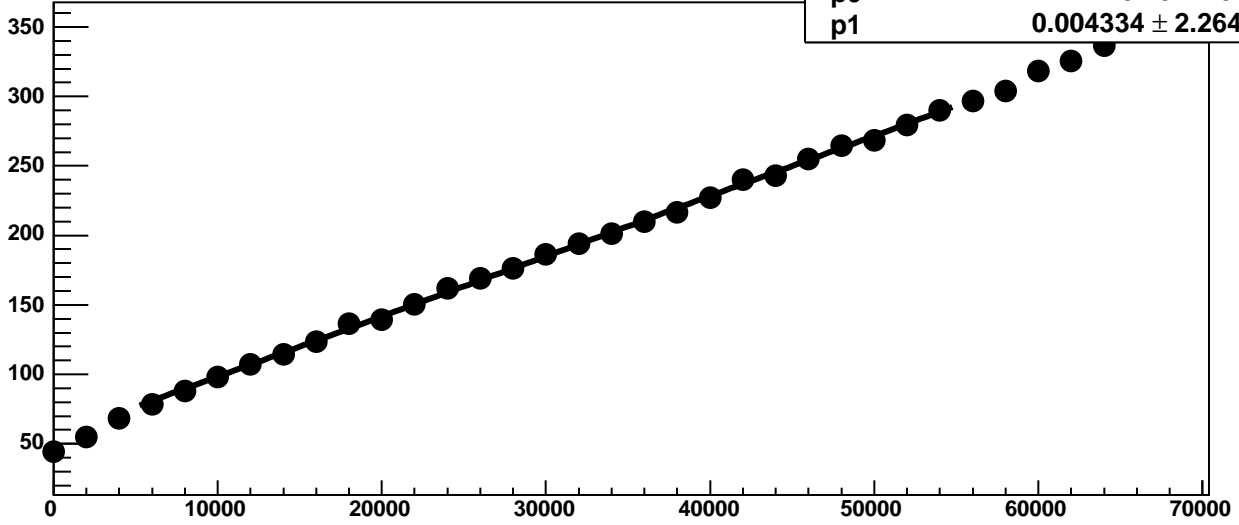
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

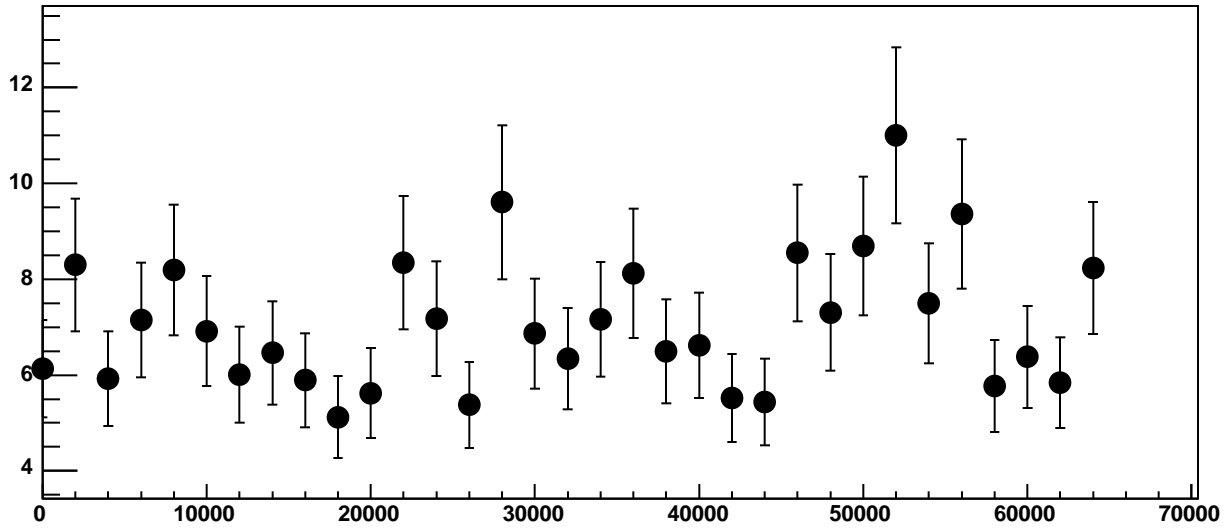


Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

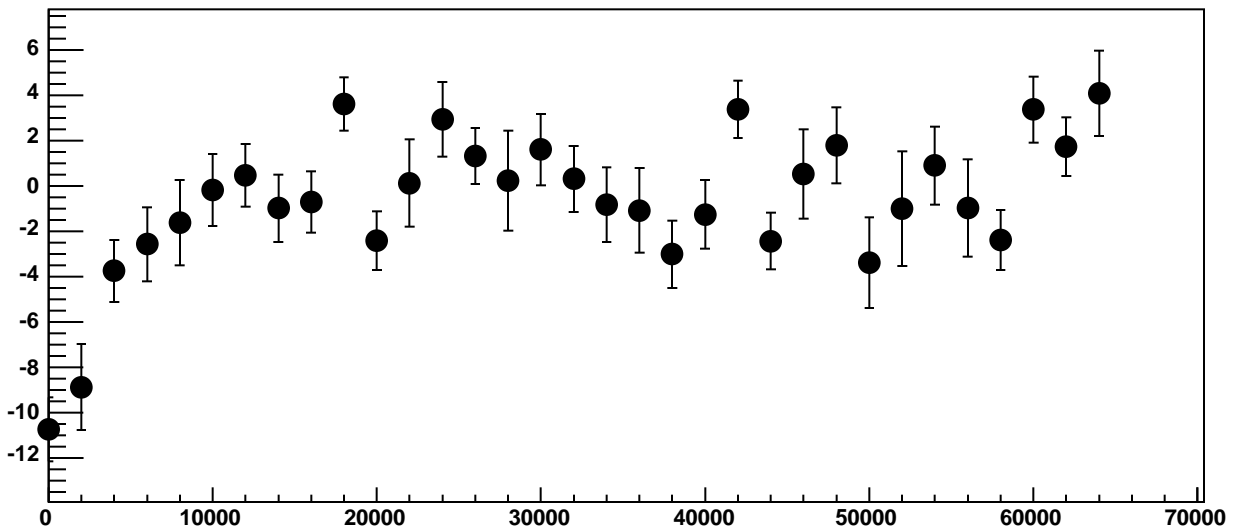


χ^2 / ndf 43.37 / 23
p0 54.94 ± 0.7191
p1 $0.004334 \pm 2.264e-05$

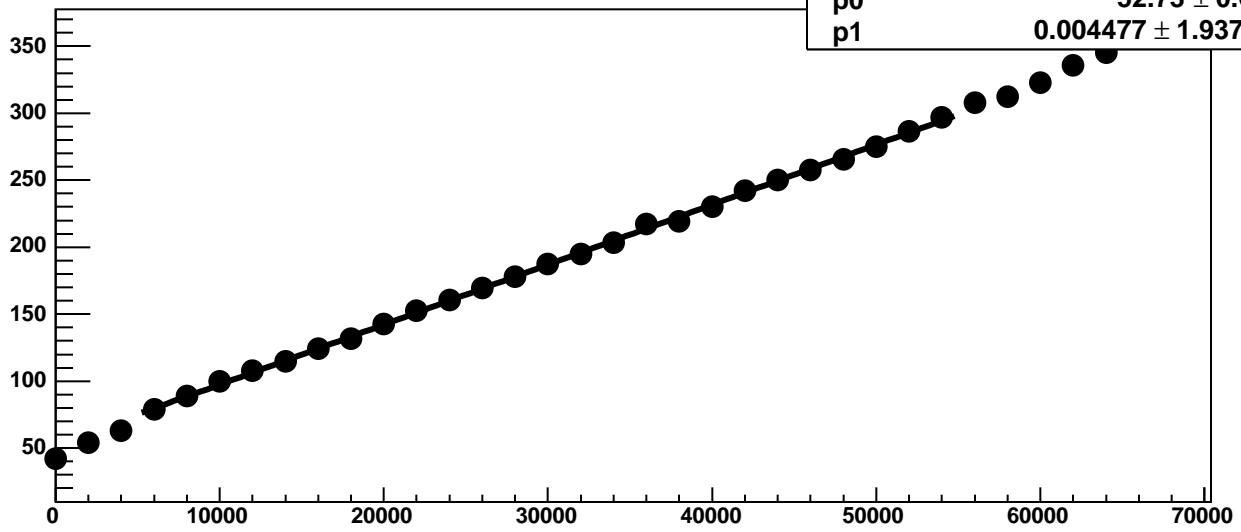
Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

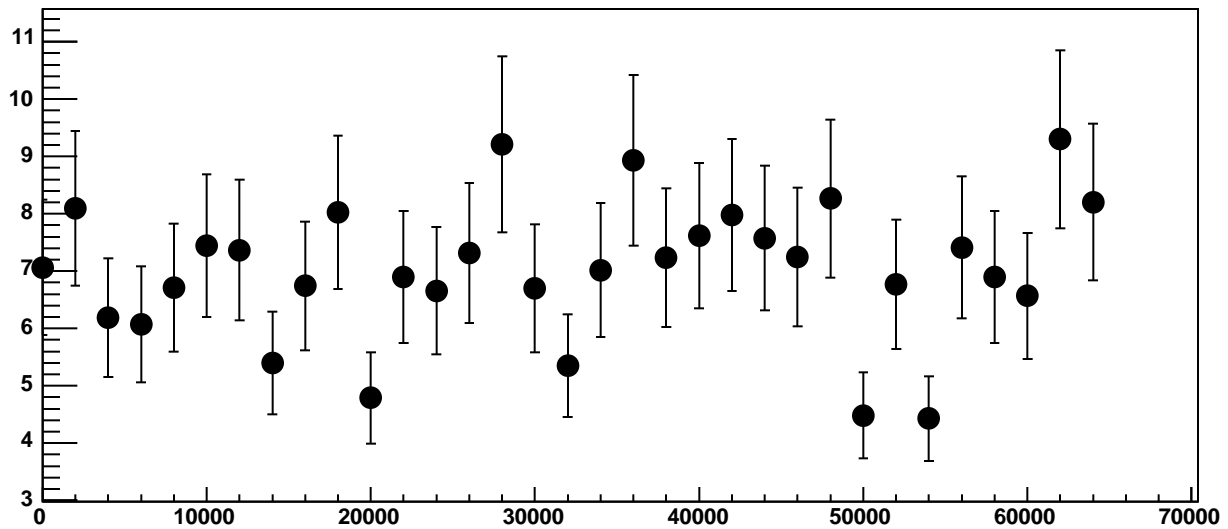


Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

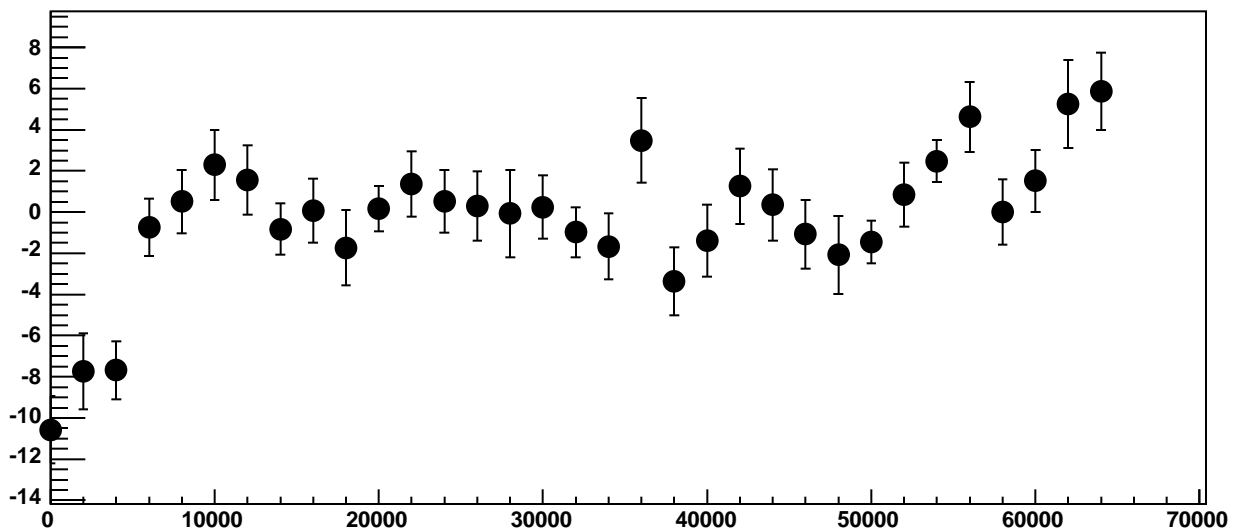


χ^2 / ndf 25.04 / 23
p0 52.73 ± 0.6689
p1 $0.004477 \pm 1.937e-05$

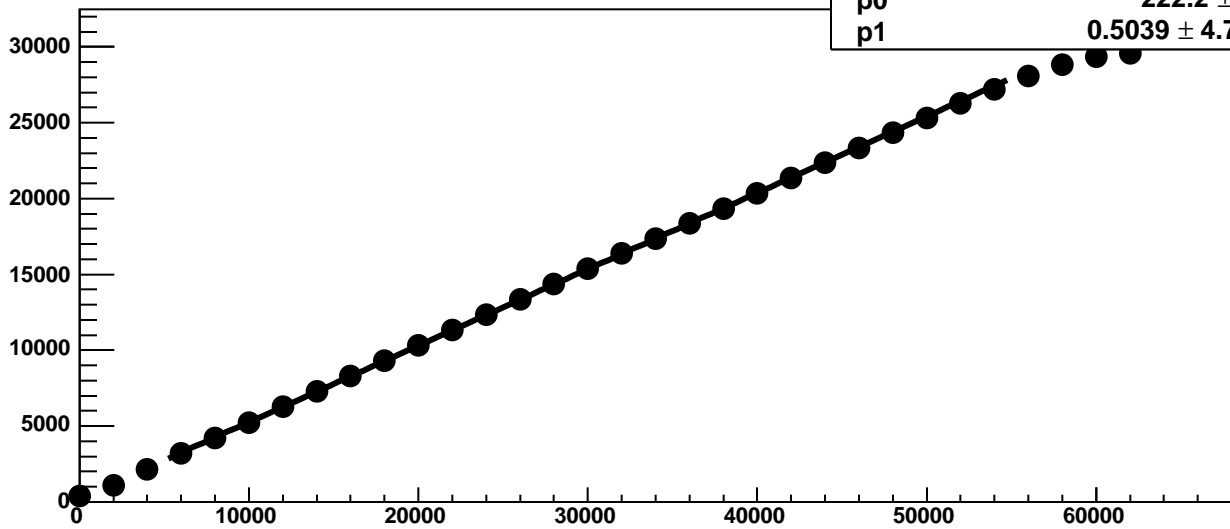
Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



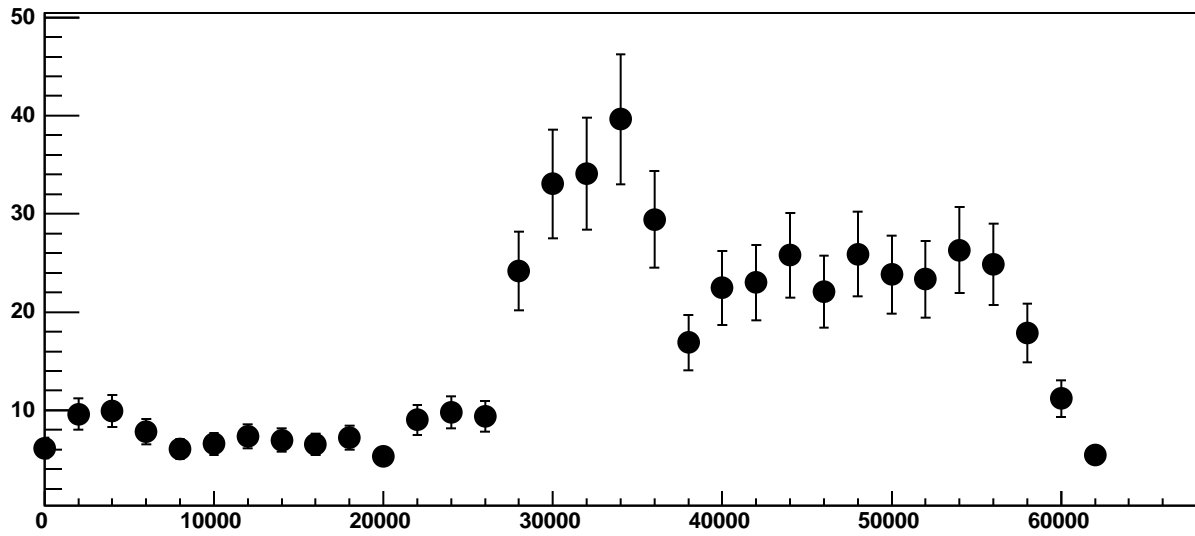
Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



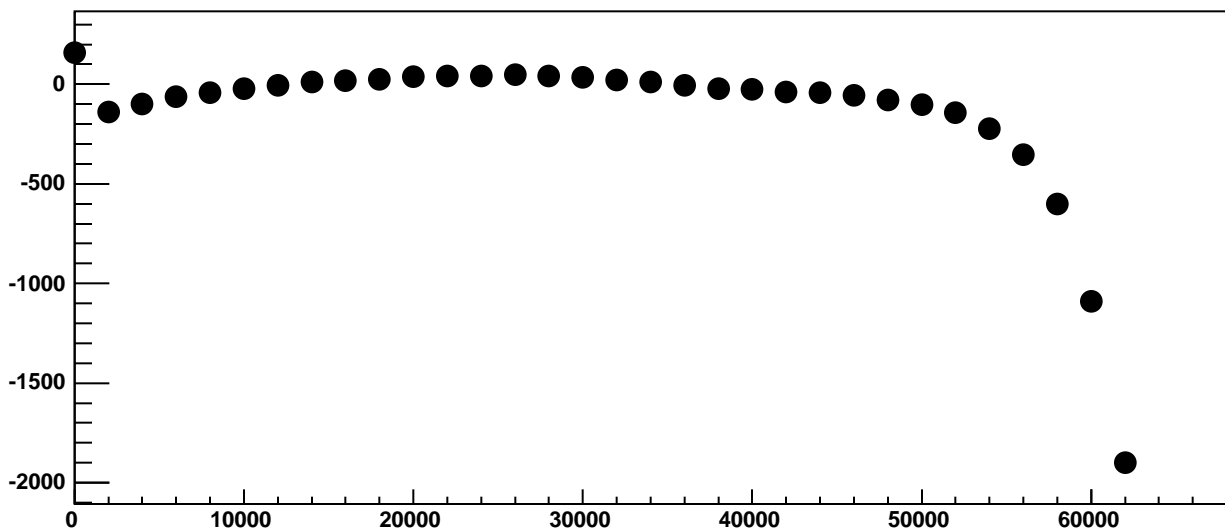
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



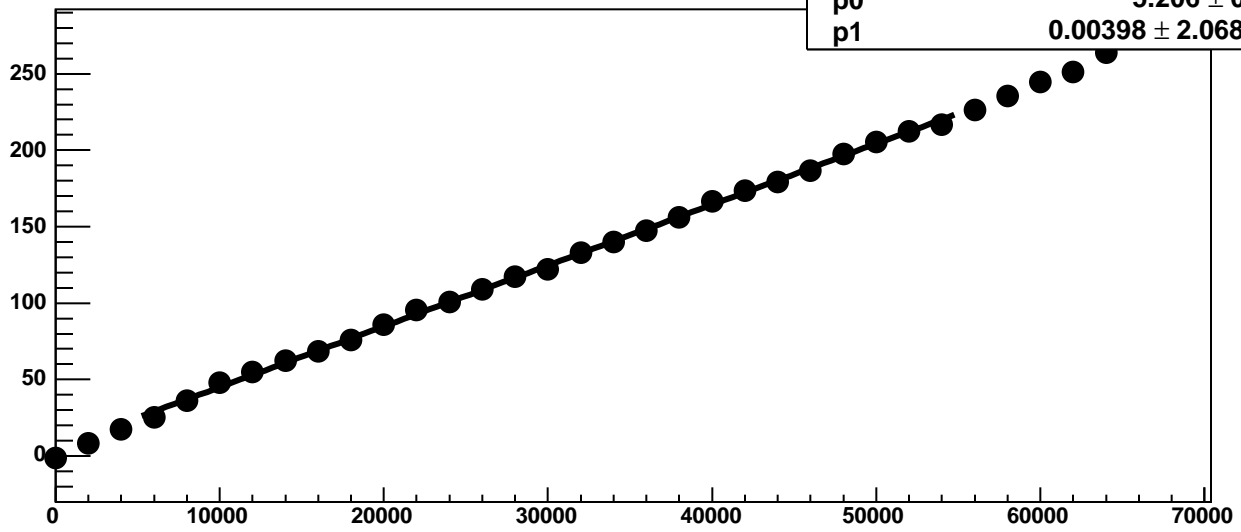
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

29.58 / 23

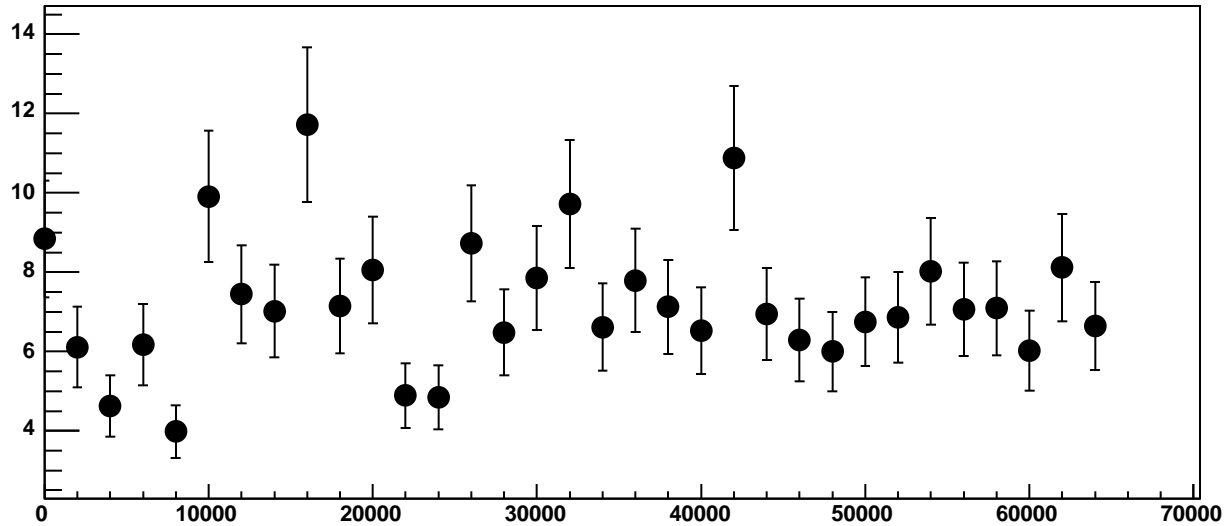
p0

5.206 ± 0.664

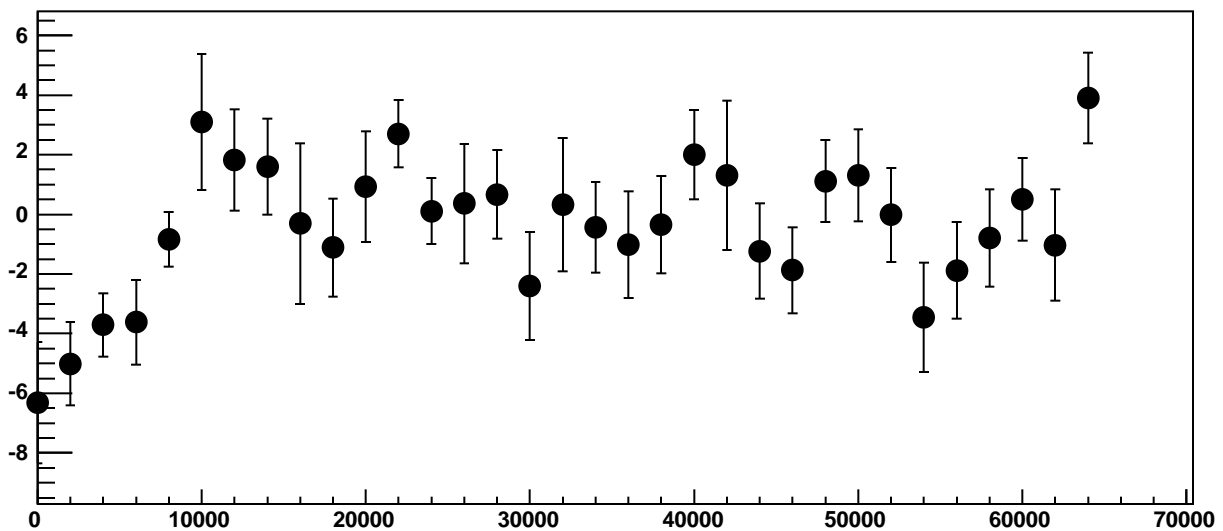
p1

$0.00398 \pm 2.068e-05$

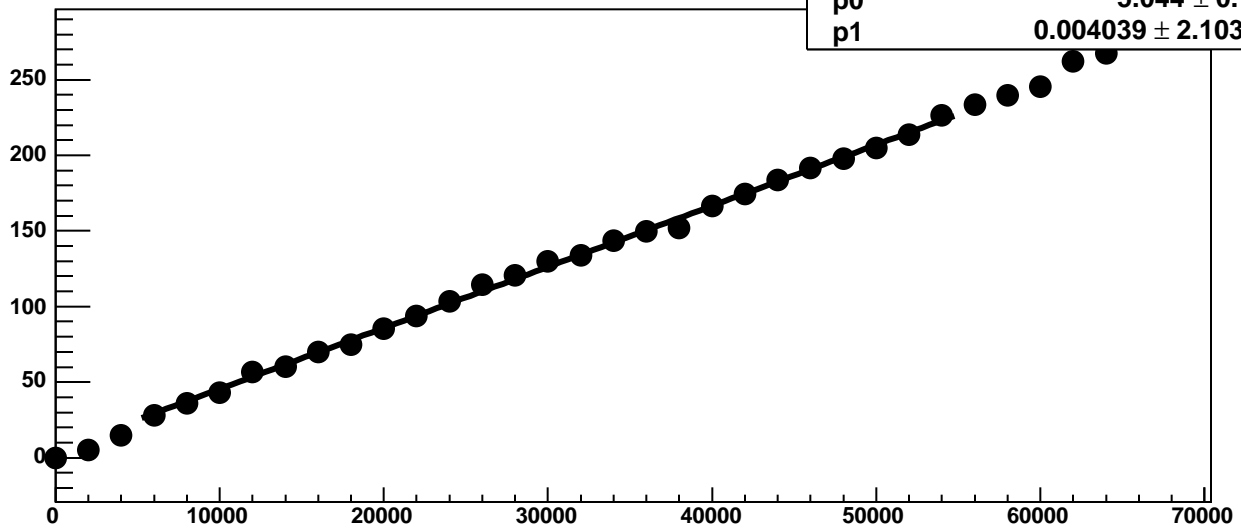
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

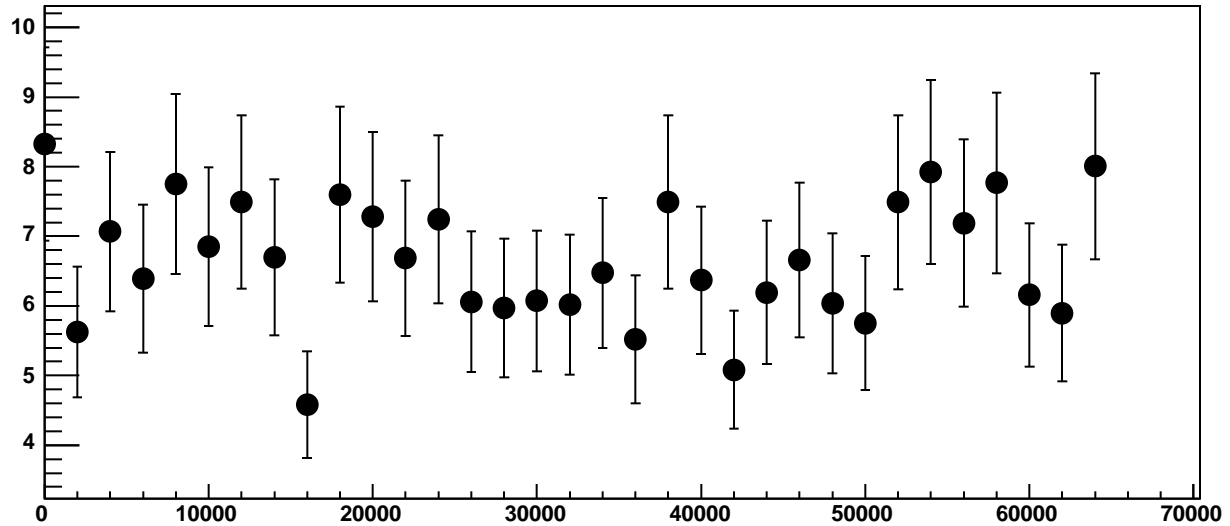


Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

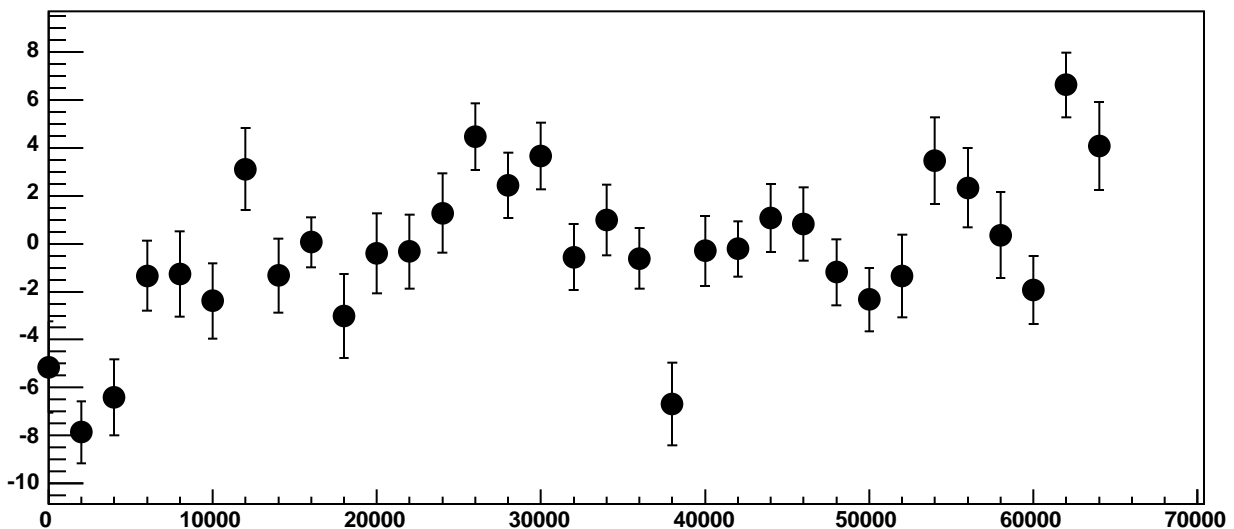


χ^2 / ndf 56.8 / 23
p0 5.044 ± 0.7023
p1 $0.004039 \pm 2.103e-05$

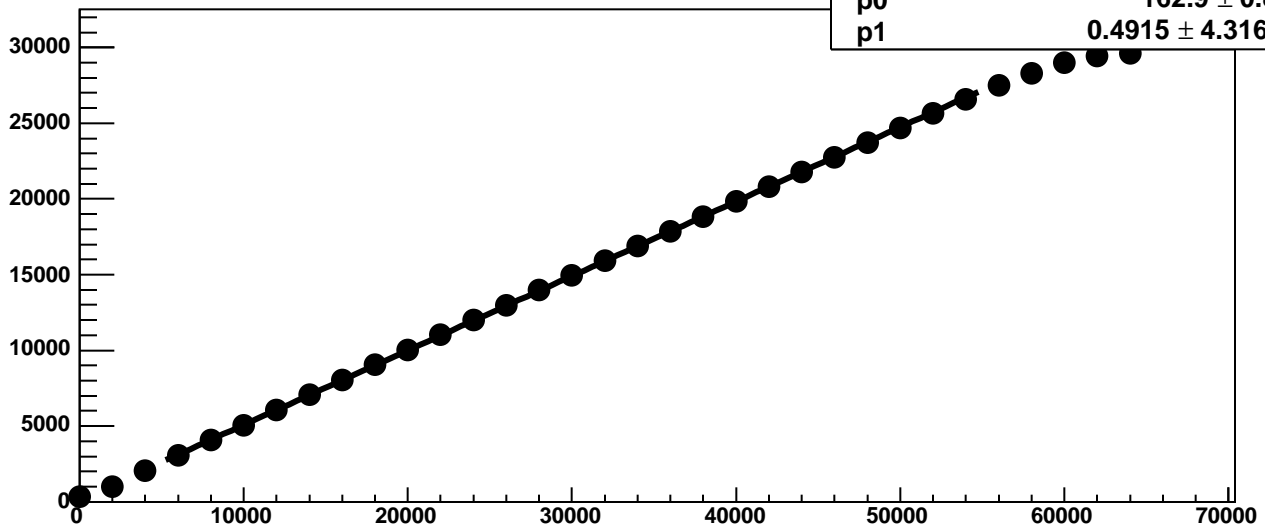
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



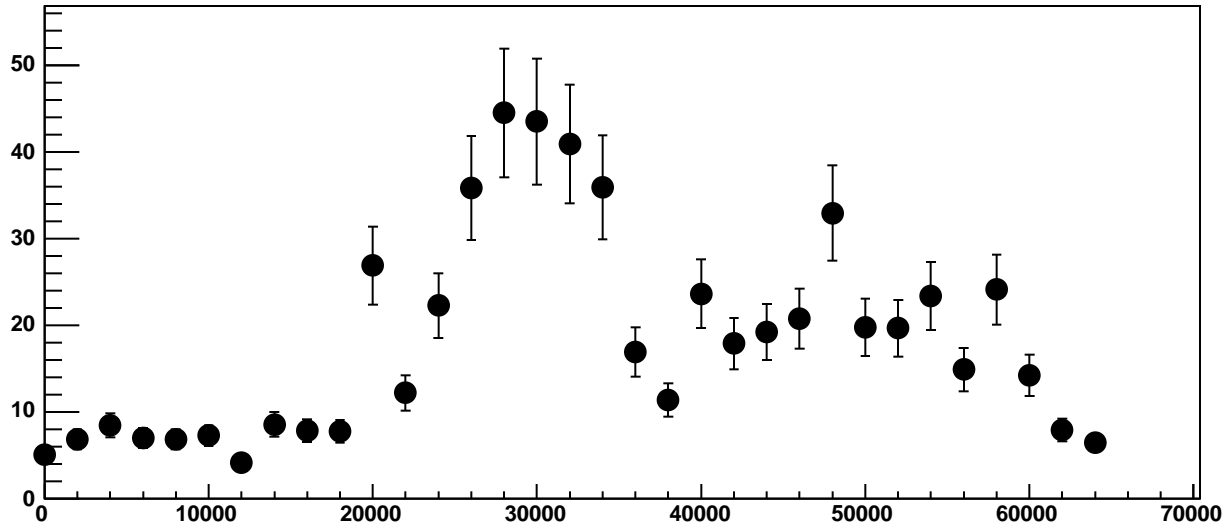
Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



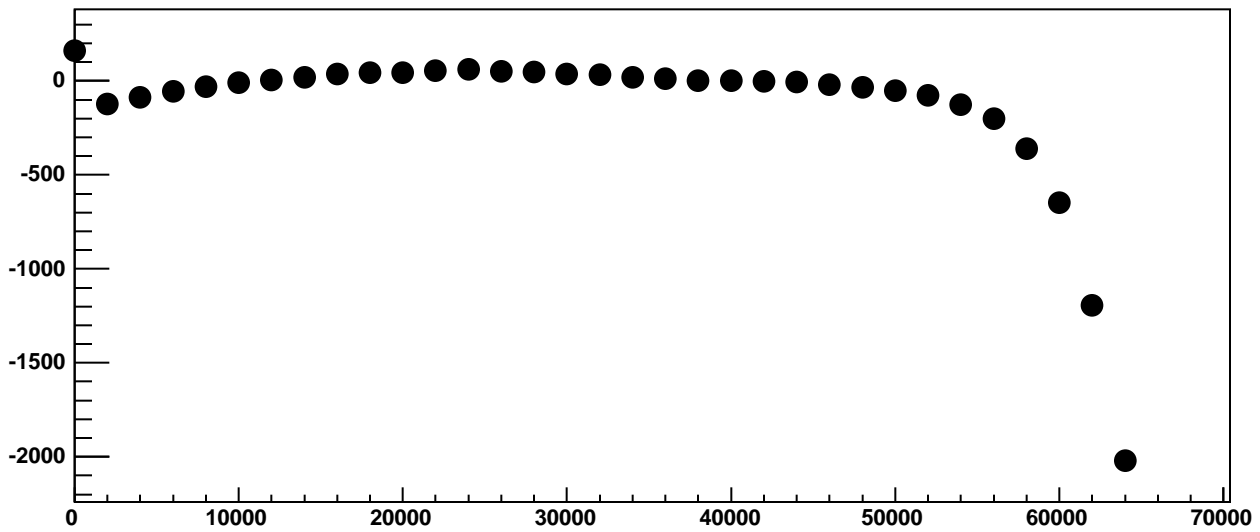
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



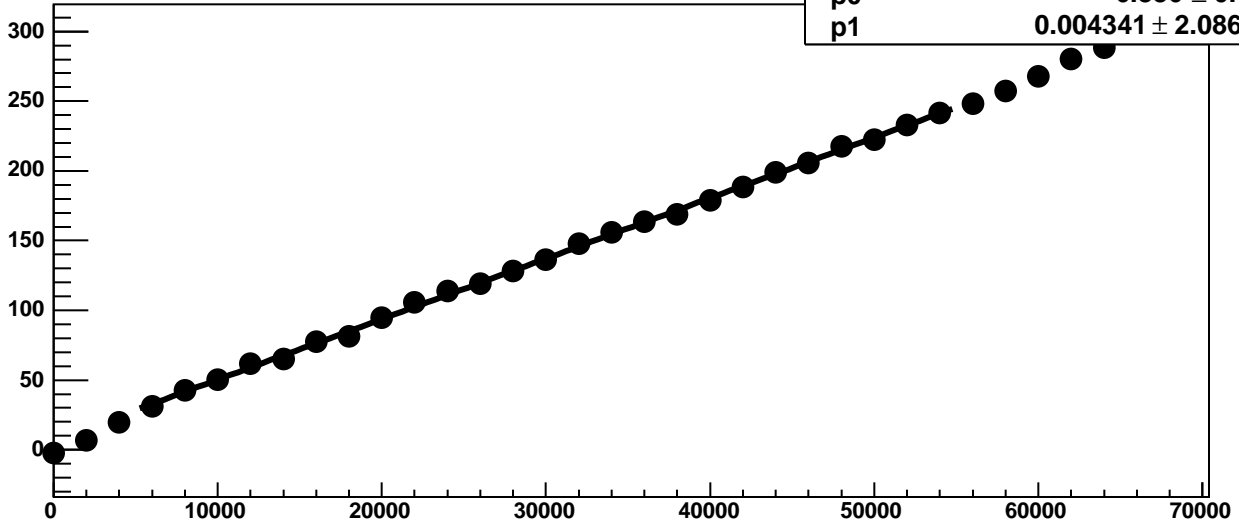
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



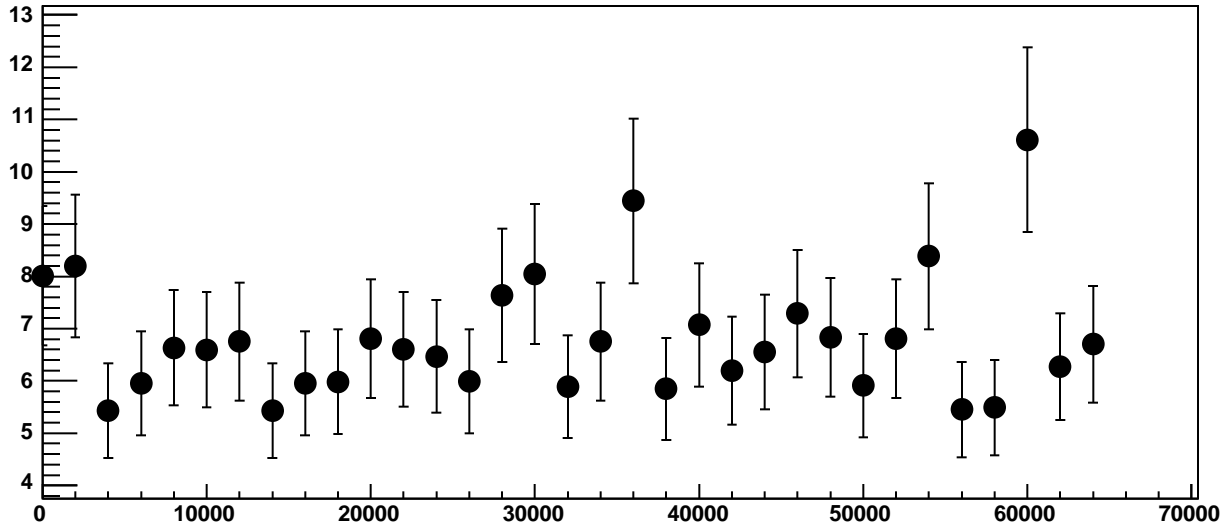
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



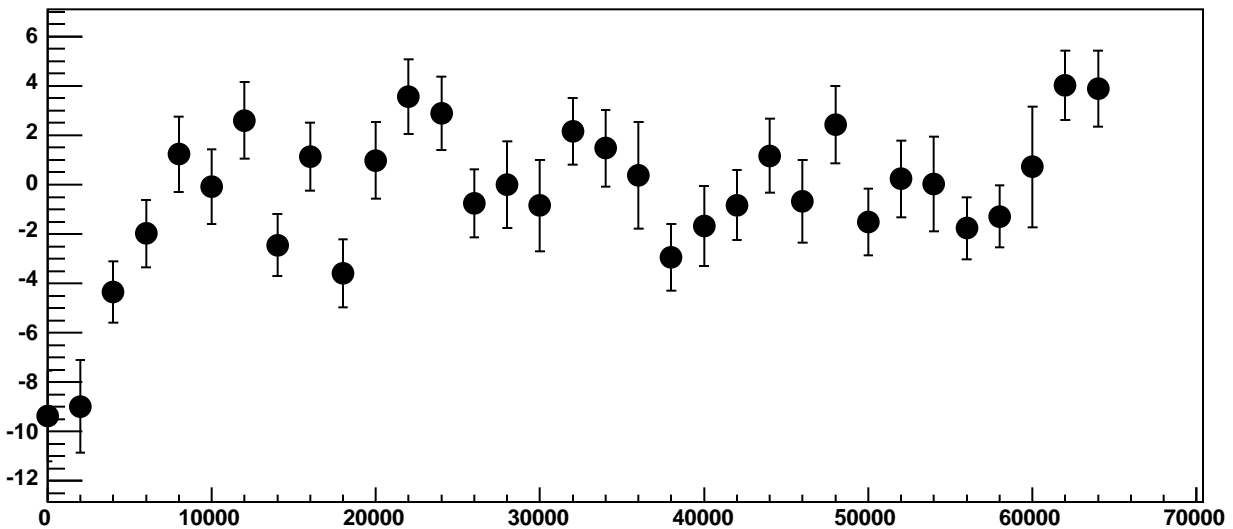
Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



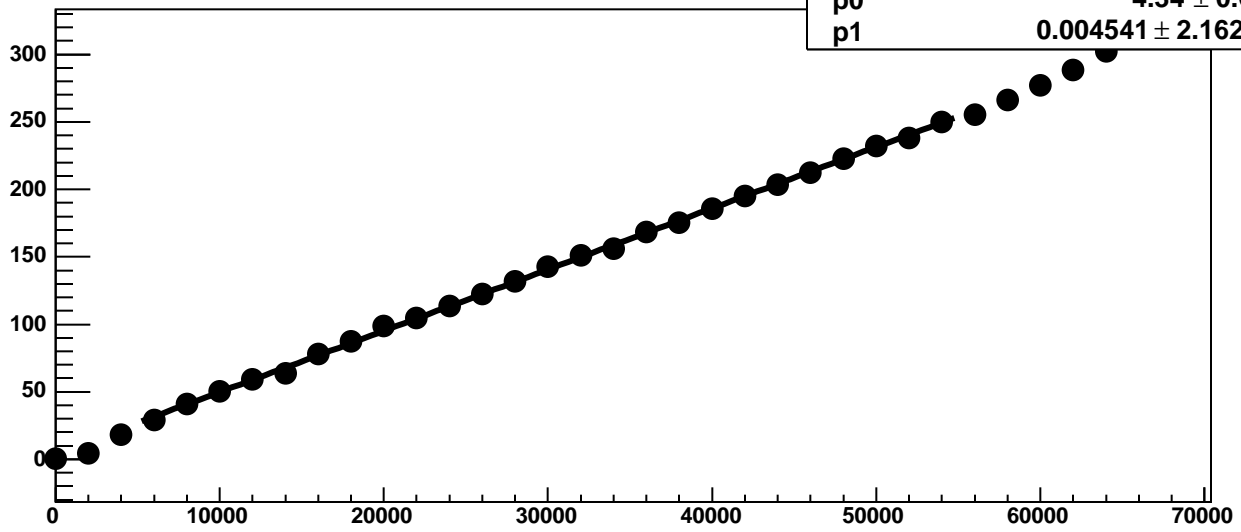
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

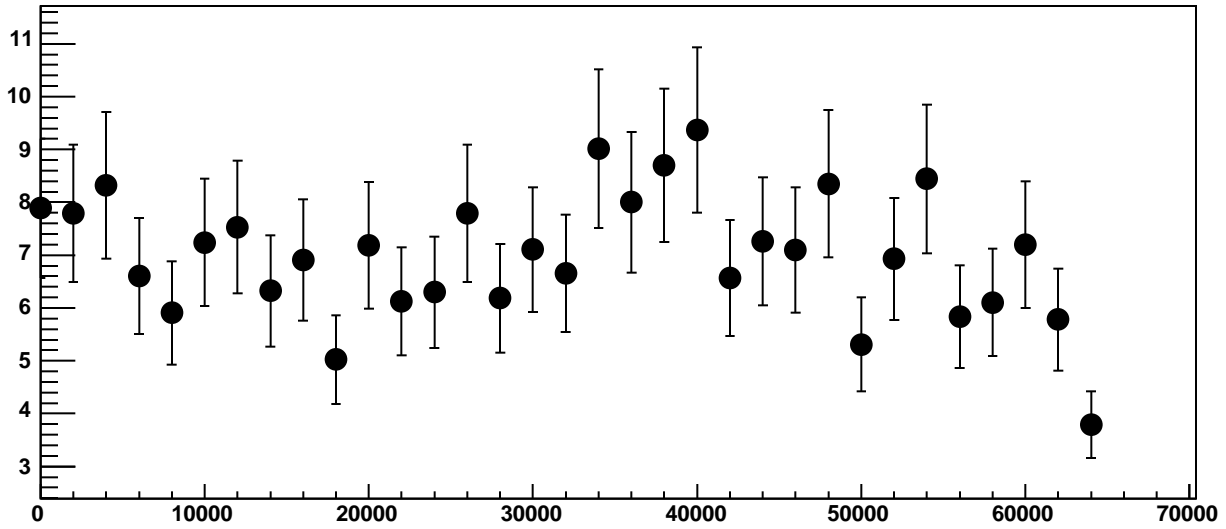


Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

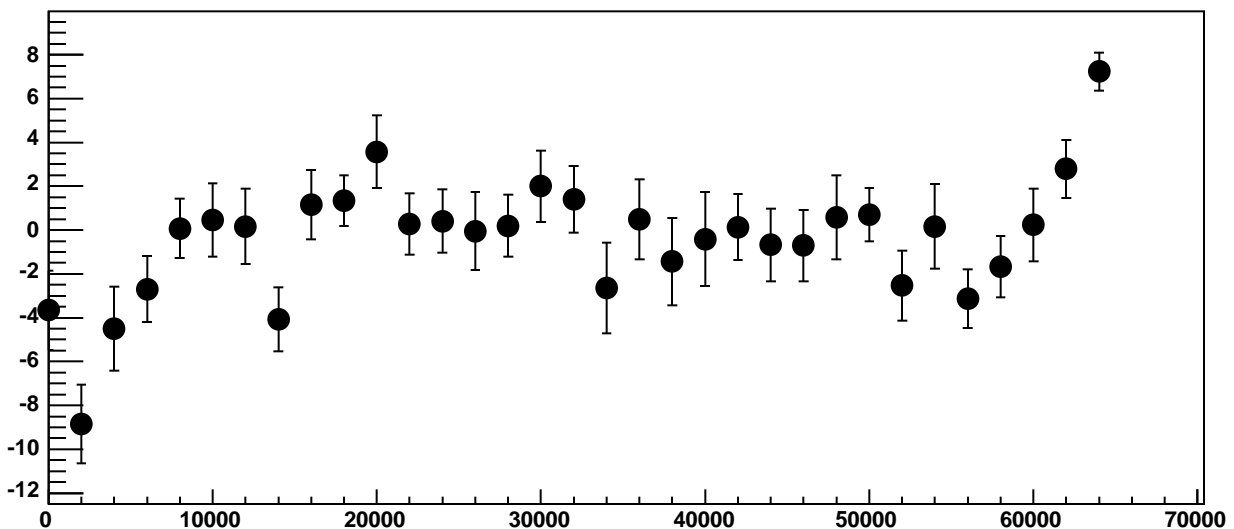


χ^2 / ndf 25.77 / 23
p0 4.34 ± 0.6959
p1 $0.004541 \pm 2.162e-05$

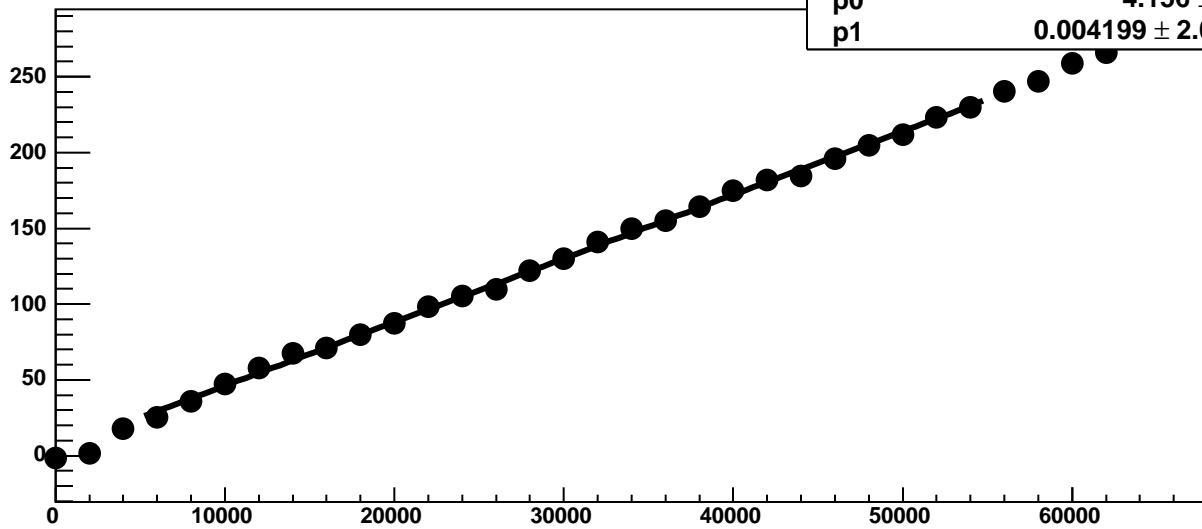
Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



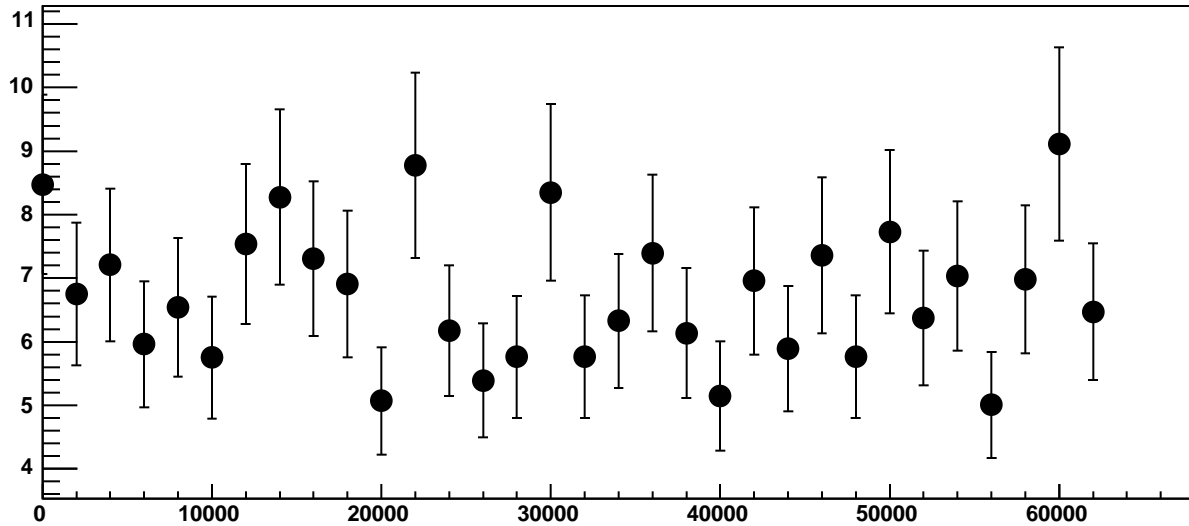
Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



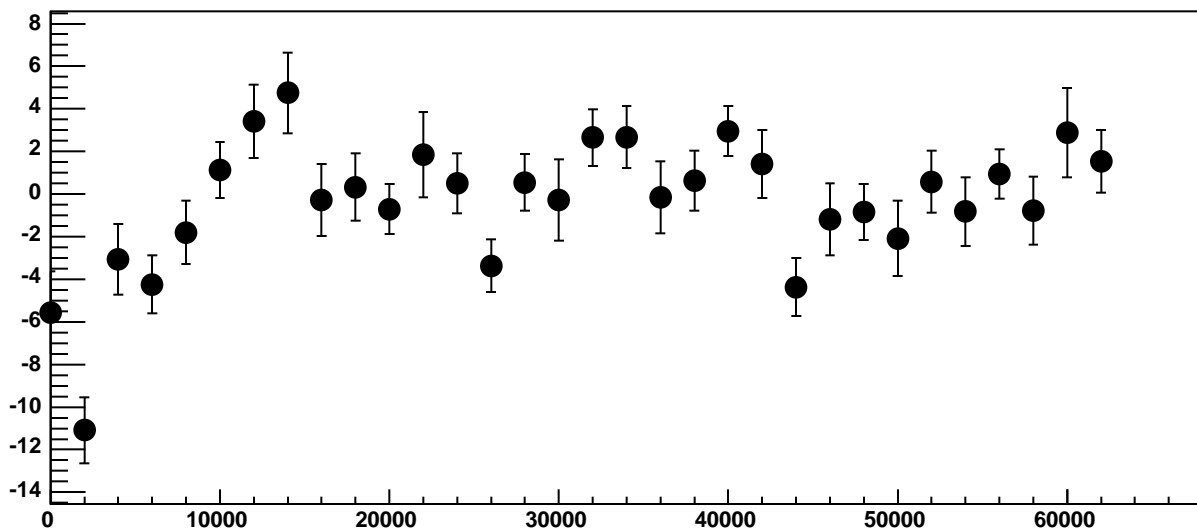
Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



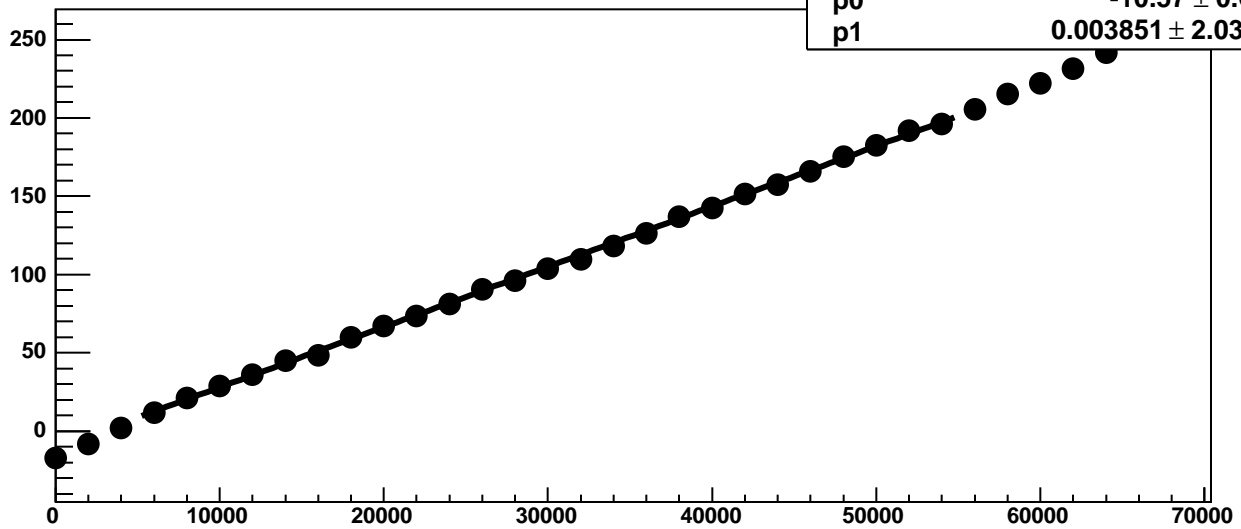
Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



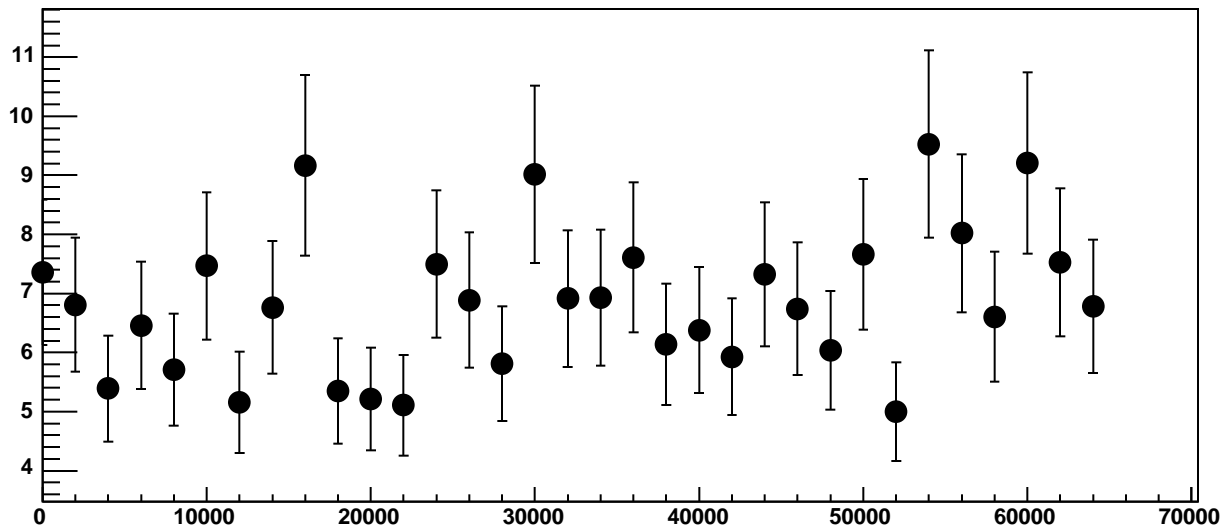
Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



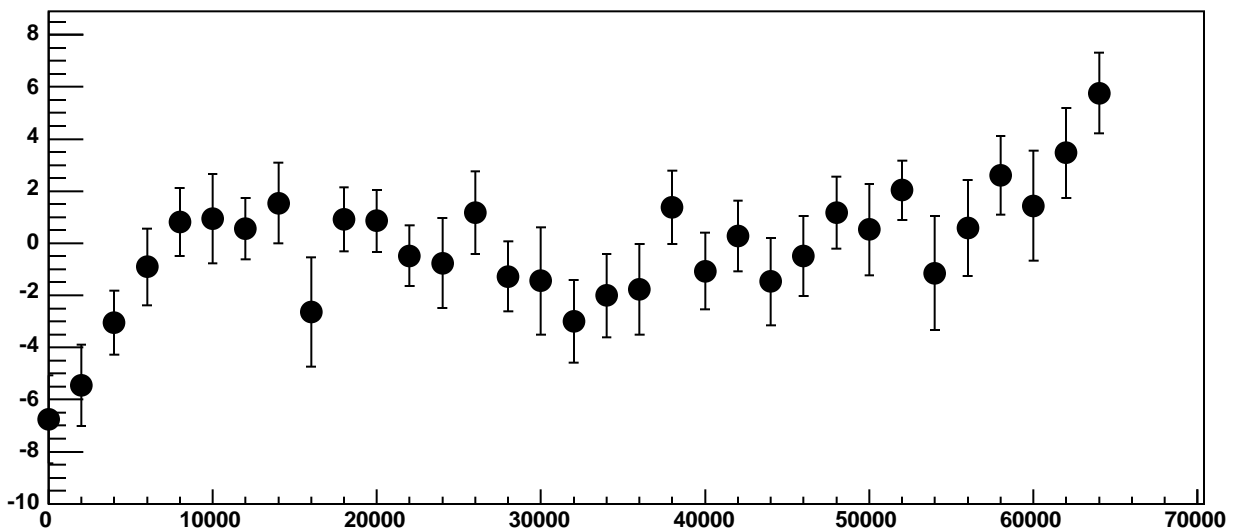
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



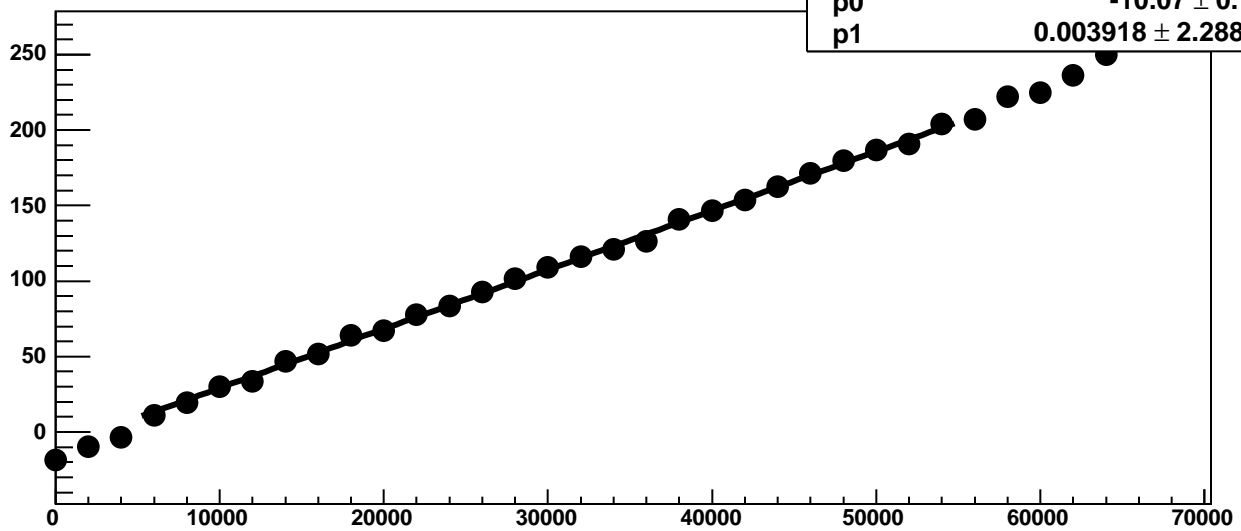
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



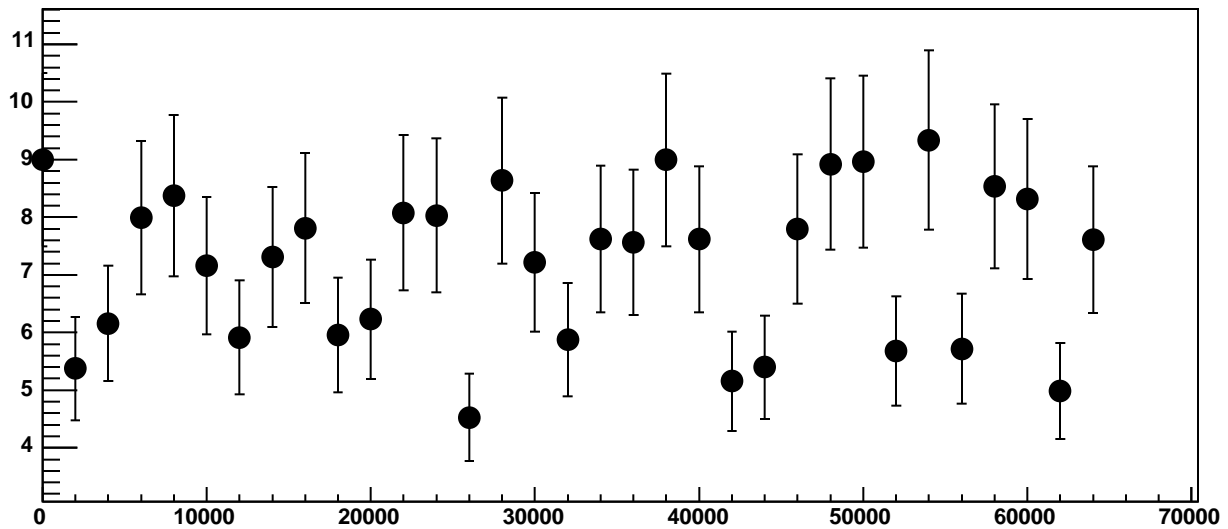
Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



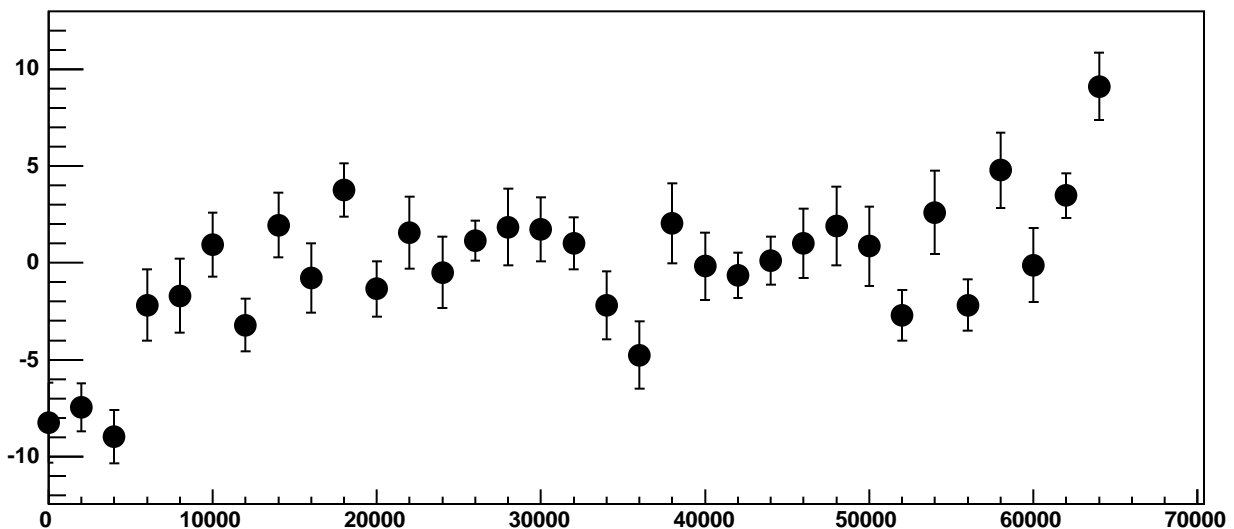
Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



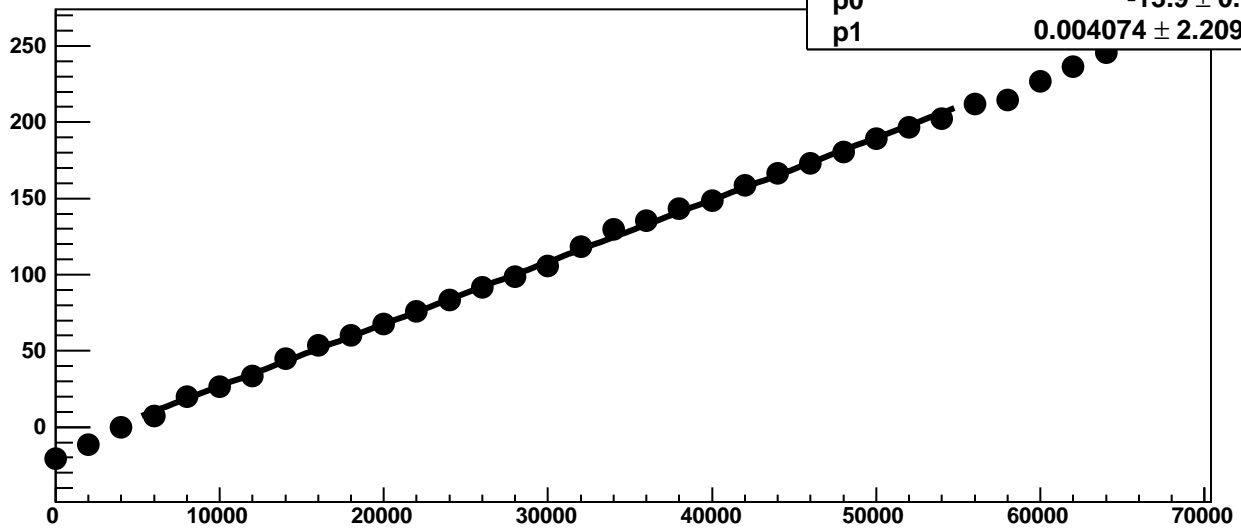
Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

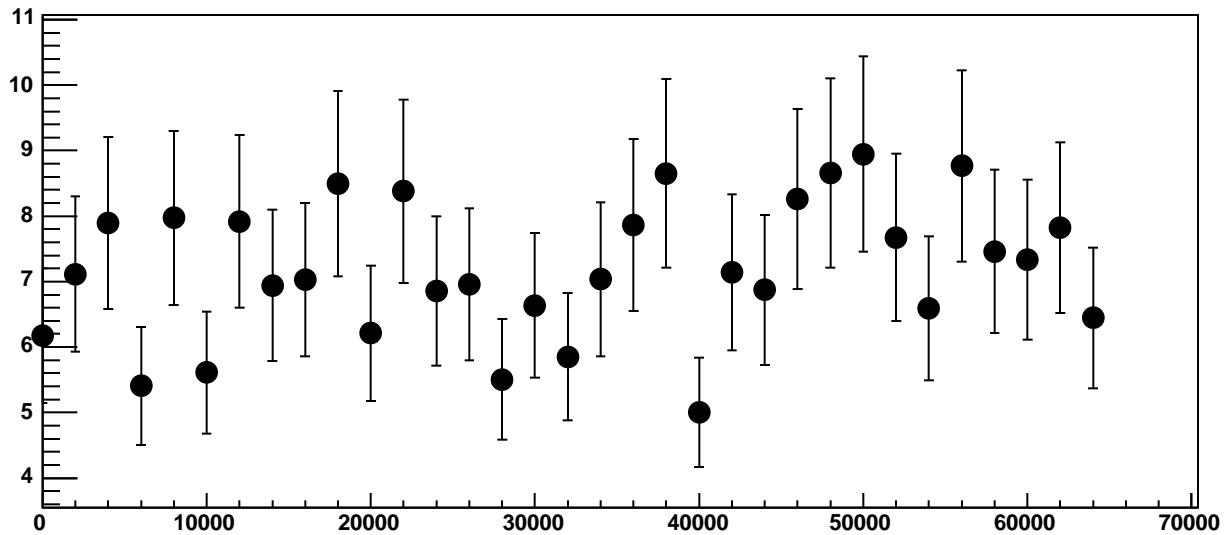


Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

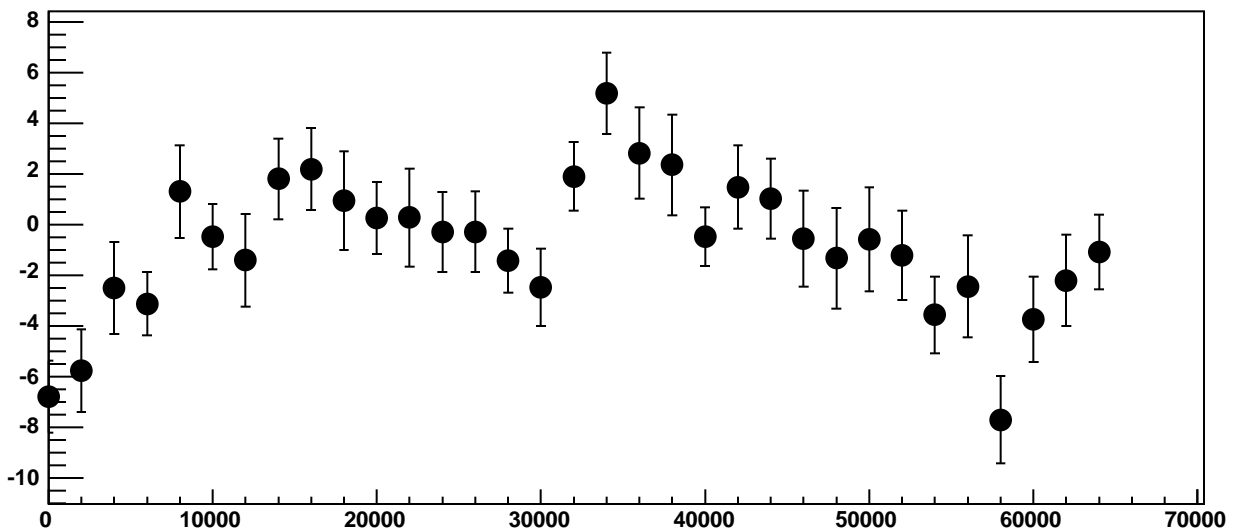


χ^2 / ndf 39.19 / 23
p0 -13.9 ± 0.7113
p1 $0.004074 \pm 2.209e-05$

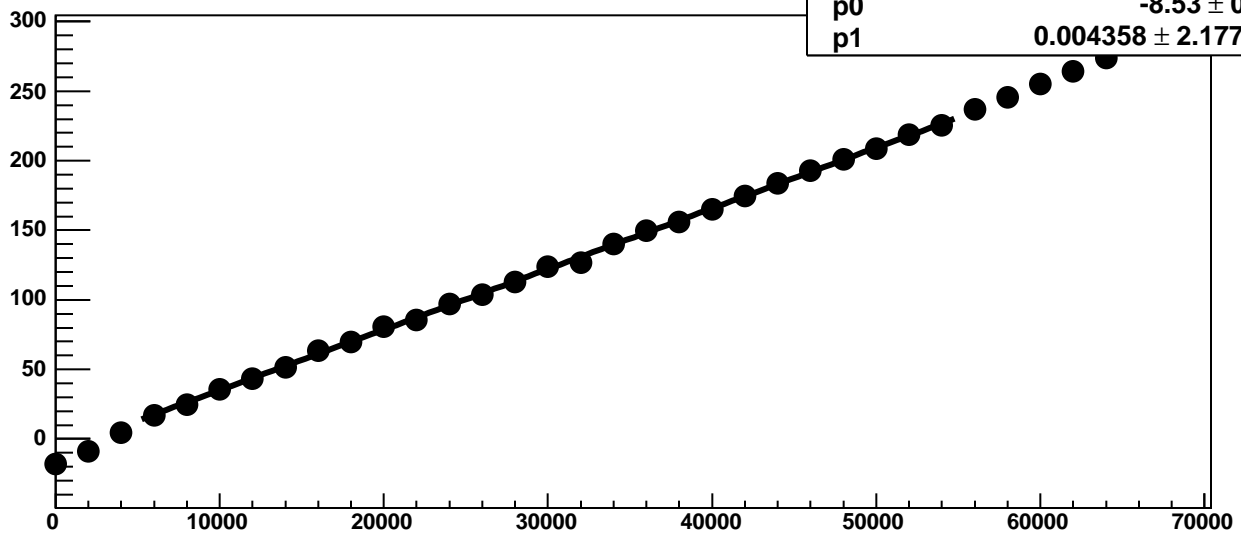
Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

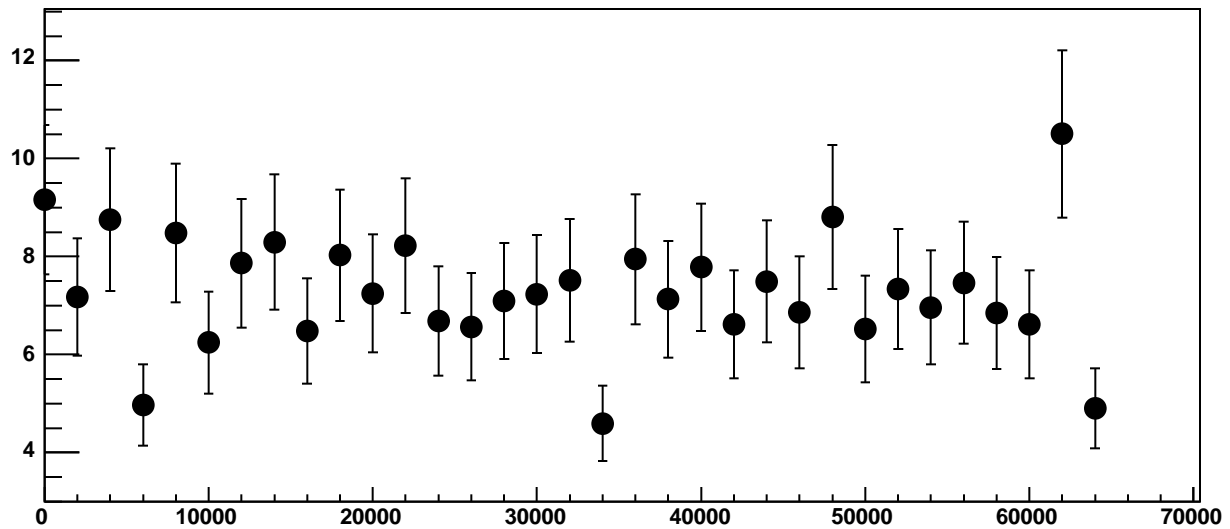


Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

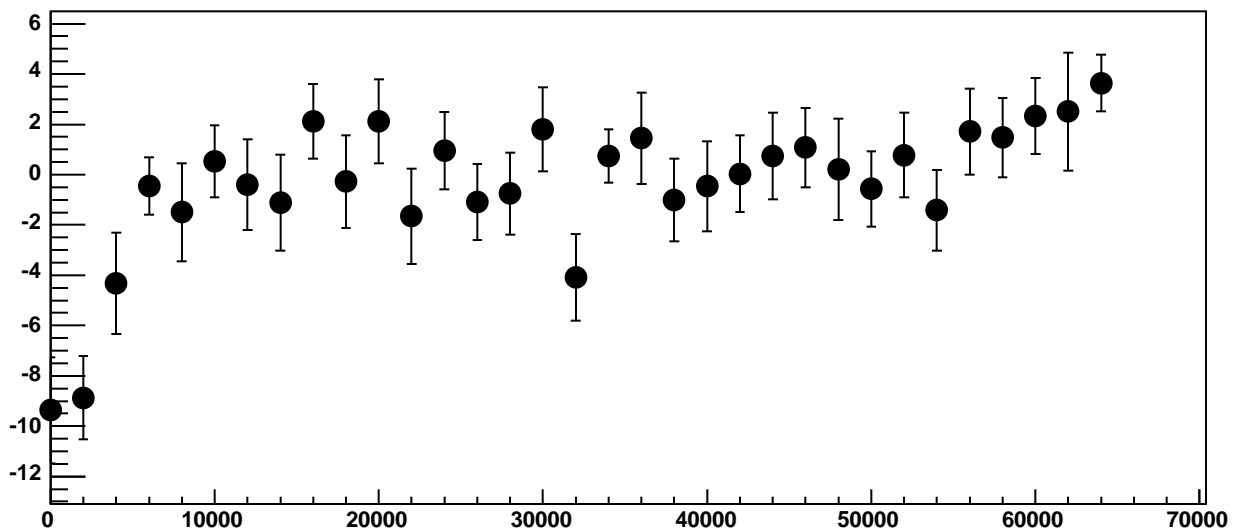


χ^2 / ndf 16.95 / 23
p0 -8.53 ± 0.715
p1 $0.004358 \pm 2.177\text{e-}05$

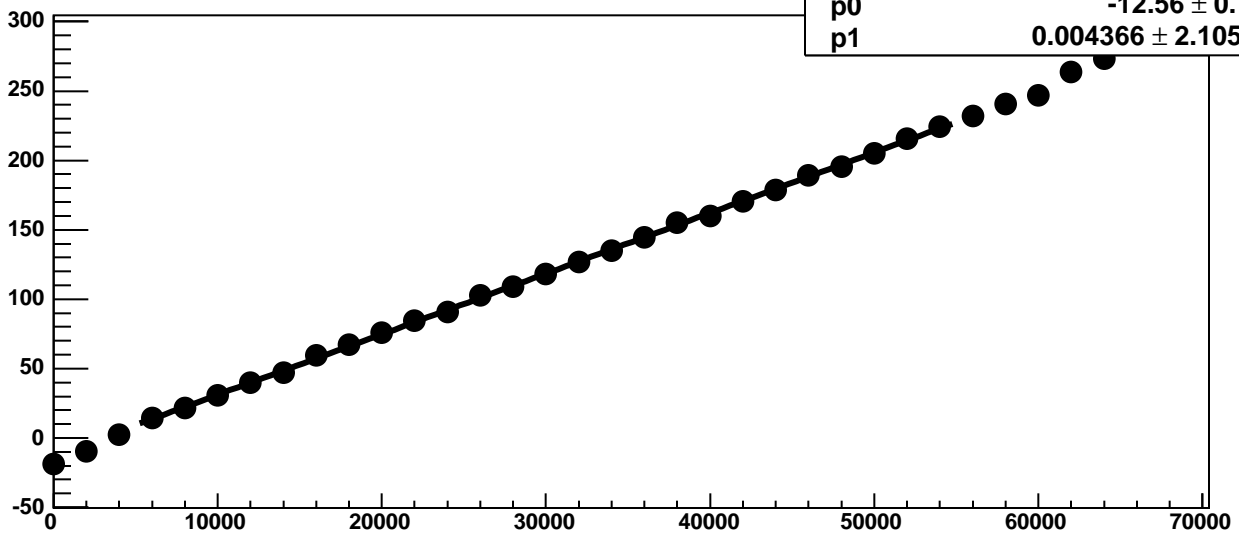
Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



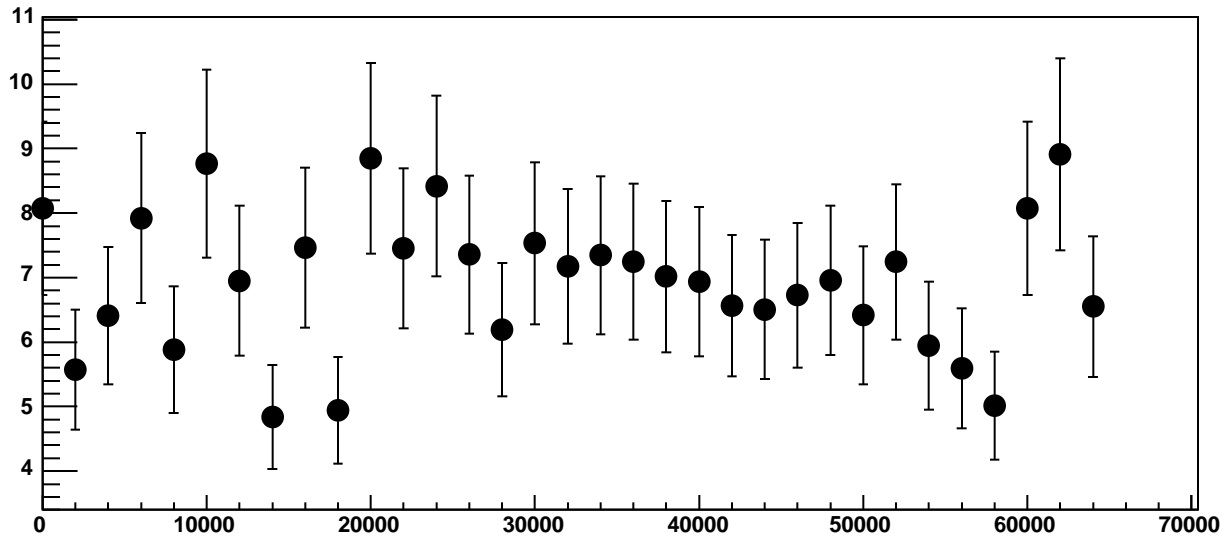
Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



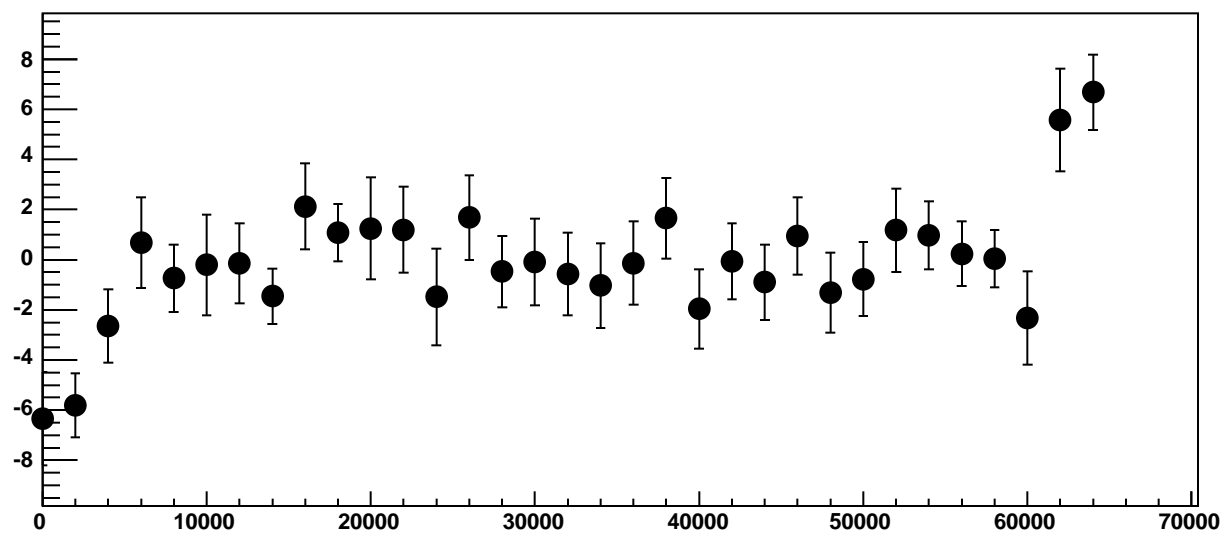
Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



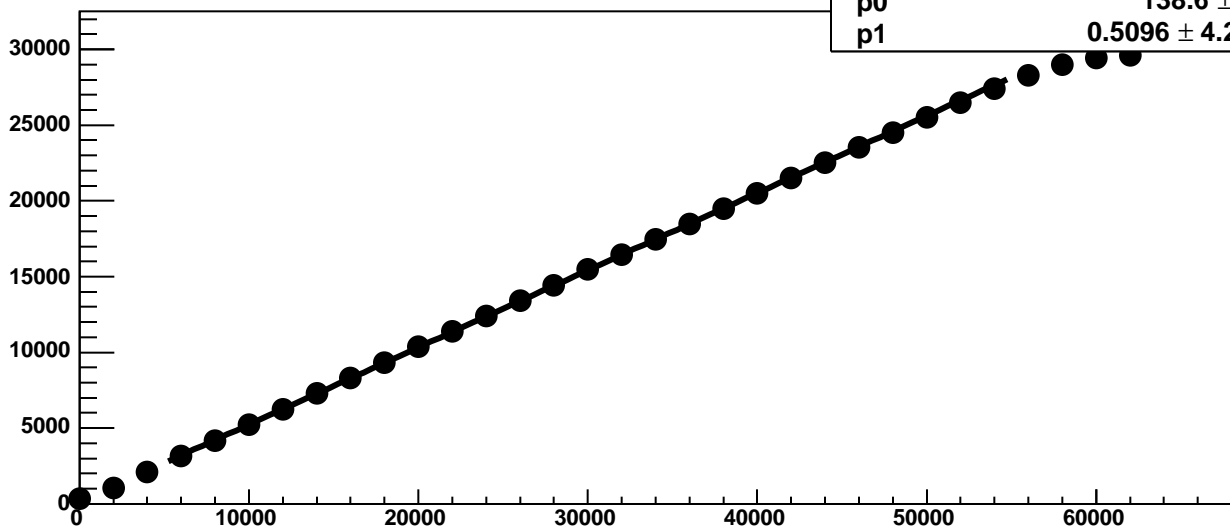
Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



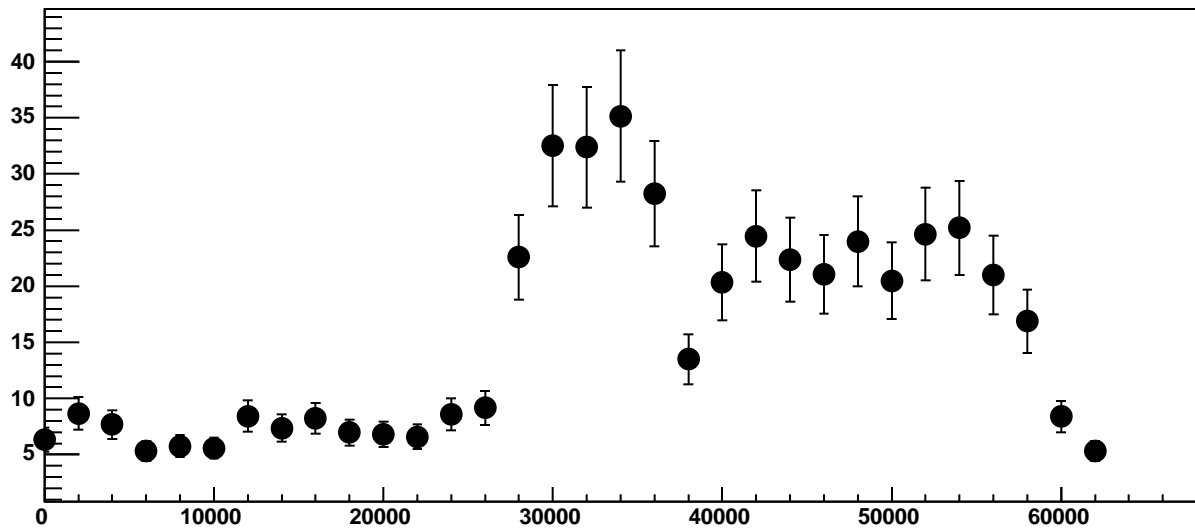
Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



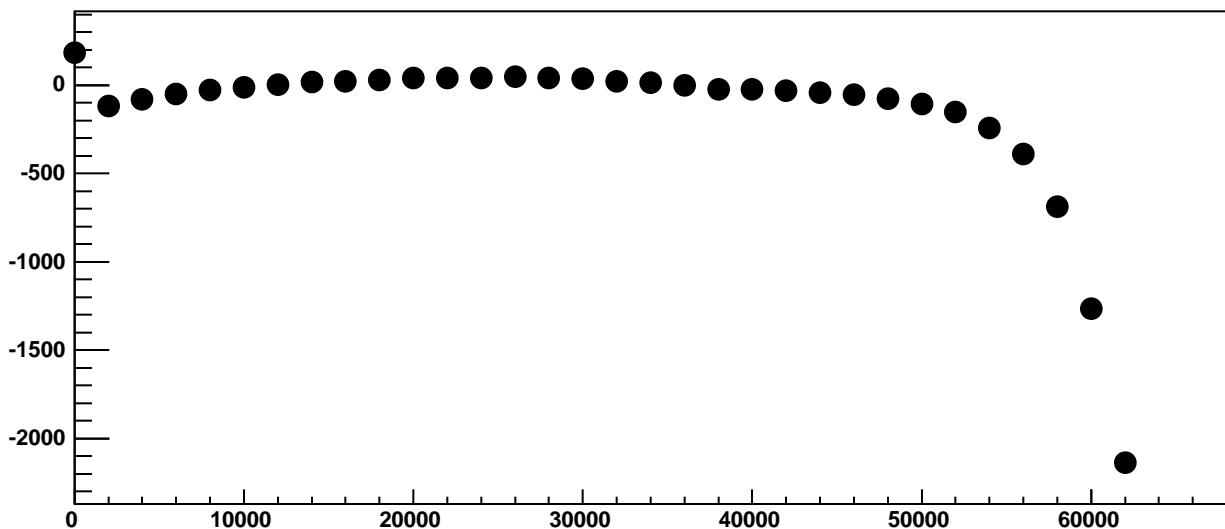
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



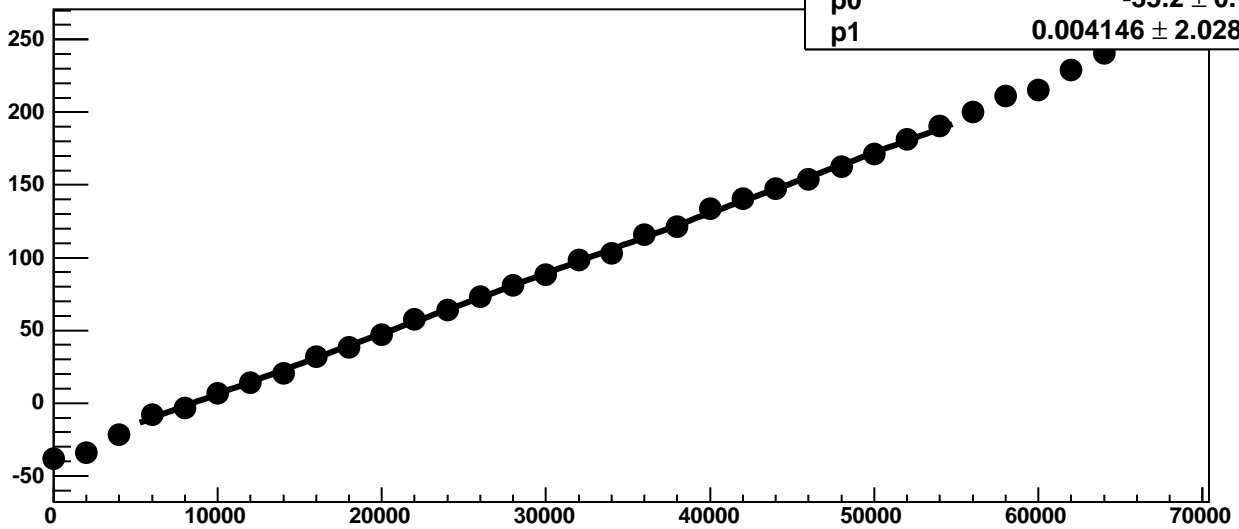
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

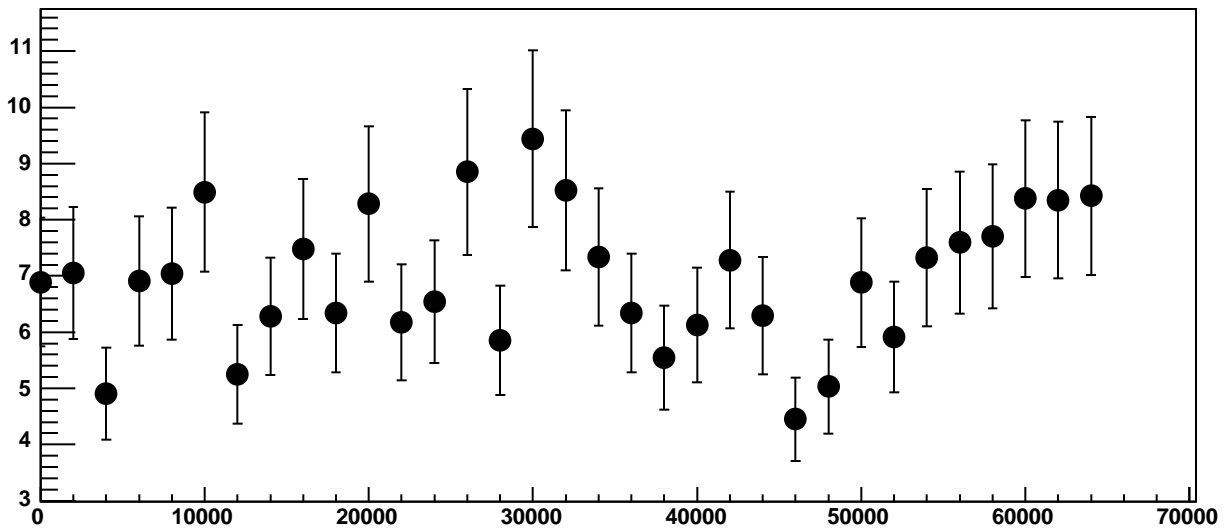


Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

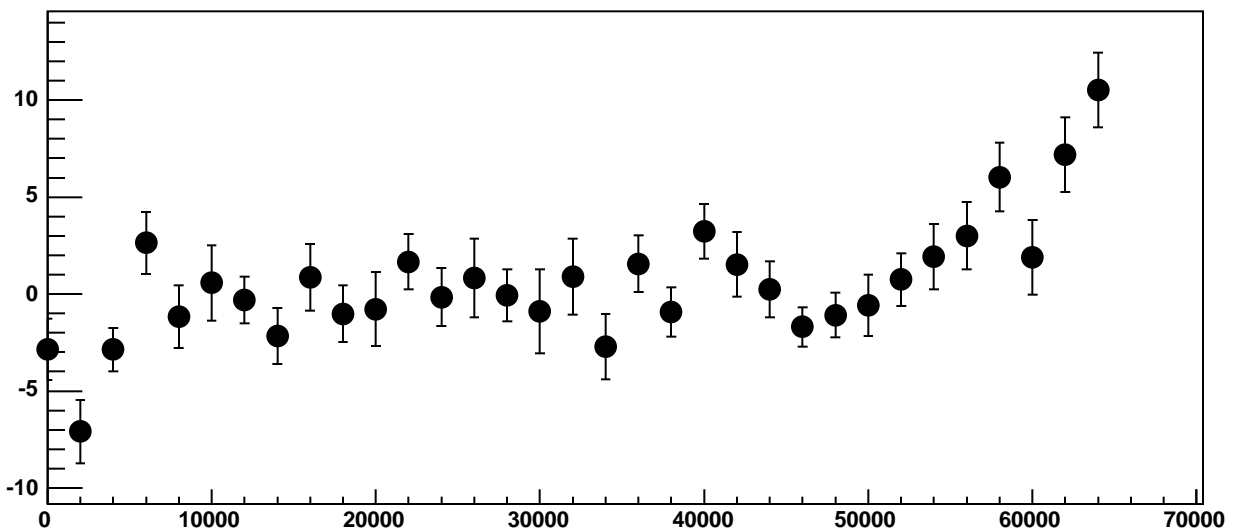


χ^2 / ndf 24.42 / 23
p0 -35.2 ± 0.7062
p1 $0.004146 \pm 2.028e-05$

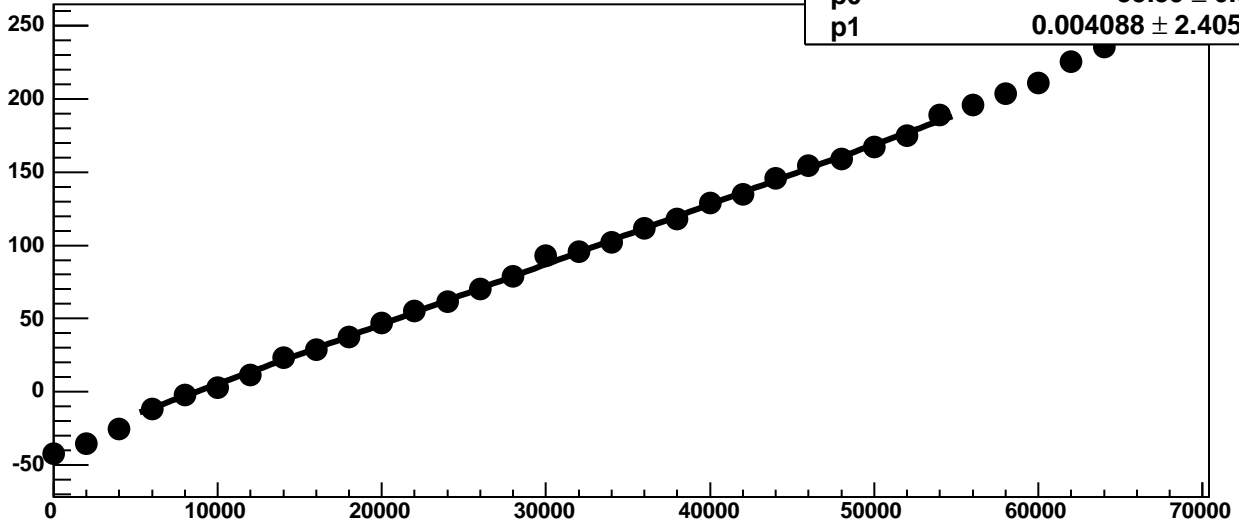
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

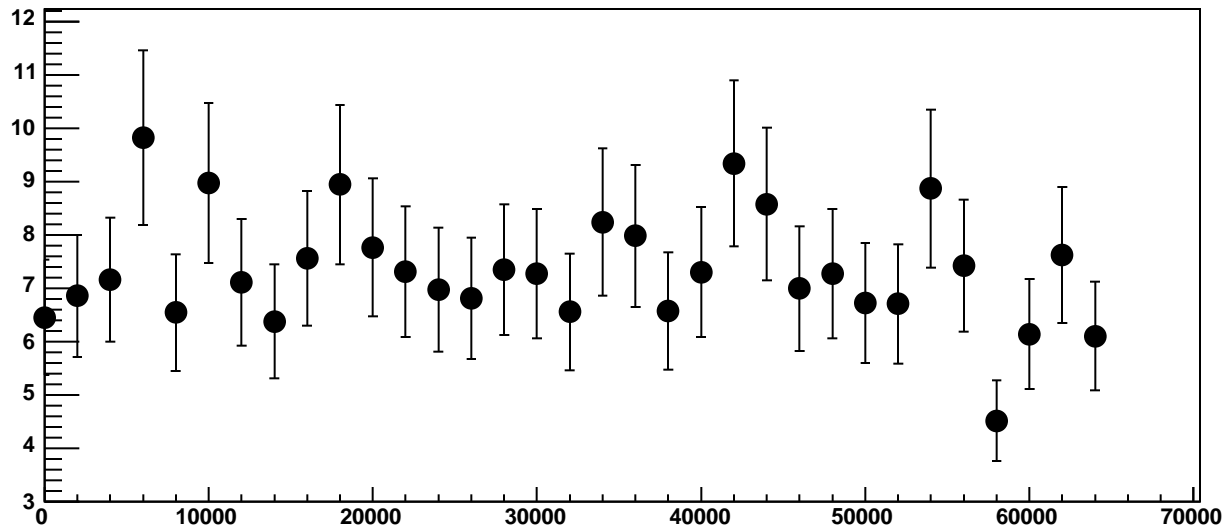


Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

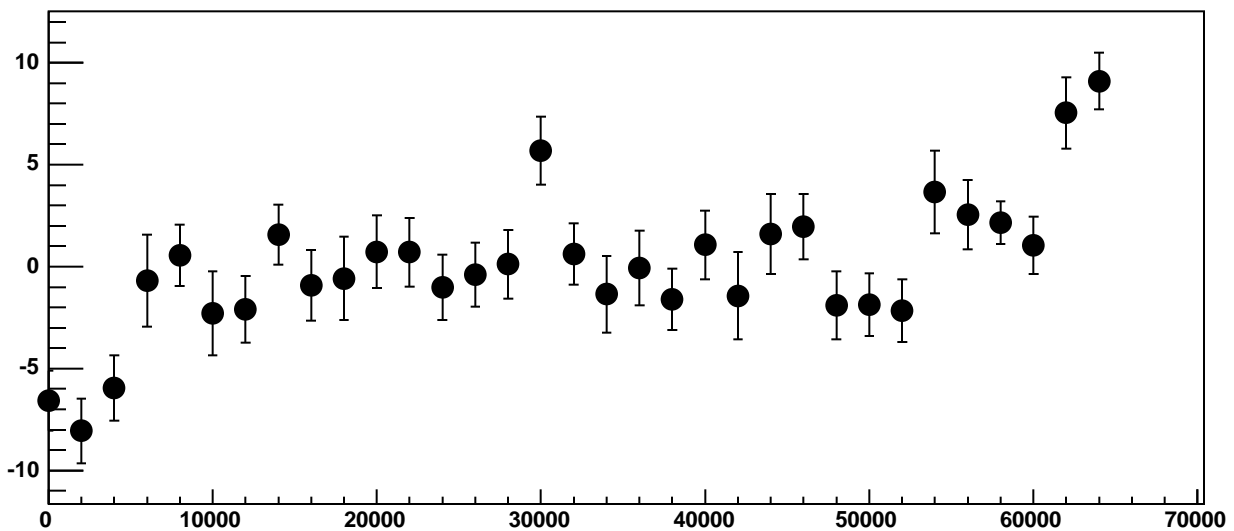


χ^2 / ndf 29.82 / 23
p0 -35.59 ± 0.8024
p1 $0.004088 \pm 2.405e-05$

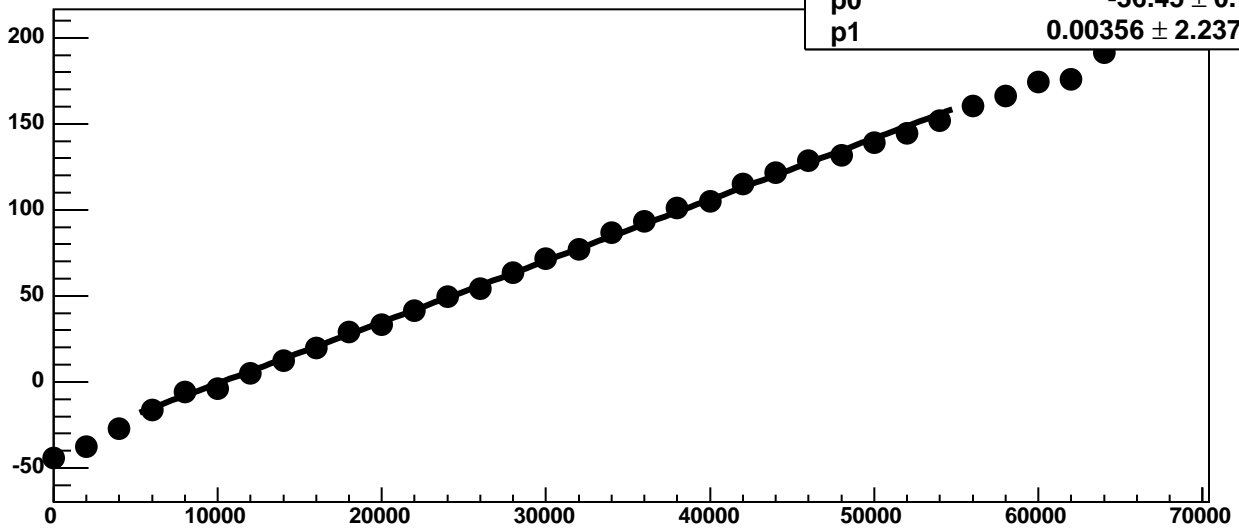
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



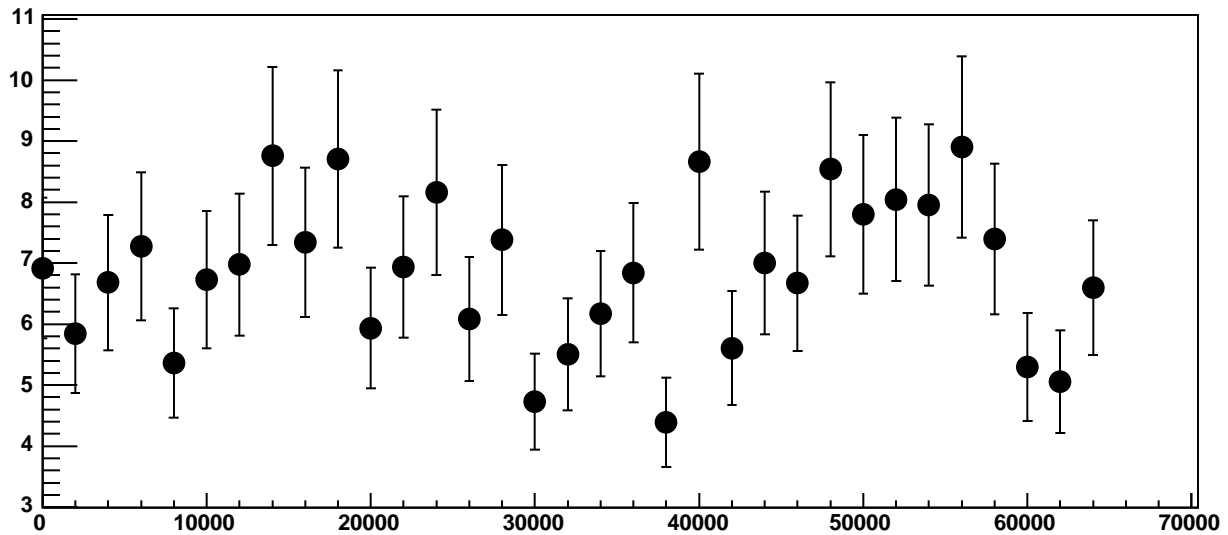
Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



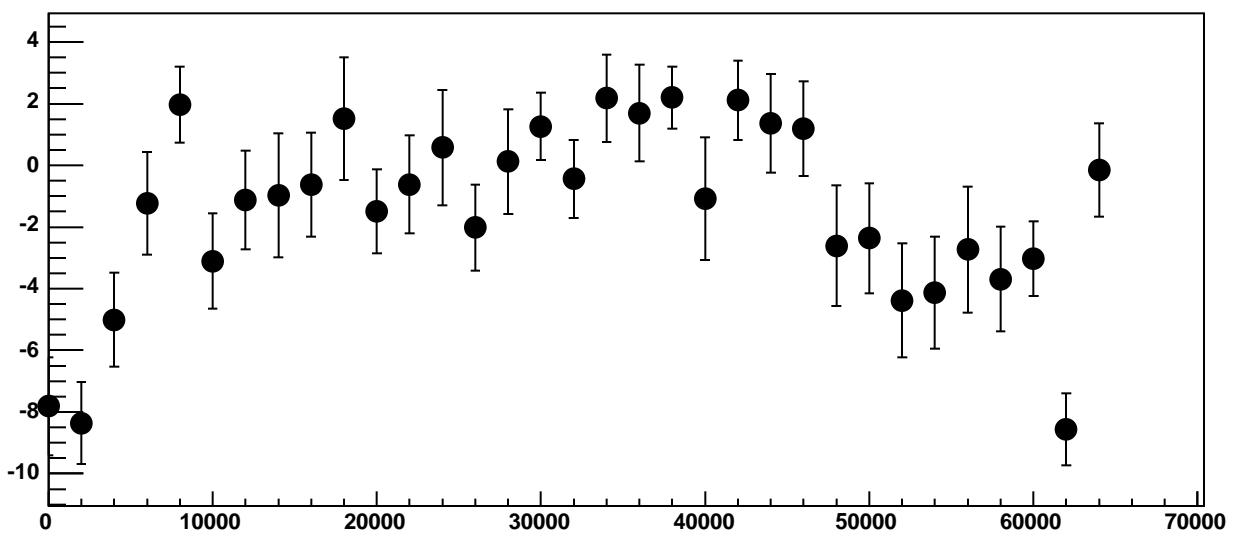
Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



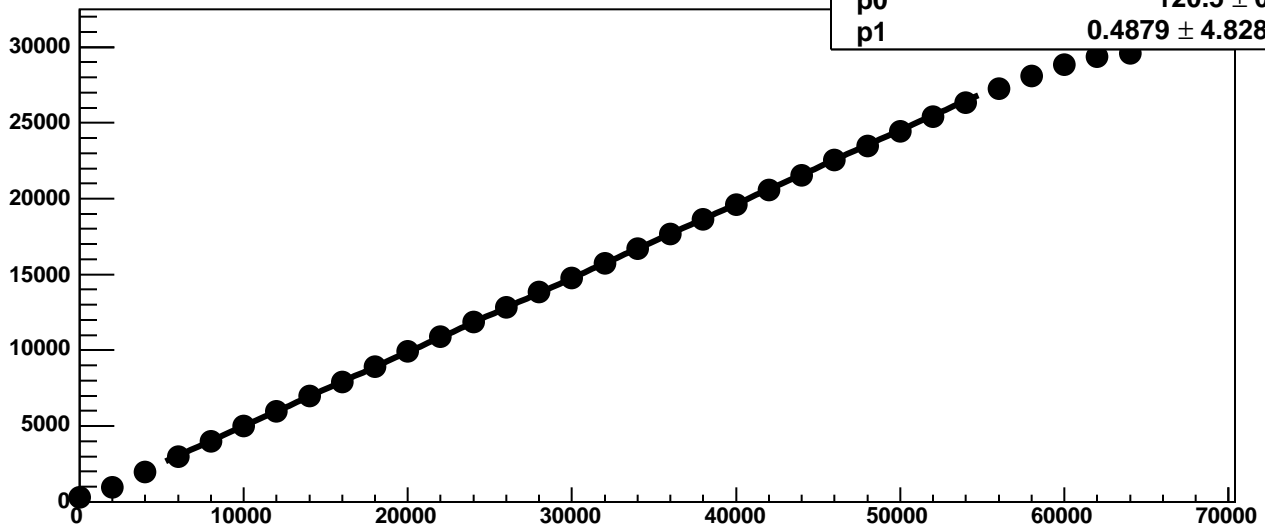
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



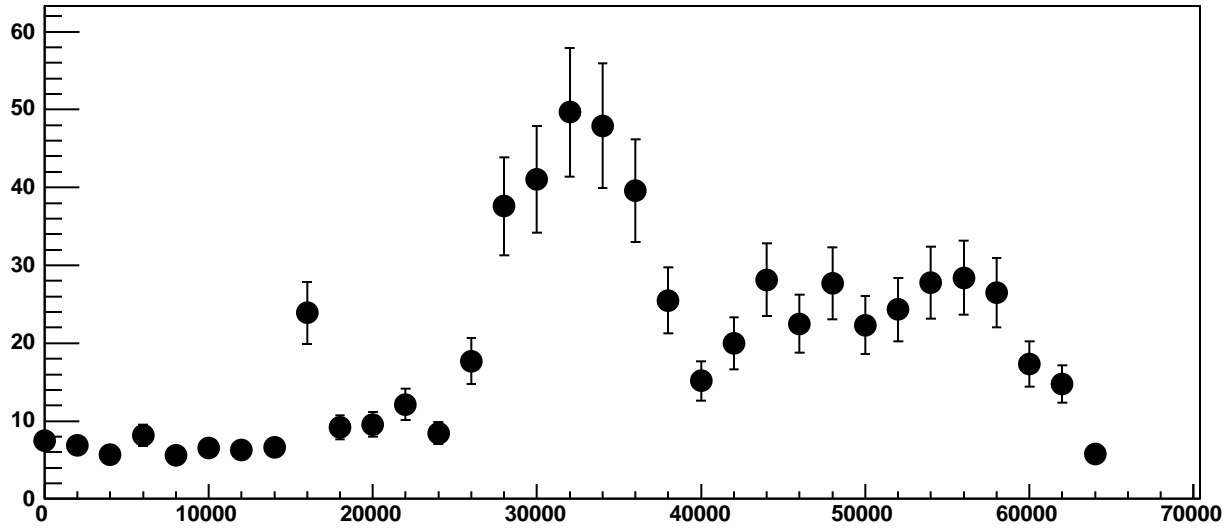
Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



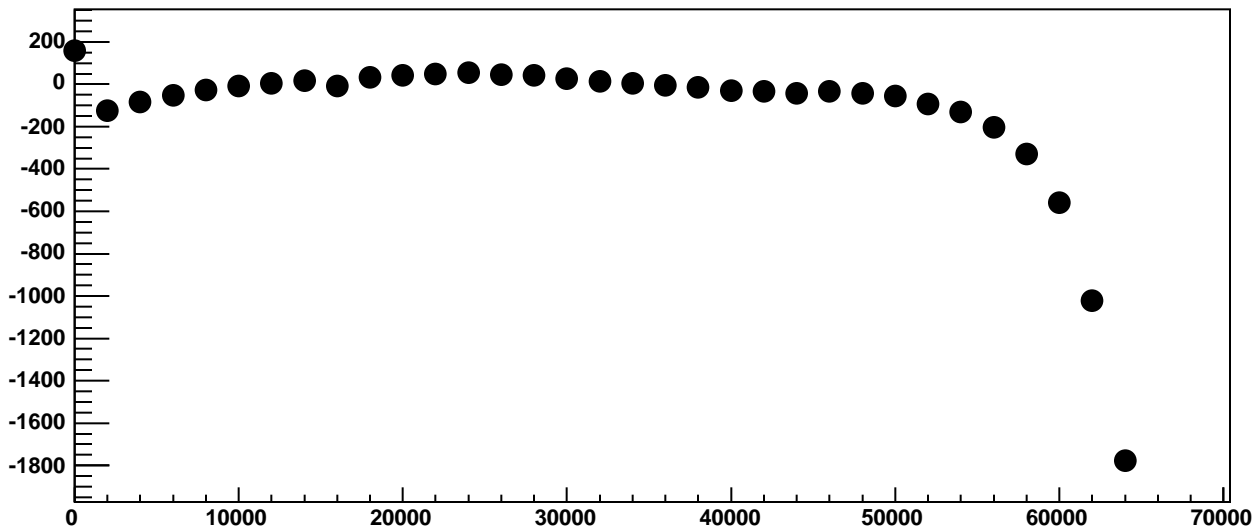
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



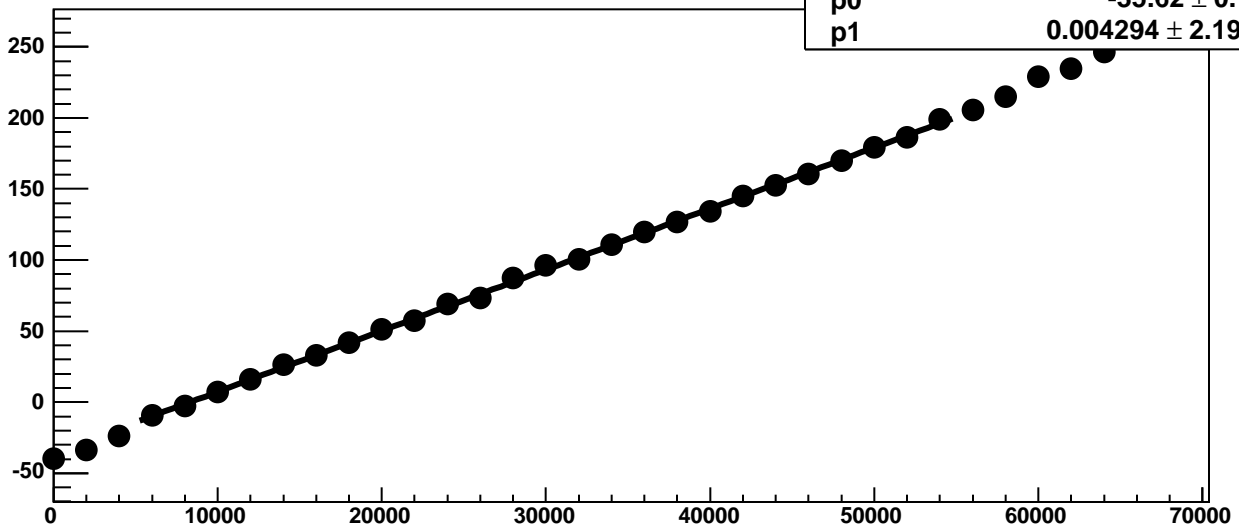
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

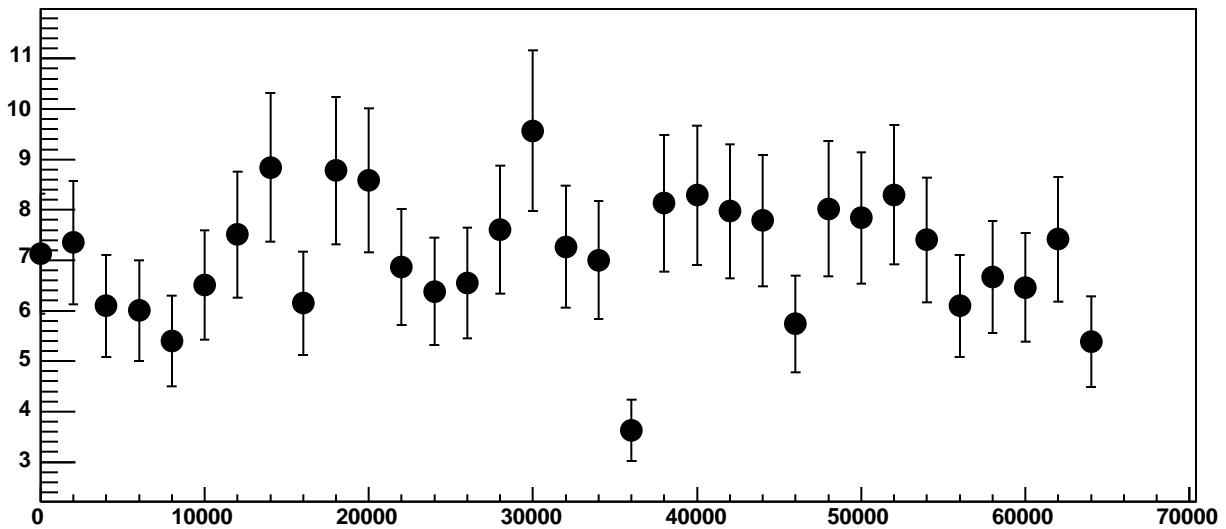


Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

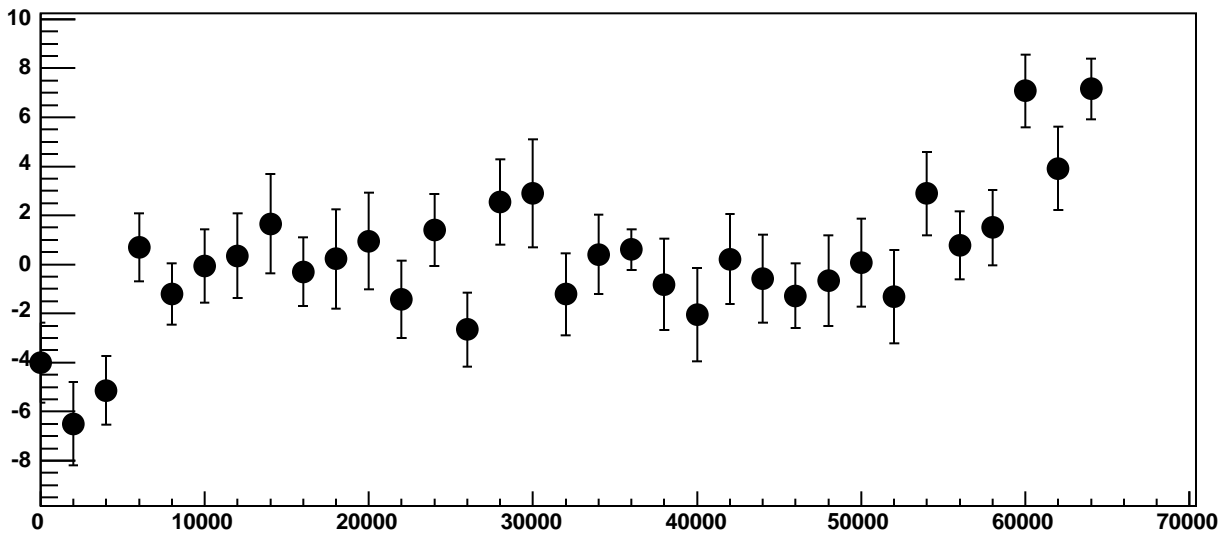


χ^2 / ndf 18 / 23
p0 -35.62 ± 0.7124
p1 $0.004294 \pm 2.19\text{e-}05$

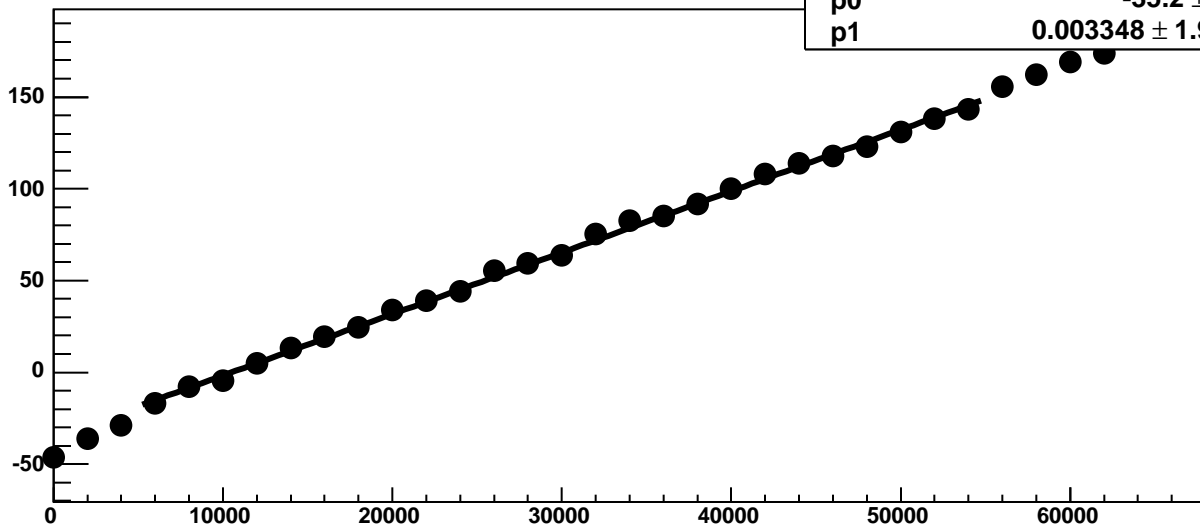
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

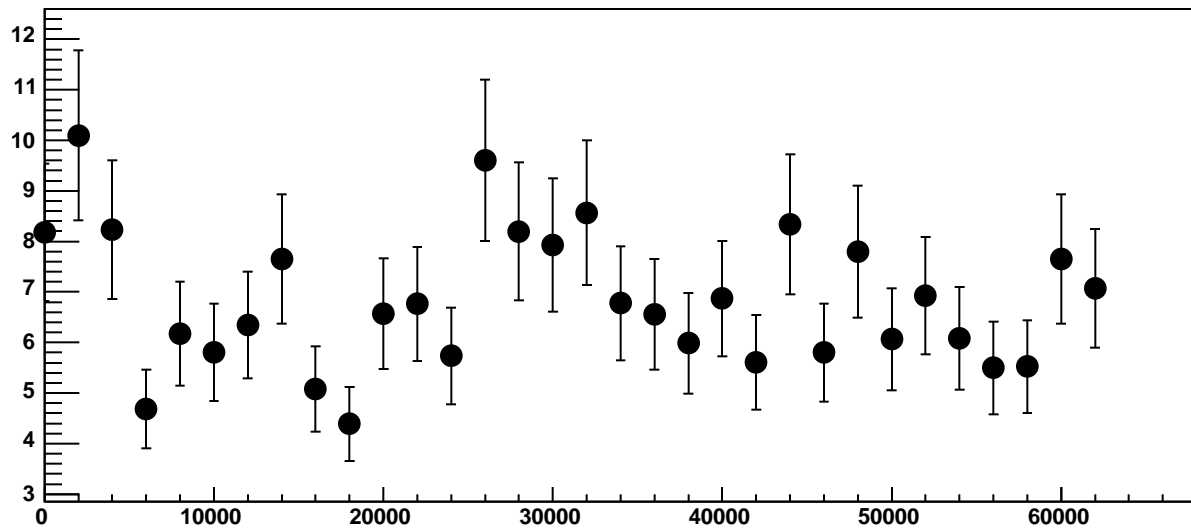


Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

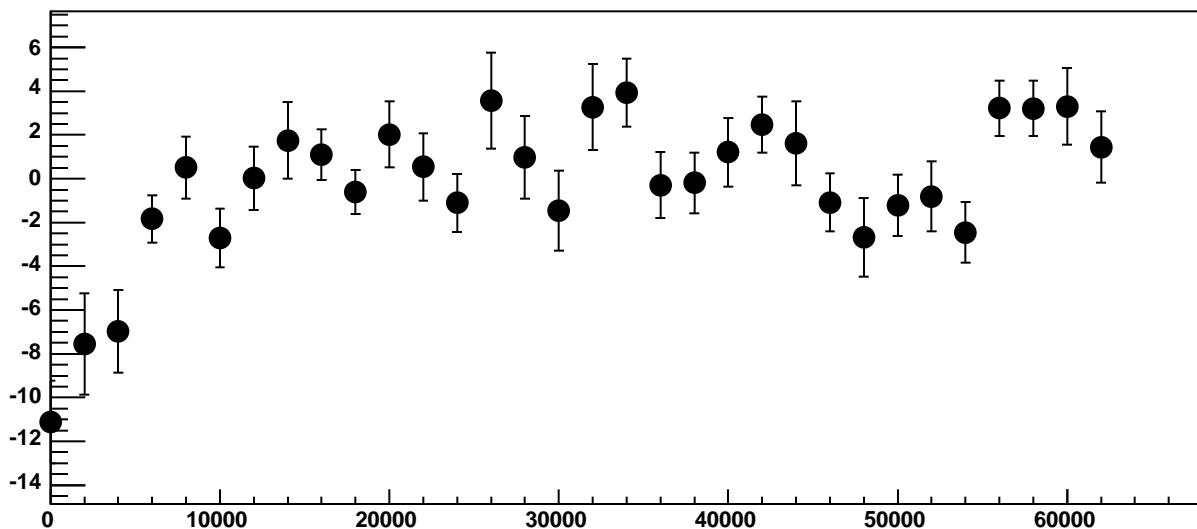


χ^2 / ndf 36.78 / 23
p0 -35.2 ± 0.6134
p1 $0.003348 \pm 1.916e-05$

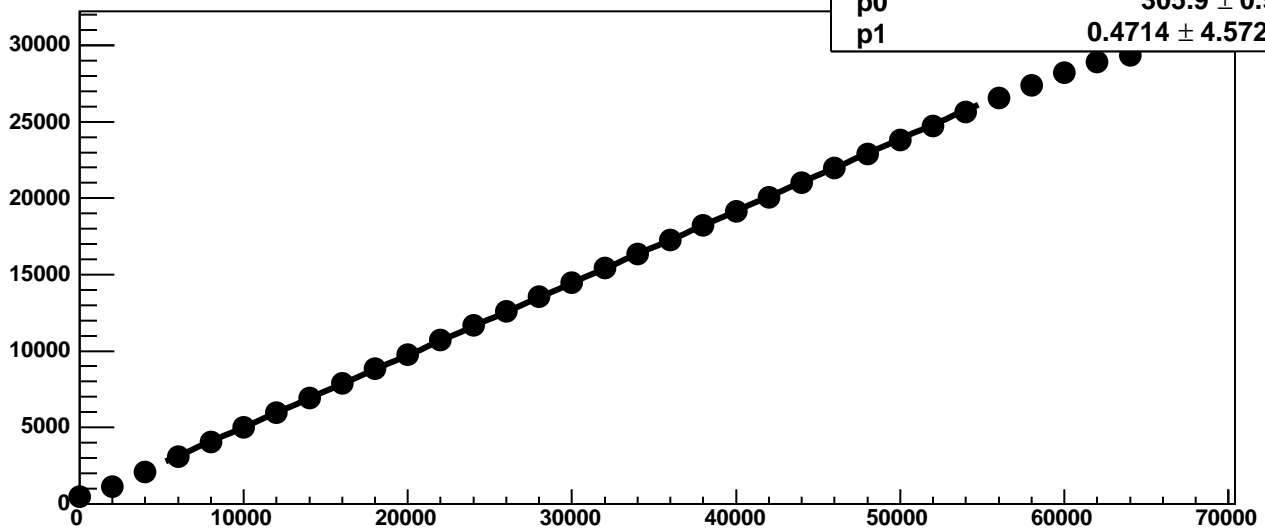
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



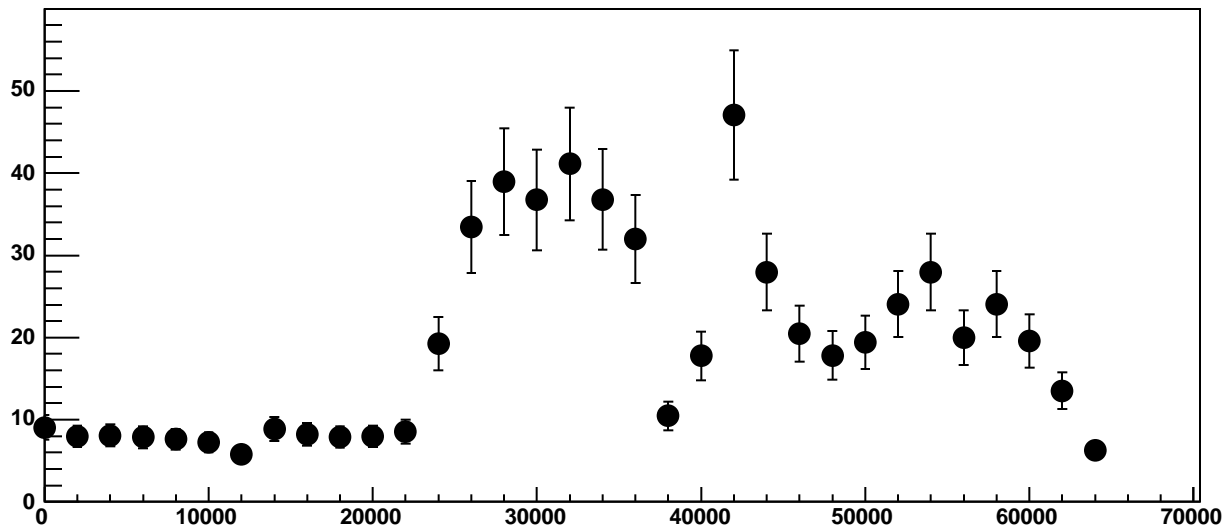
Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



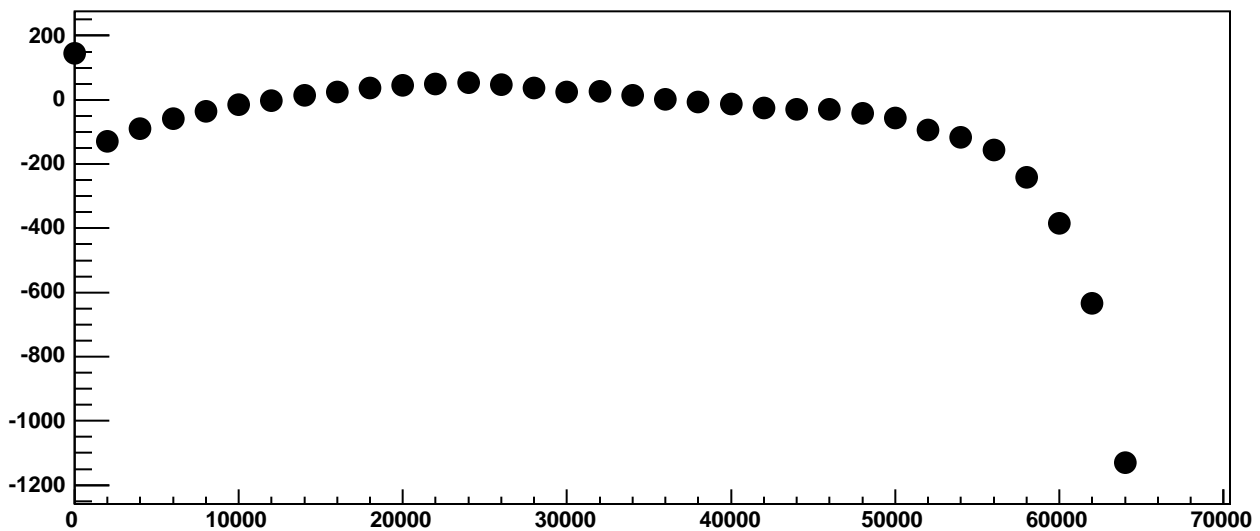
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



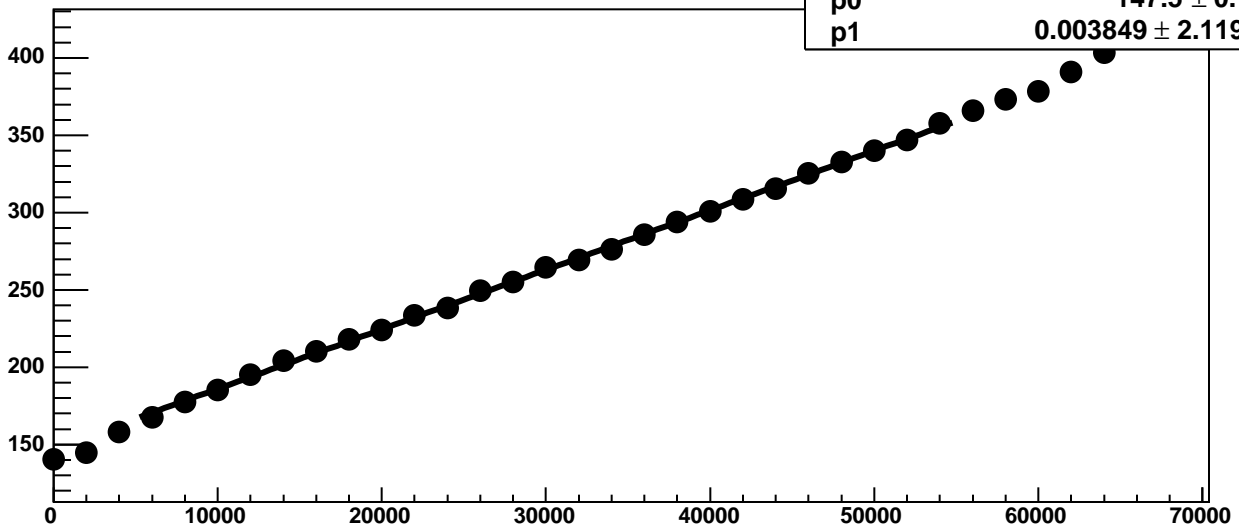
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

20.42 / 23

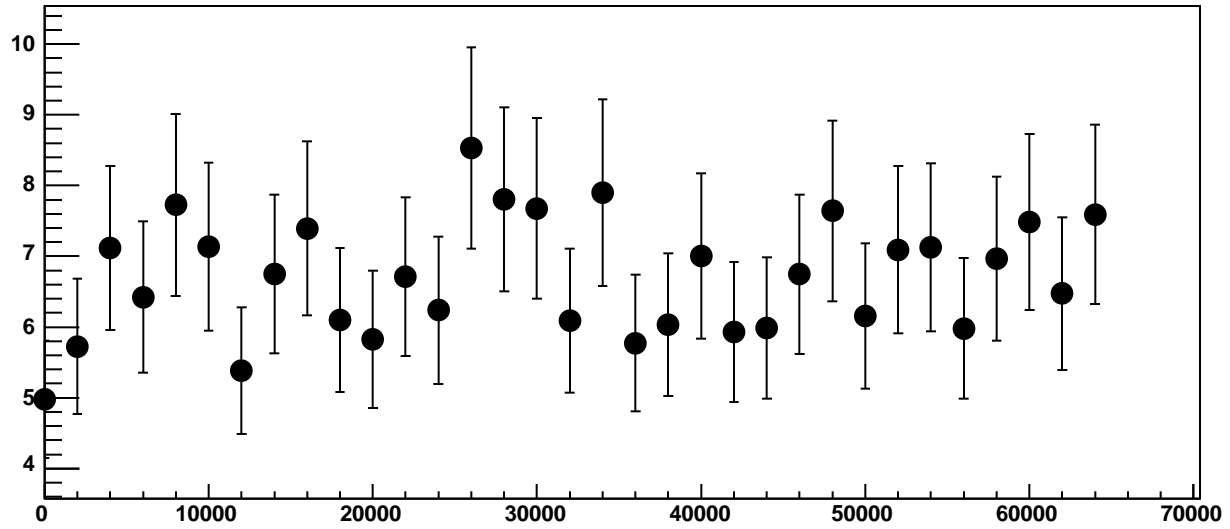
p0

147.5 ± 0.7026

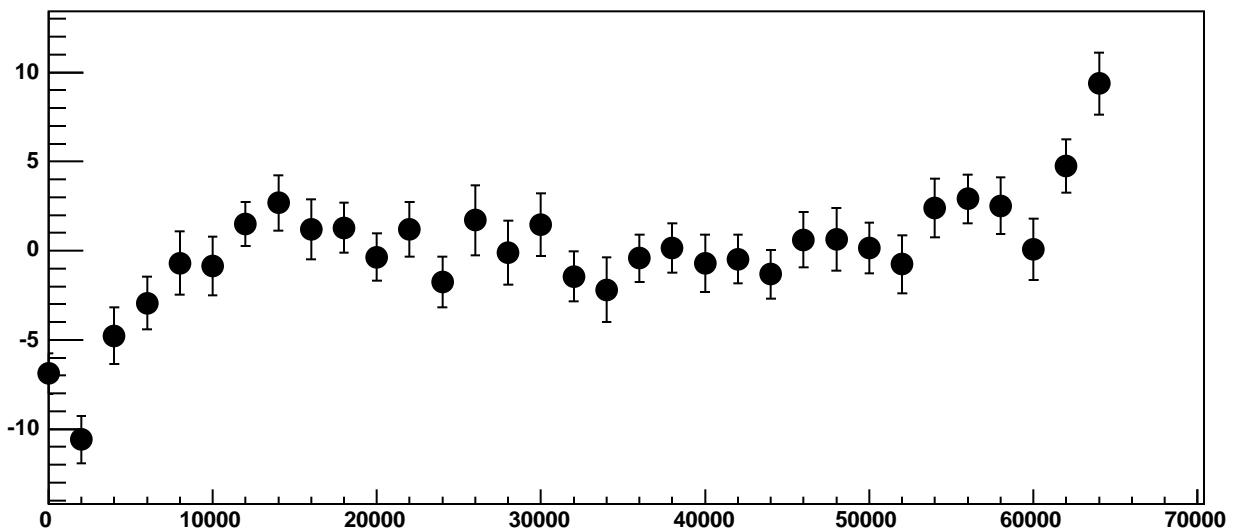
p1

$0.003849 \pm 2.119e-05$

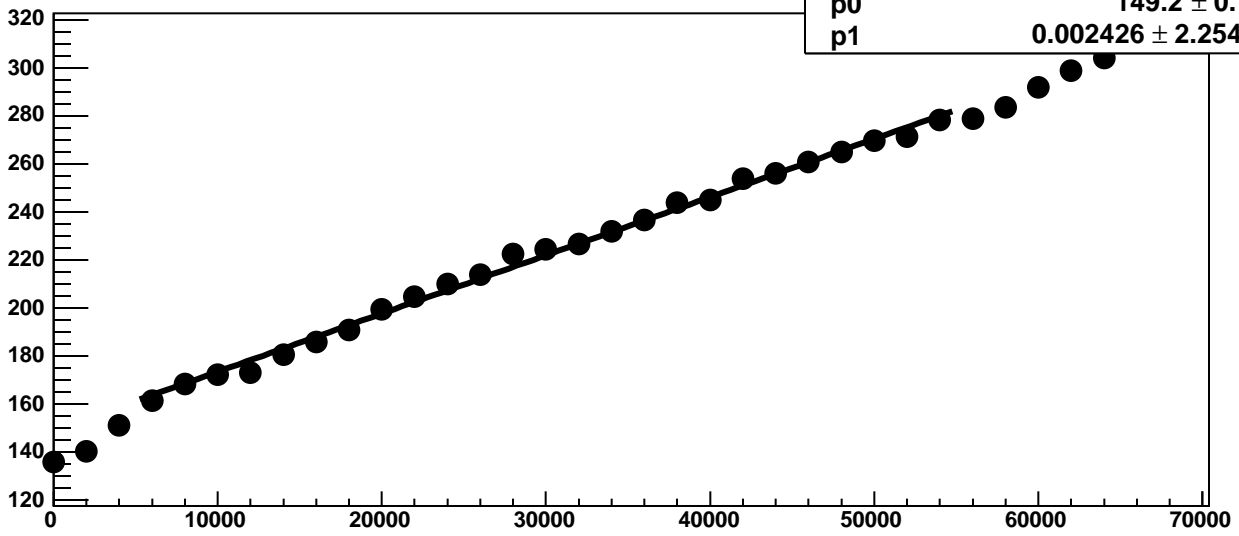
Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

51.83 / 23

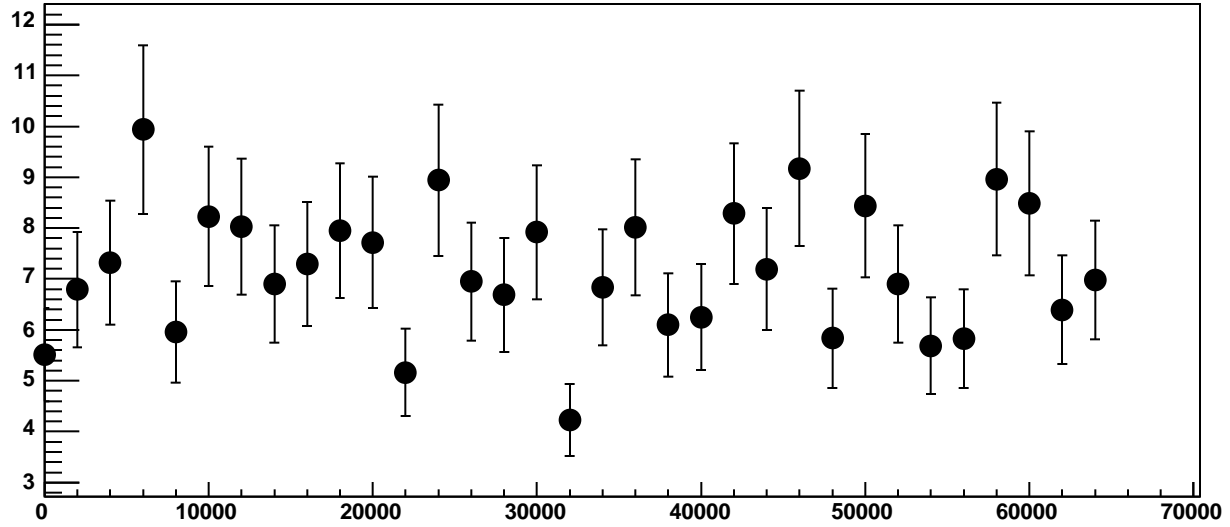
p0

149.2 ± 0.7647

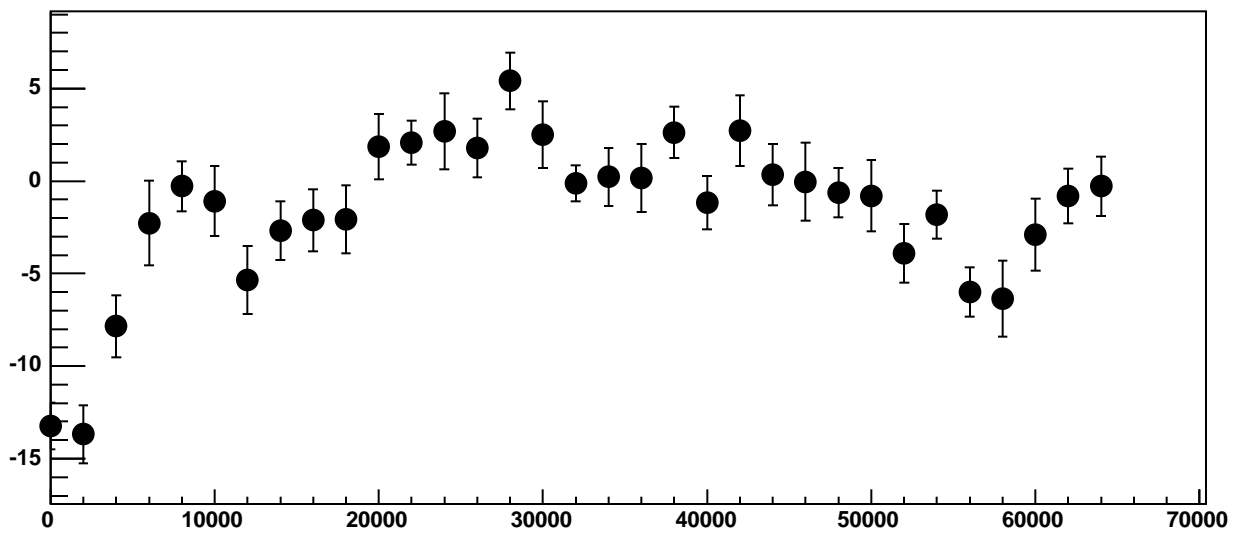
p1

$0.002426 \pm 2.254e-05$

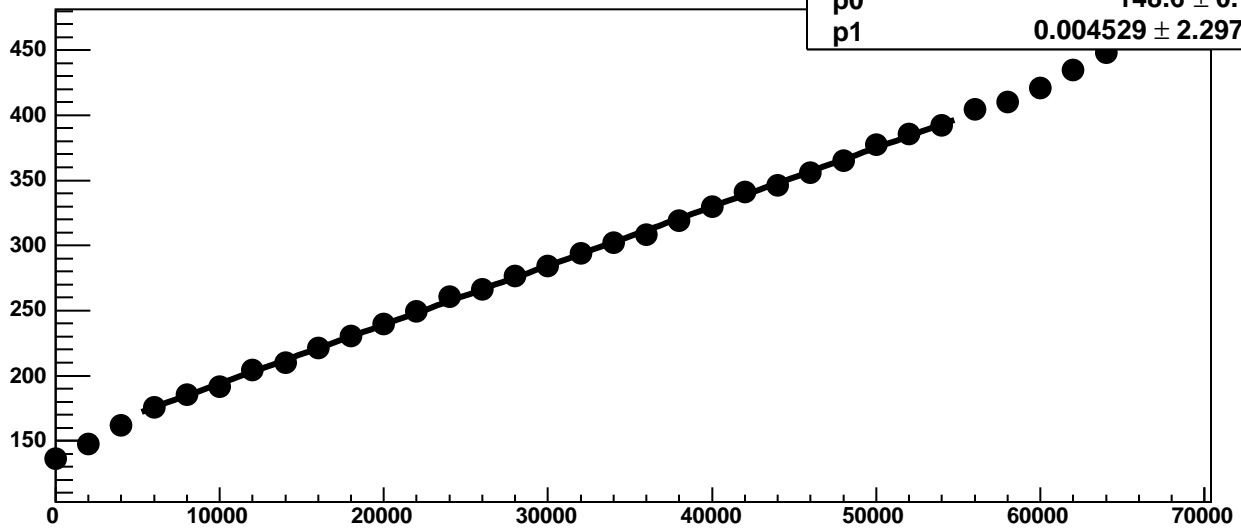
Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



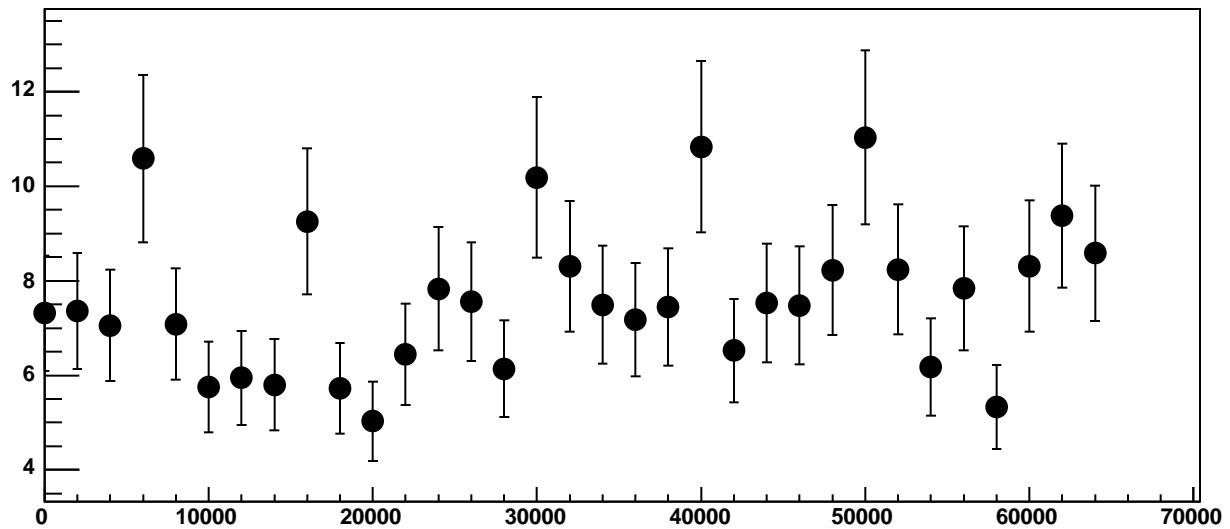
Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



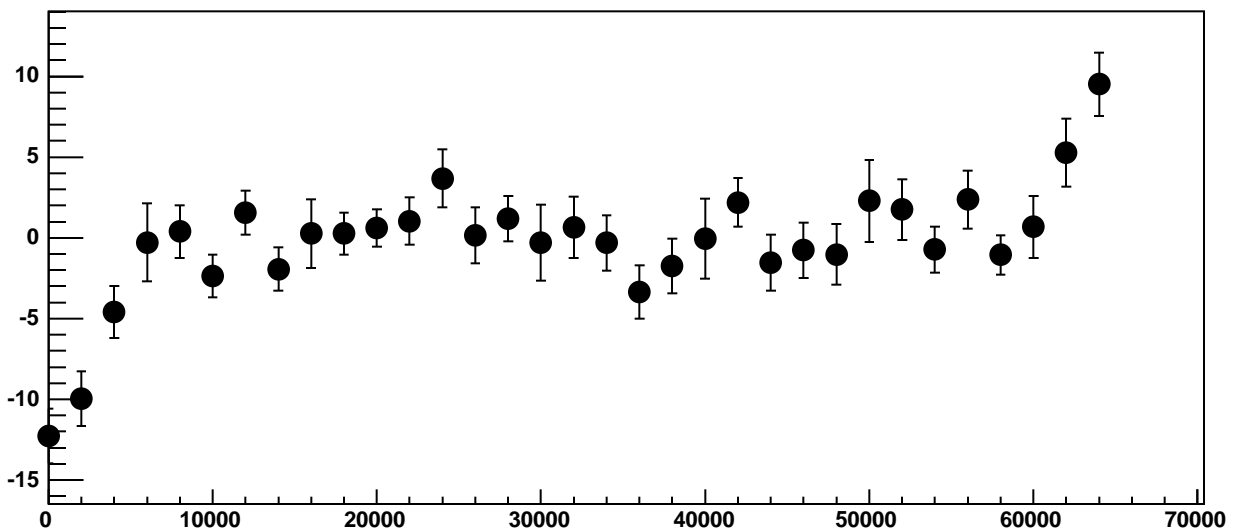
Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



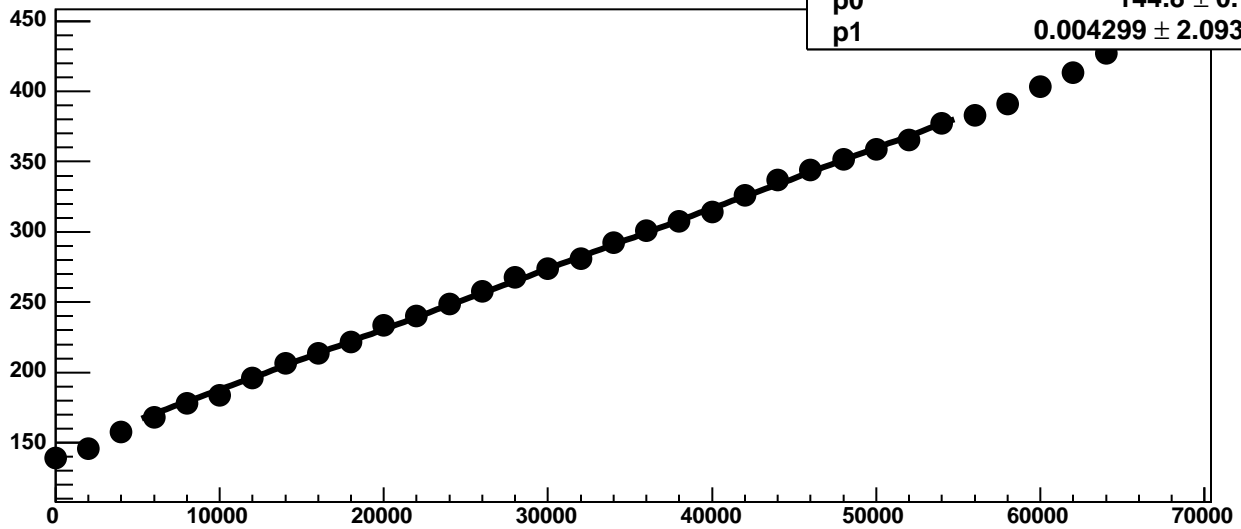
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

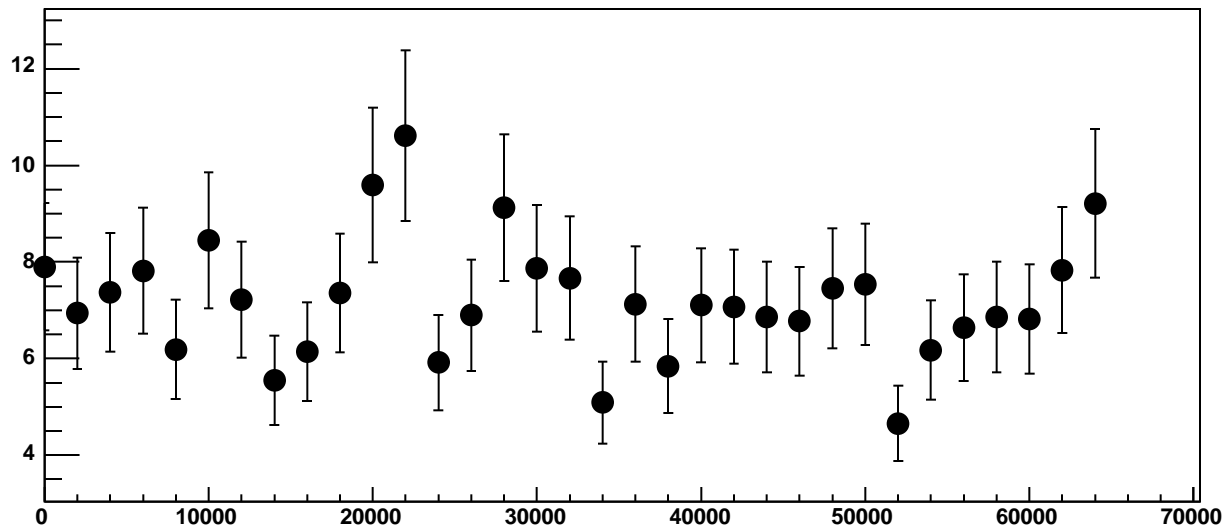


Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

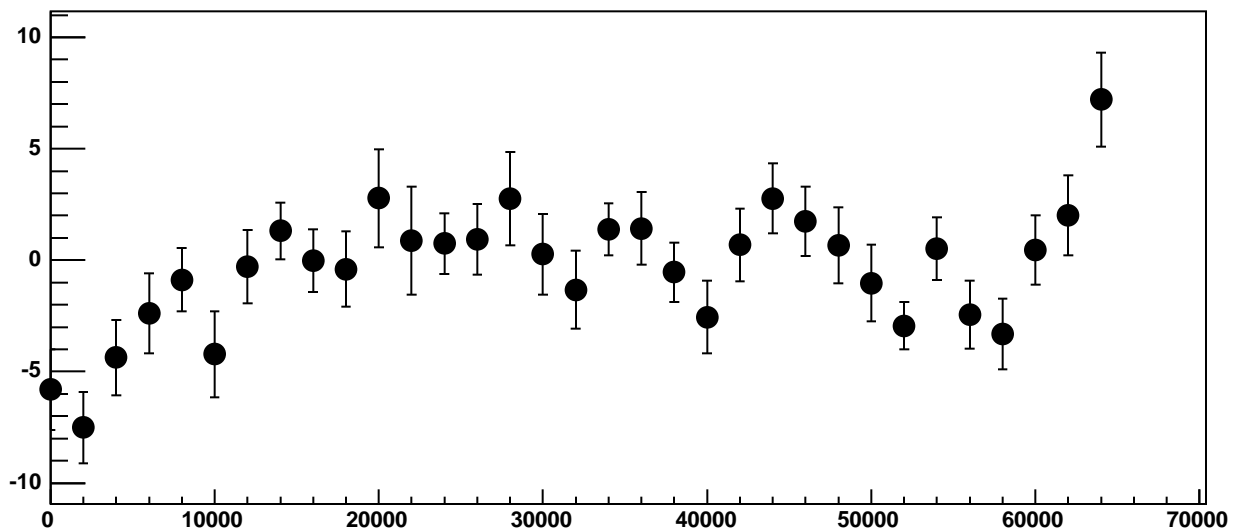


χ^2 / ndf 30.36 / 23
p0 144.8 ± 0.7273
p1 $0.004299 \pm 2.093e-05$

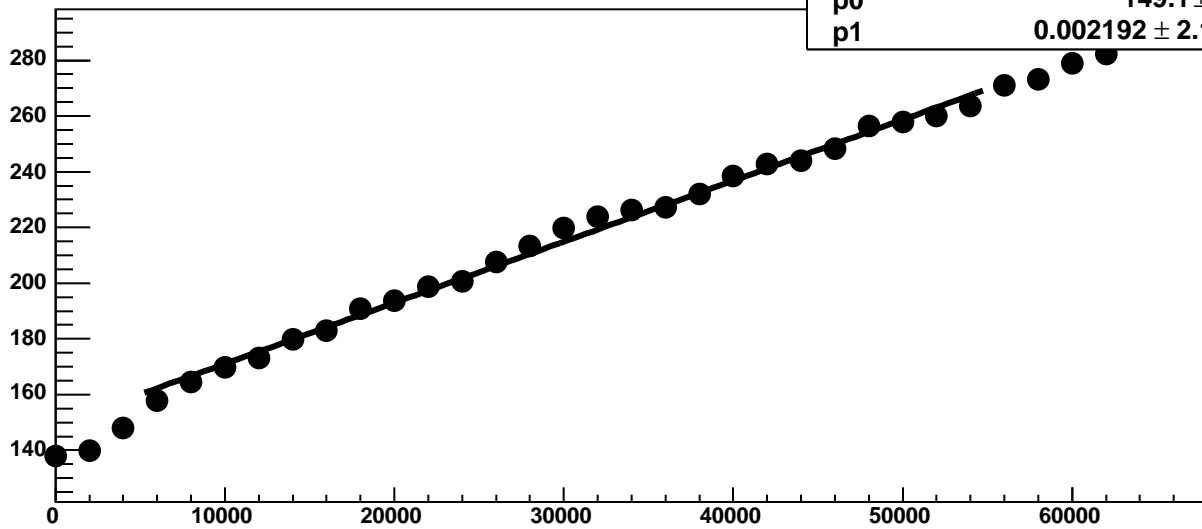
Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

62.98 / 23

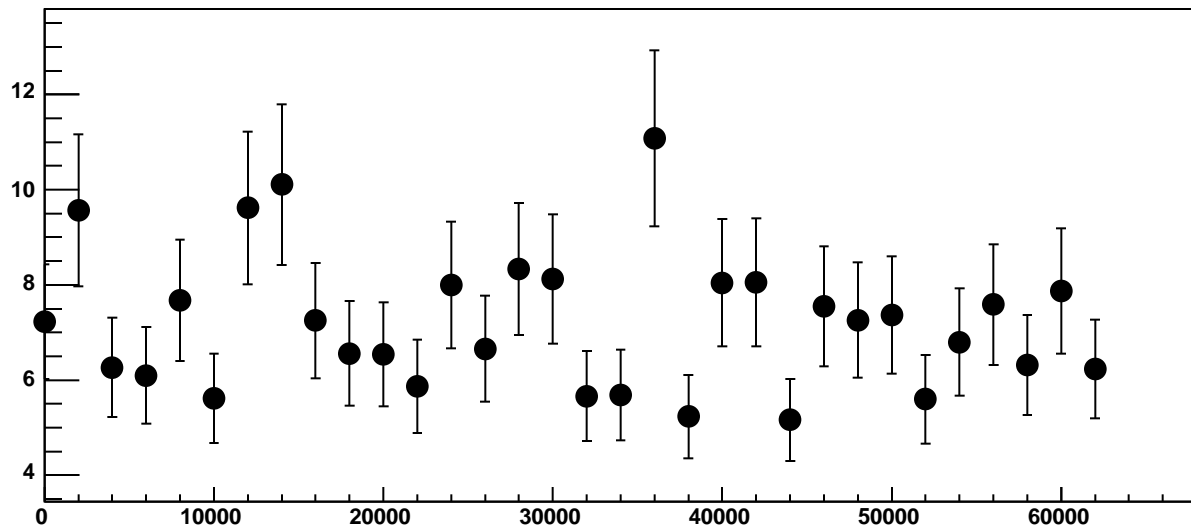
p0

149.1 ± 0.7278

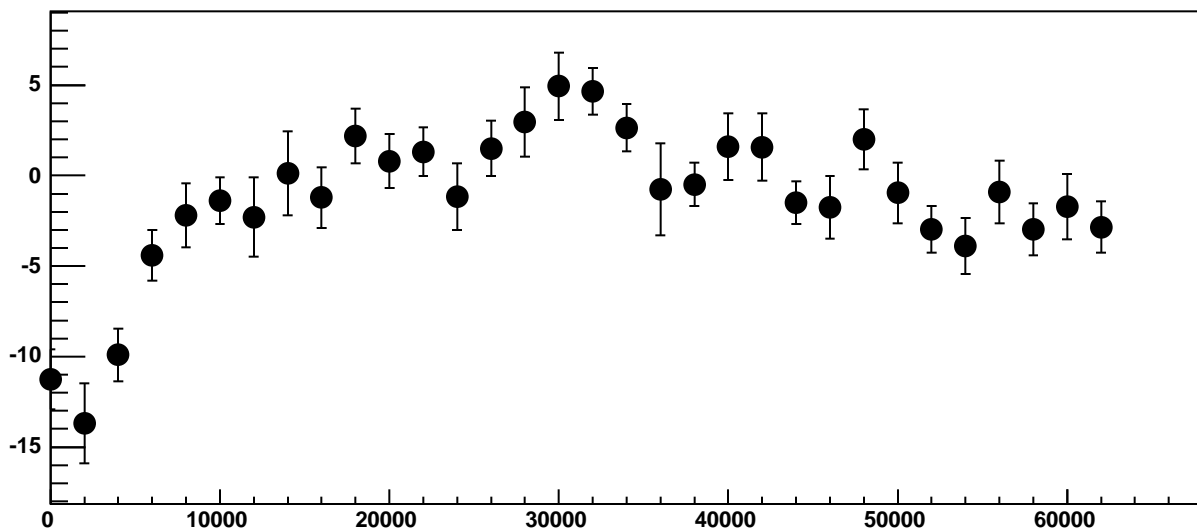
p1

$0.002192 \pm 2.143e-05$

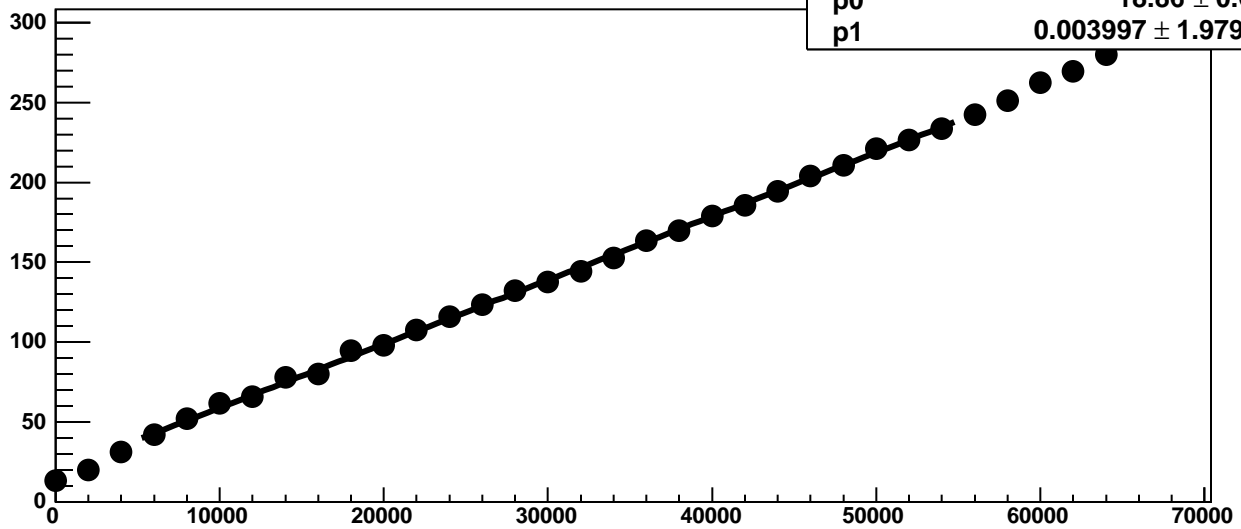
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



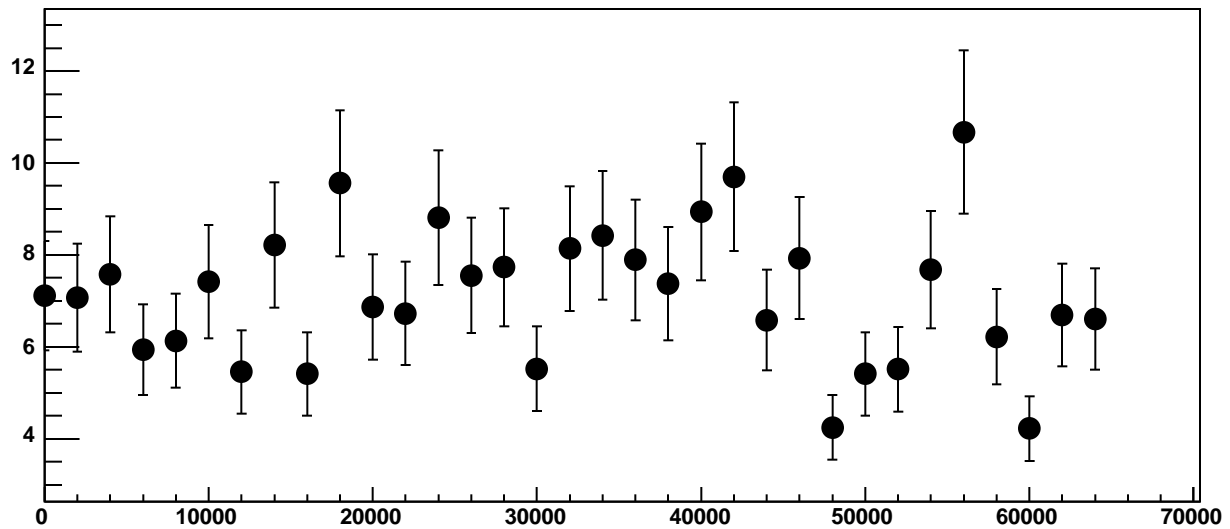
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



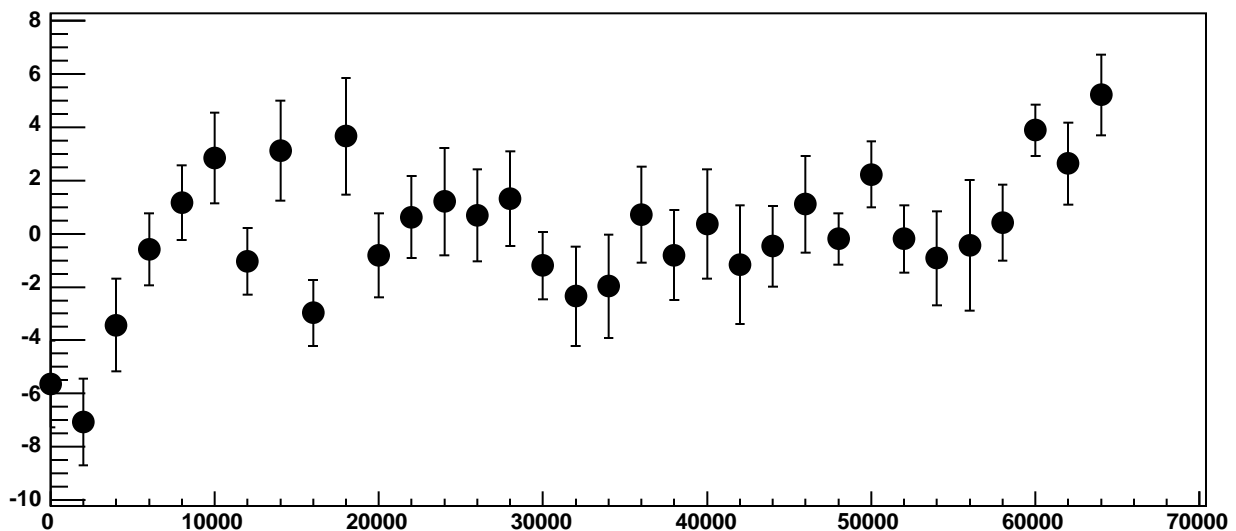
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



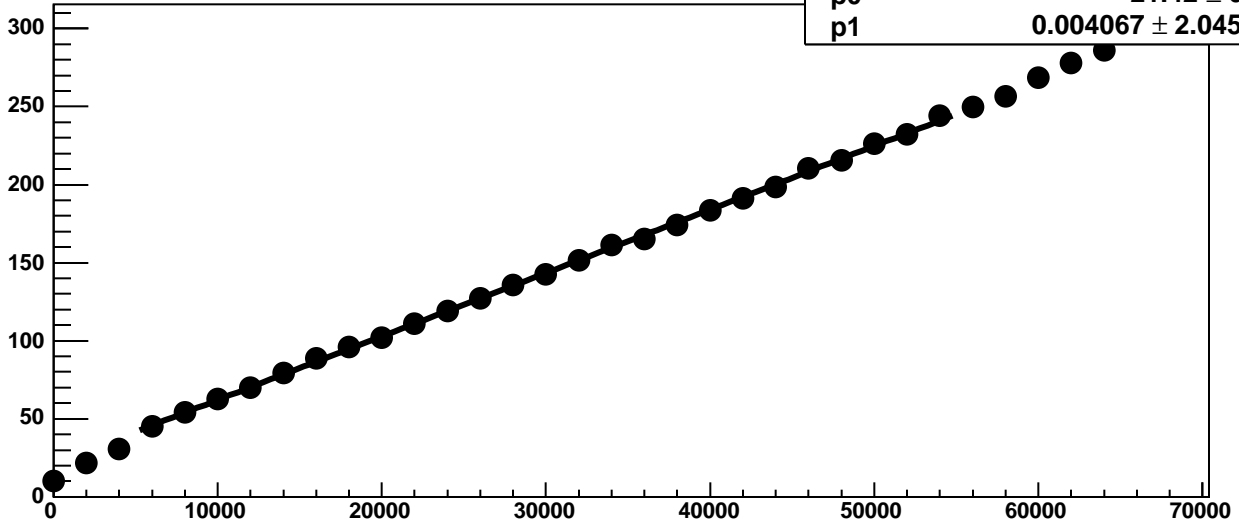
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

18.82 / 23

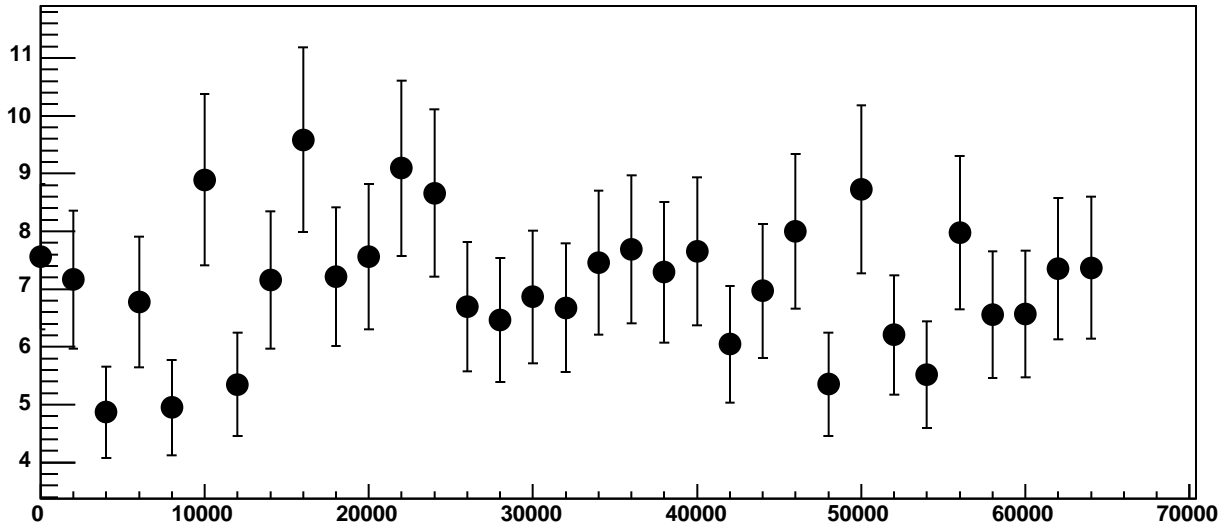
p0

21.42 ± 0.695

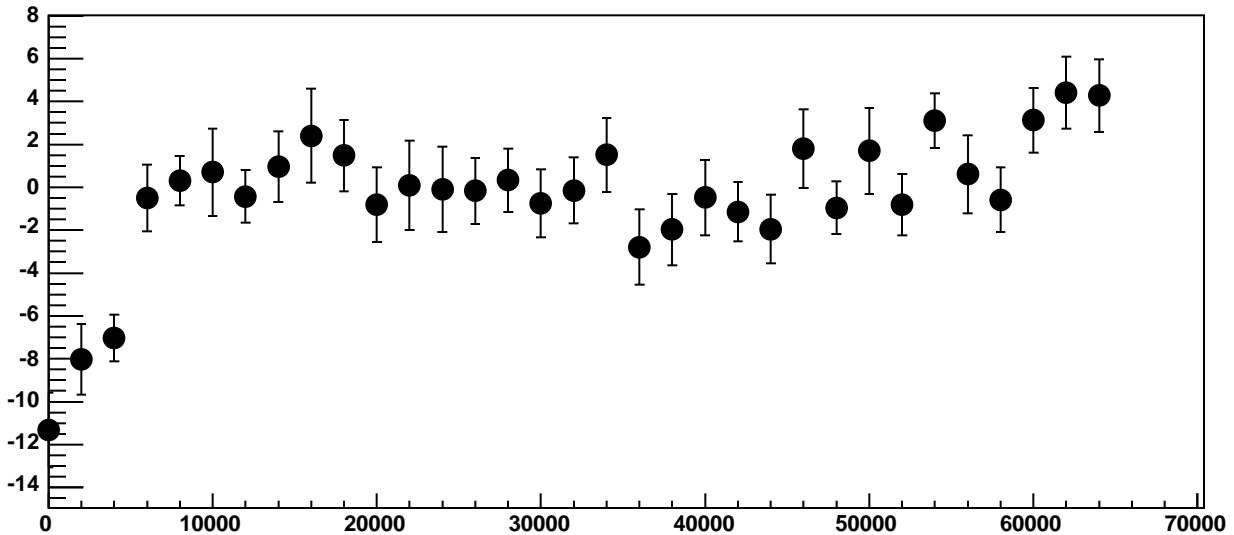
p1

$0.004067 \pm 2.045e-05$

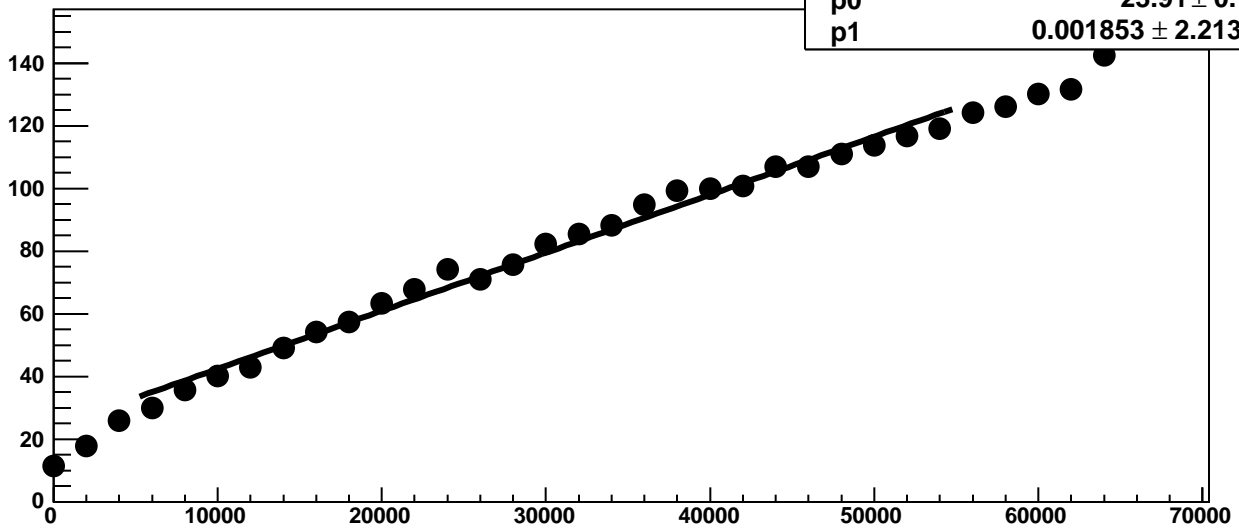
Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



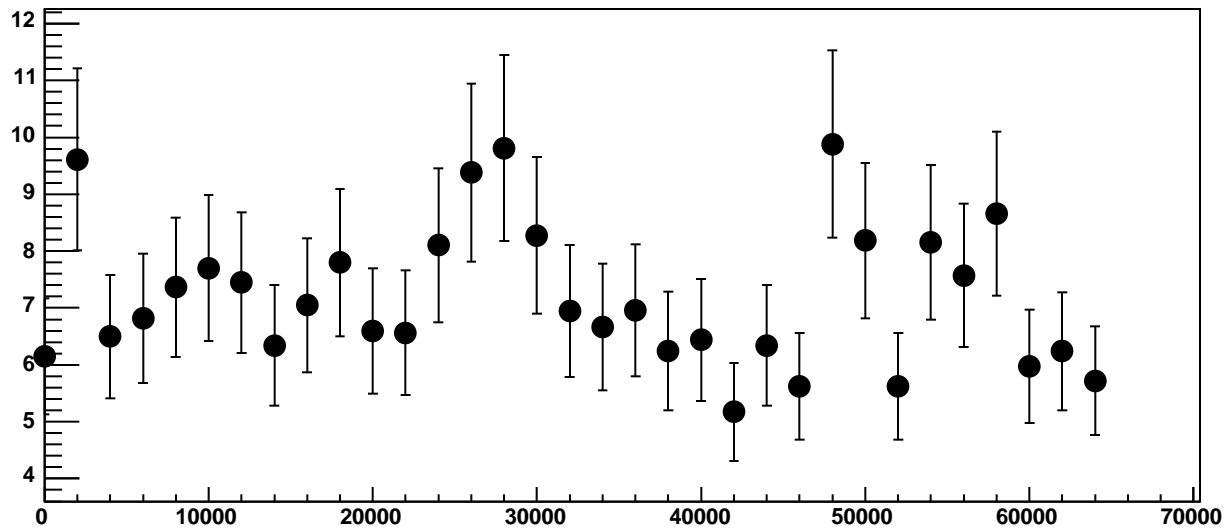
Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



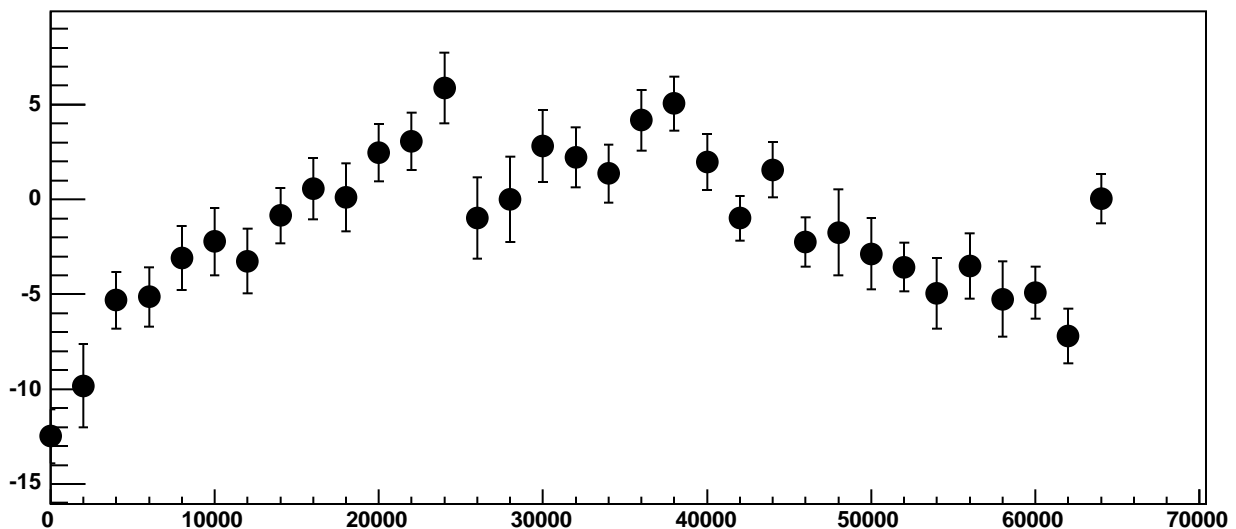
Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

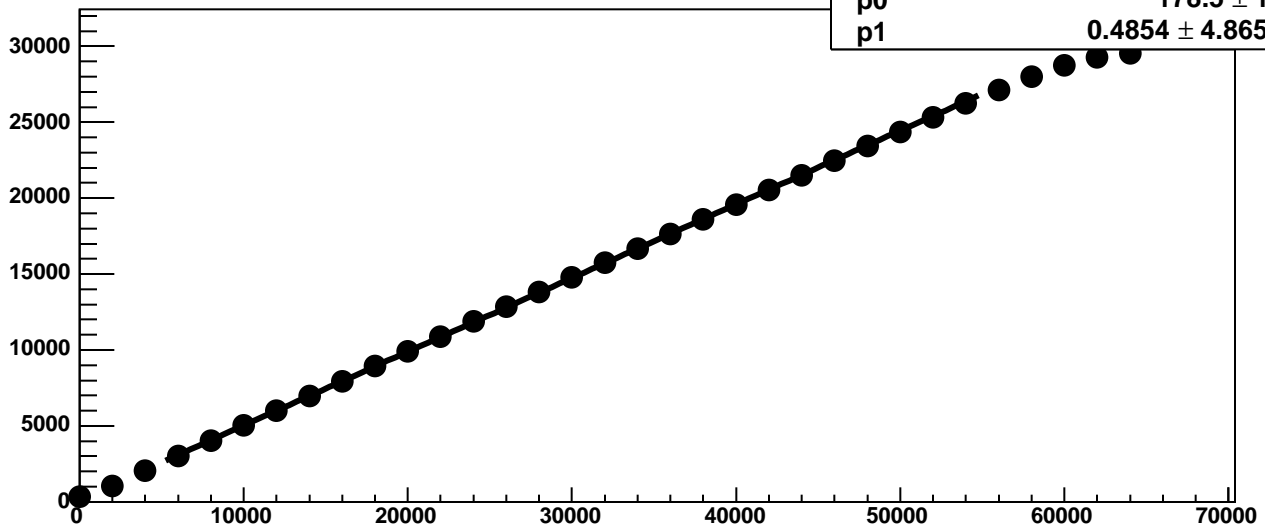


Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

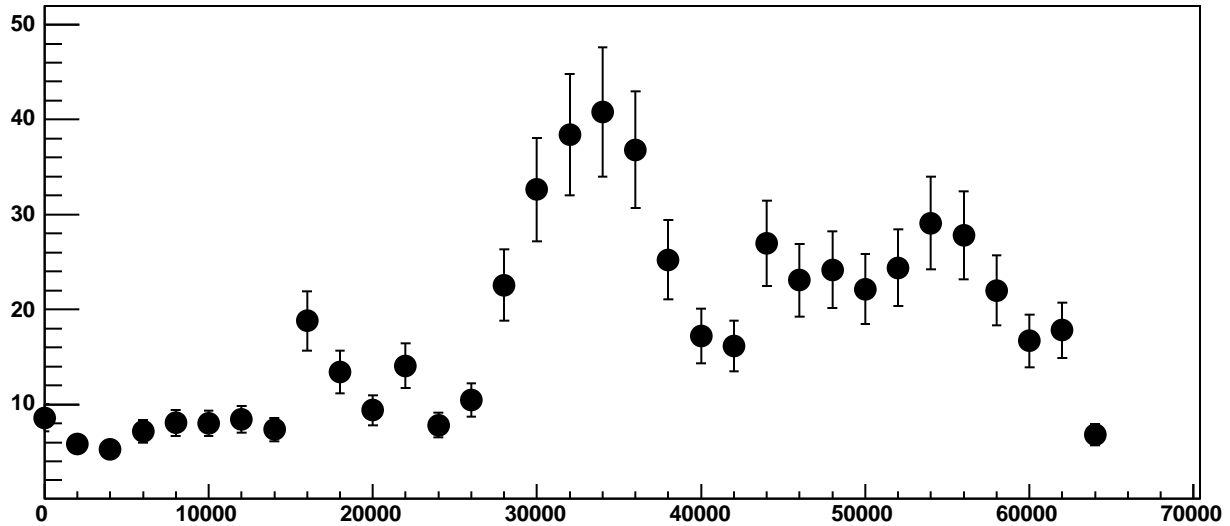


Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

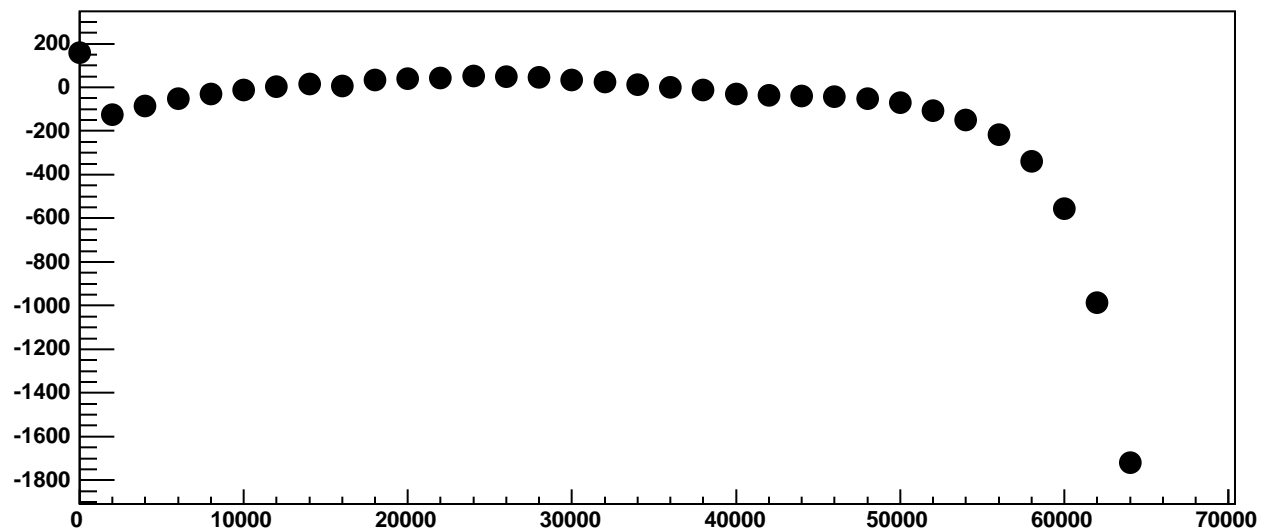
χ^2 / ndf 4855 / 23
p0 178.5 ± 1.077
p1 $0.4854 \pm 4.865e-05$



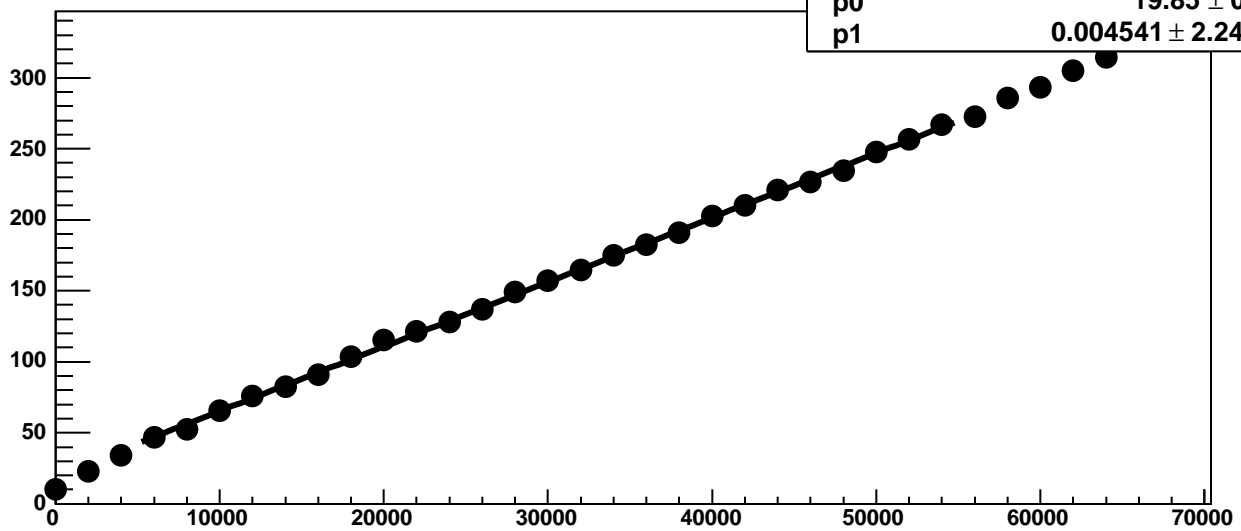
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf

30.08 / 23

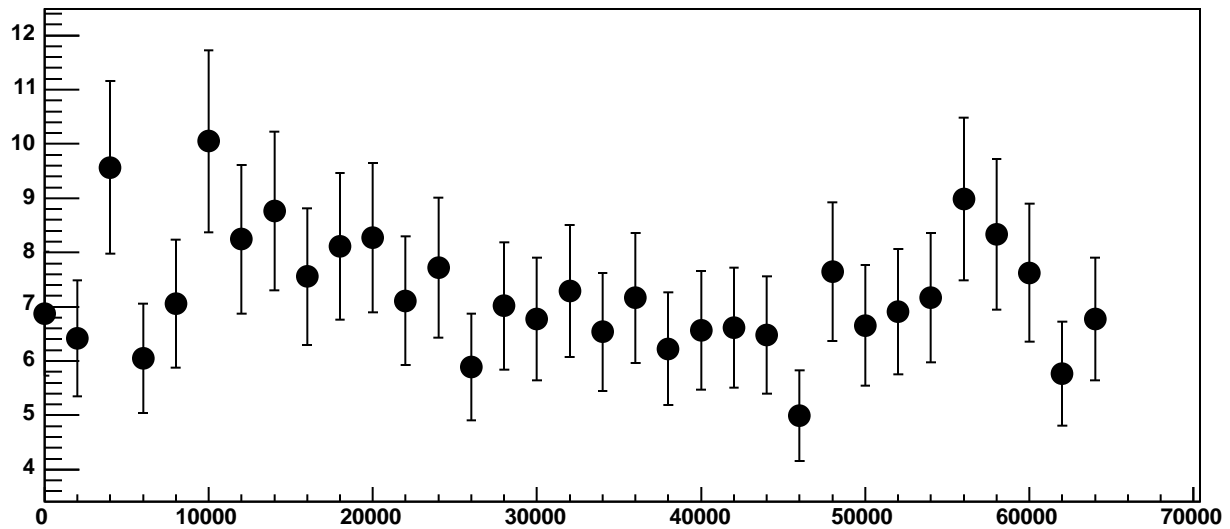
p0

19.85 ± 0.778

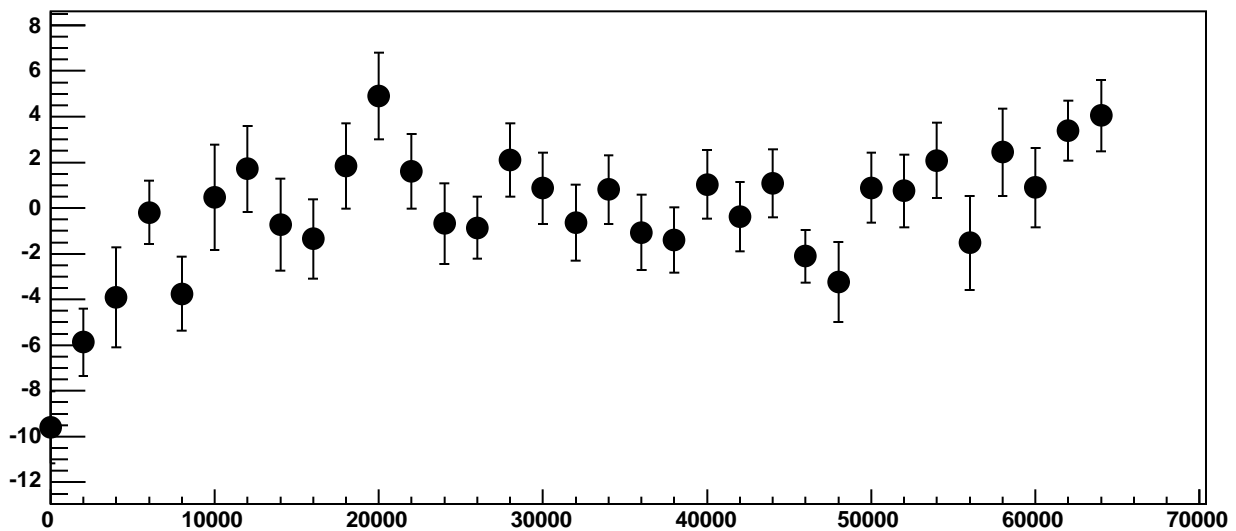
p1

$0.004541 \pm 2.24e-05$

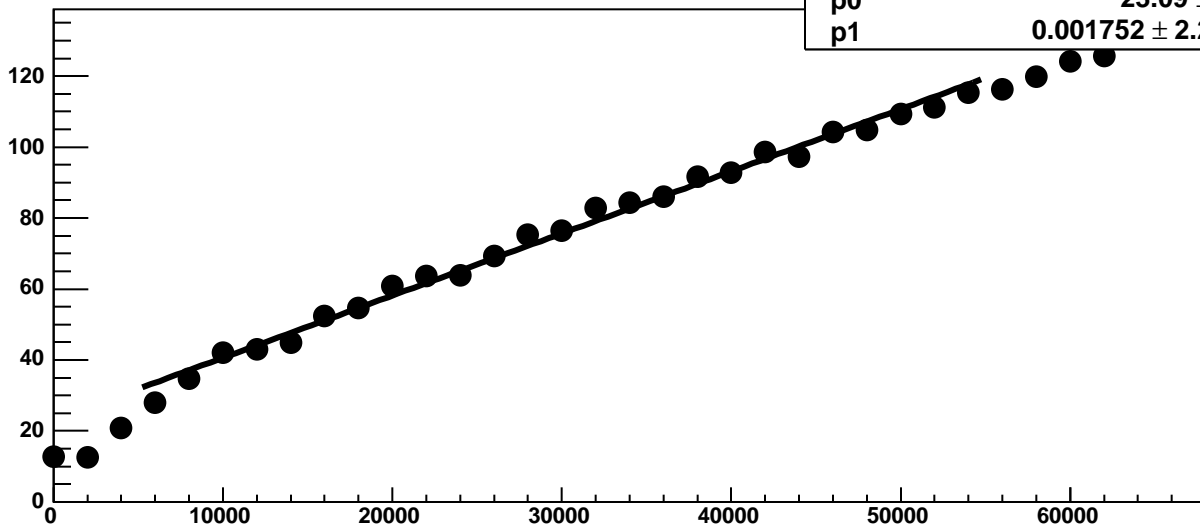
Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



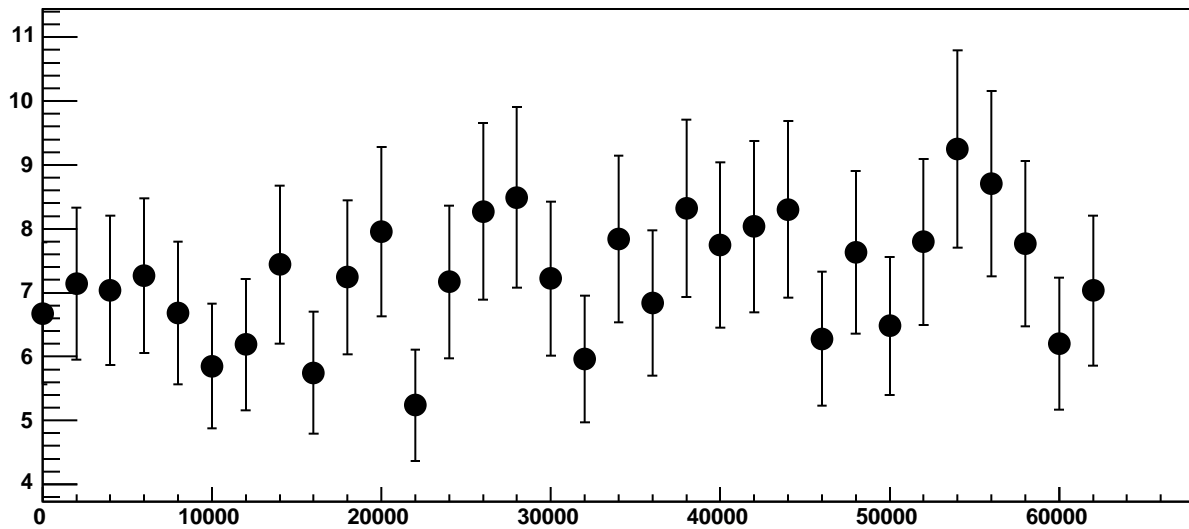
Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



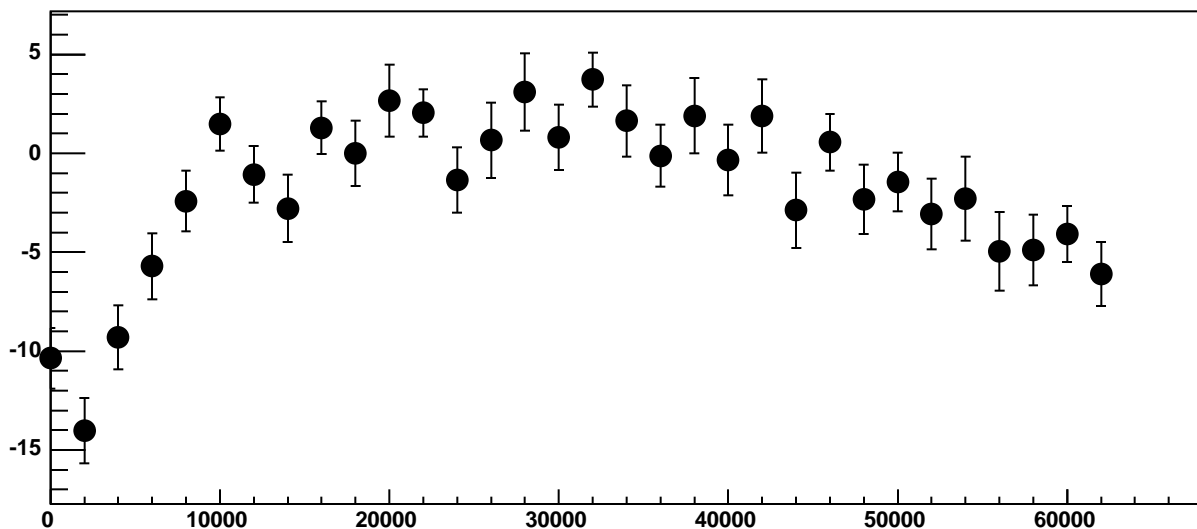
Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



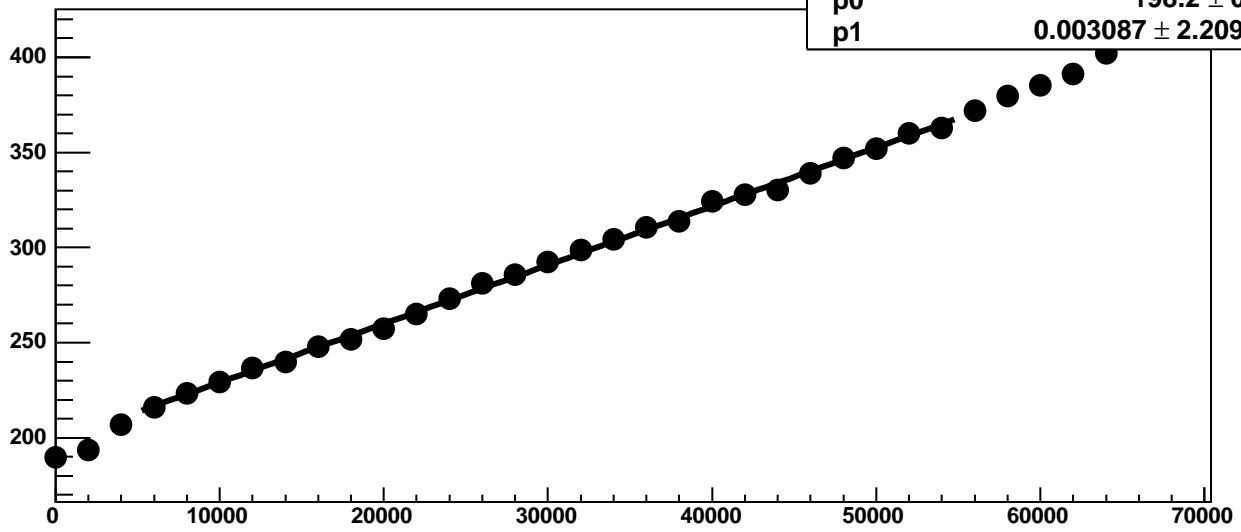
Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.74 / 23

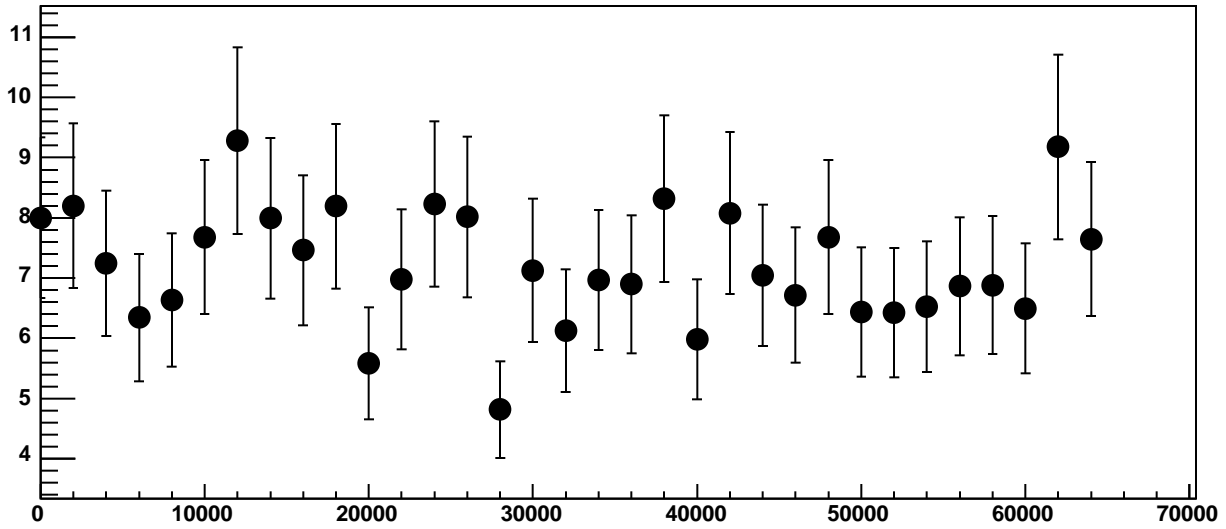
p0

198.2 ± 0.746

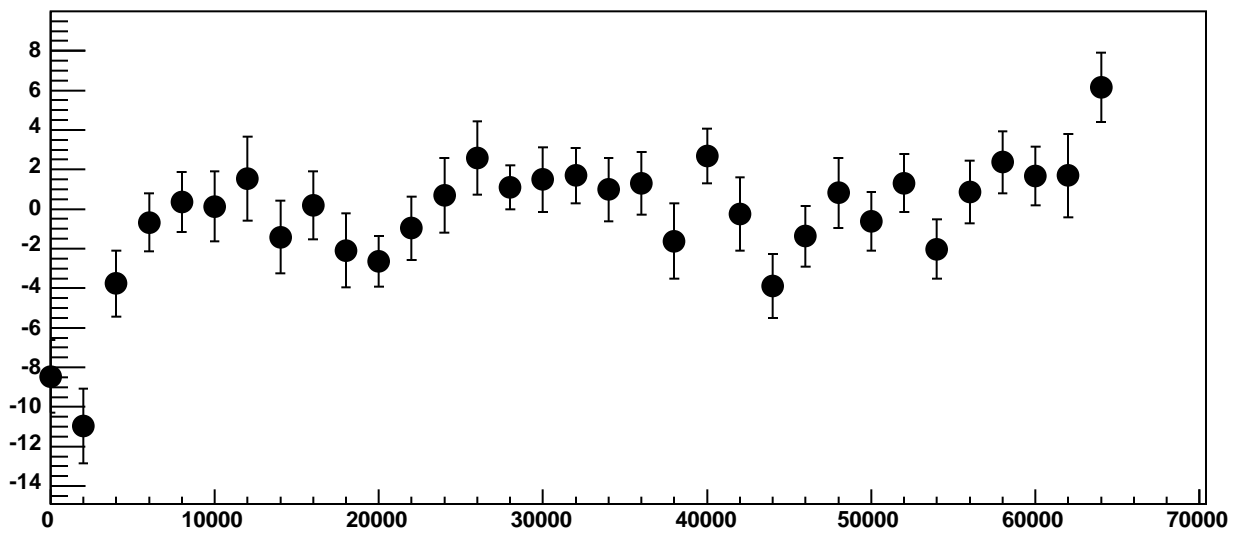
p1

$0.003087 \pm 2.209\text{e-}05$

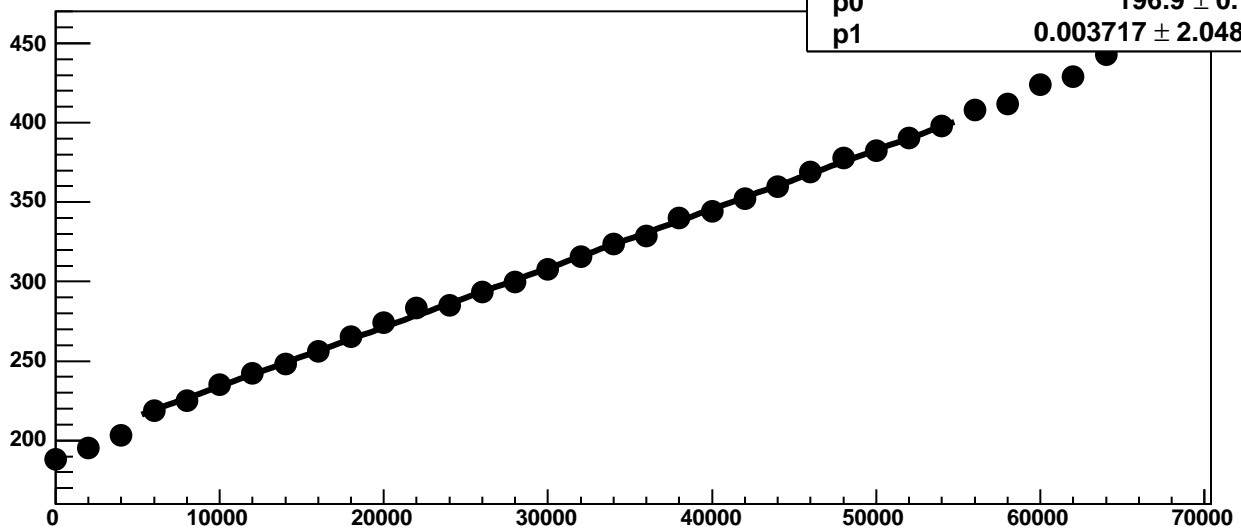
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

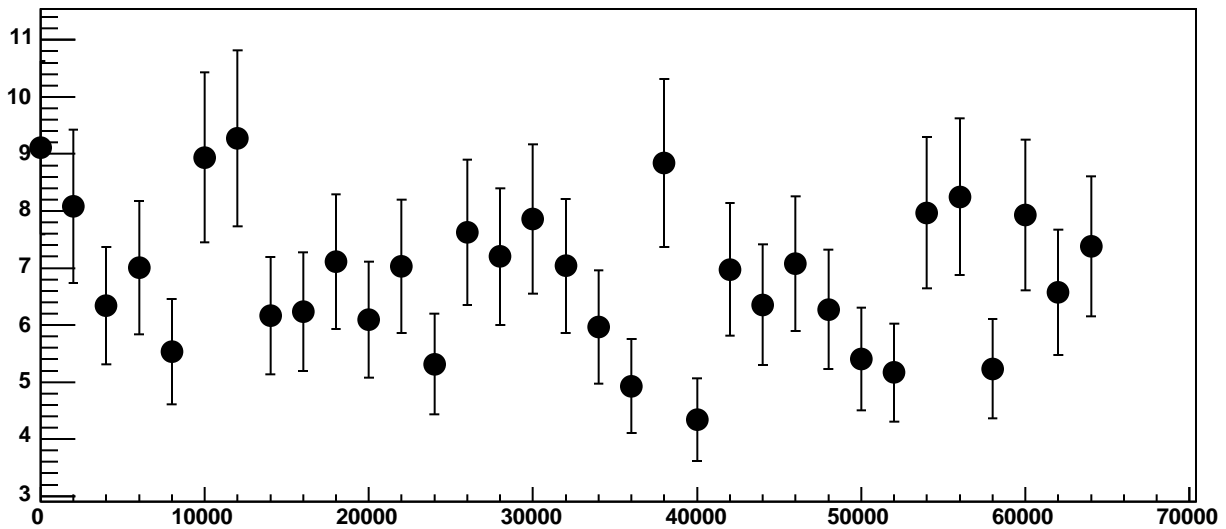


Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

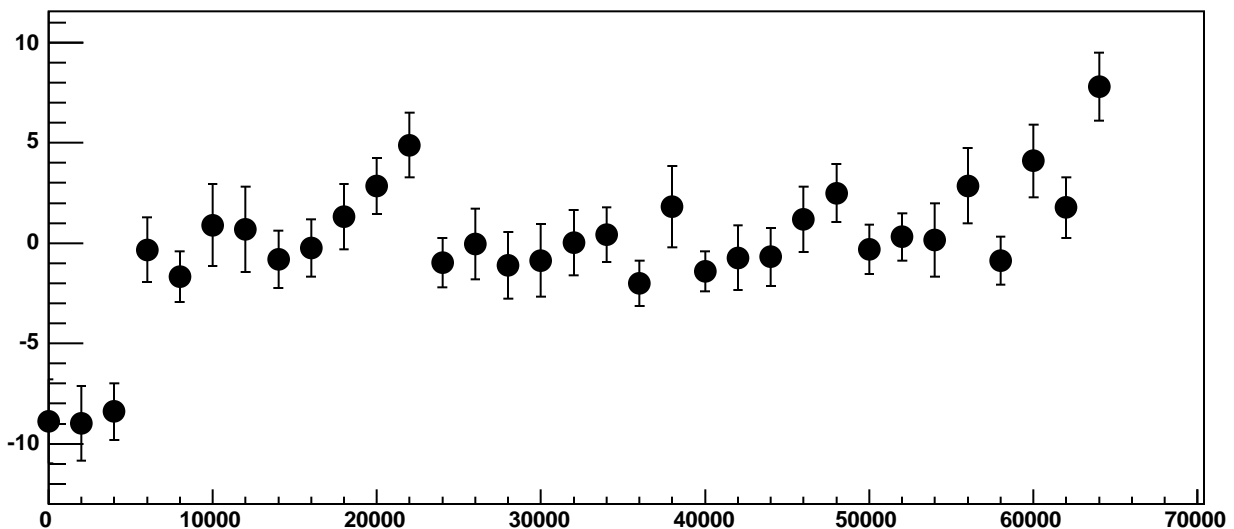


χ^2 / ndf 27.8 / 23
p0 196.9 ± 0.7058
p1 $0.003717 \pm 2.048e-05$

Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

χ^2 / ndf

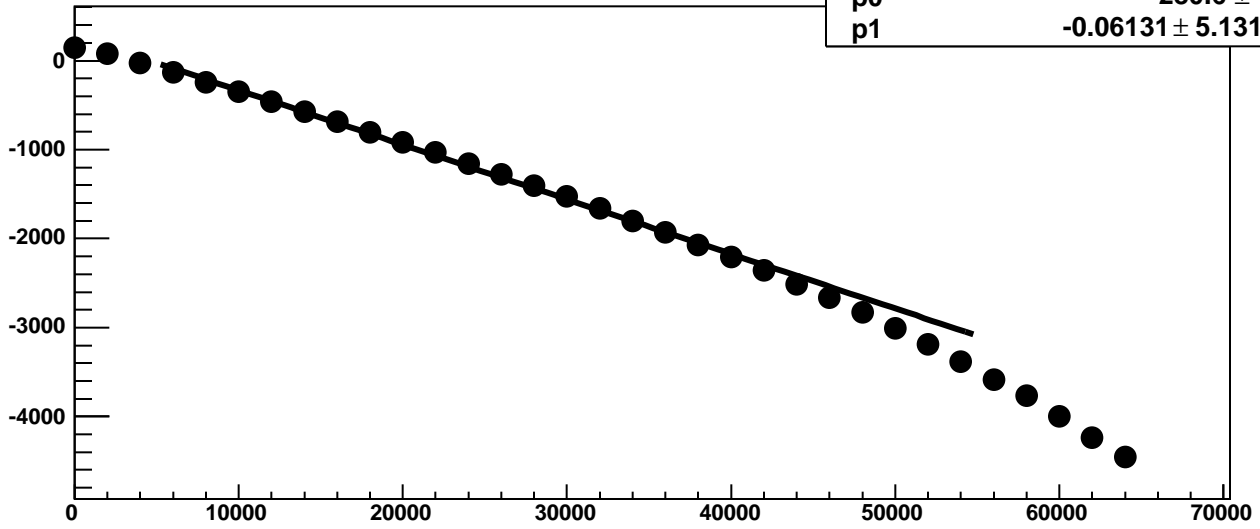
5881 / 23

p0

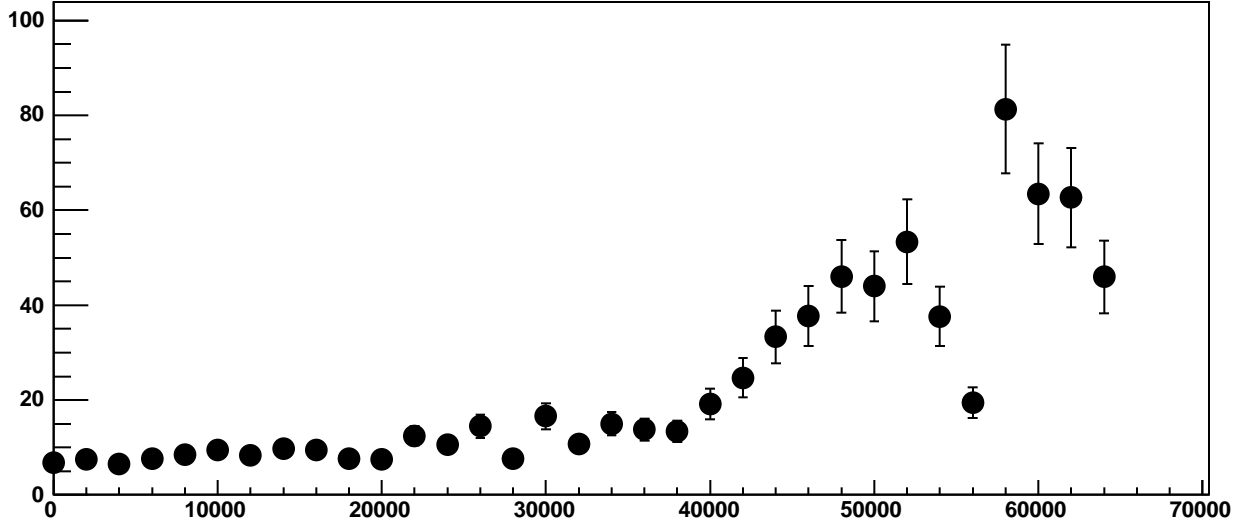
280.6 ± 1.16

p1

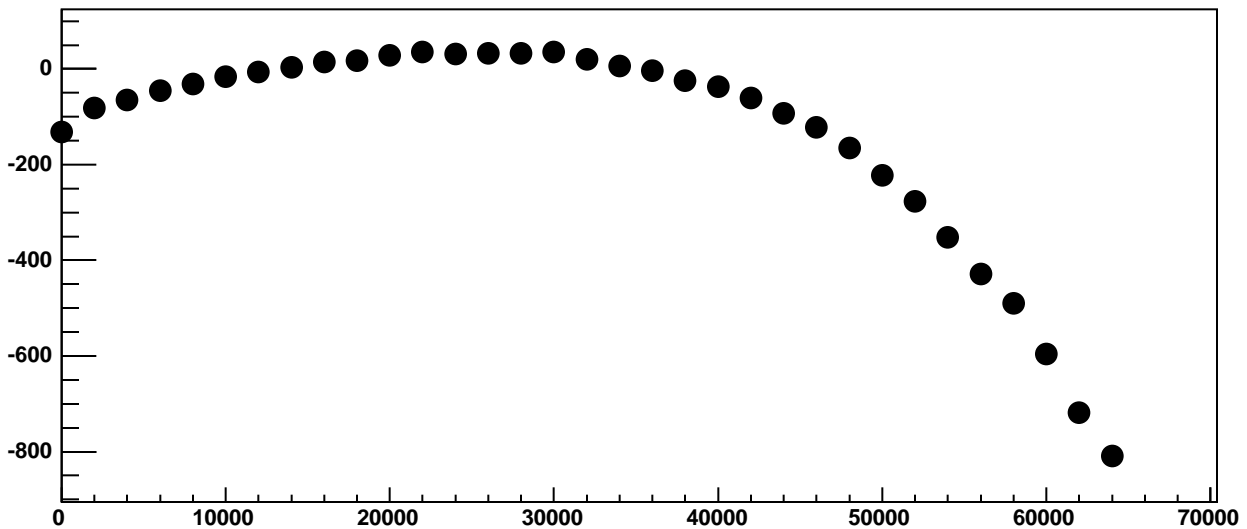
$-0.06131 \pm 5.131\text{e-}05$



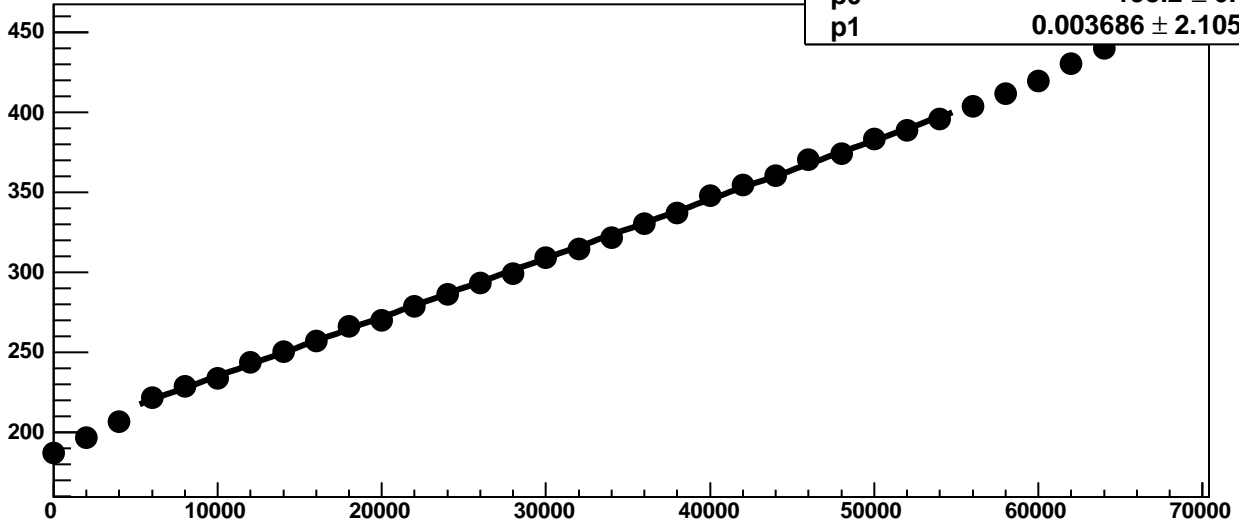
Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

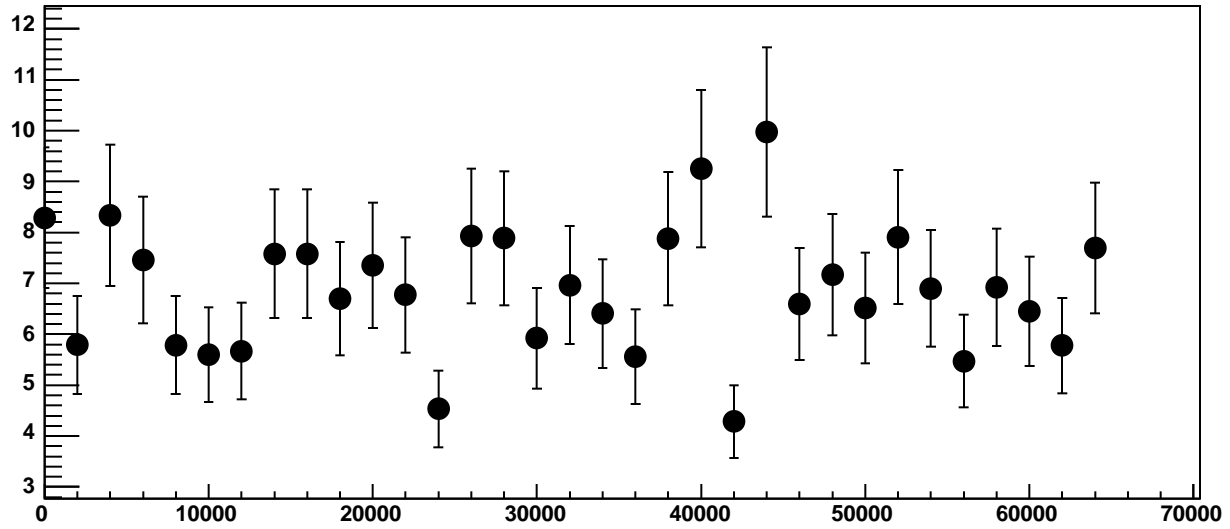


Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

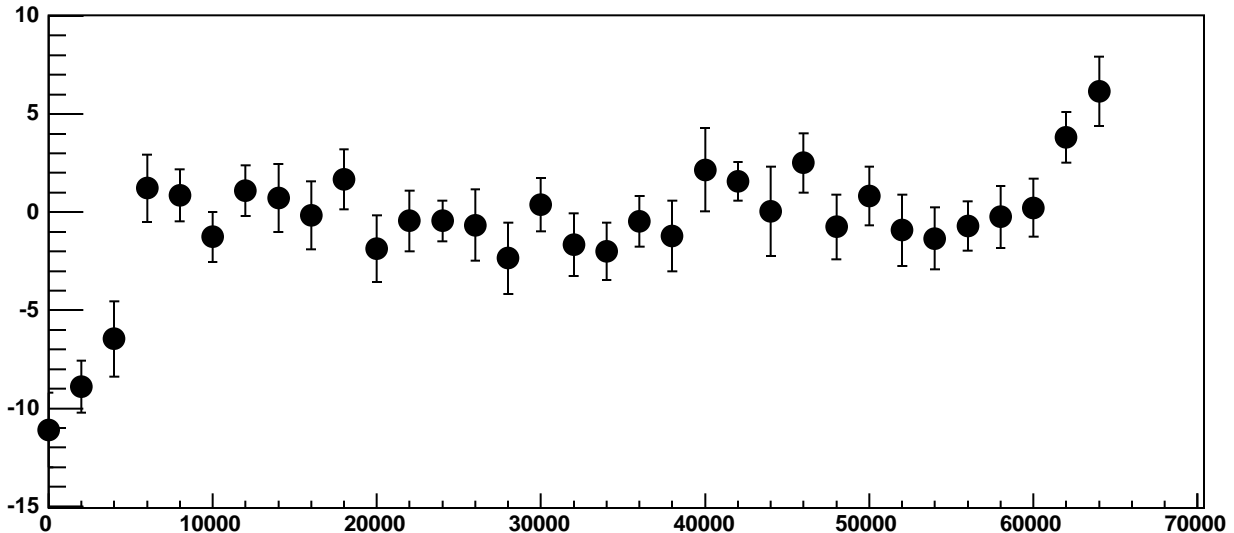


χ^2 / ndf 18.69 / 23
p0 198.2 ± 0.6854
p1 $0.003686 \pm 2.105e-05$

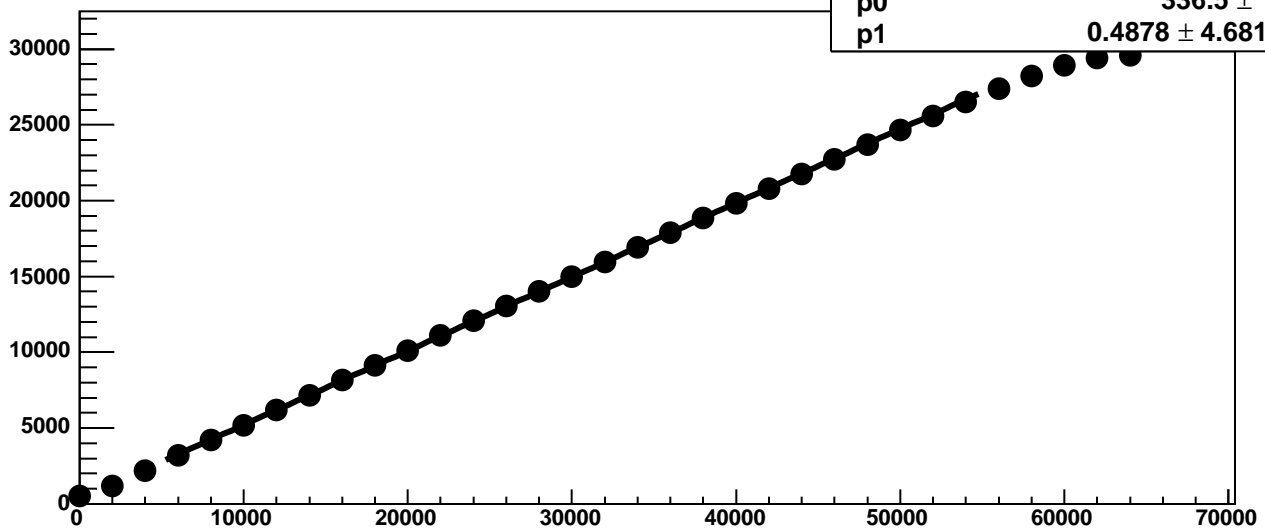
Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



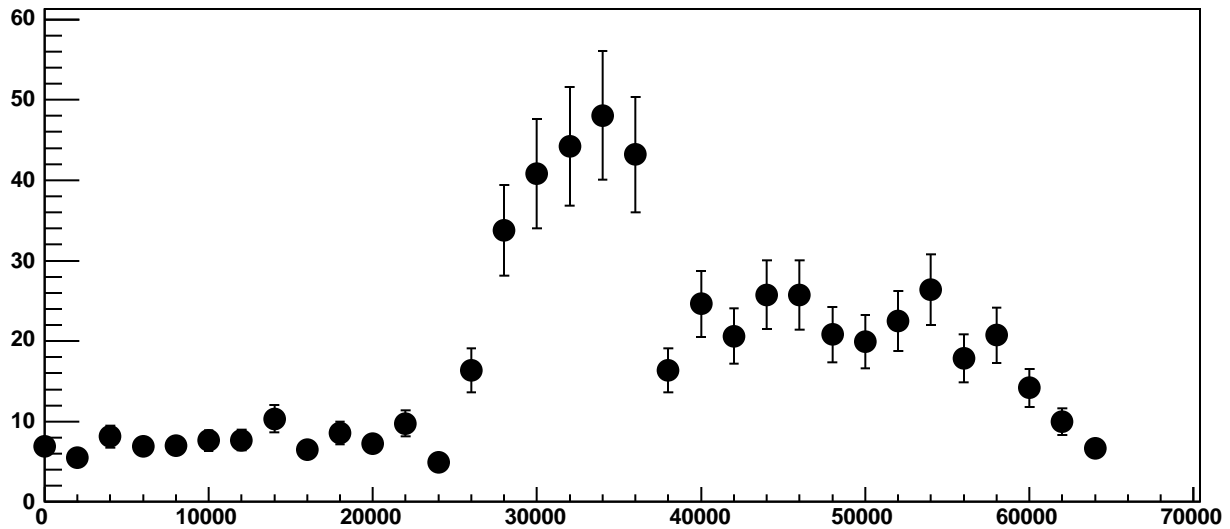
Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



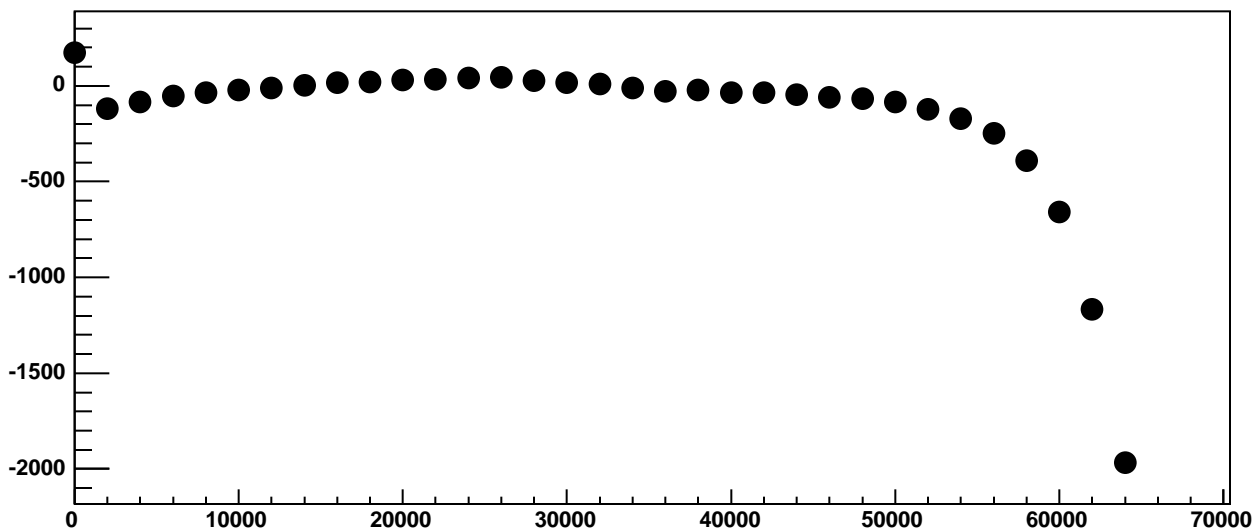
Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



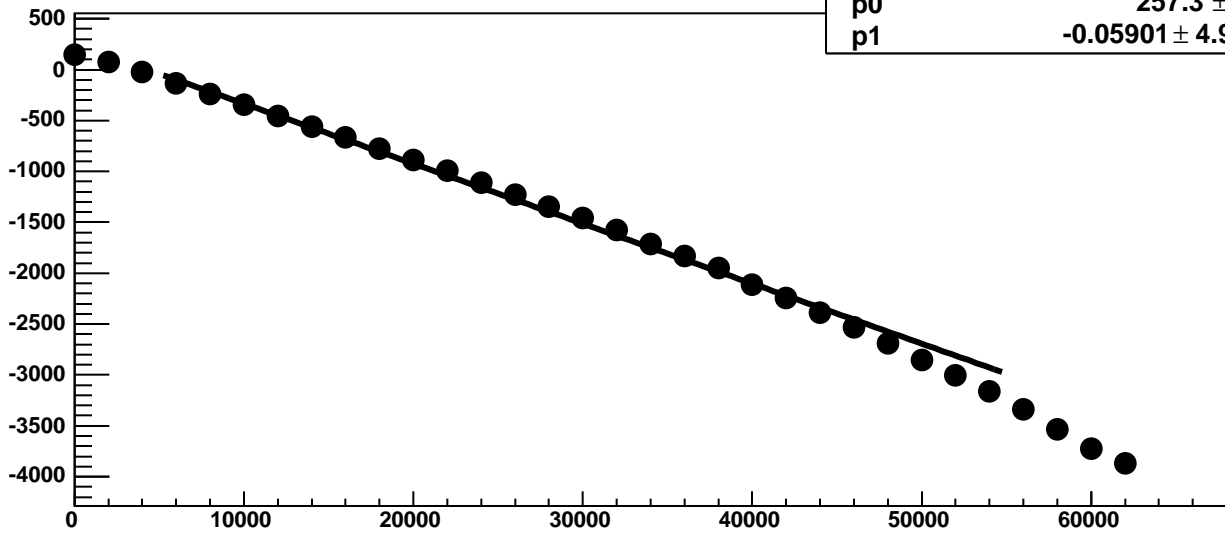
Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

5664 / 23

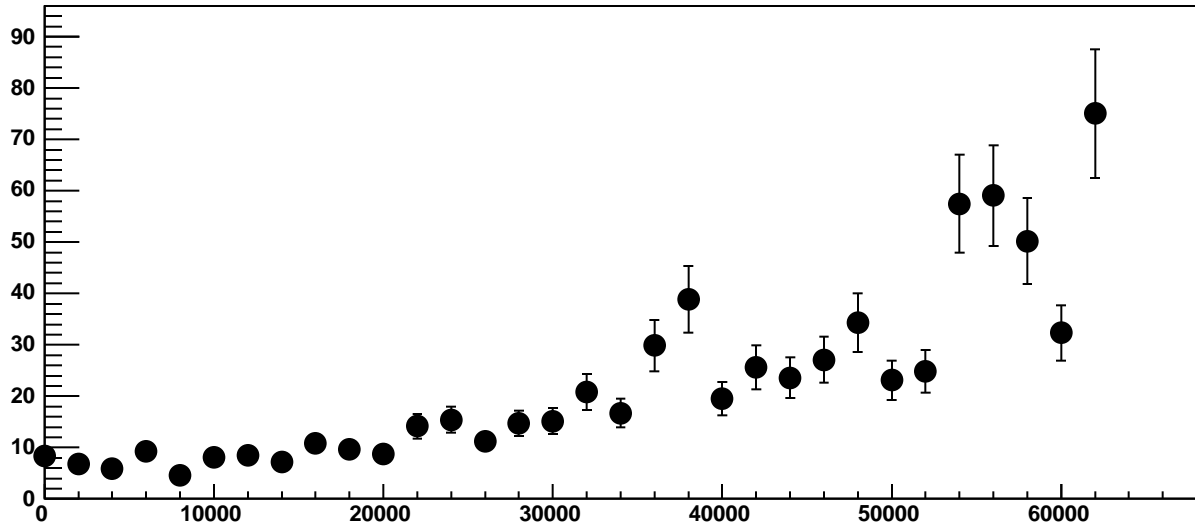
p0

257.3 ± 0.9983

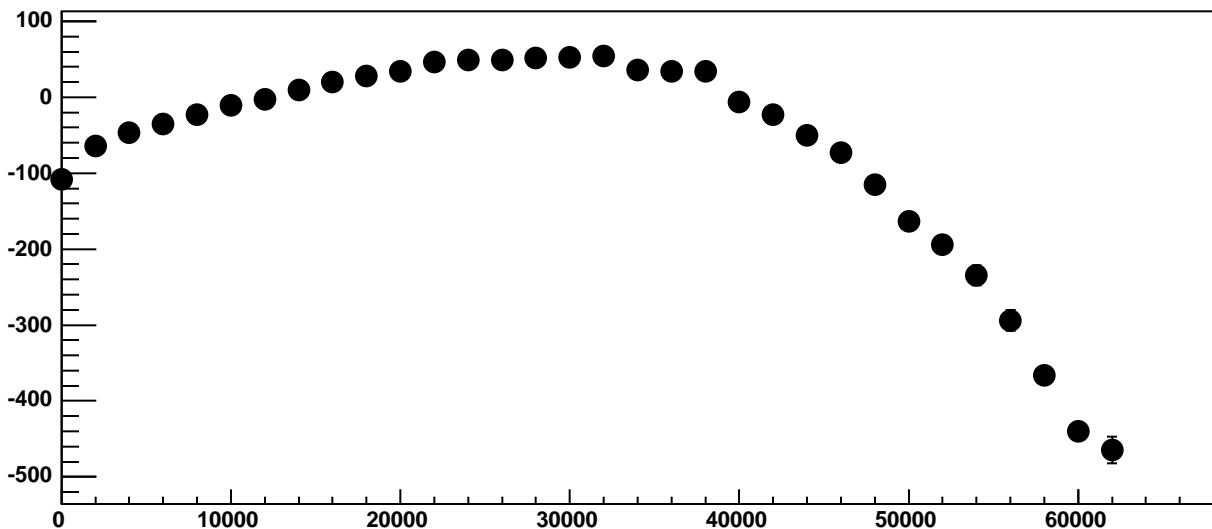
p1

$-0.05901 \pm 4.998e-05$

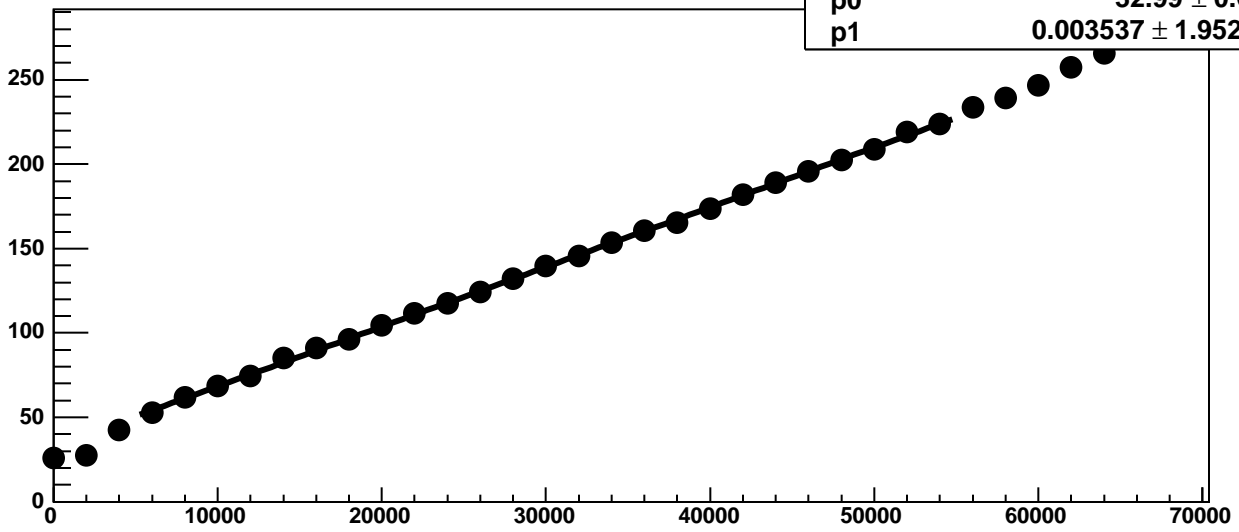
Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

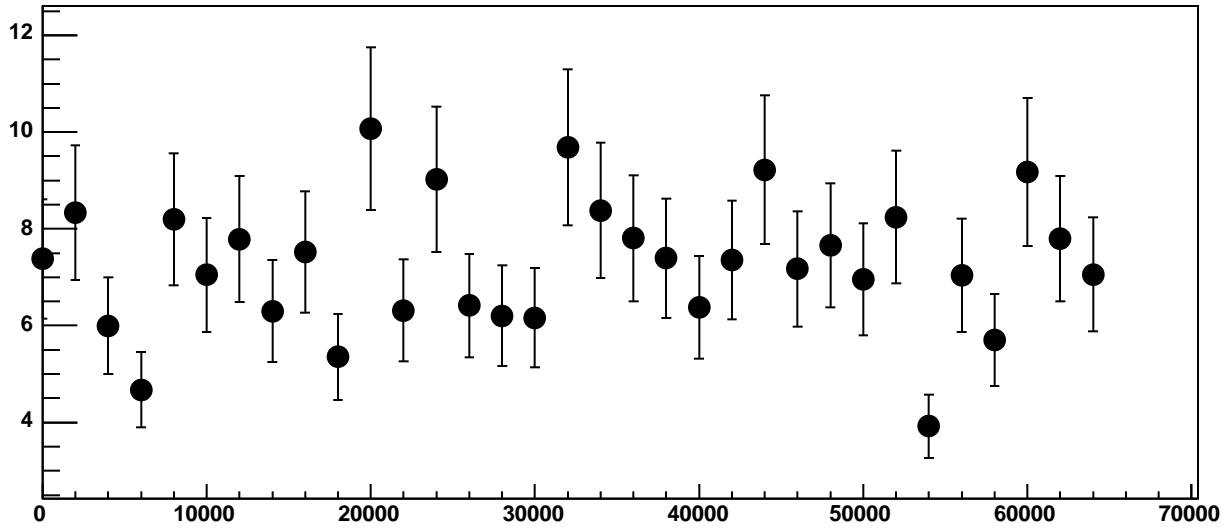


Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

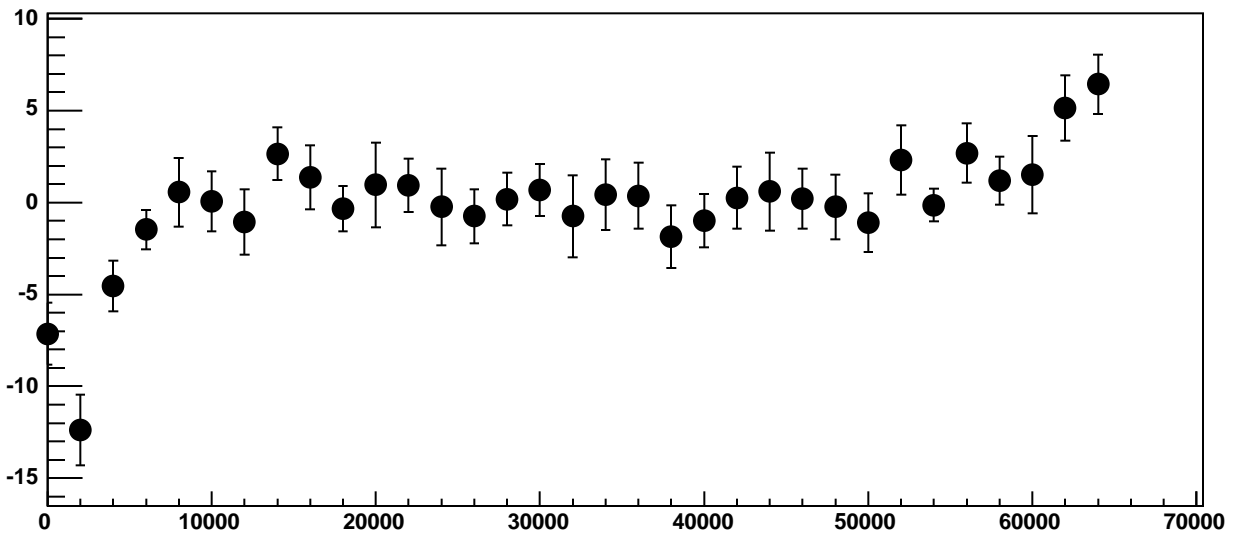


χ^2 / ndf 11.5 / 23
p0 32.99 ± 0.6672
p1 $0.003537 \pm 1.952e-05$

Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

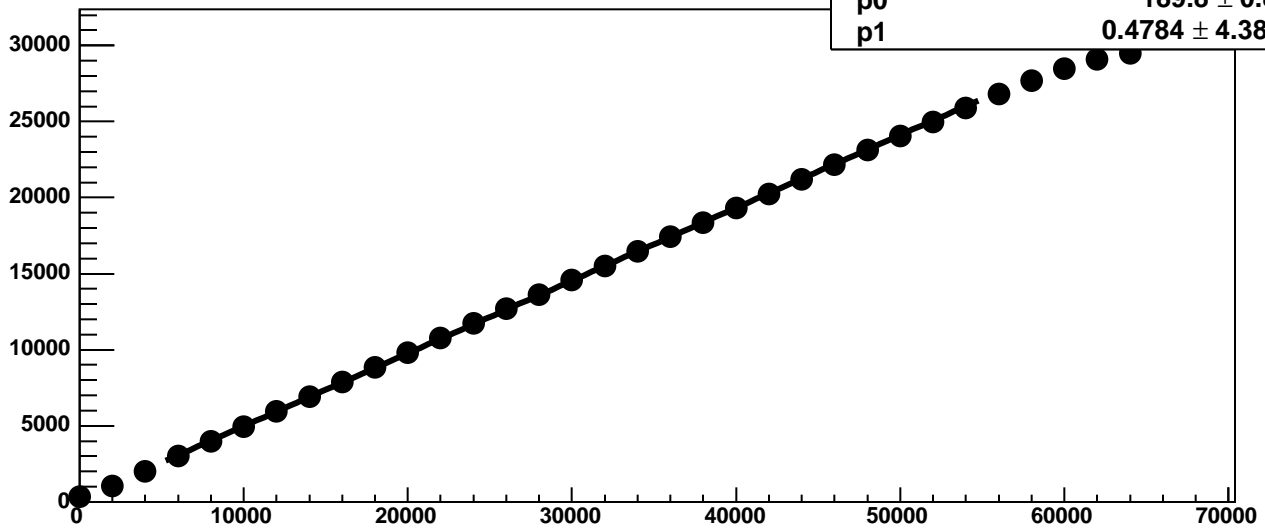


Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

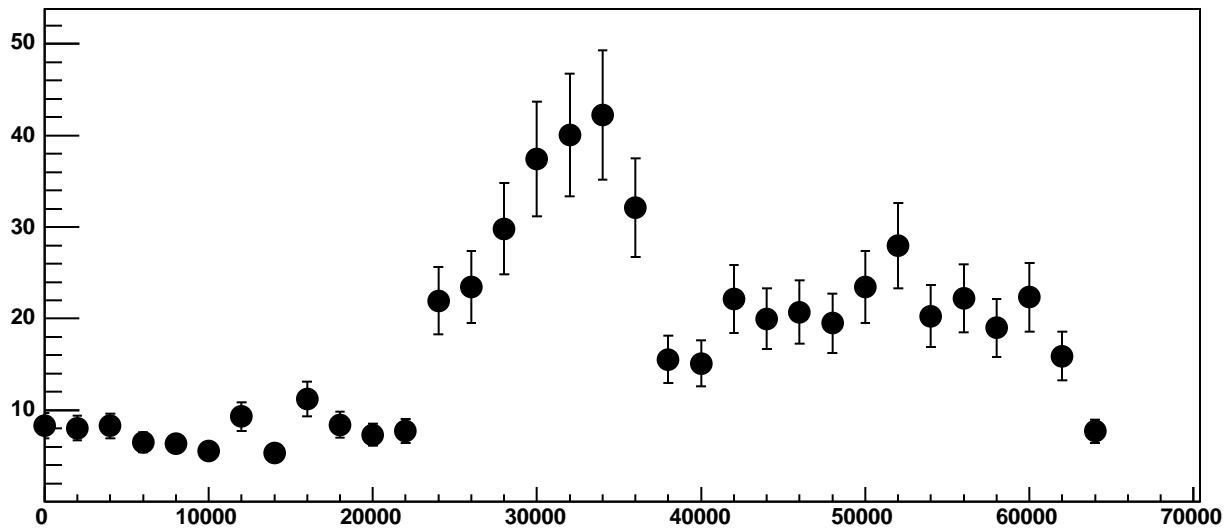


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

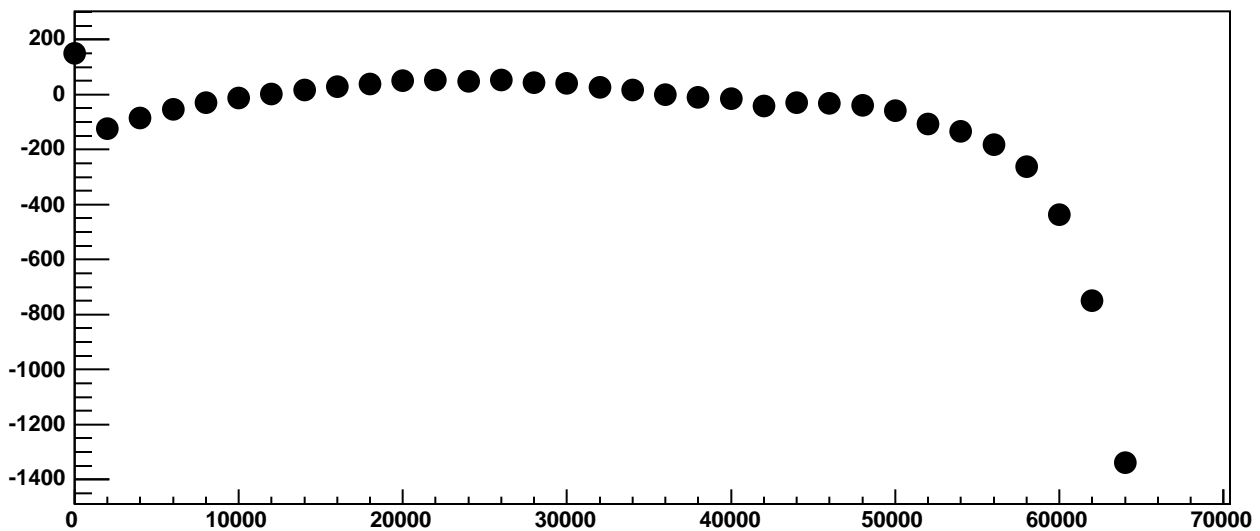
χ^2 / ndf 6002 / 23
p0 189.8 ± 0.8963
p1 $0.4784 \pm 4.38e-05$



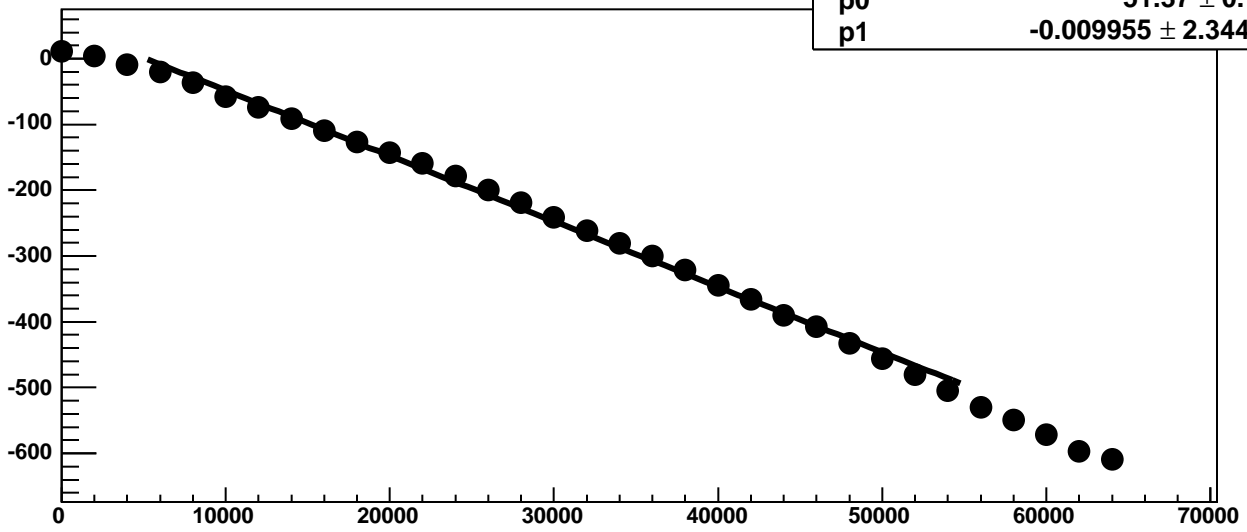
Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



χ^2 / ndf

566.1 / 23

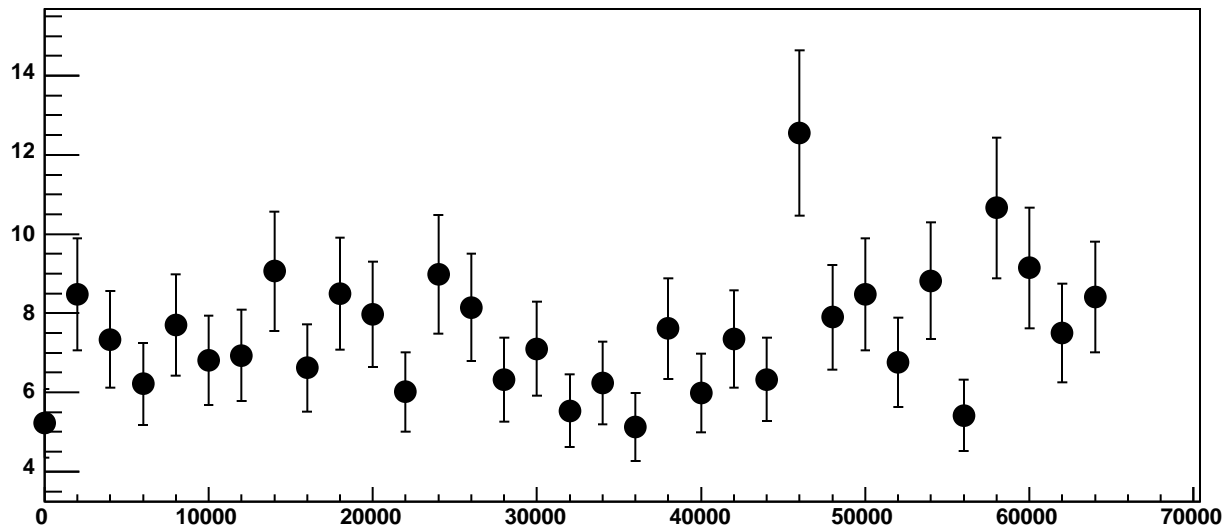
p0

51.37 ± 0.7654

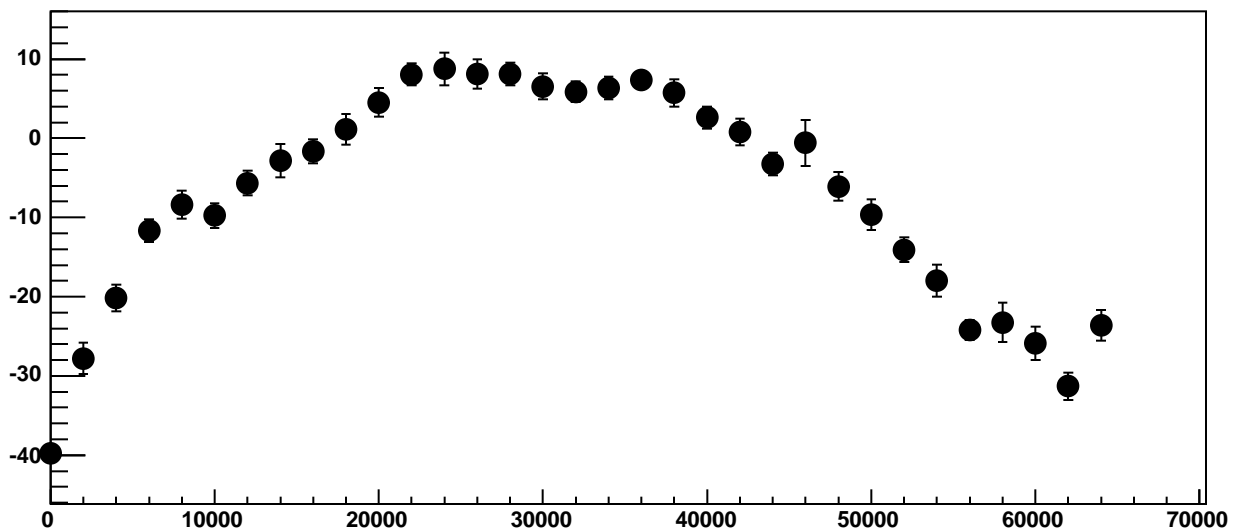
p1

$-0.009955 \pm 2.344e-05$

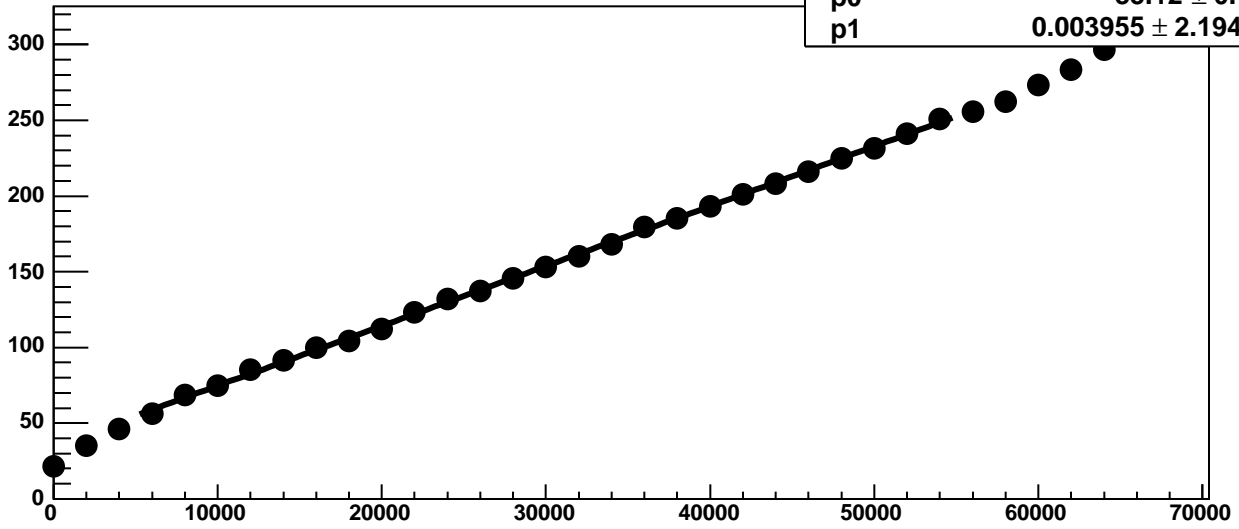
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

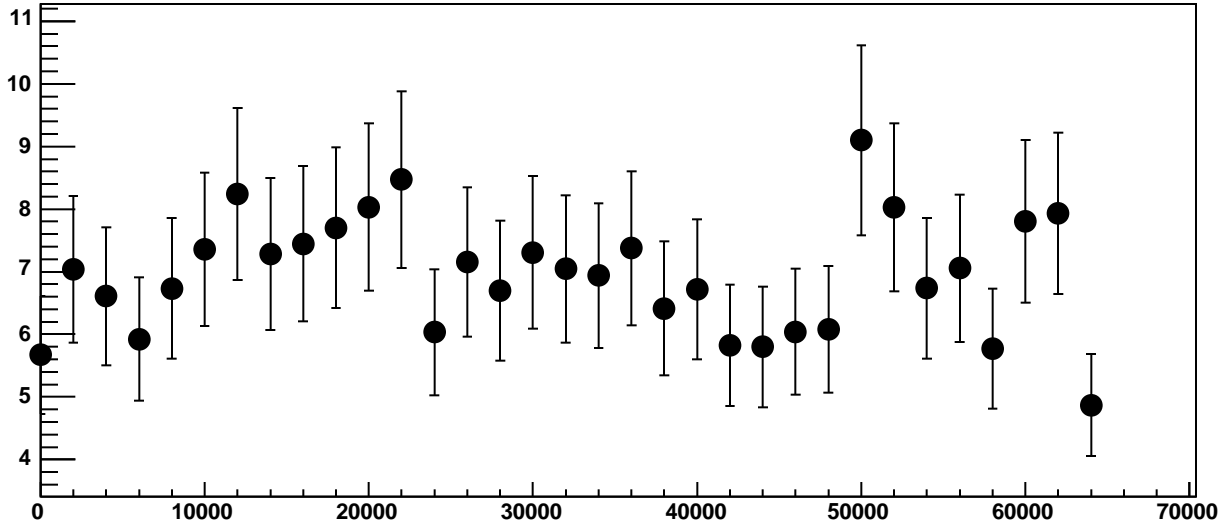


Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

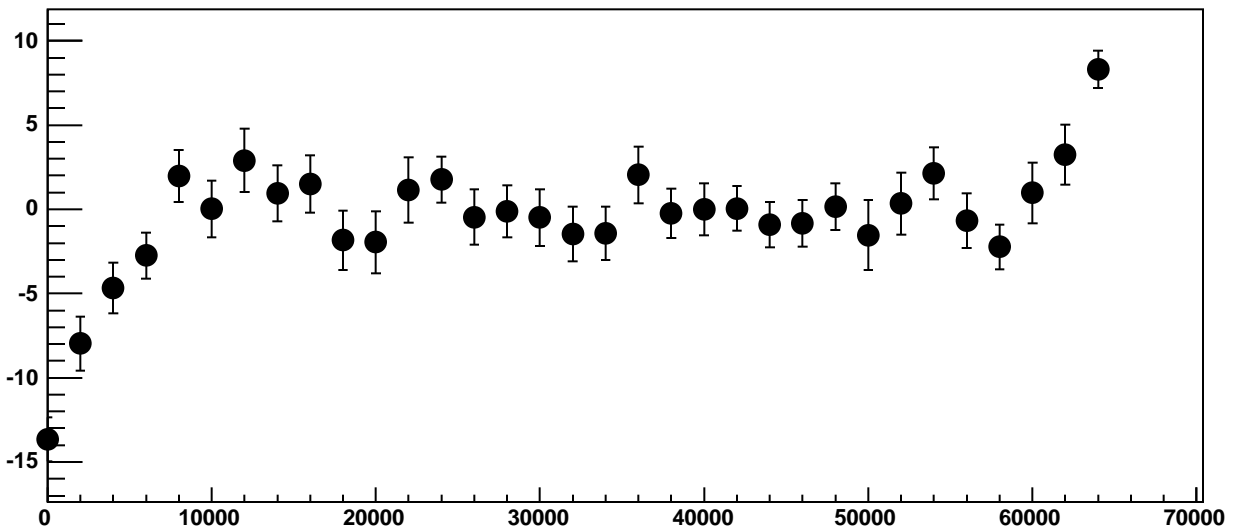


χ^2 / ndf 19.86 / 23
p0 35.12 ± 0.7433
p1 $0.003955 \pm 2.194e-05$

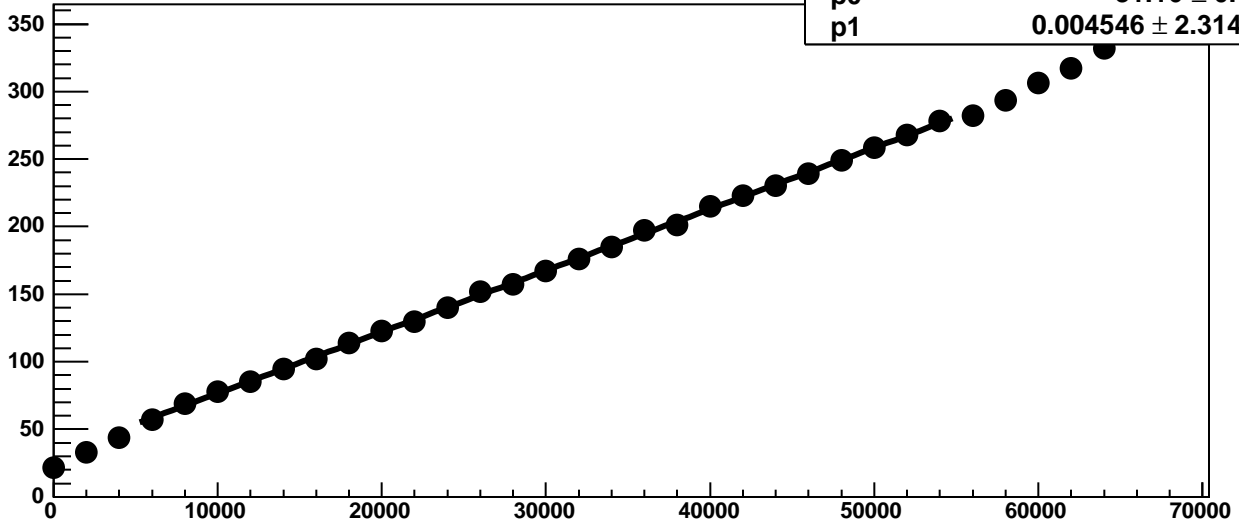
Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

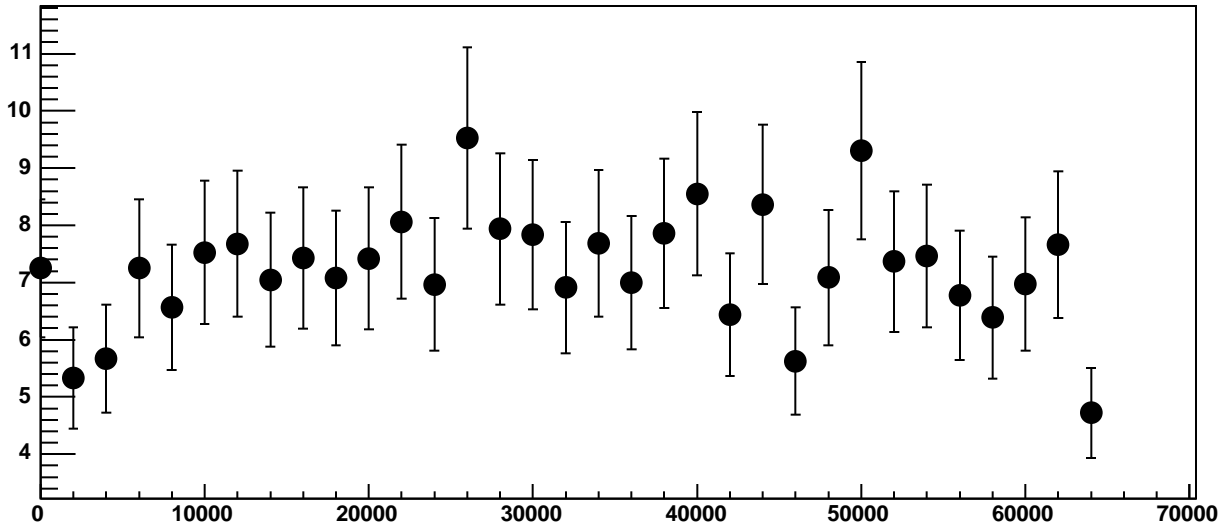


Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

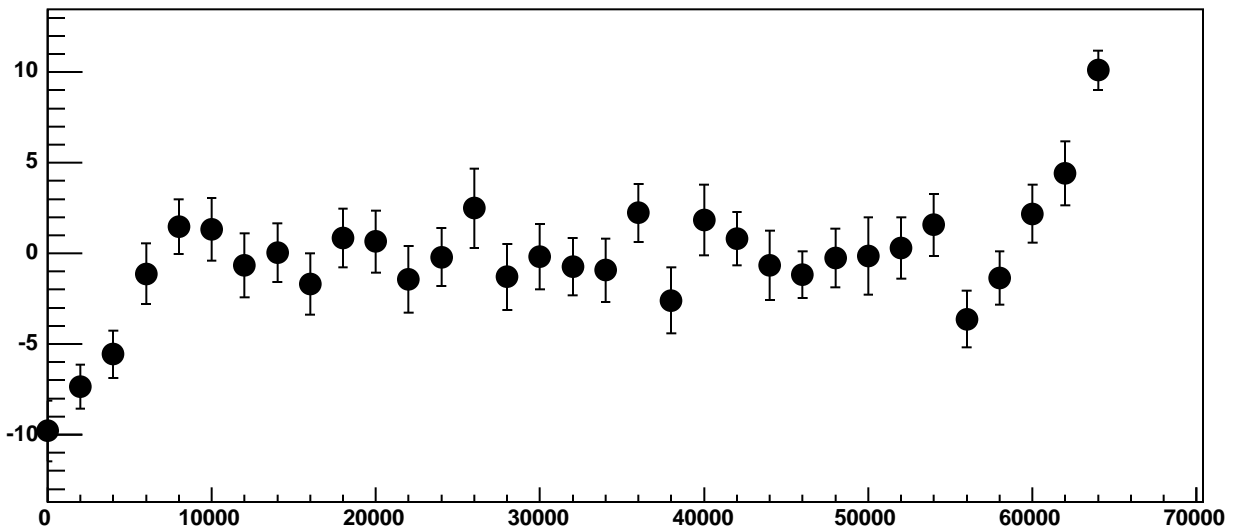


χ^2 / ndf 13.49 / 23
p0 31.16 ± 0.7713
p1 0.004546 ± 2.314e-05

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

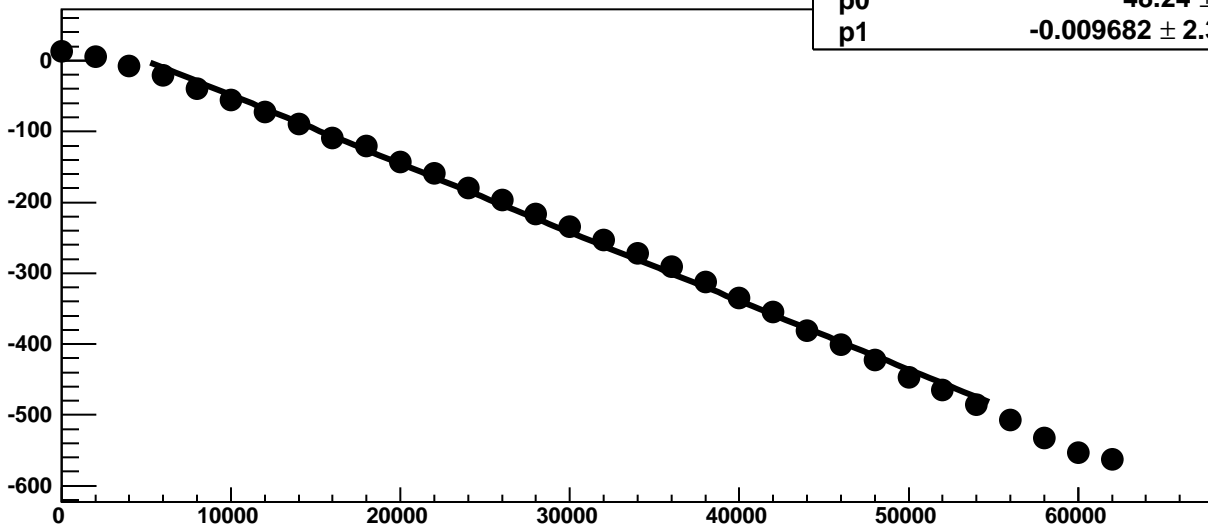


Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

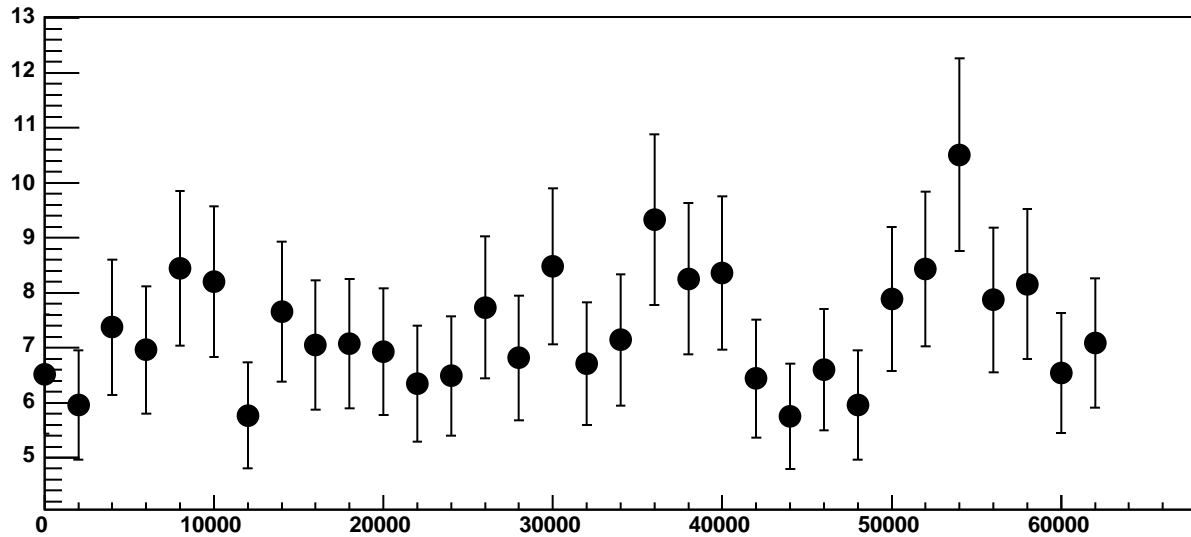


Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

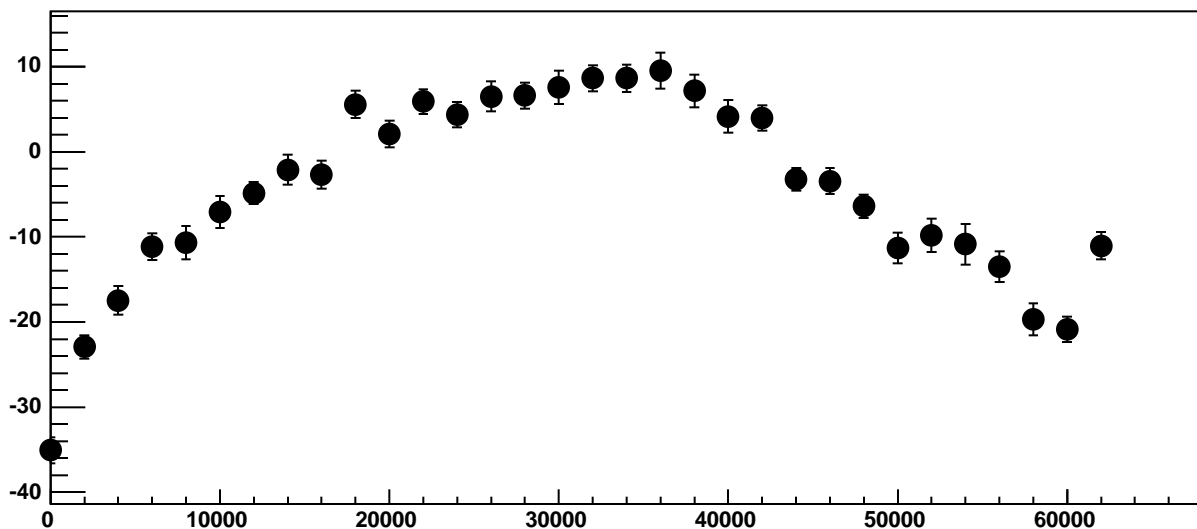
χ^2 / ndf 419.6 / 23
p0 48.24 ± 0.7654
p1 -0.009682 ± 2.337e-05



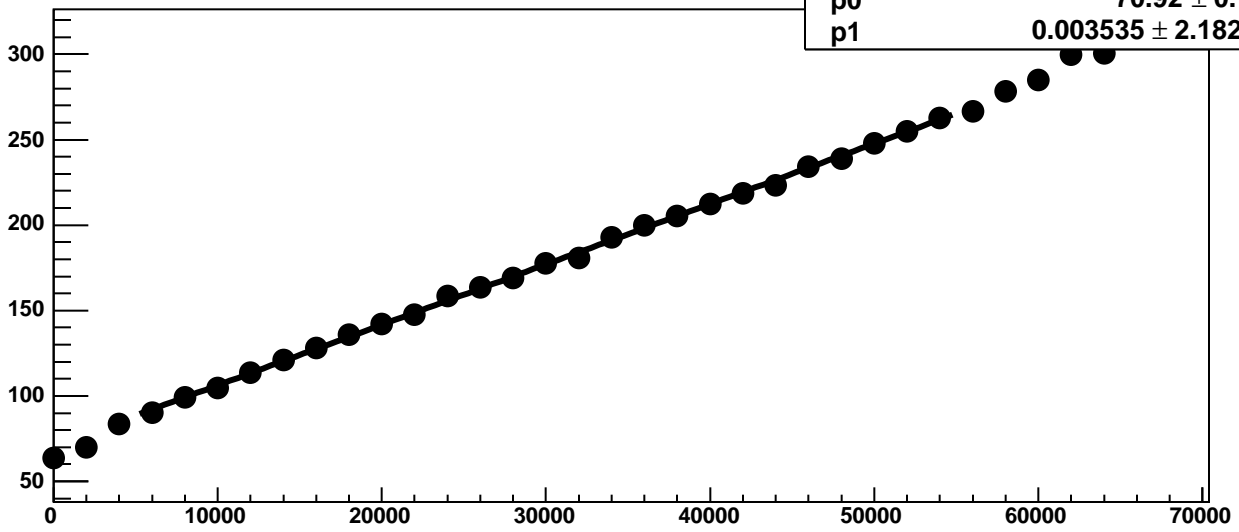
Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

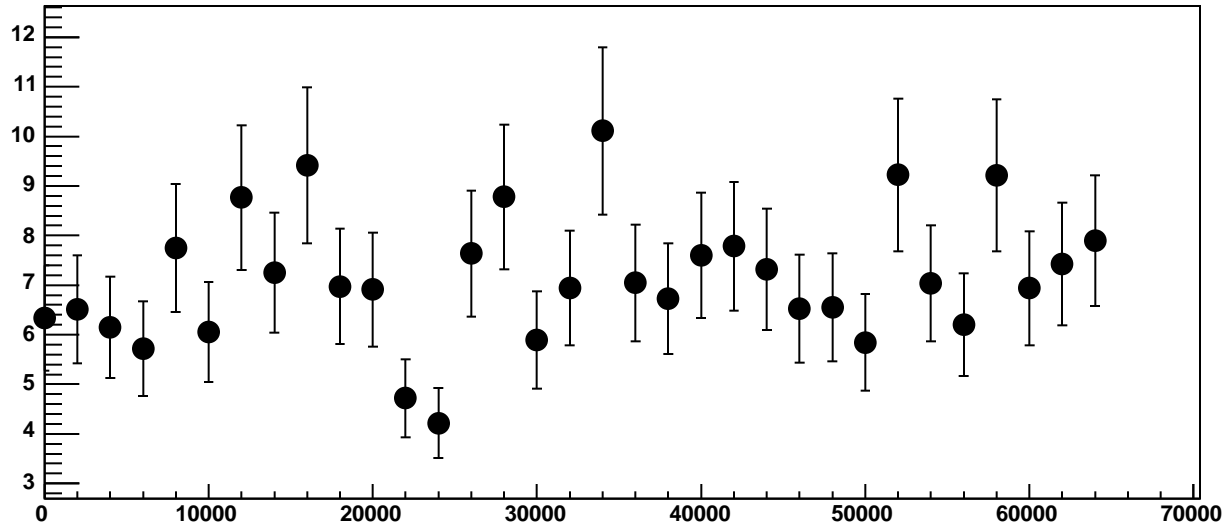


Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

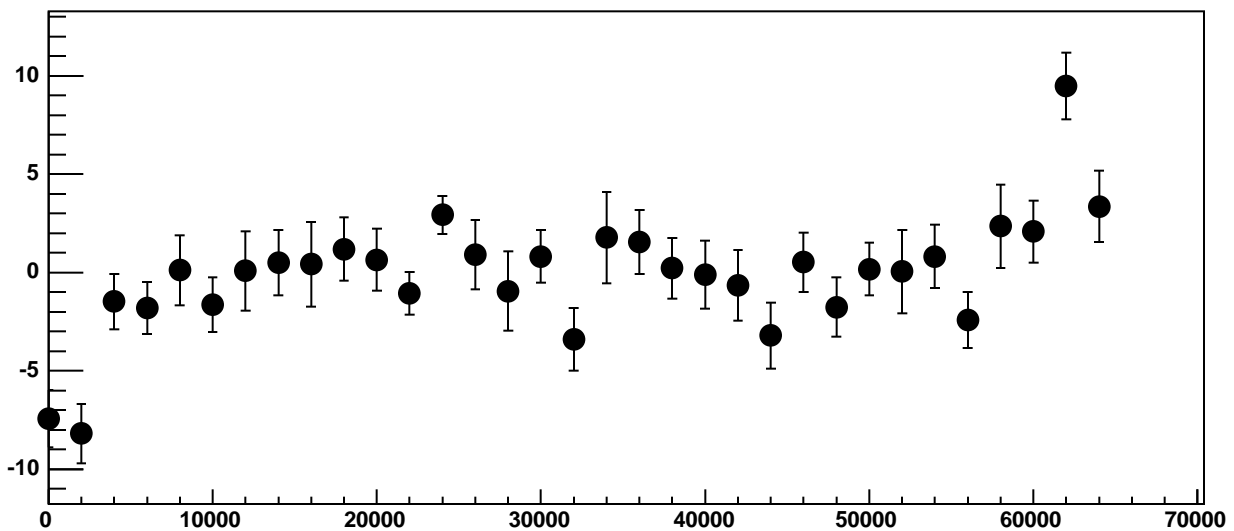


χ^2 / ndf 26.75 / 23
p0 70.92 ± 0.7074
p1 $0.003535 \pm 2.182e-05$

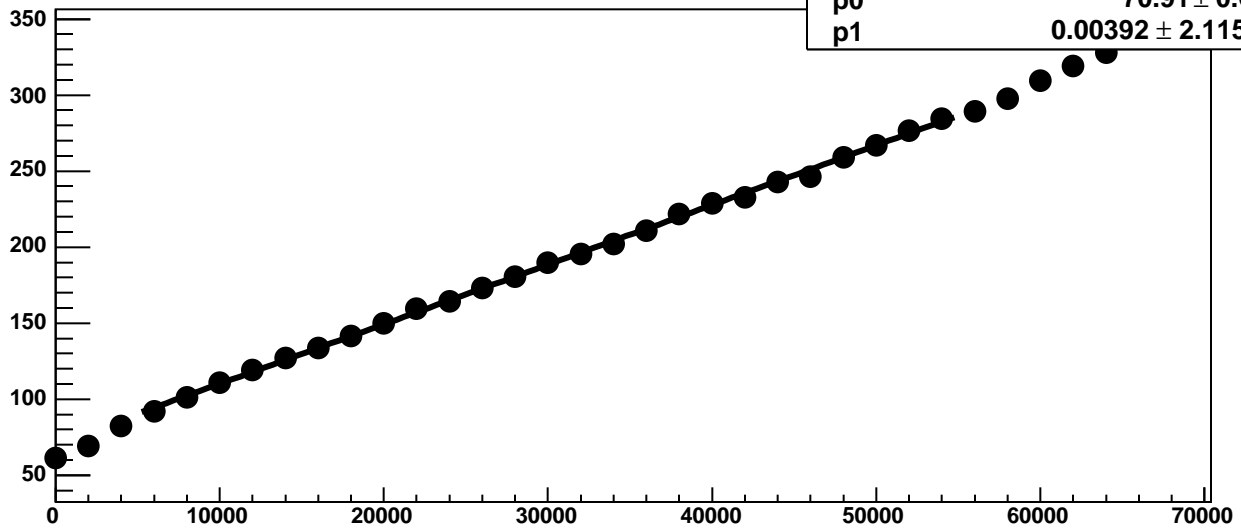
Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



χ^2 / ndf

27.16 / 23

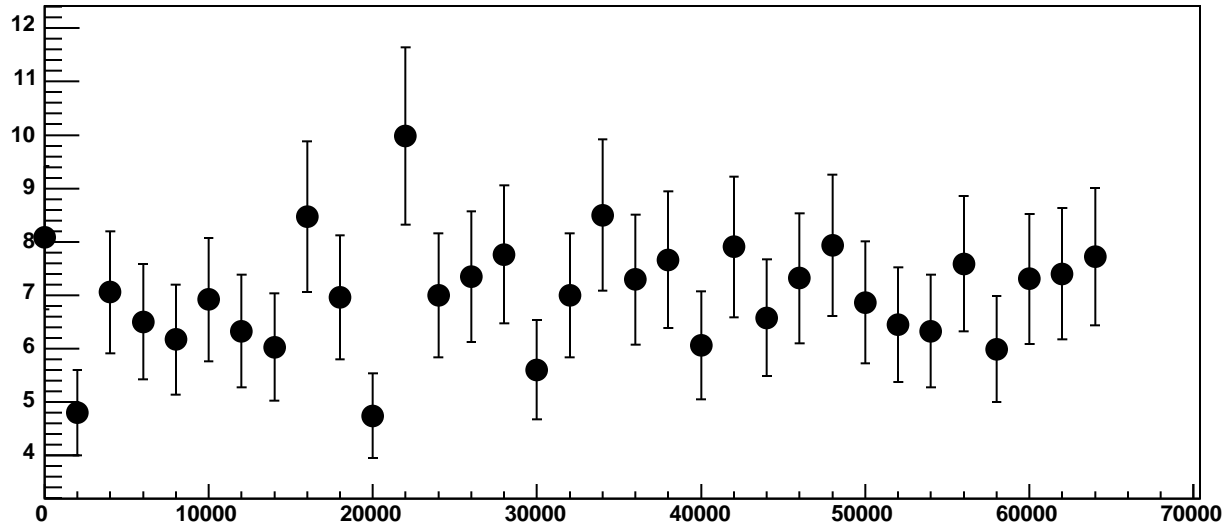
p0

70.91 ± 0.6937

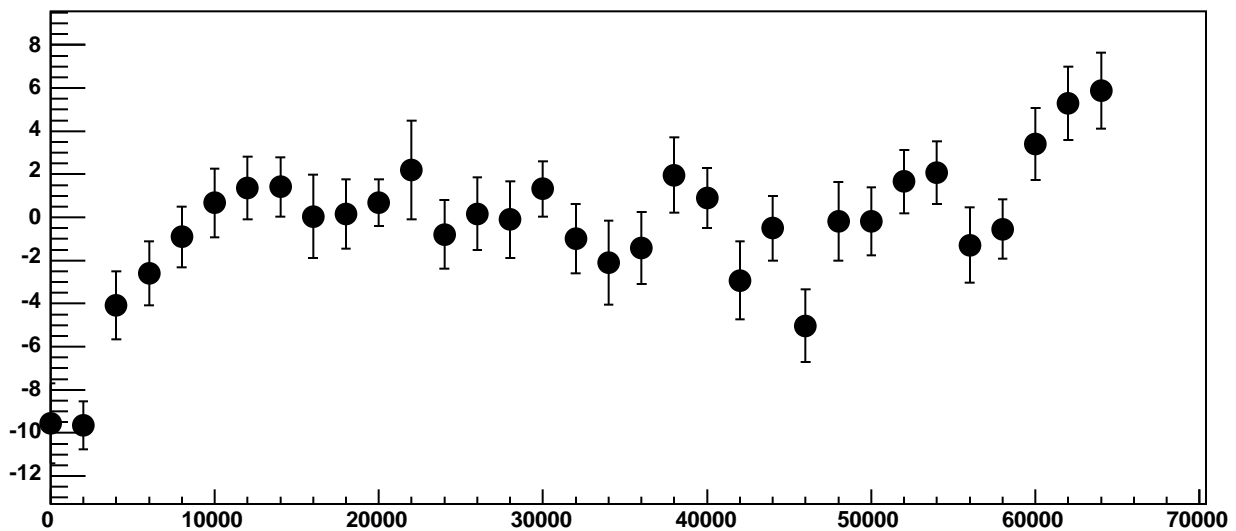
p1

$0.00392 \pm 2.115e-05$

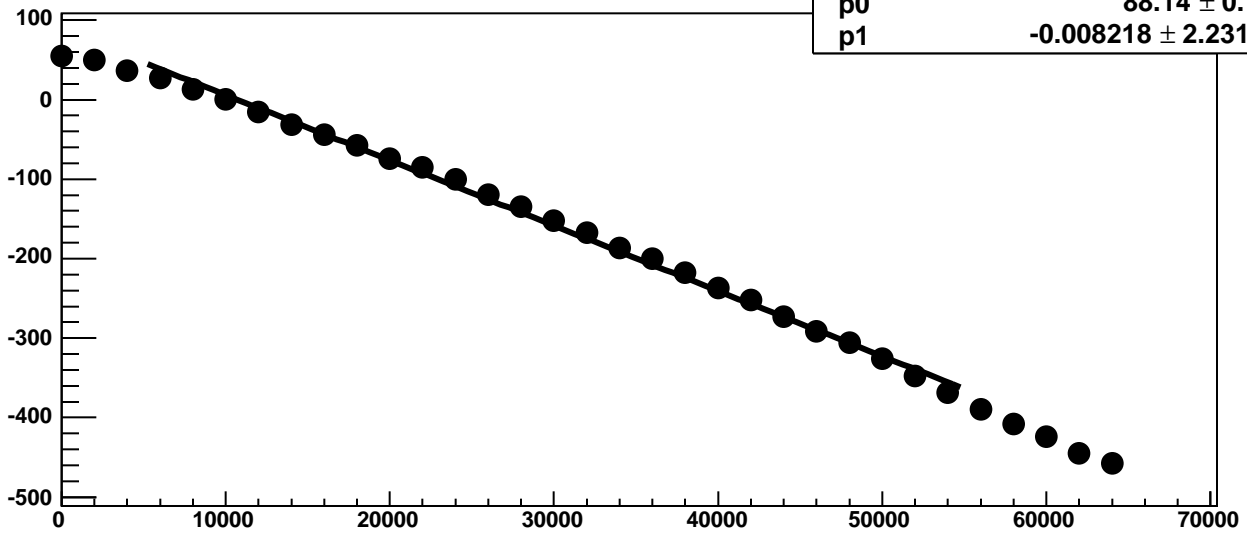
Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

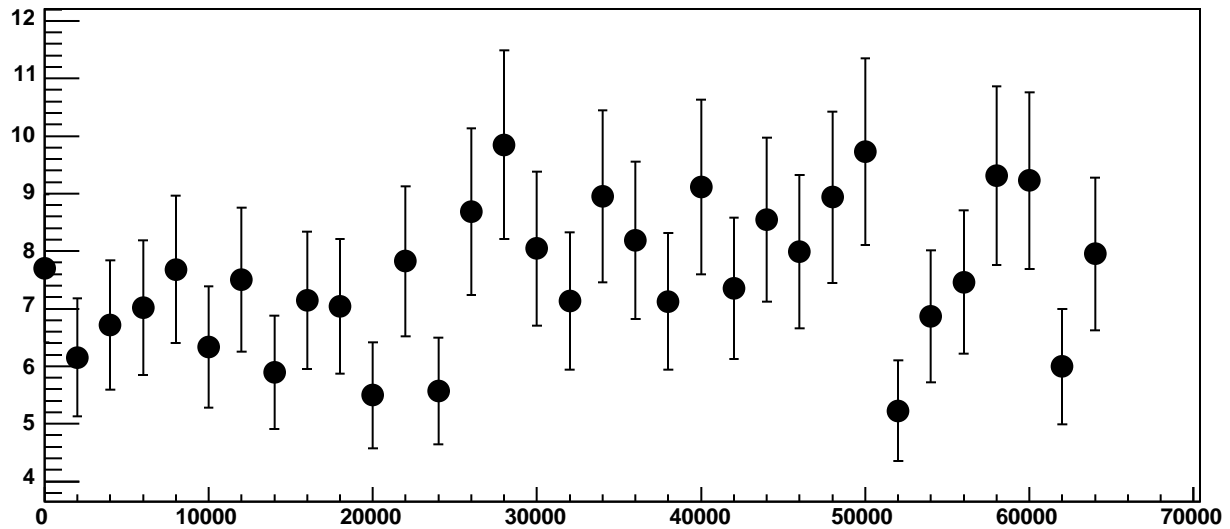


Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

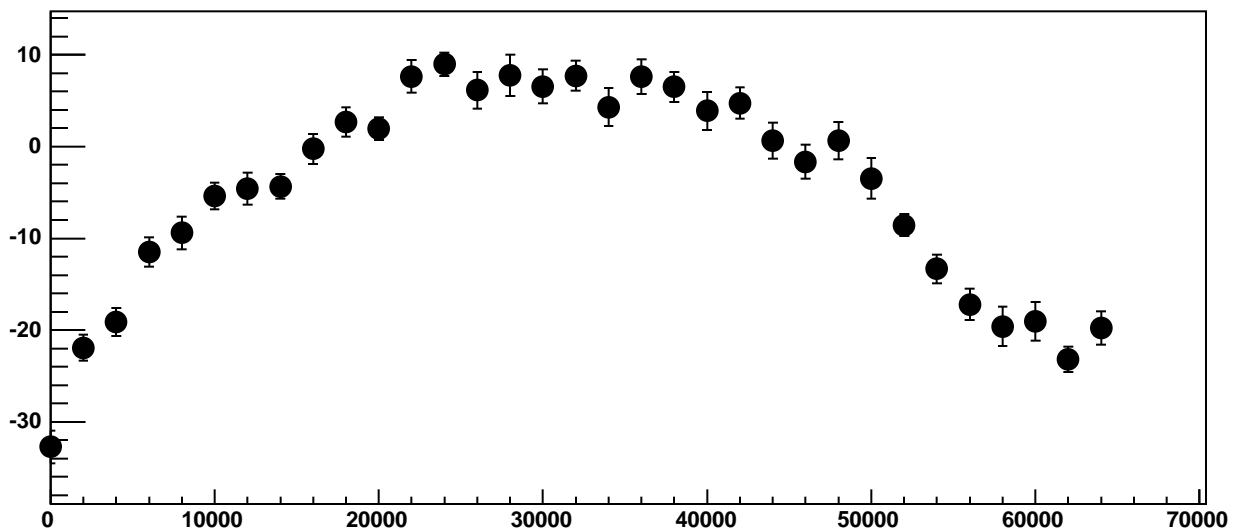


χ^2 / ndf	413.3 / 23
p0	88.14 ± 0.7257
p1	$-0.008218 \pm 2.231e-05$

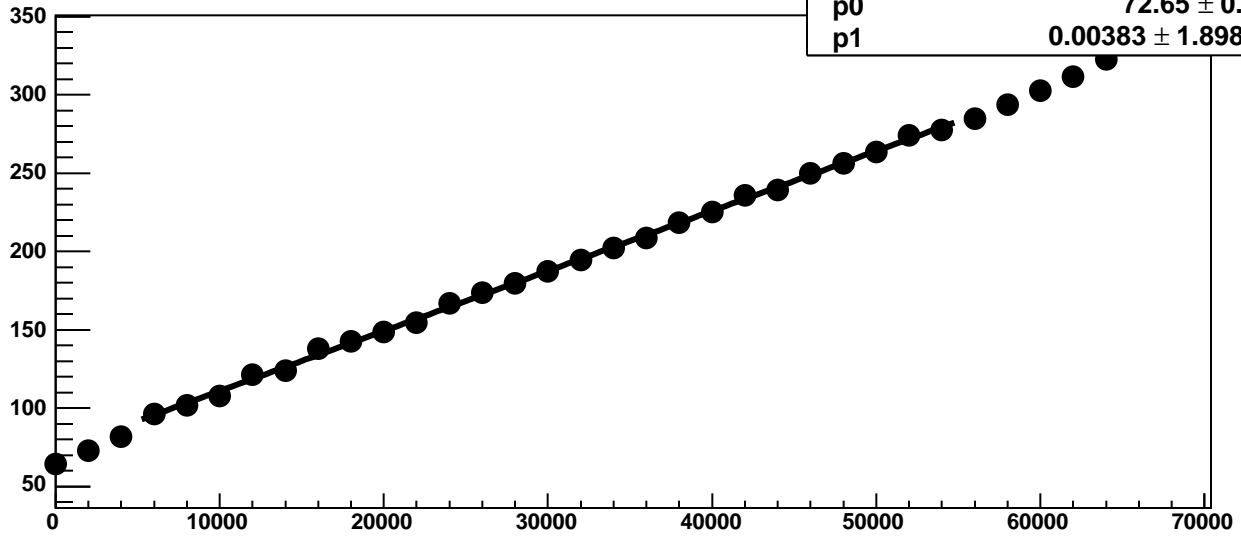
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



χ^2 / ndf

40.73 / 23

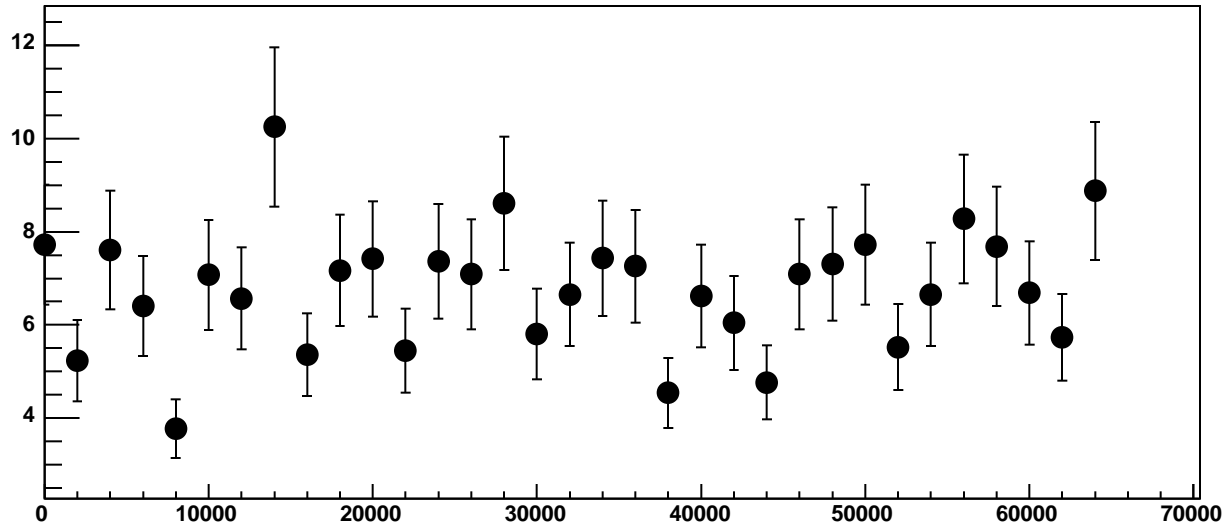
p0

72.65 ± 0.6251

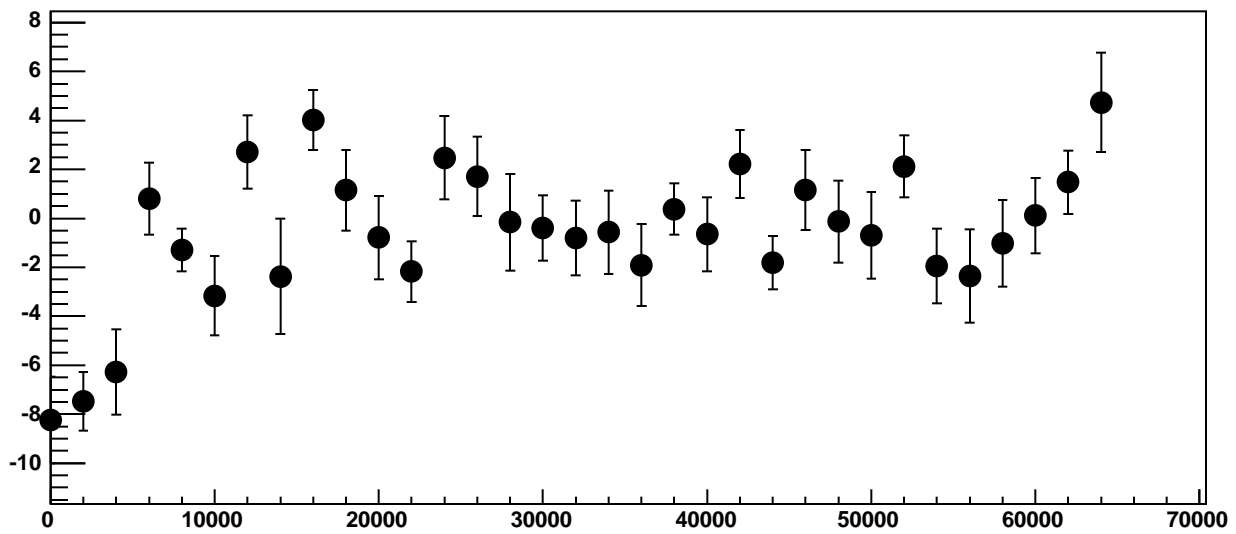
p1

$0.00383 \pm 1.898e-05$

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

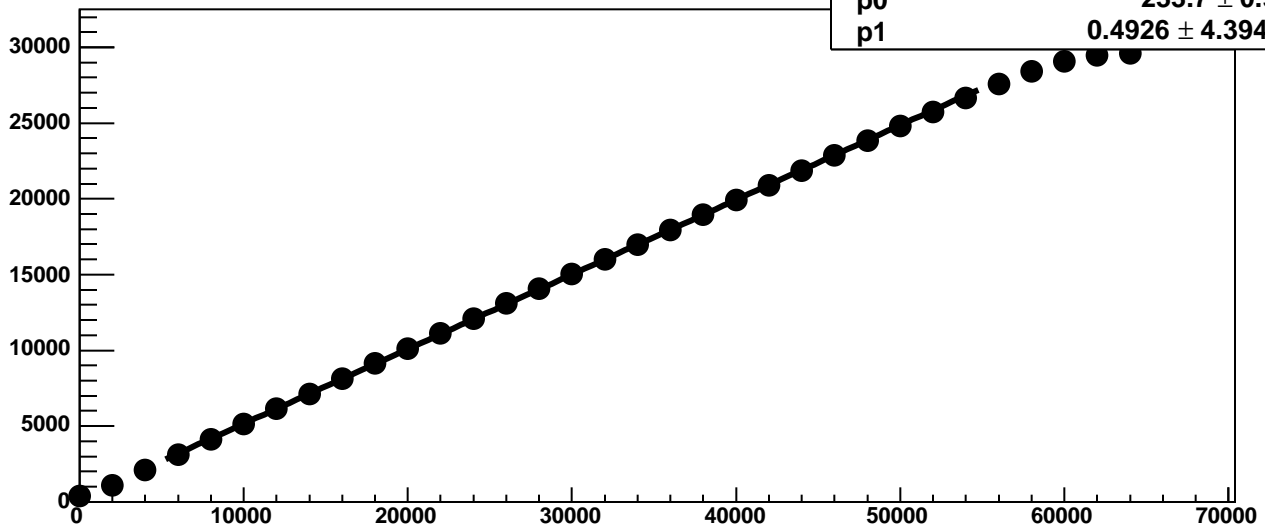


Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

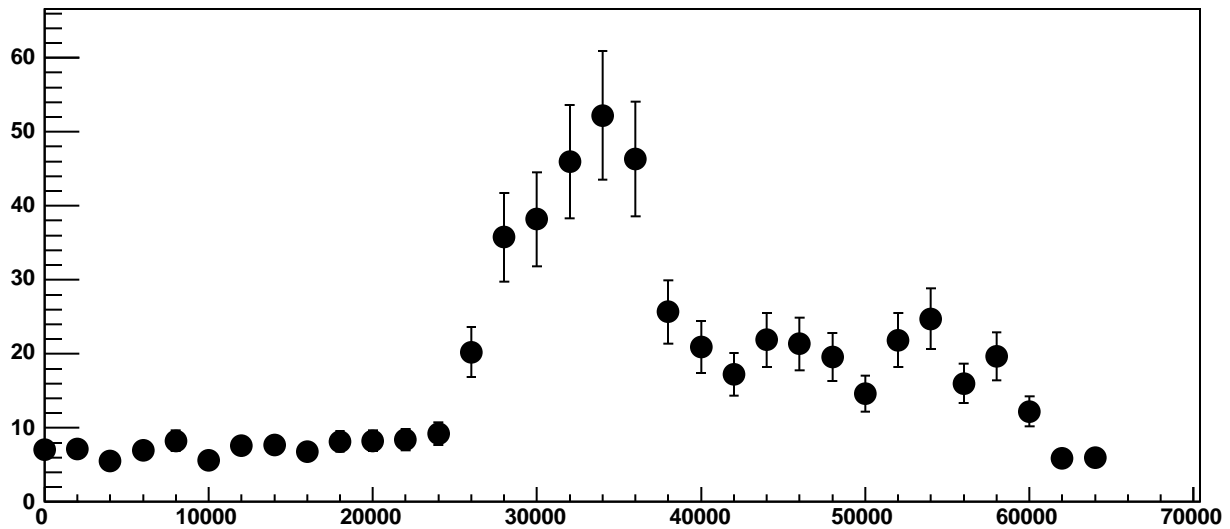


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

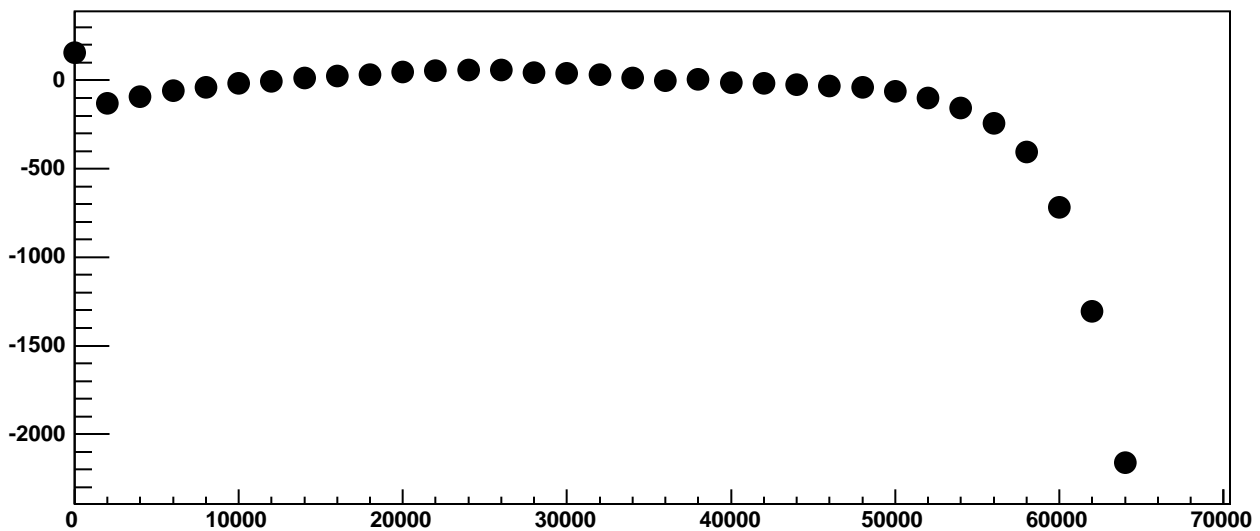
χ^2 / ndf 6650 / 23
p0 233.7 ± 0.9353
p1 $0.4926 \pm 4.394e-05$



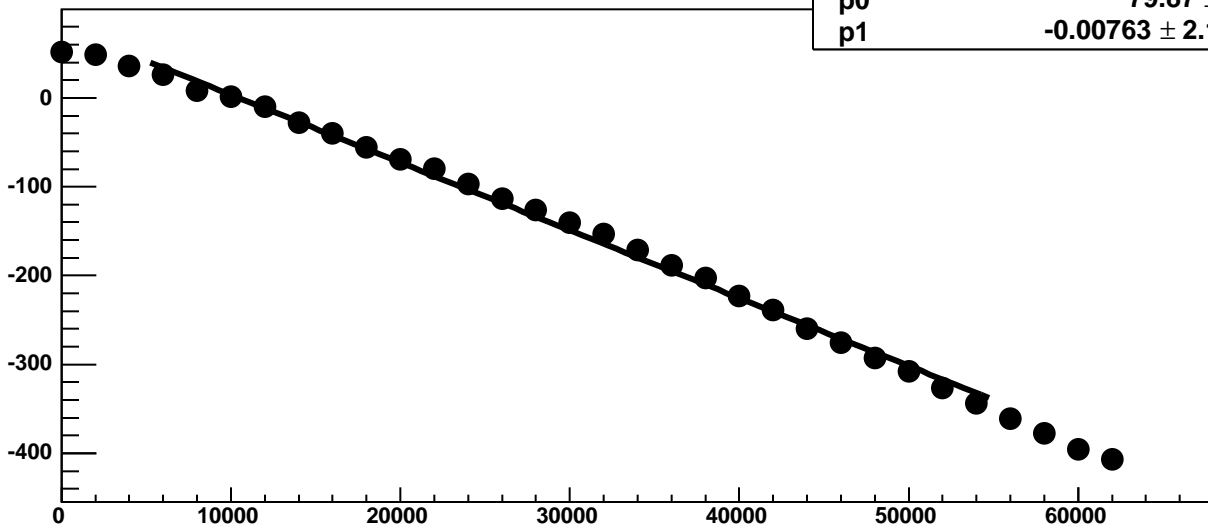
Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



χ^2 / ndf

374 / 23

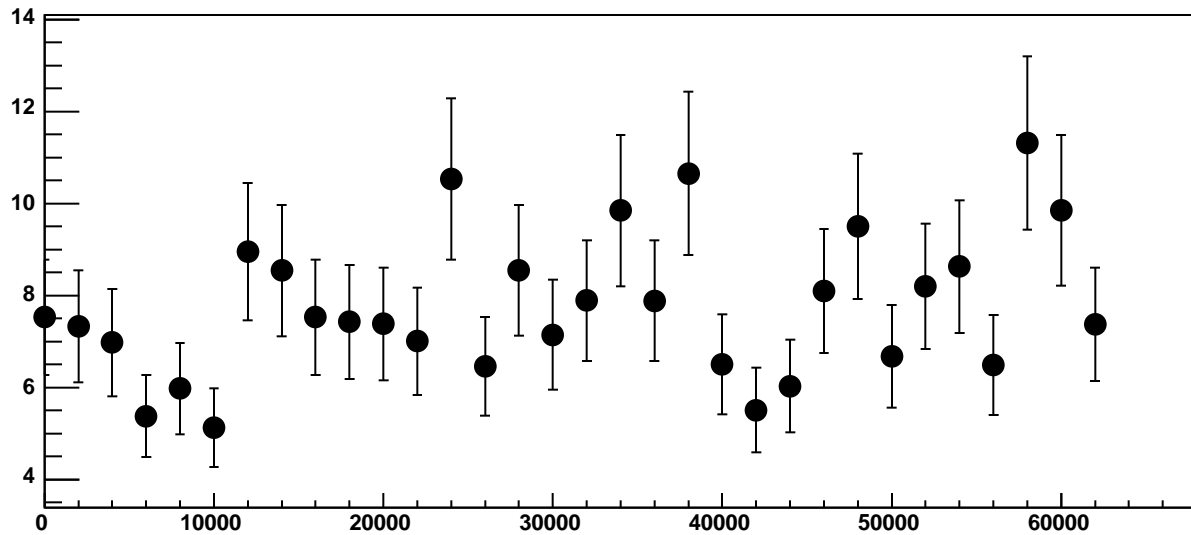
p0

79.87 ± 0.7011

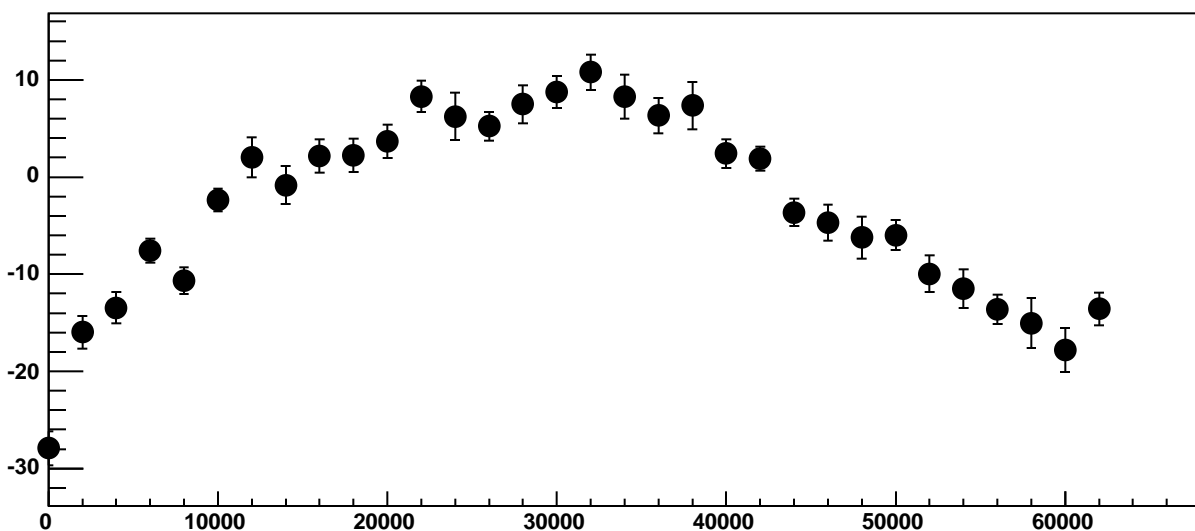
p1

$-0.00763 \pm 2.187e-05$

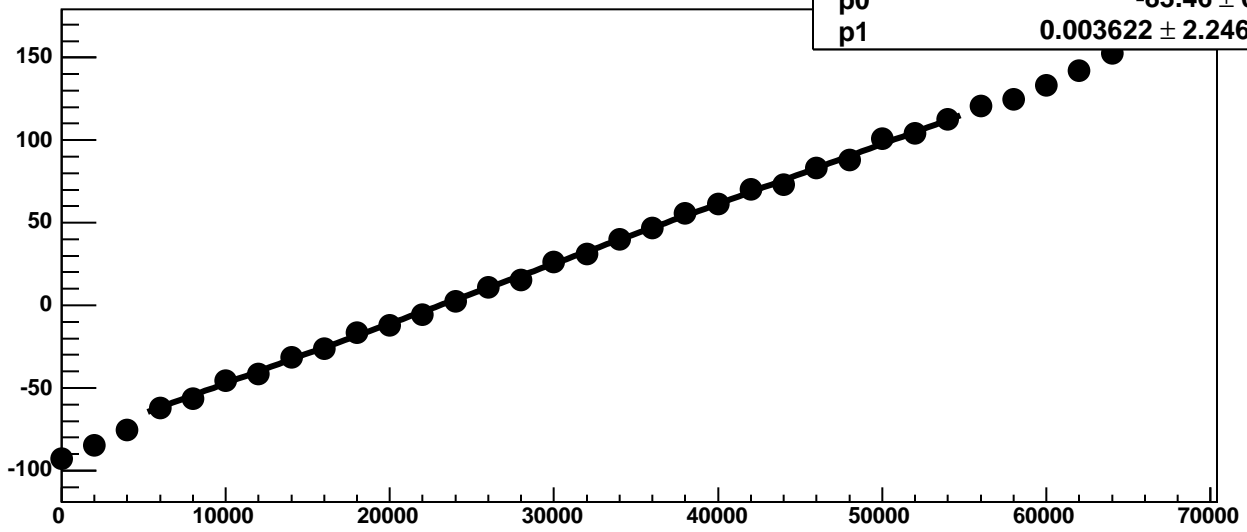
Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



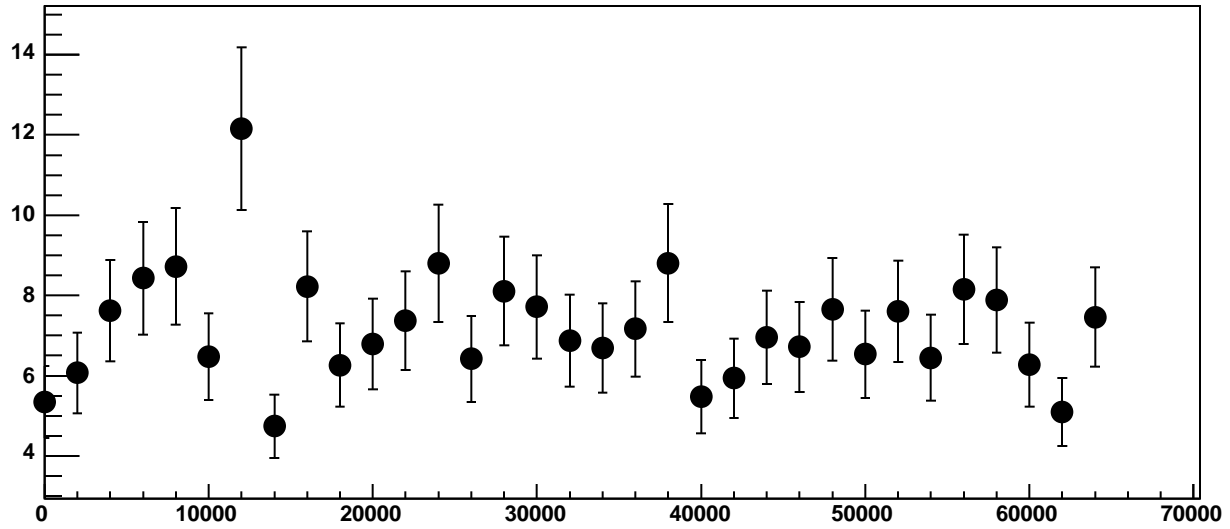
Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



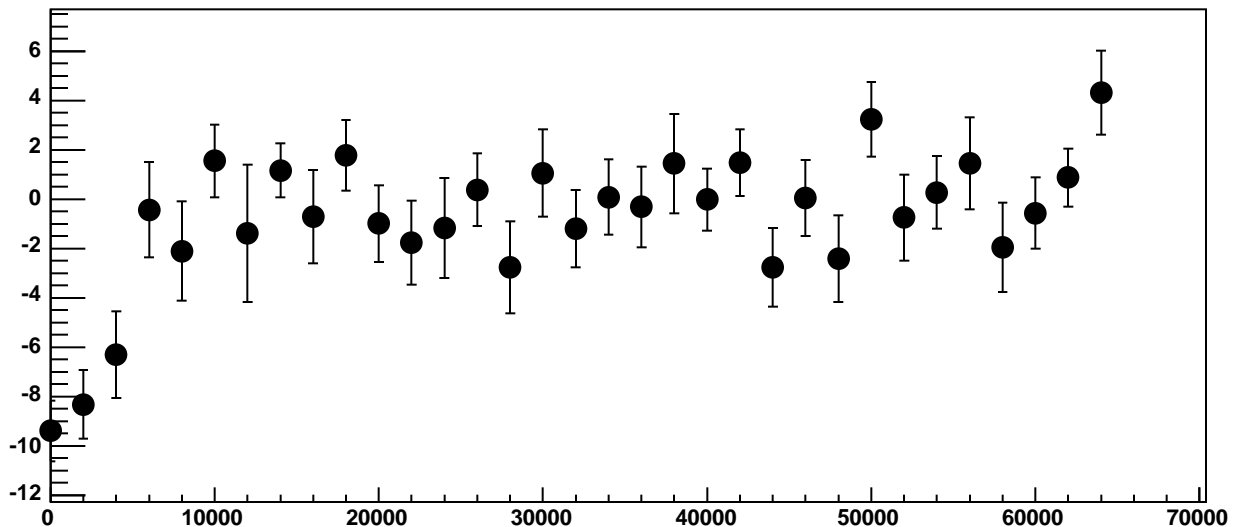
Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



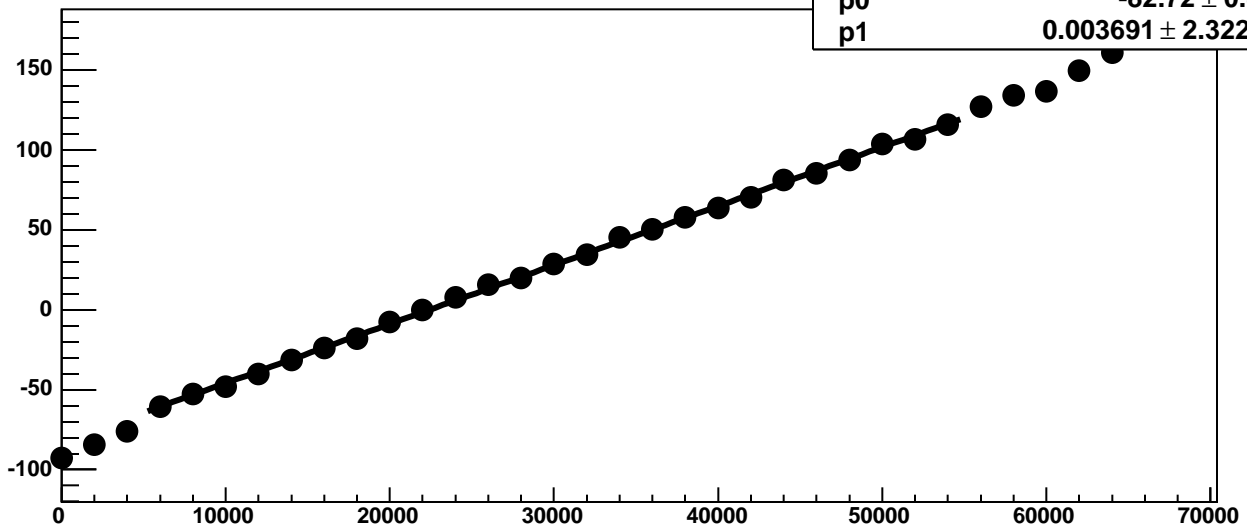
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



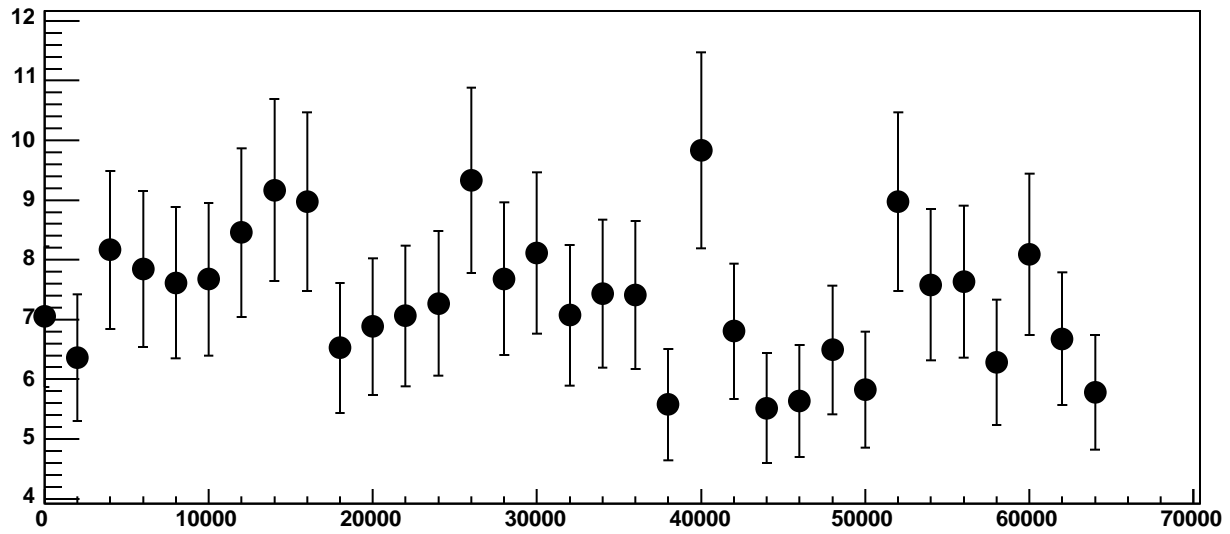
Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



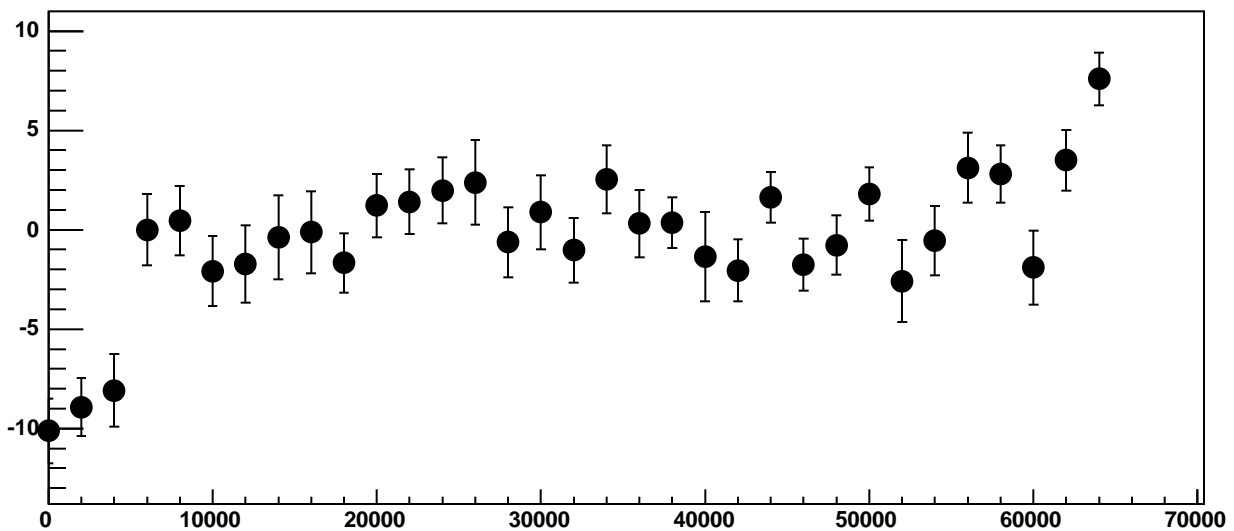
Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



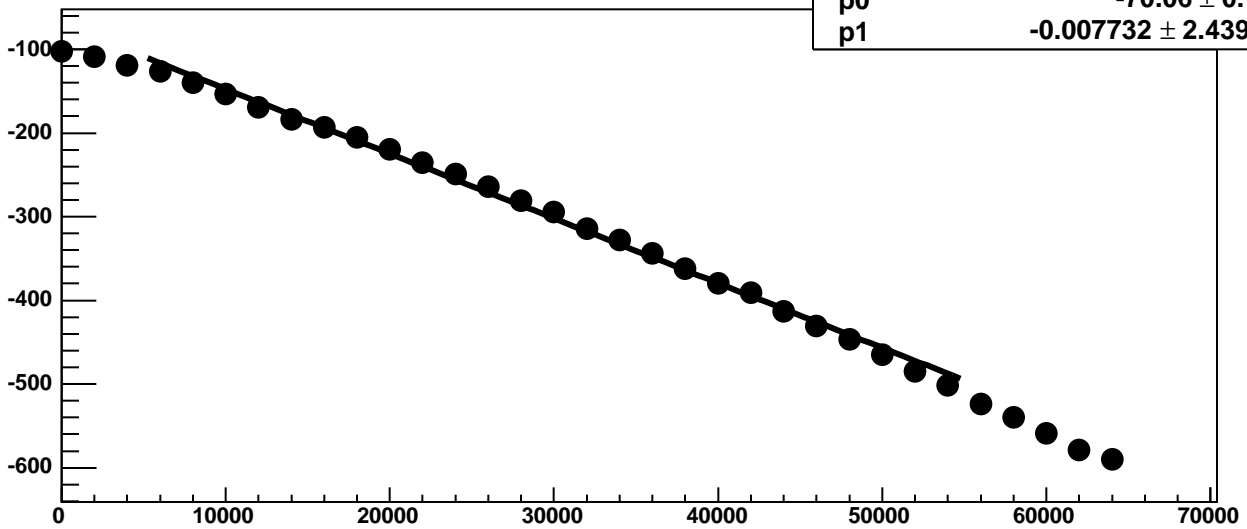
Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



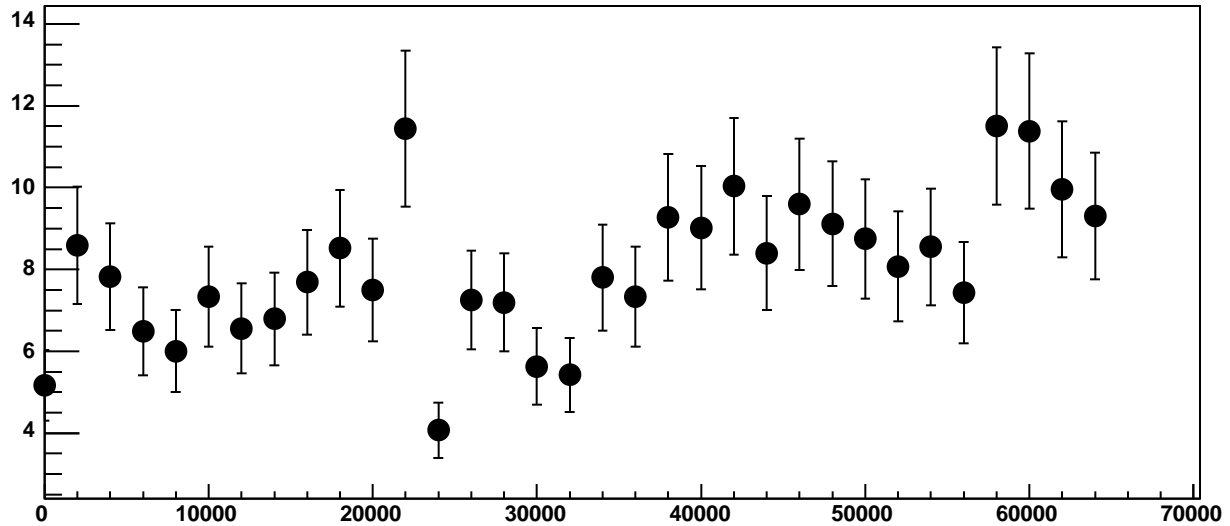
Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



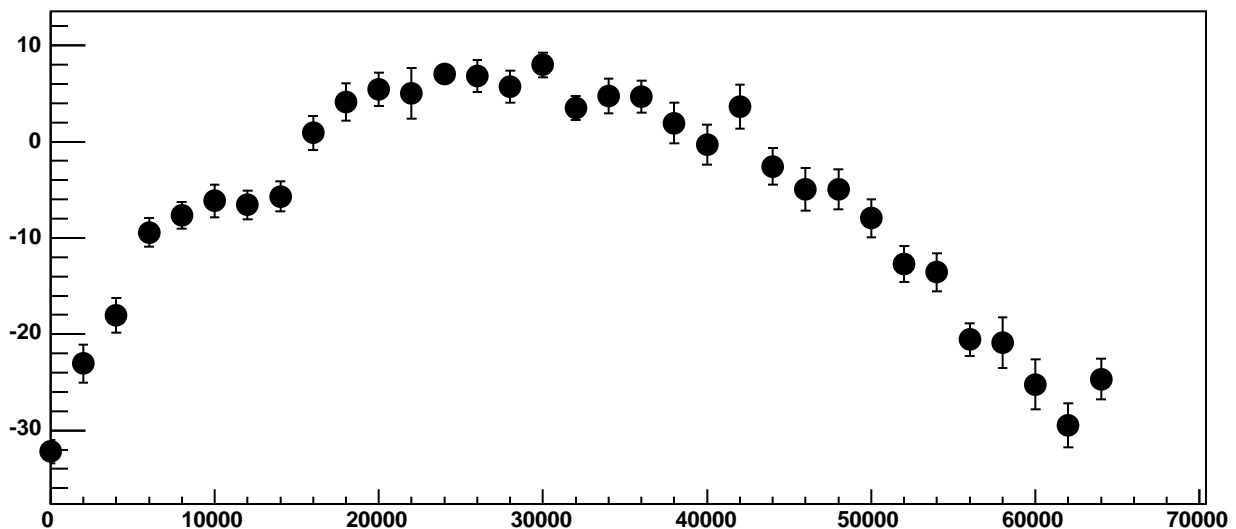
Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC



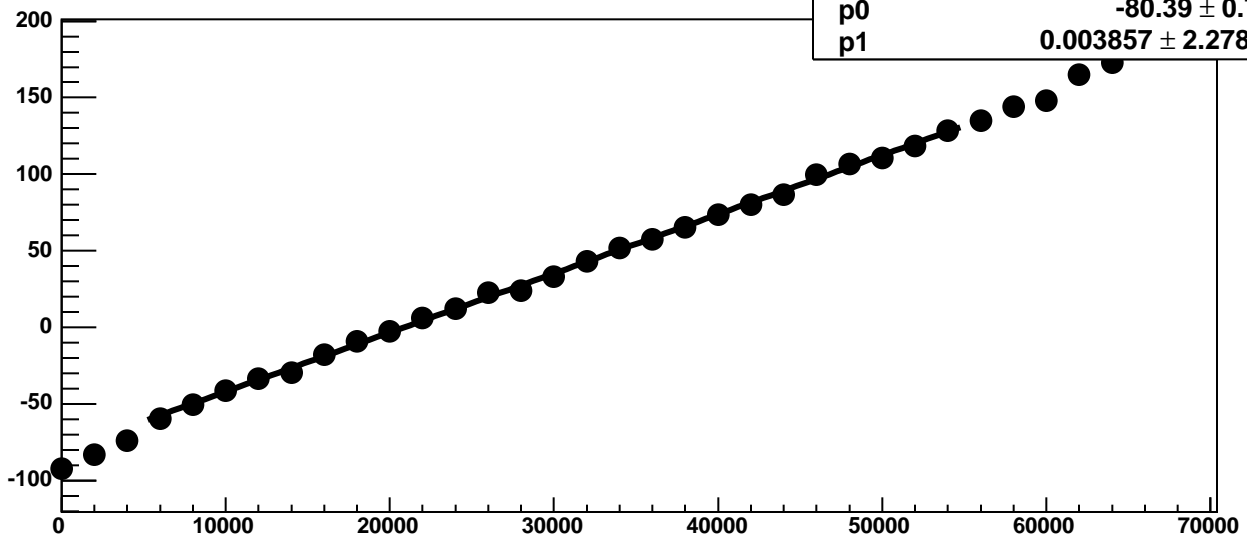
Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



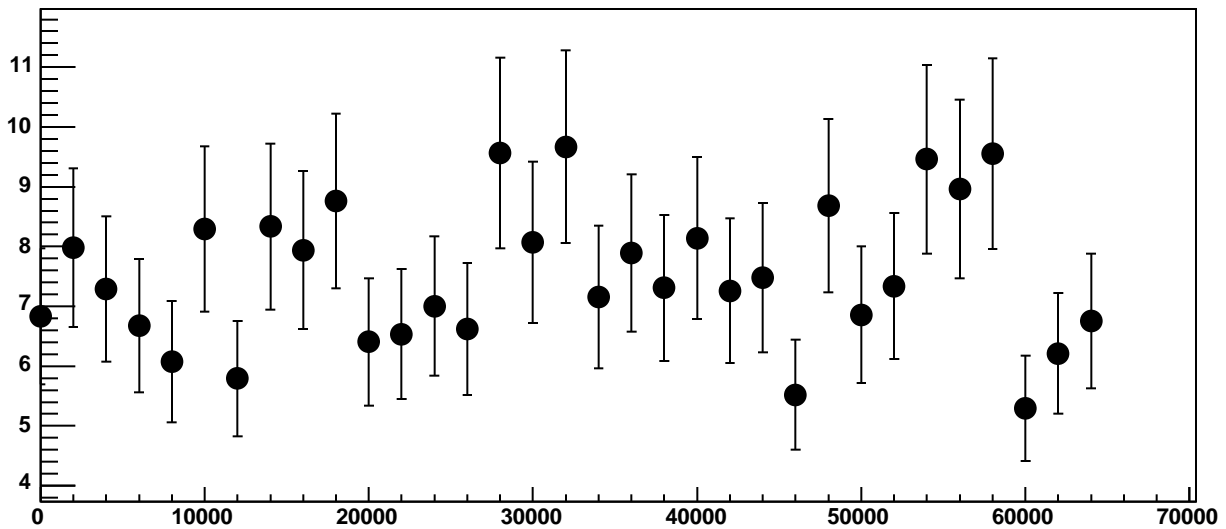
Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



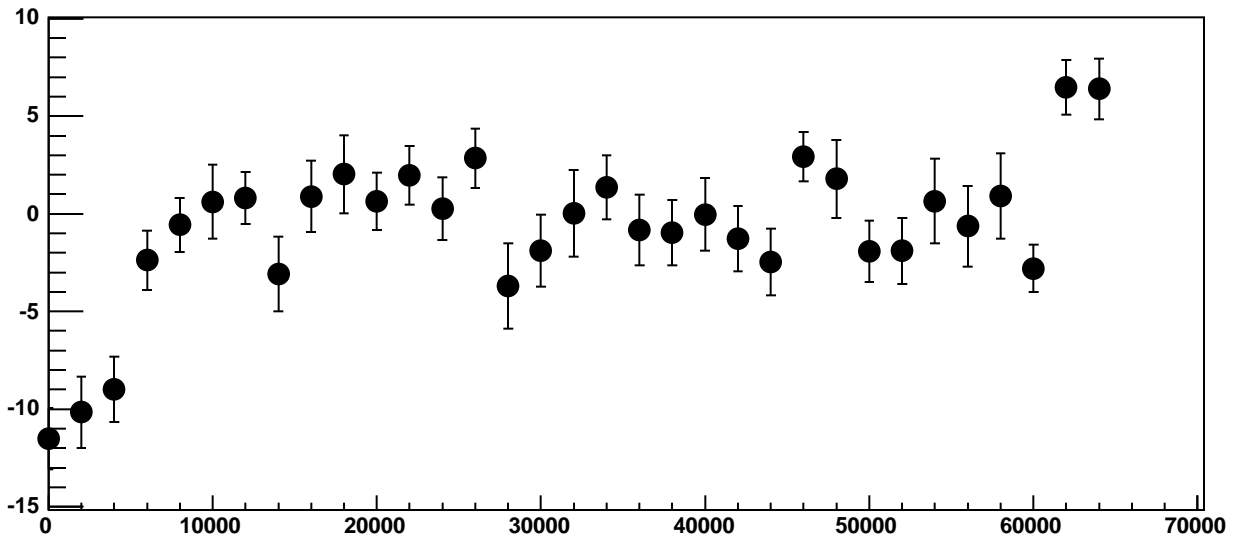
Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



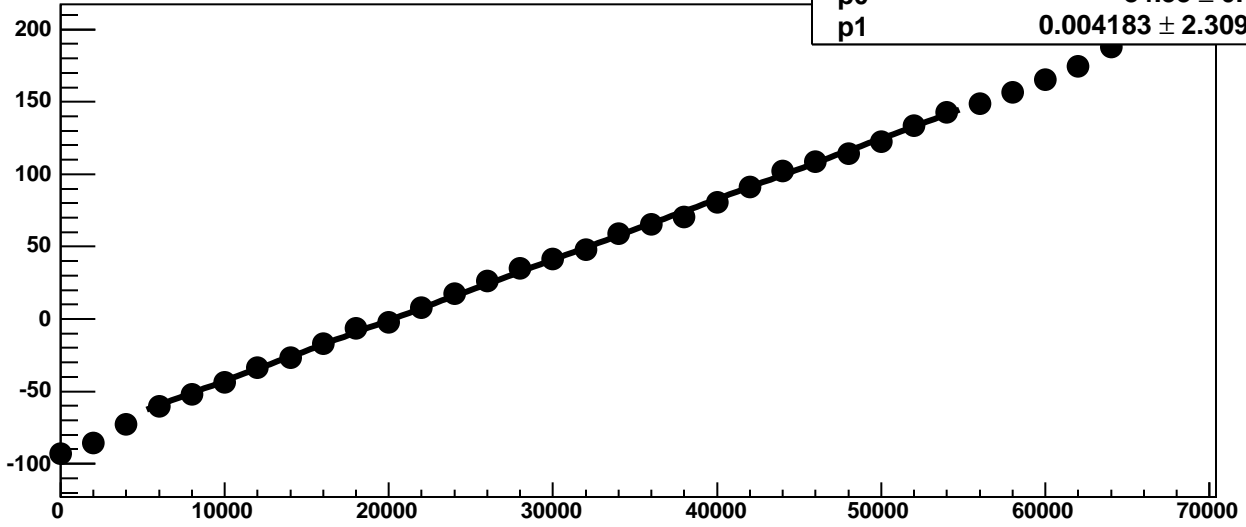
Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



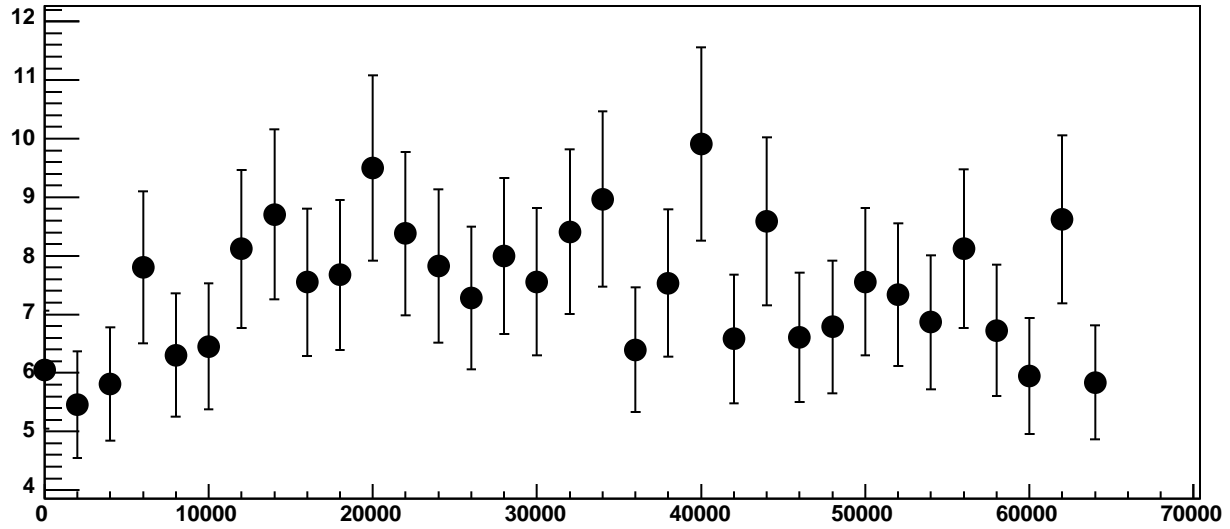
Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



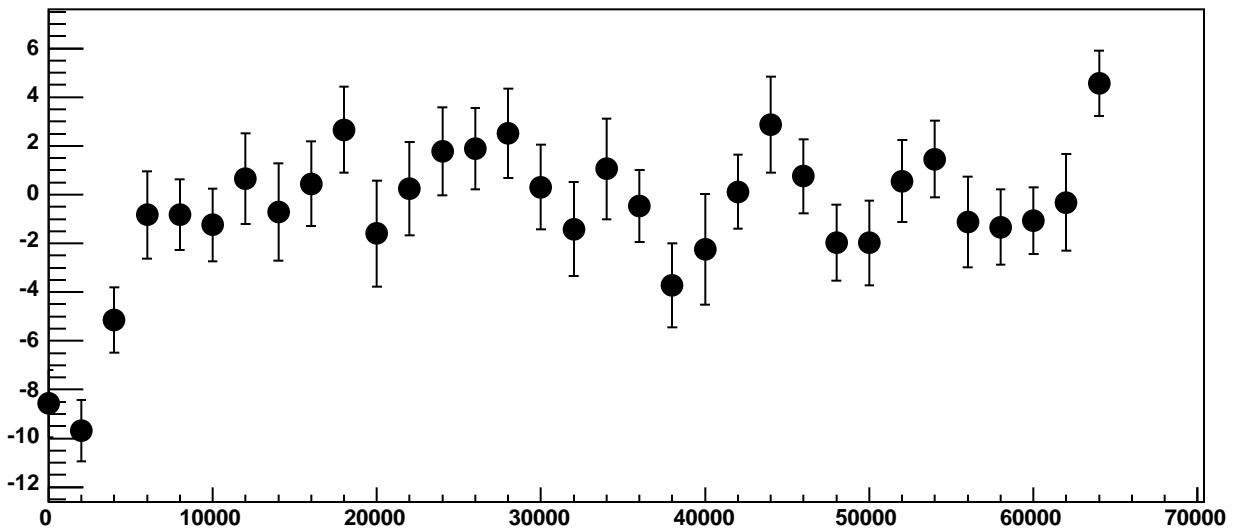
Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

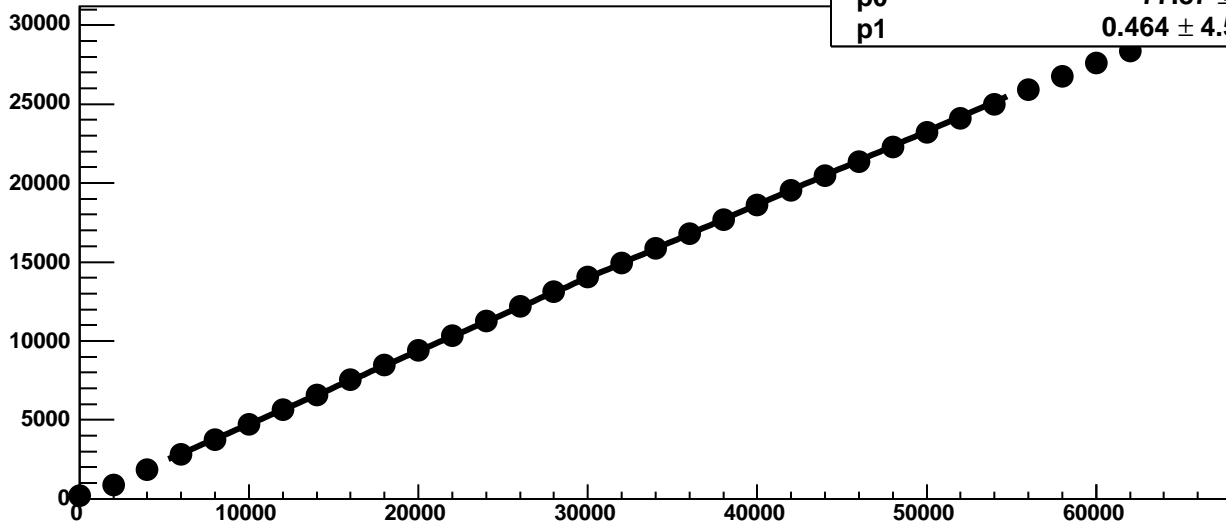


Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

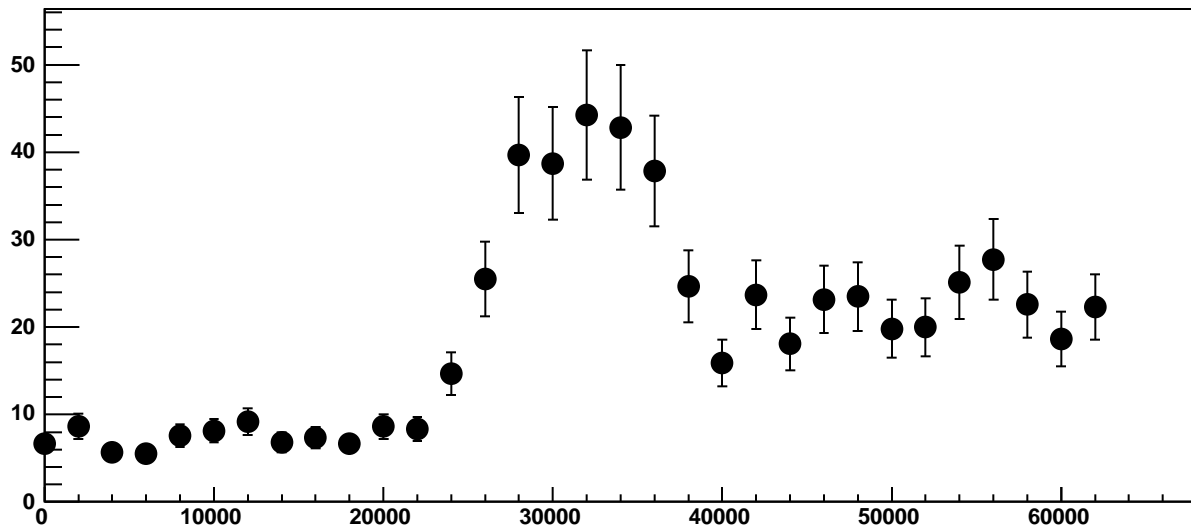


Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

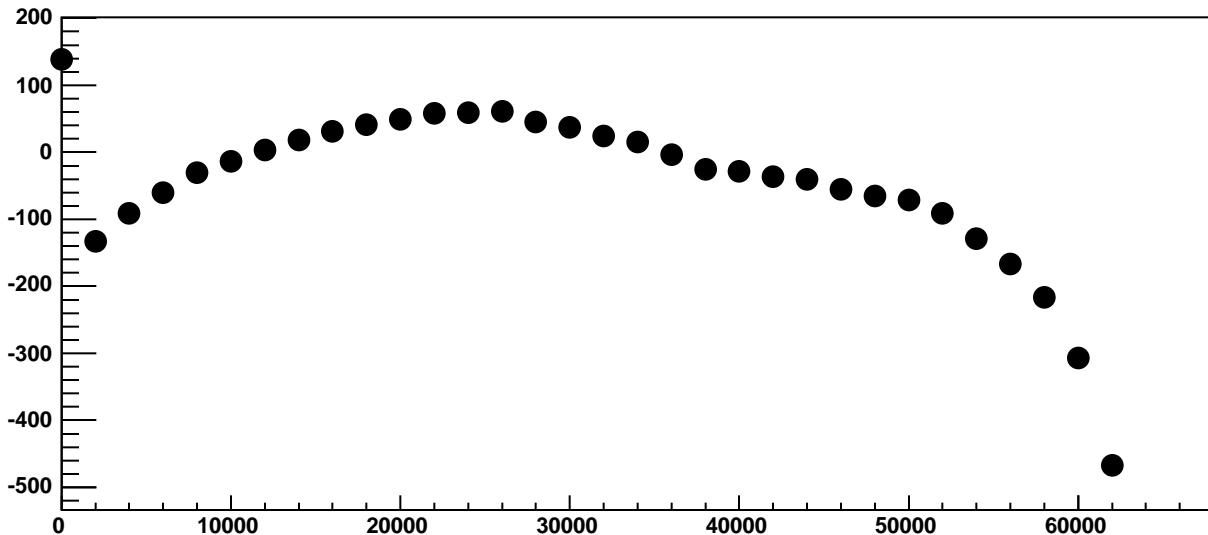
χ^2 / ndf 7474 / 23
p0 77.87 ± 0.9387
p1 $0.464 \pm 4.531\text{e-}05$



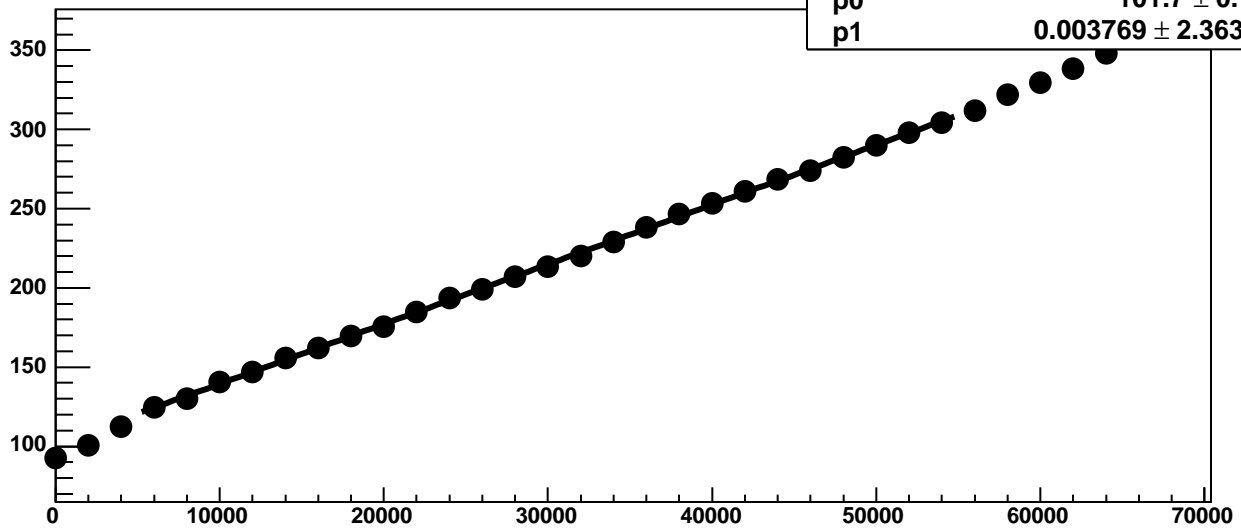
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

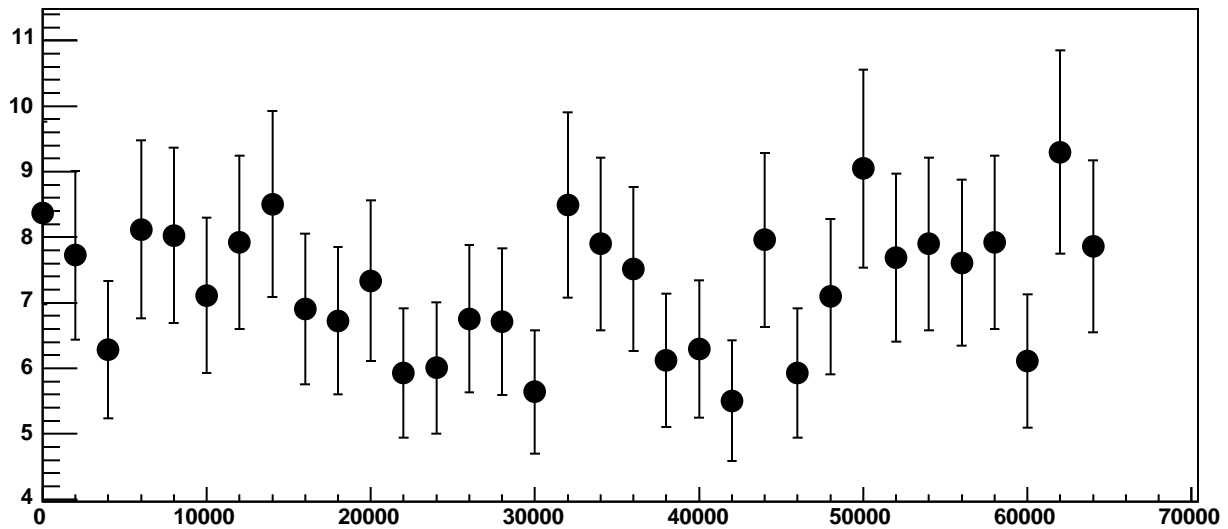


Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

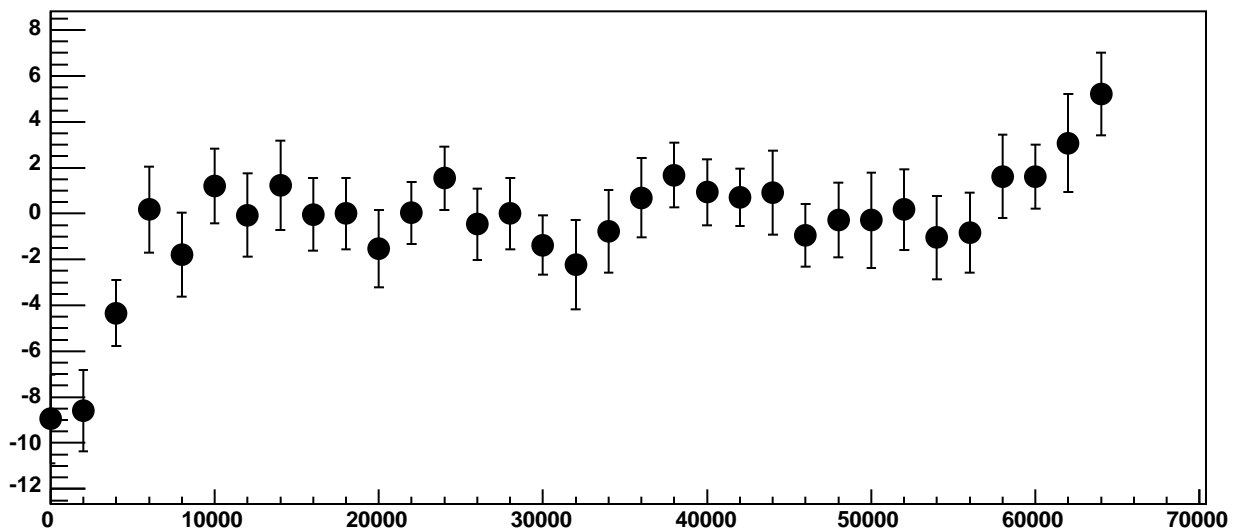


χ^2 / ndf 10.15 / 23
p0 101.7 ± 0.7867
p1 $0.003769 \pm 2.363e-05$

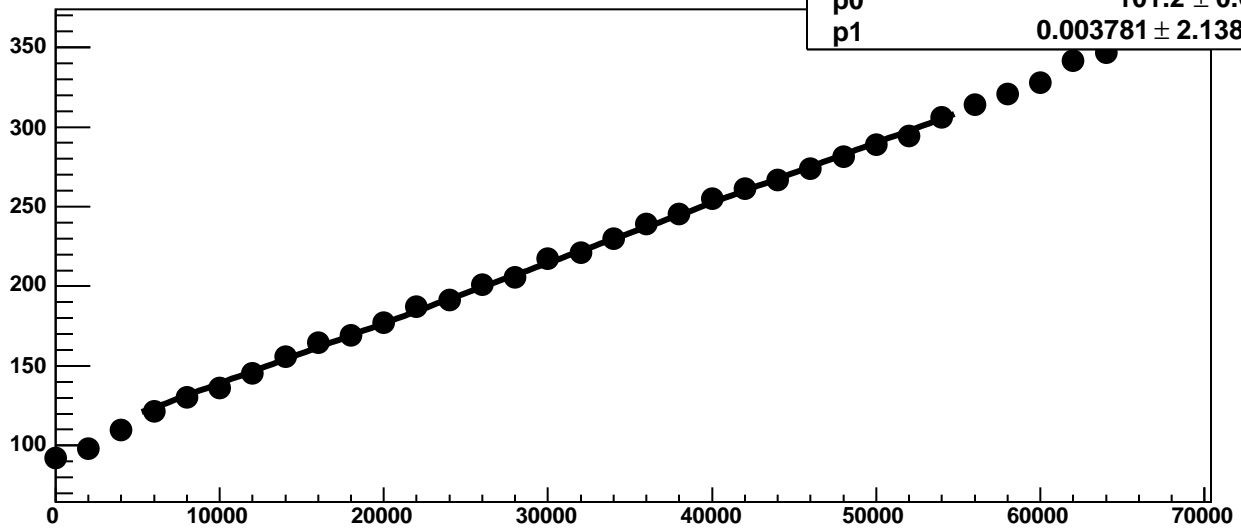
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

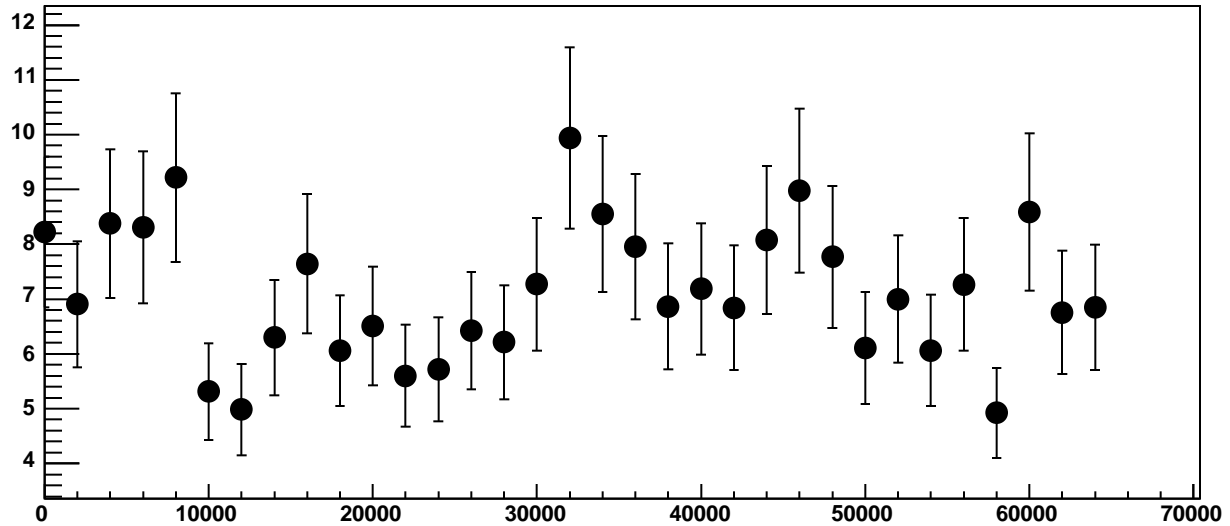


Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

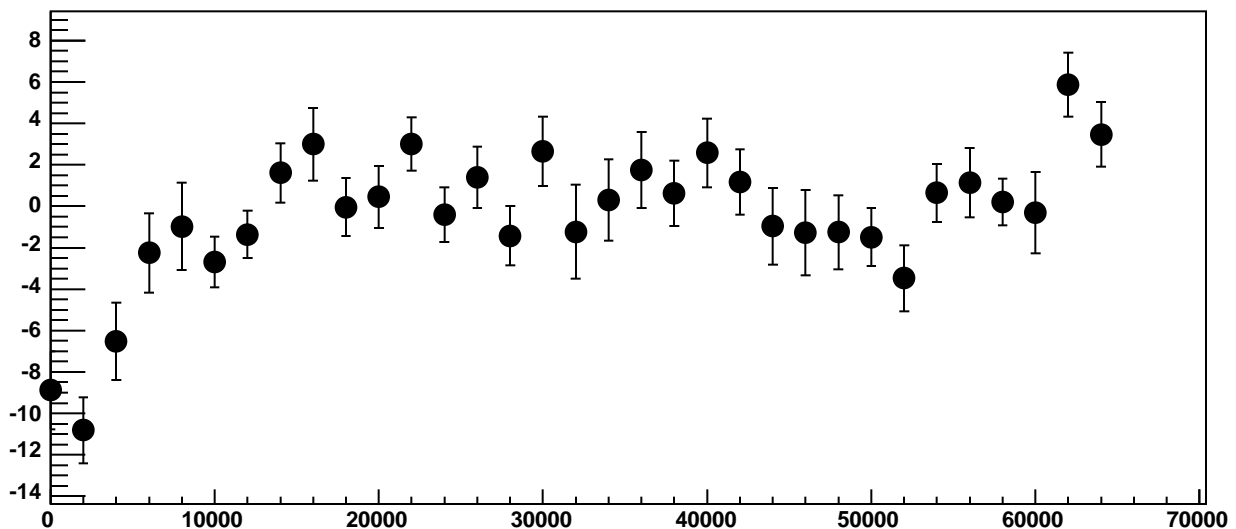


χ^2 / ndf 33.64 / 23
p0 101.2 ± 0.6882
p1 0.003781 ± 2.138e-05

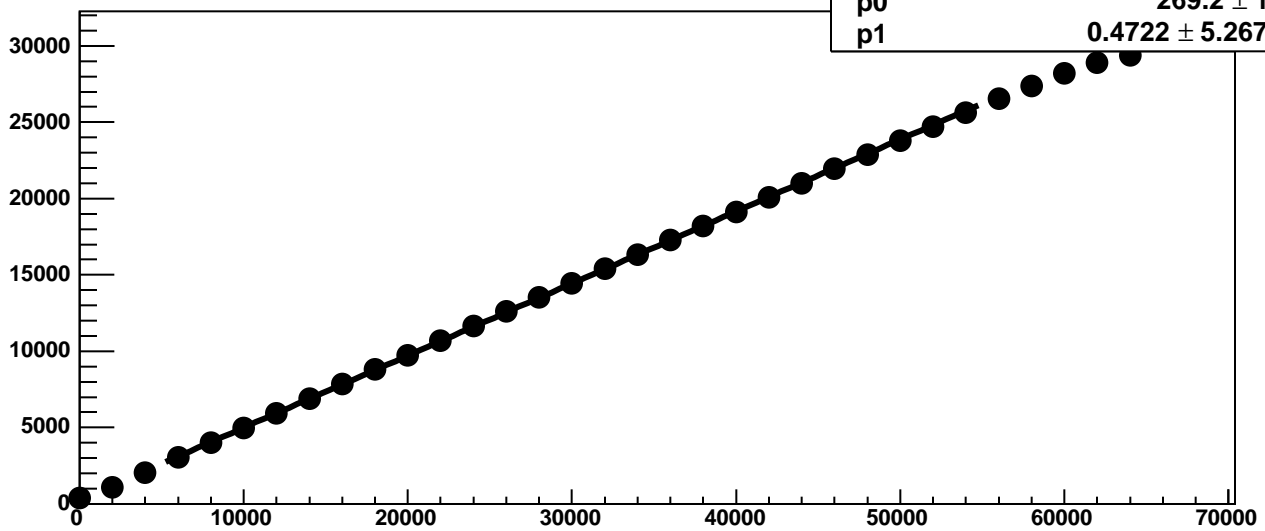
Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

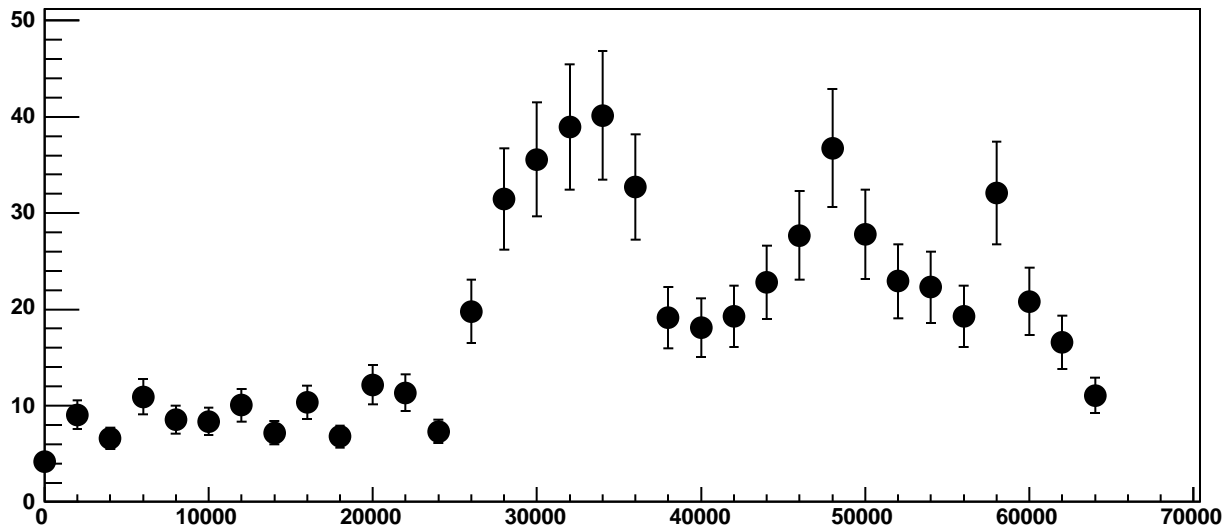


Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

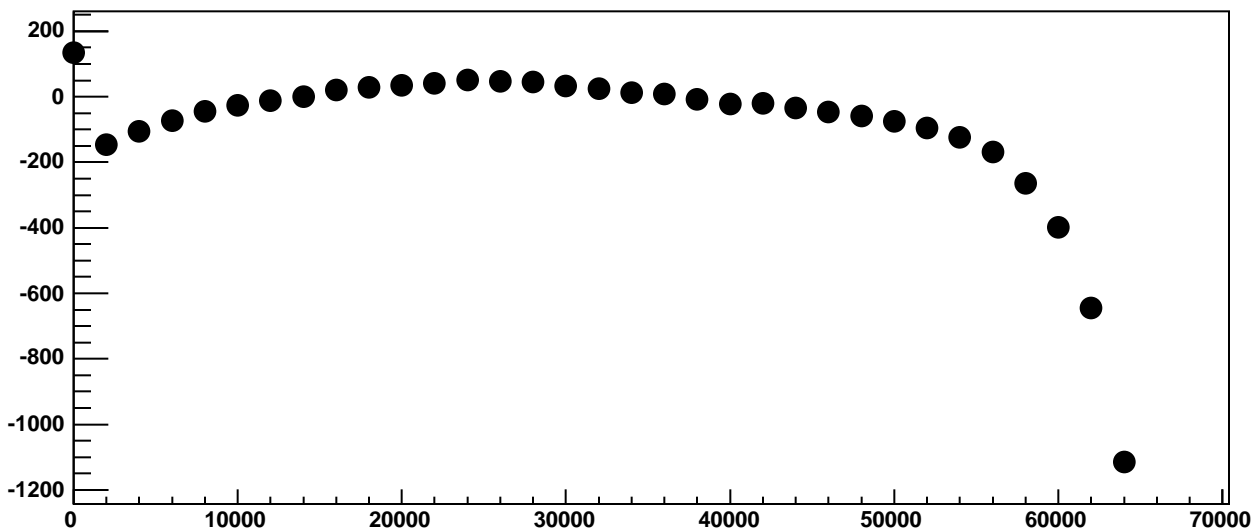


χ^2 / ndf 4762 / 23
p0 269.2 ± 1.172
p1 $0.4722 \pm 5.267\text{e-}05$

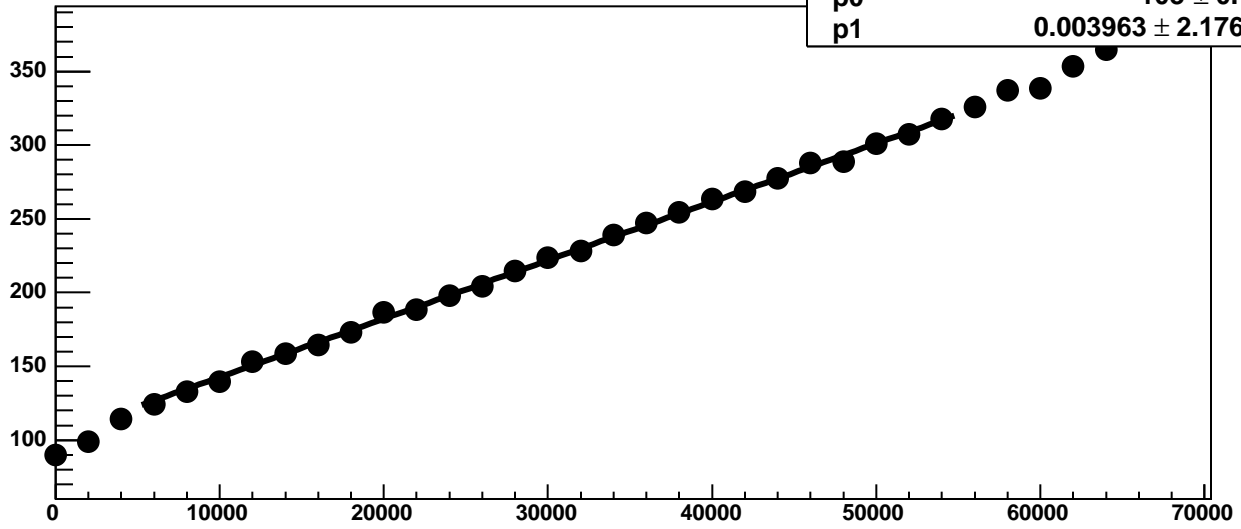
Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

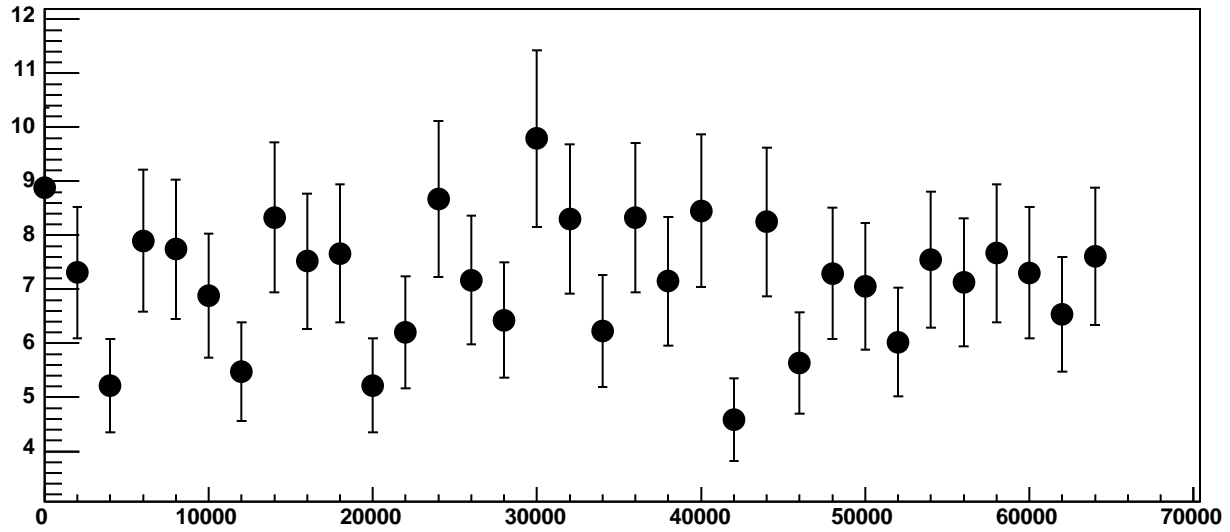


Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

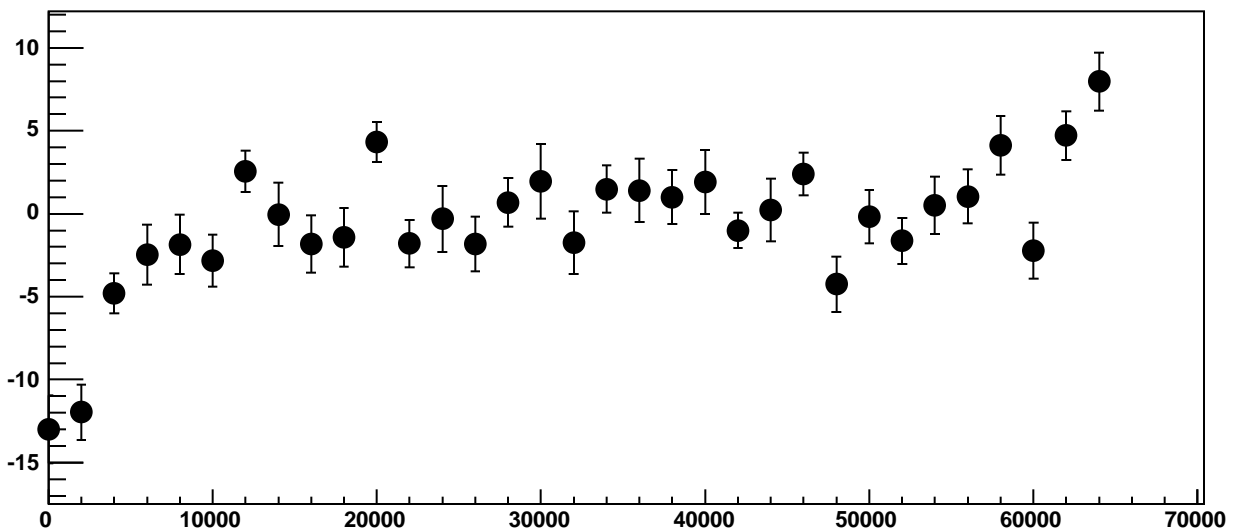


χ^2 / ndf 45.13 / 23
p0 103 ± 0.7366
p1 $0.003963 \pm 2.176e-05$

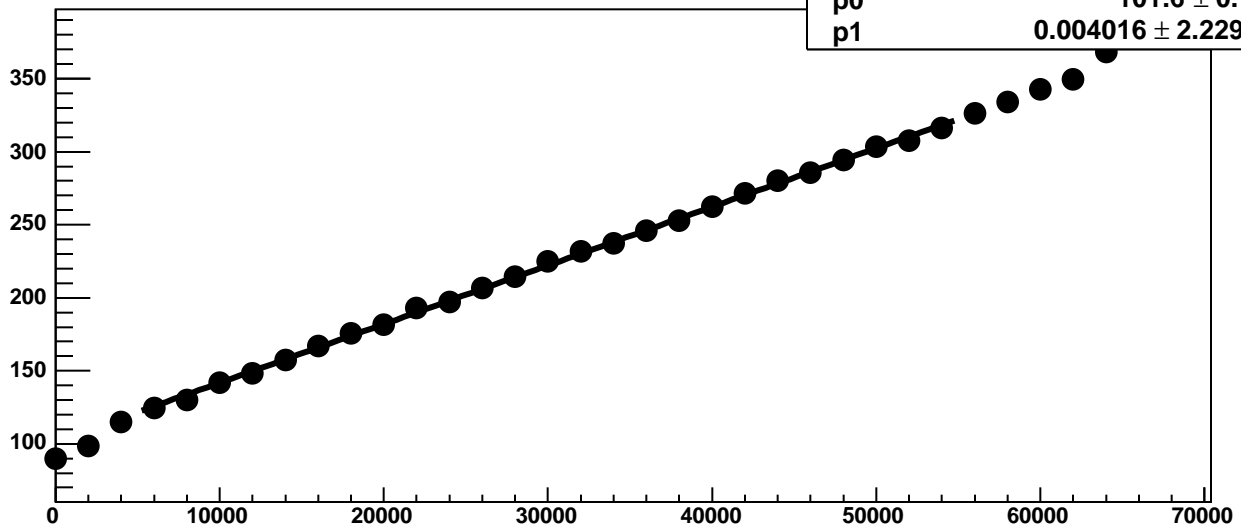
Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

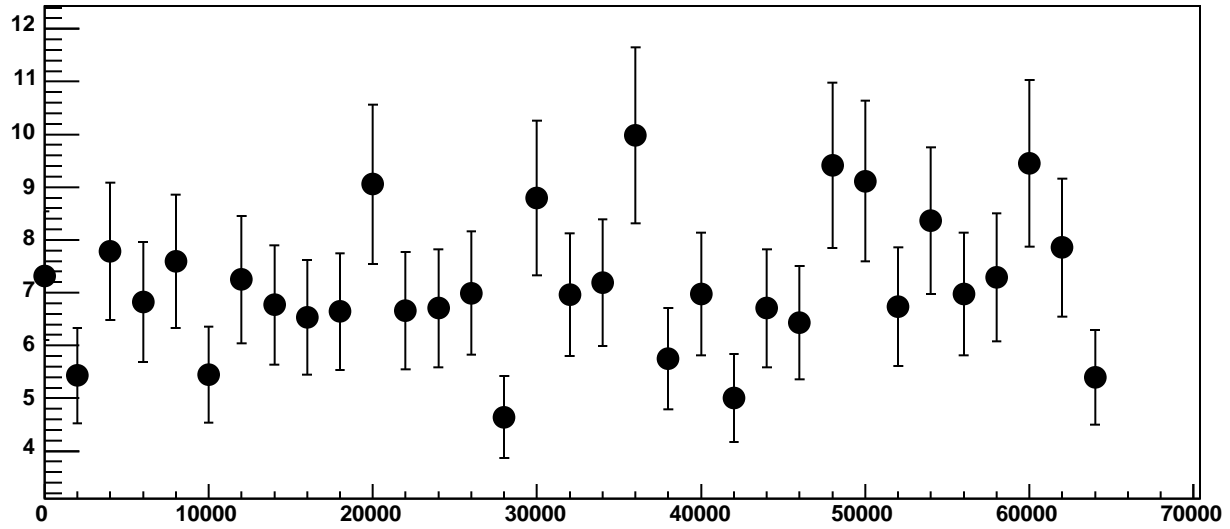


Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

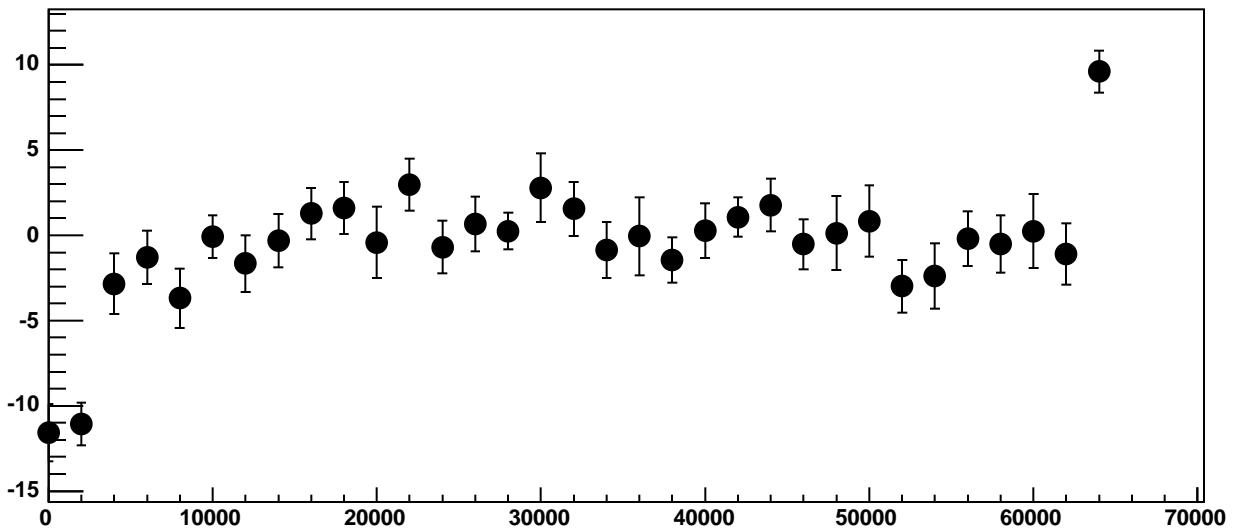


χ^2 / ndf 24.42 / 23
p0 101.6 ± 0.7229
p1 $0.004016 \pm 2.229\text{e-}05$

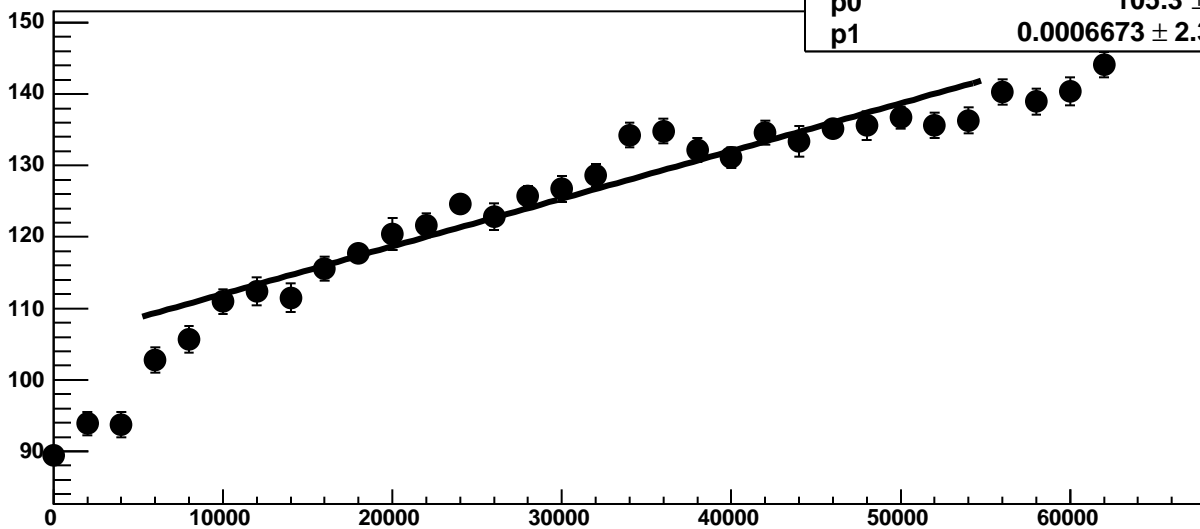
Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

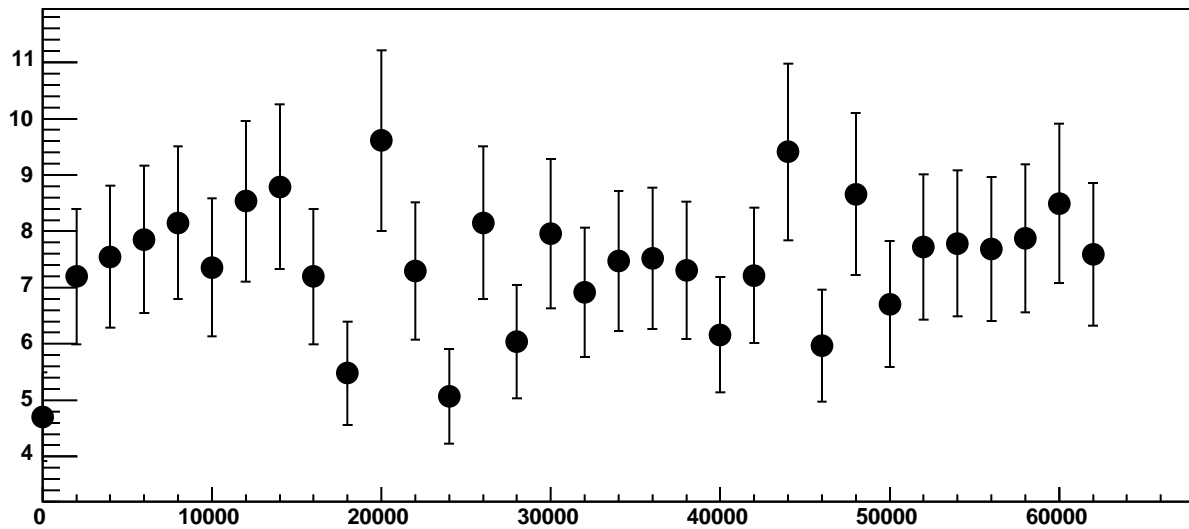


Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

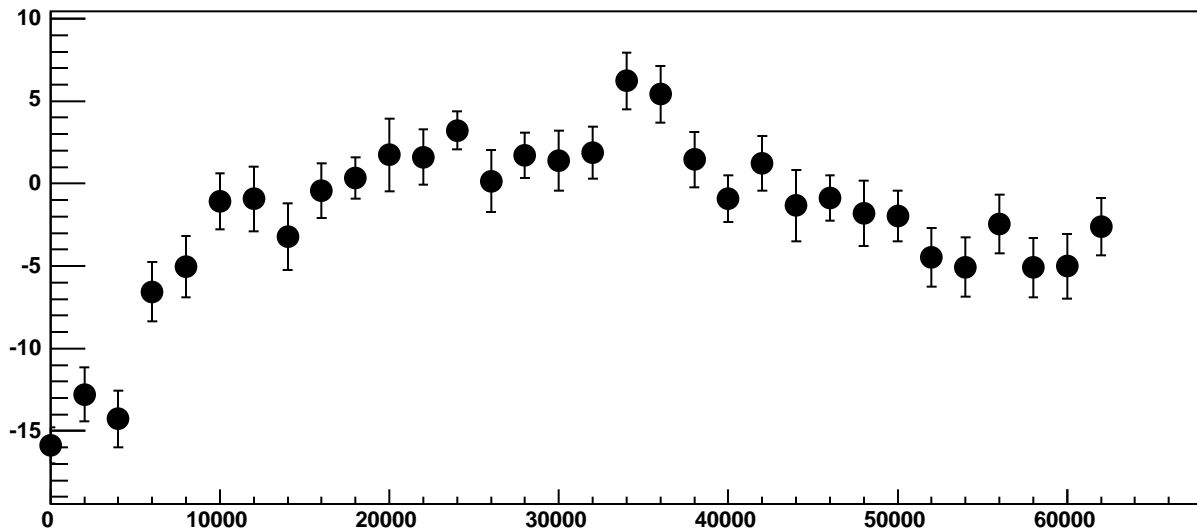


χ^2 / ndf 79 / 23
p0 105.3 ± 0.7933
p1 $0.0006673 \pm 2.389e-05$

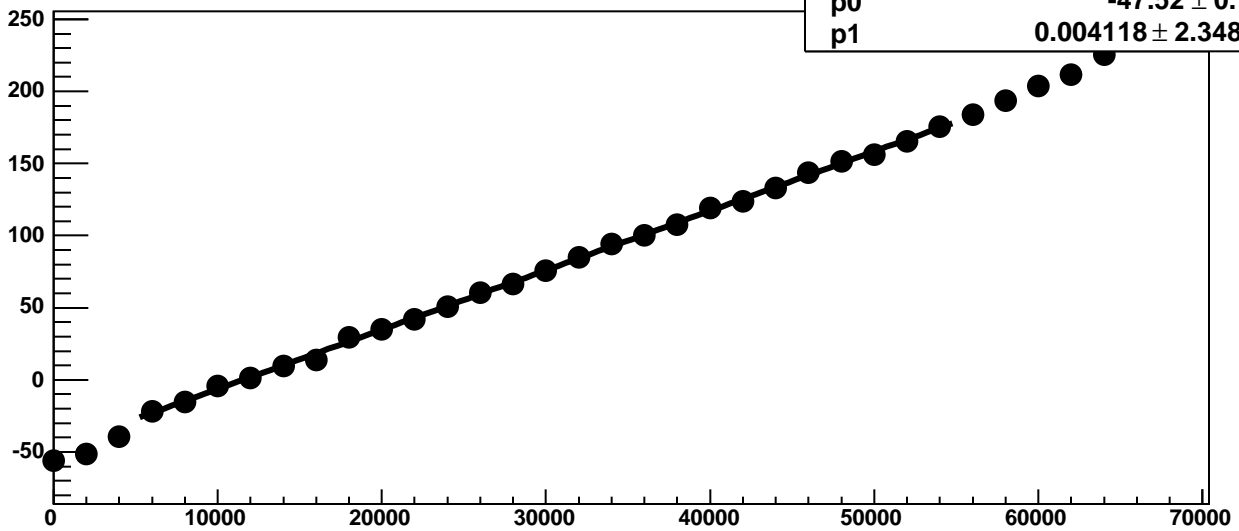
Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

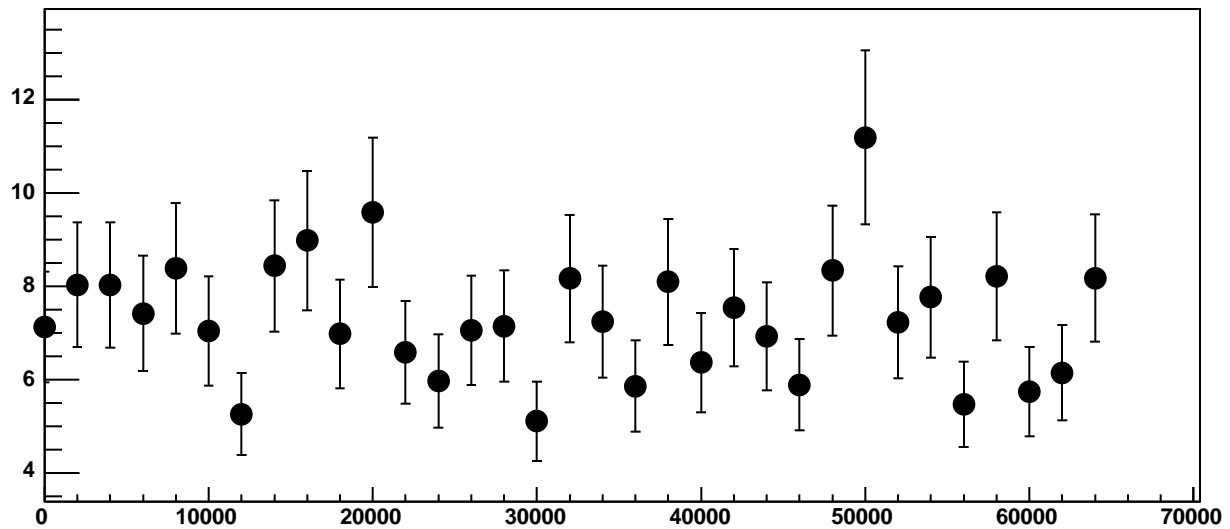


Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

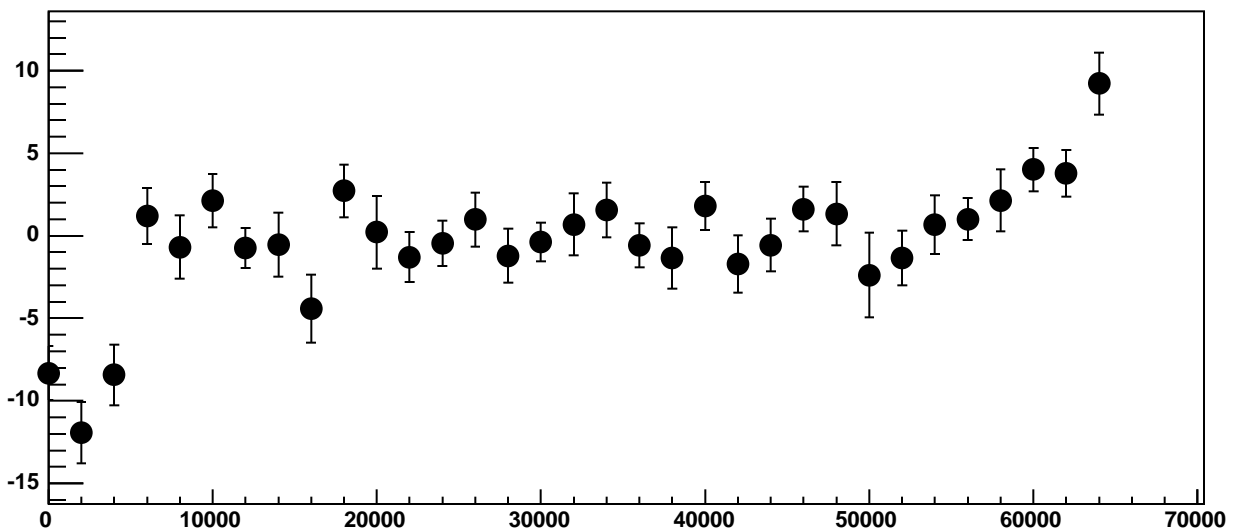


χ^2 / ndf 20.03 / 23
p0 -47.52 ± 0.7675
p1 $0.004118 \pm 2.348e-05$

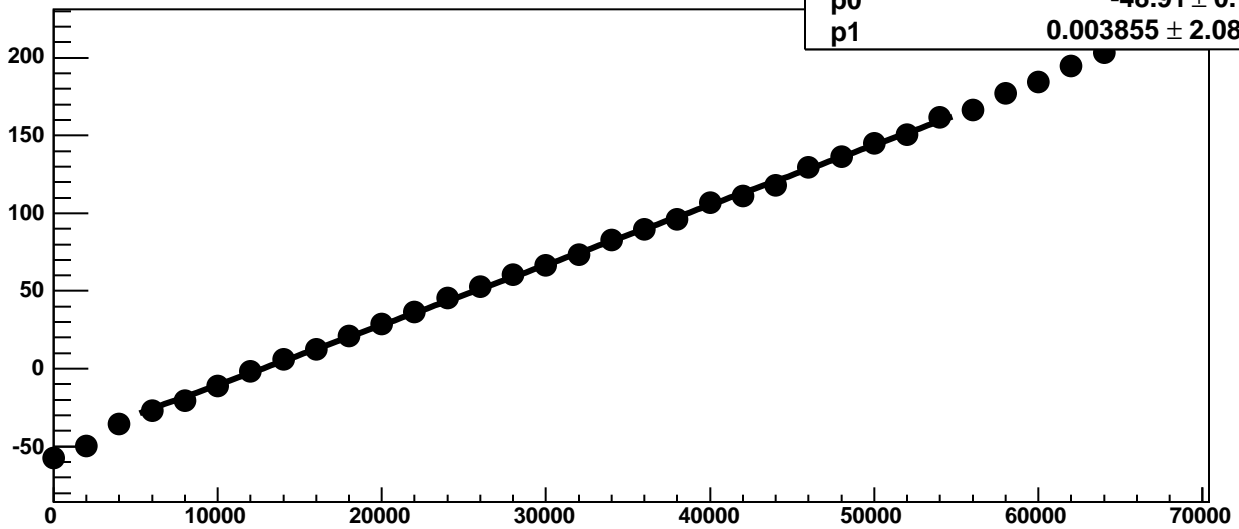
Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

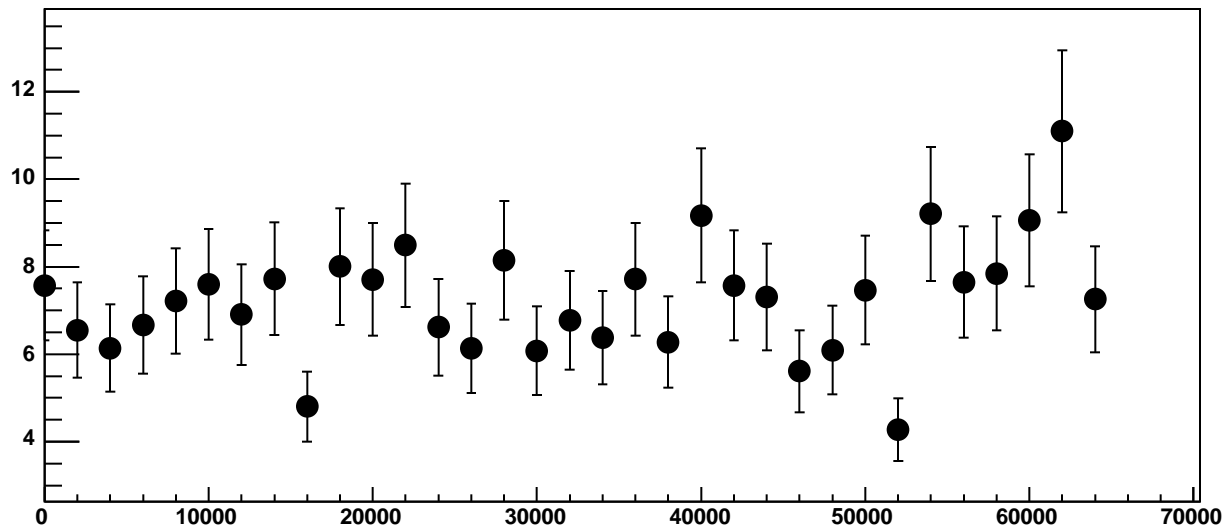


Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

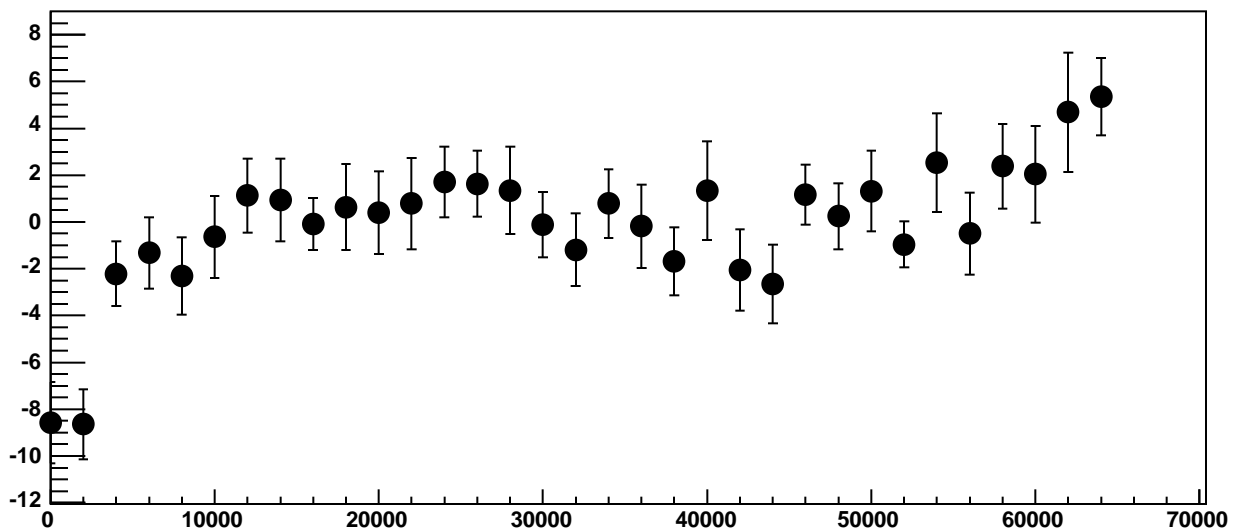


χ^2 / ndf 17.46 / 23
p0 -48.91 ± 0.7148
p1 $0.003855 \pm 2.08e-05$

Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

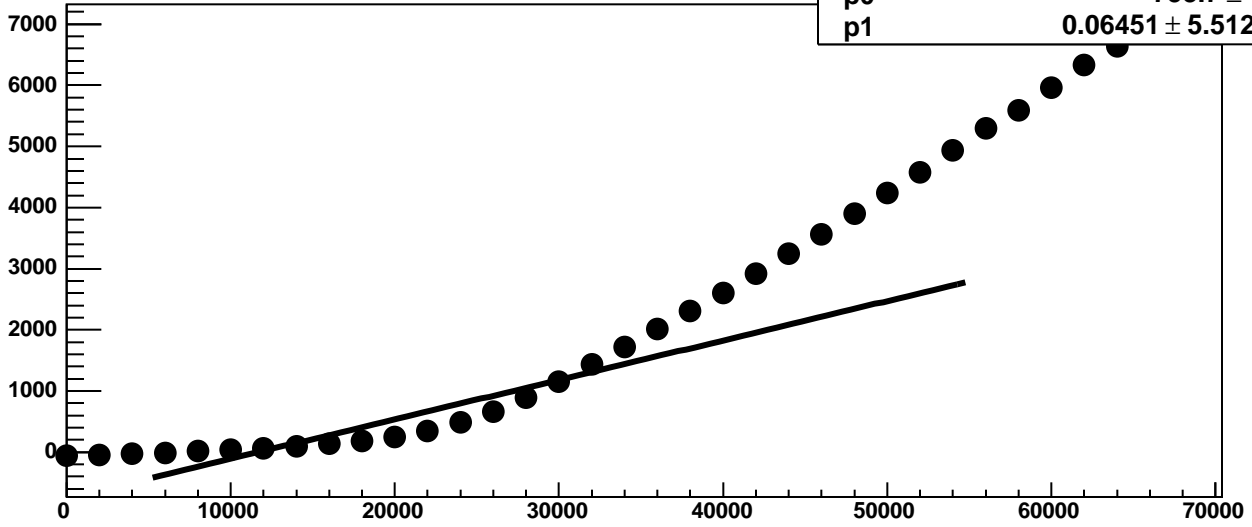


Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

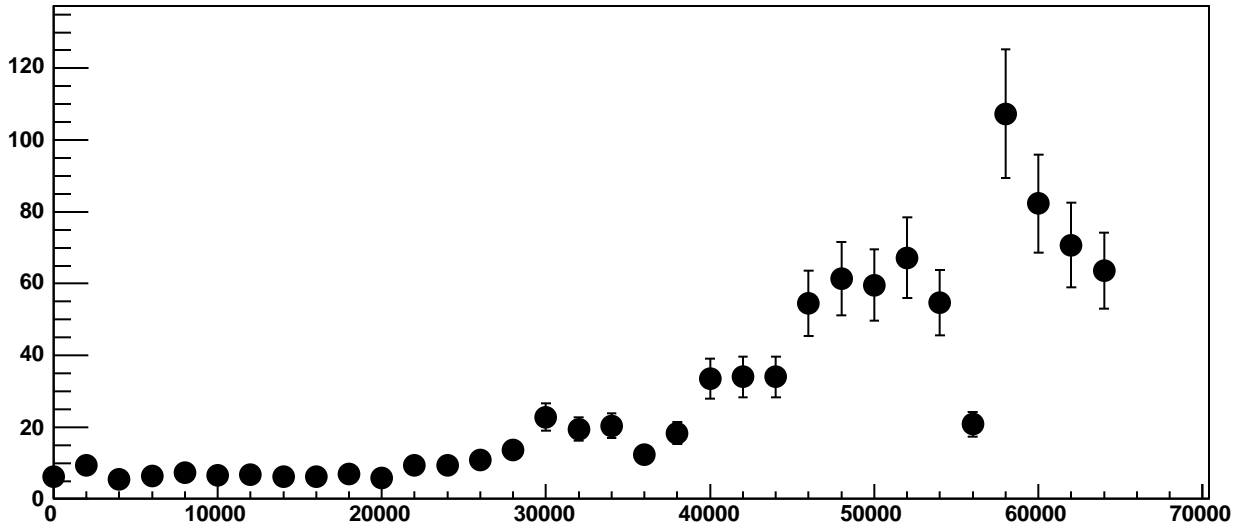


Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

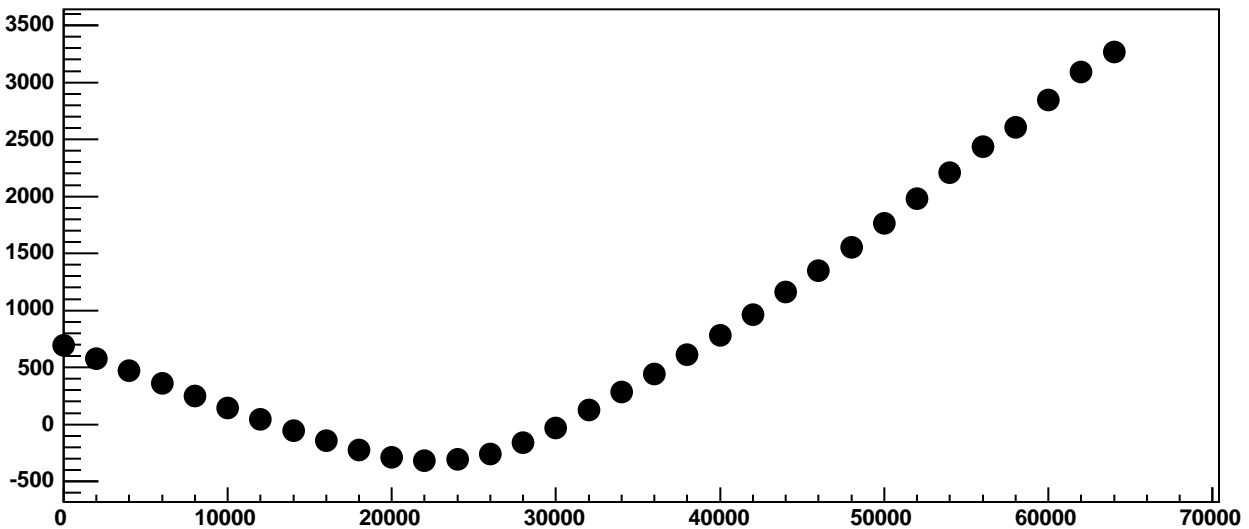
χ^2 / ndf 4.092e+05 / 23
p0 -753.7 ± 1.04
p1 0.06451 ± 5.512e-05



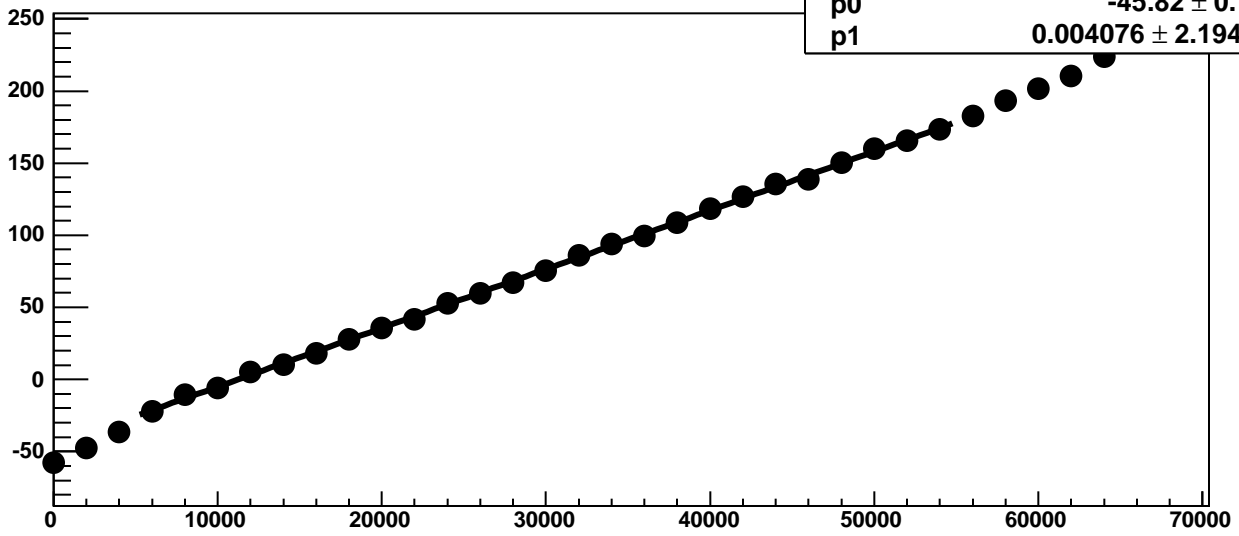
Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



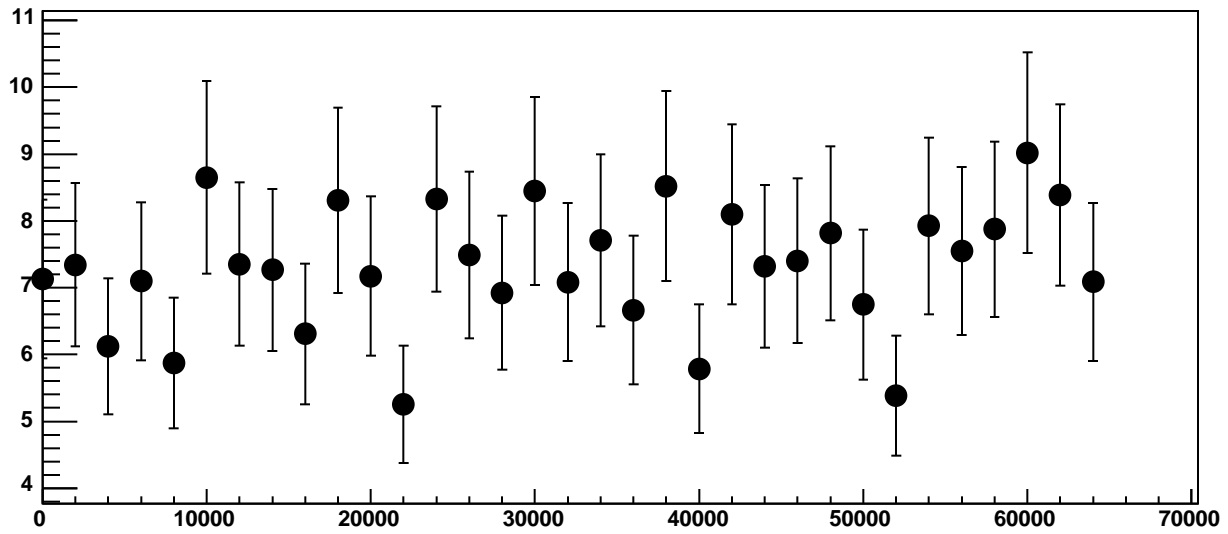
Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



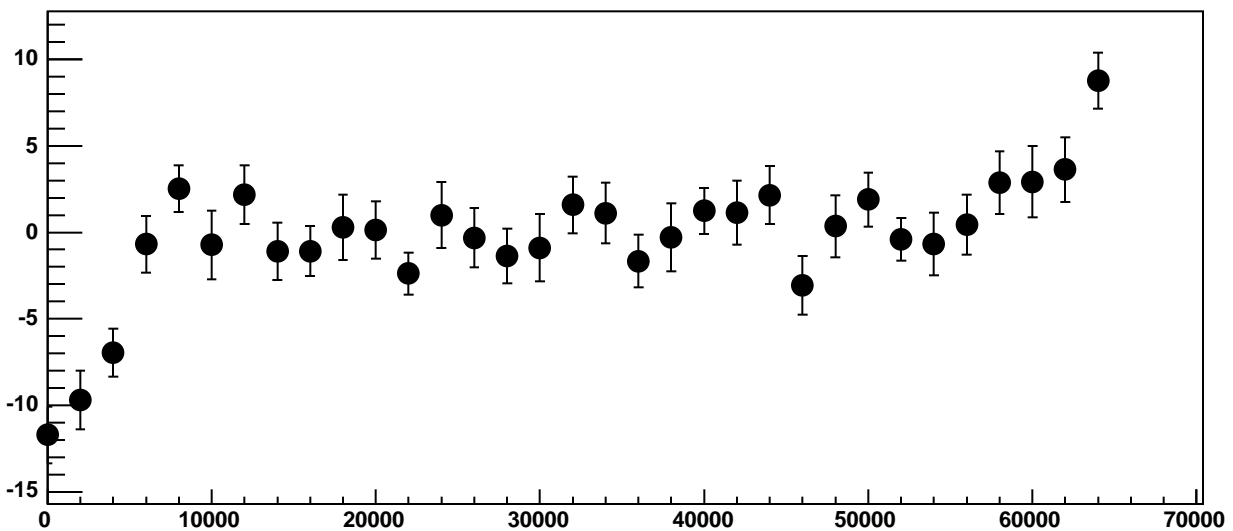
Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



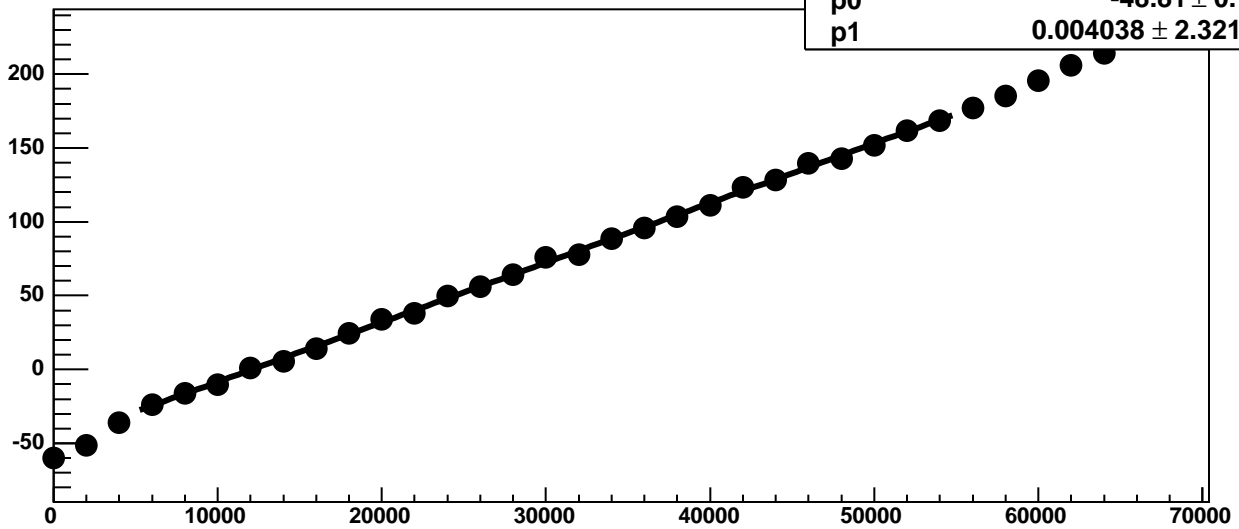
Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

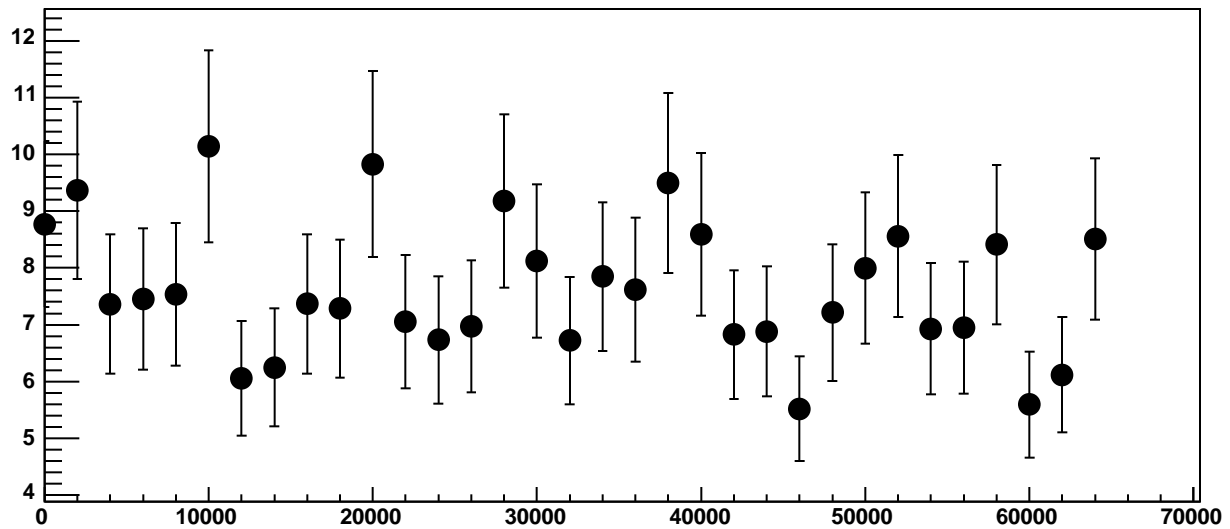


Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



χ^2 / ndf 28.17 / 23
p0 -48.81 ± 0.7773
p1 $0.004038 \pm 2.321e-05$

Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

