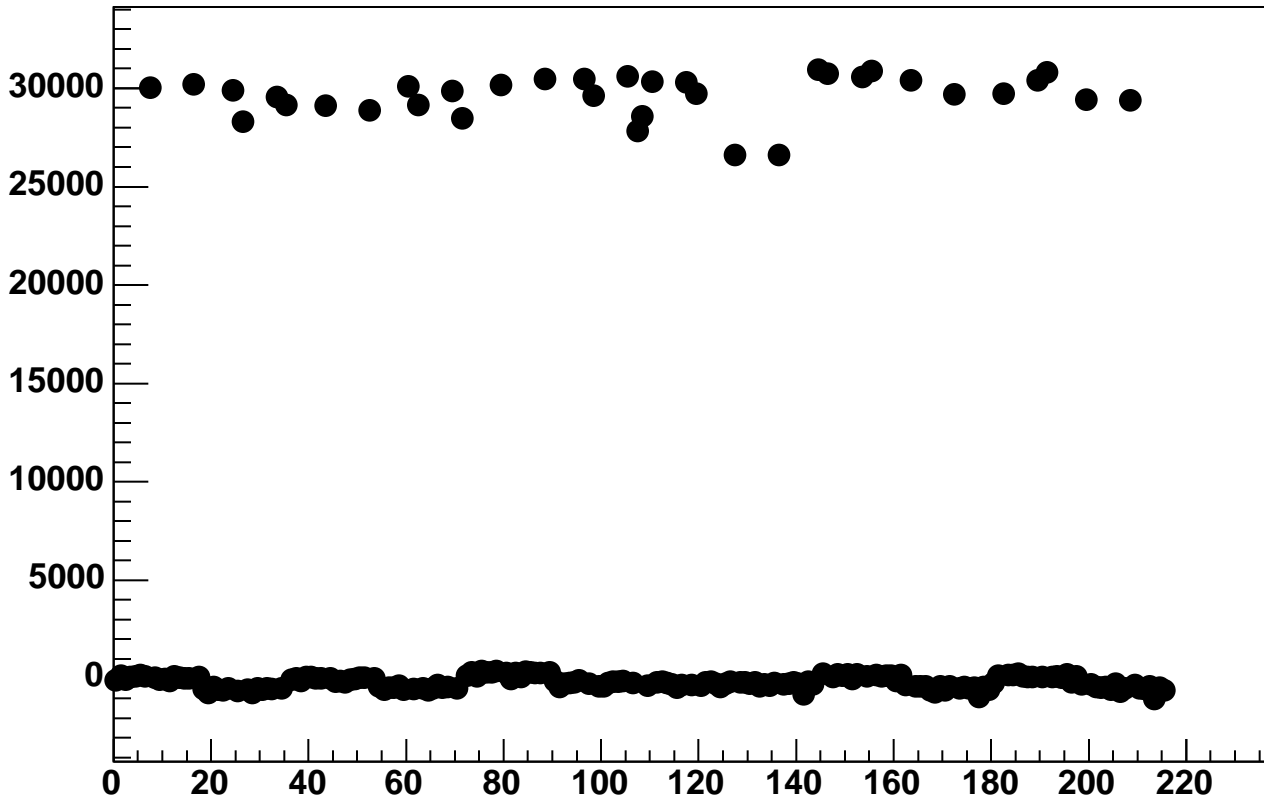
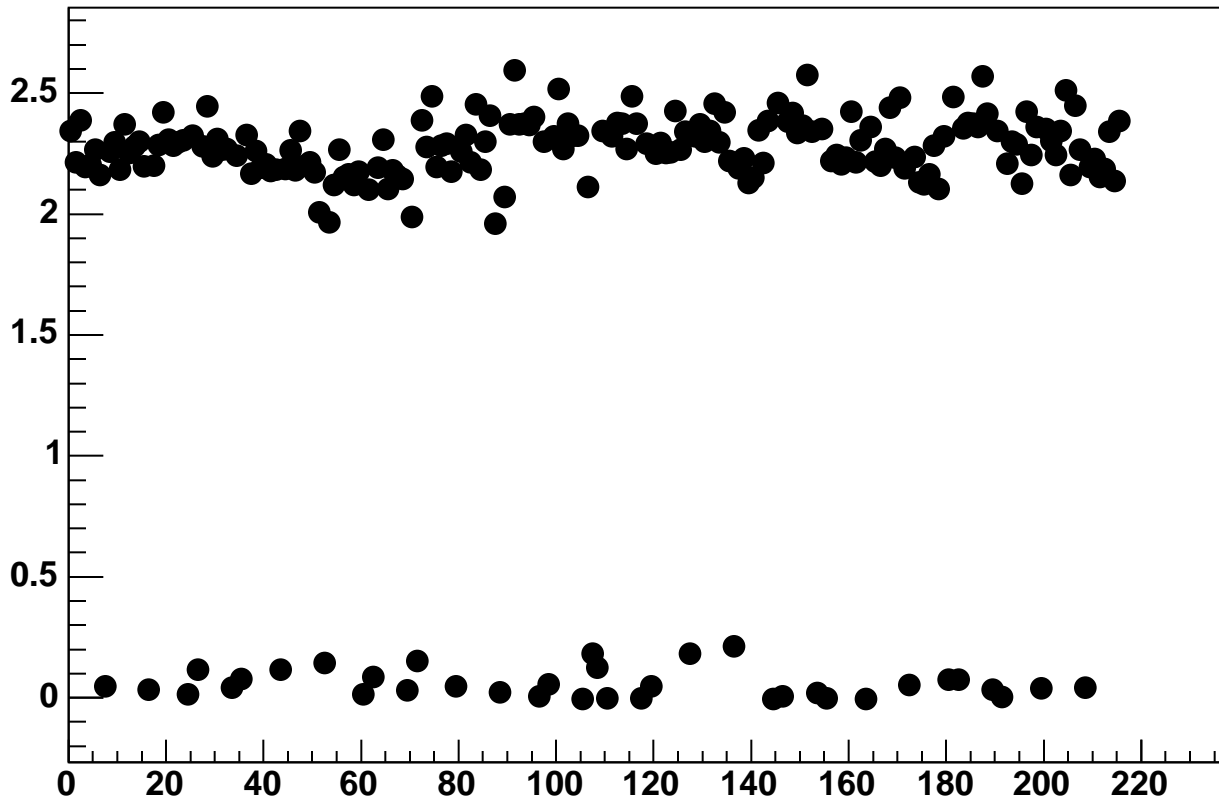


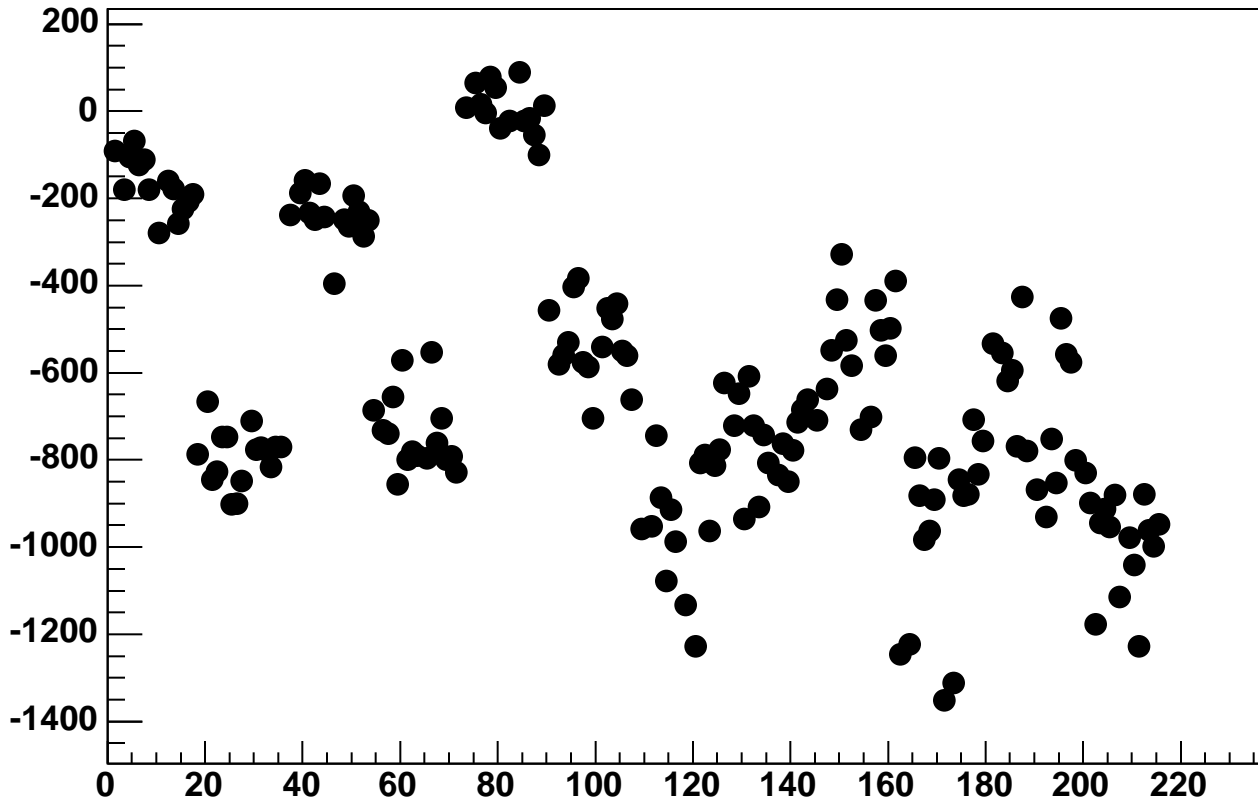
Channel Enabled, Hold=35, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



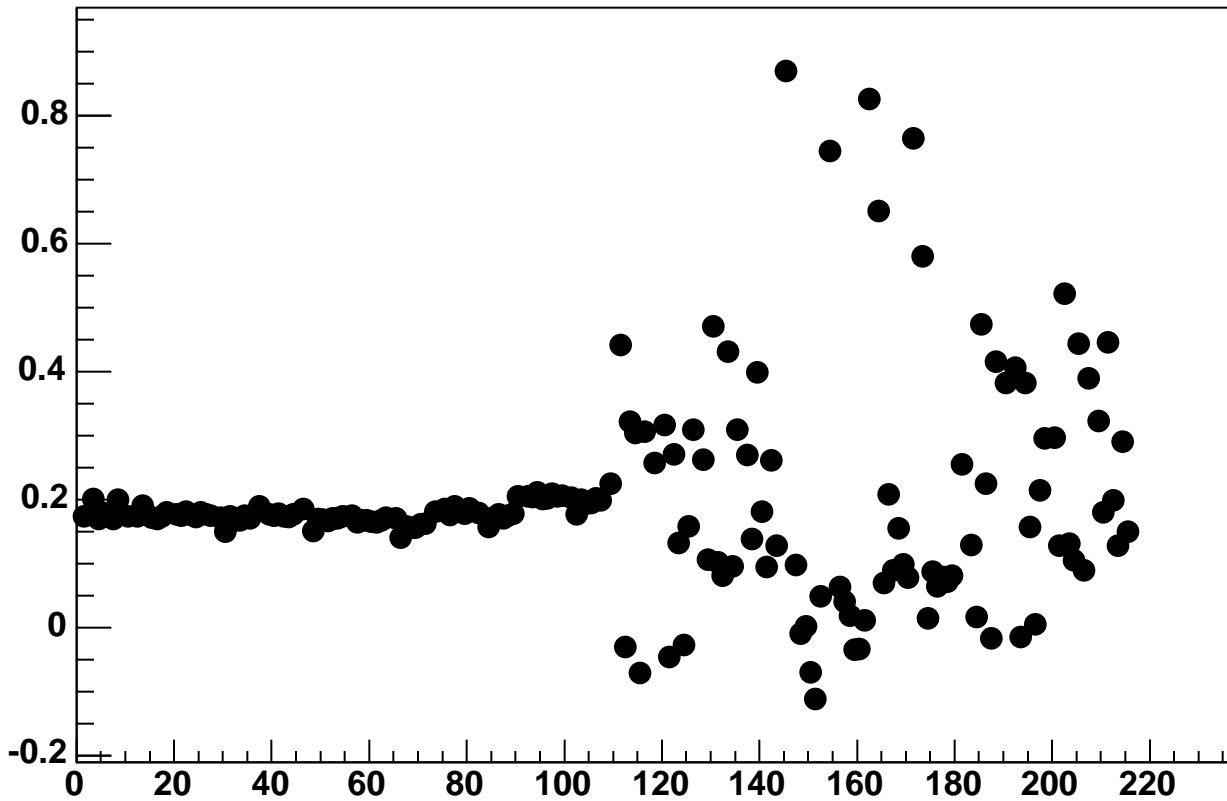
Channel Enabled, Hold=35, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



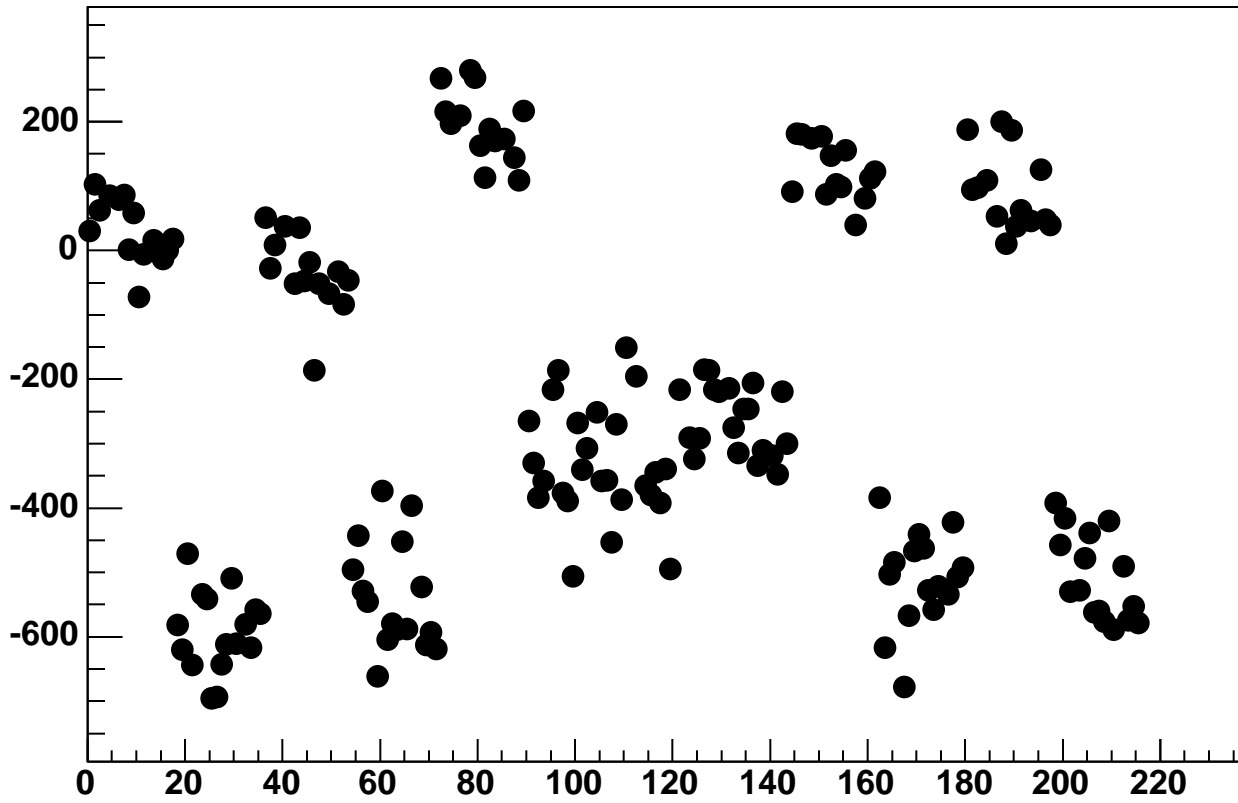
Disabled 0, Hold=35, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



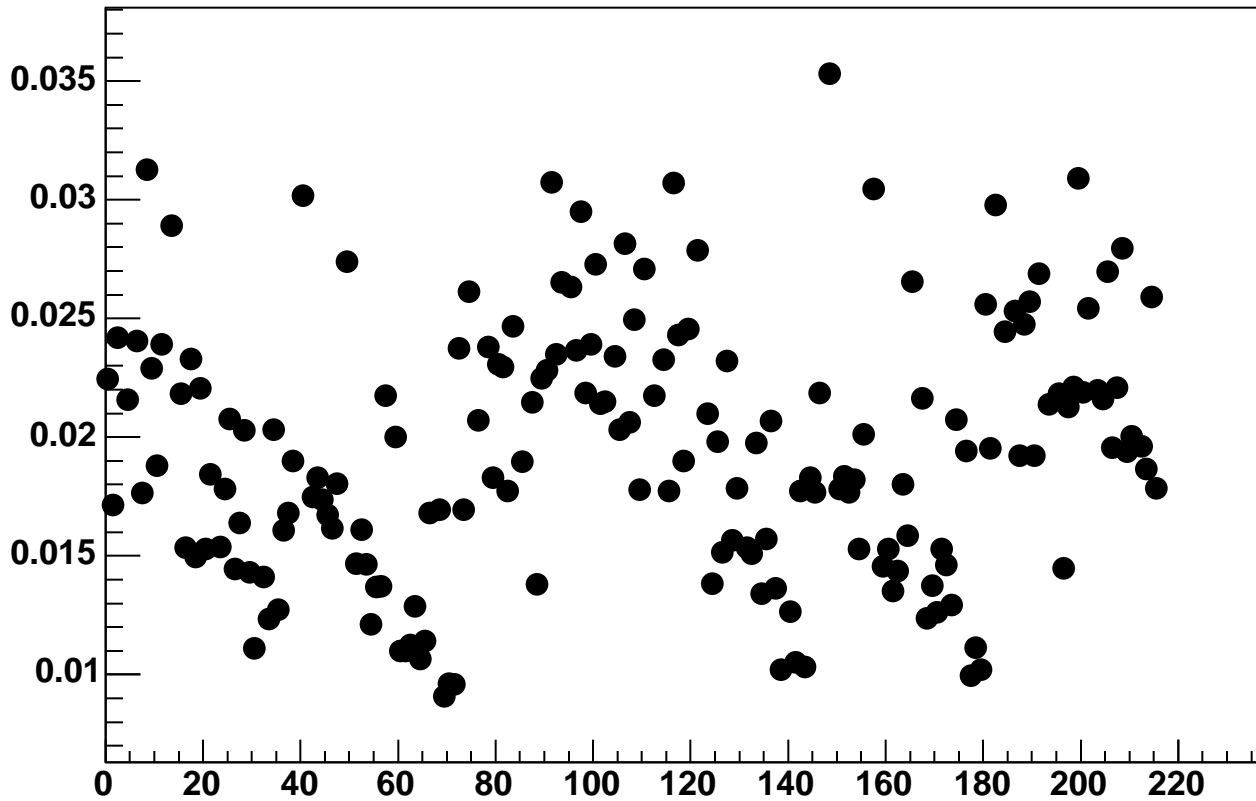
Disabled 0, Hold=35, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



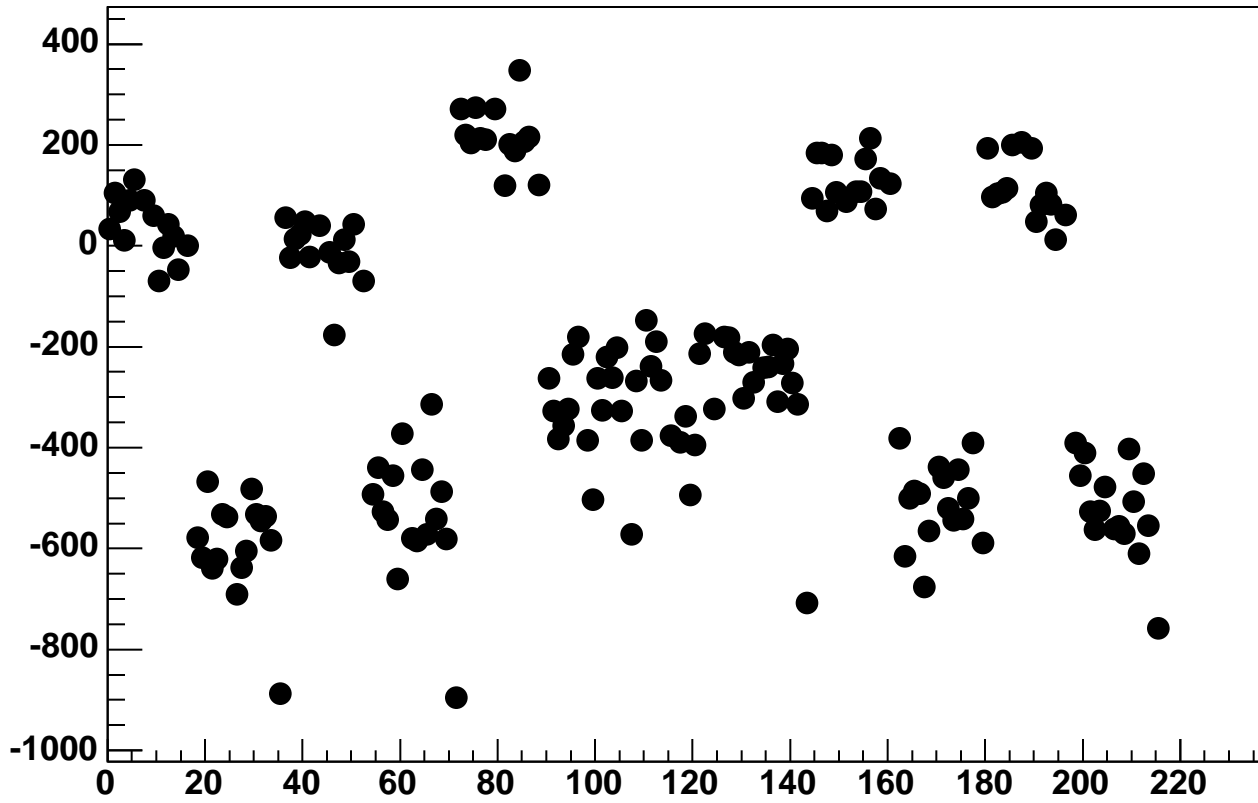
Disabled 1, Hold=35, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



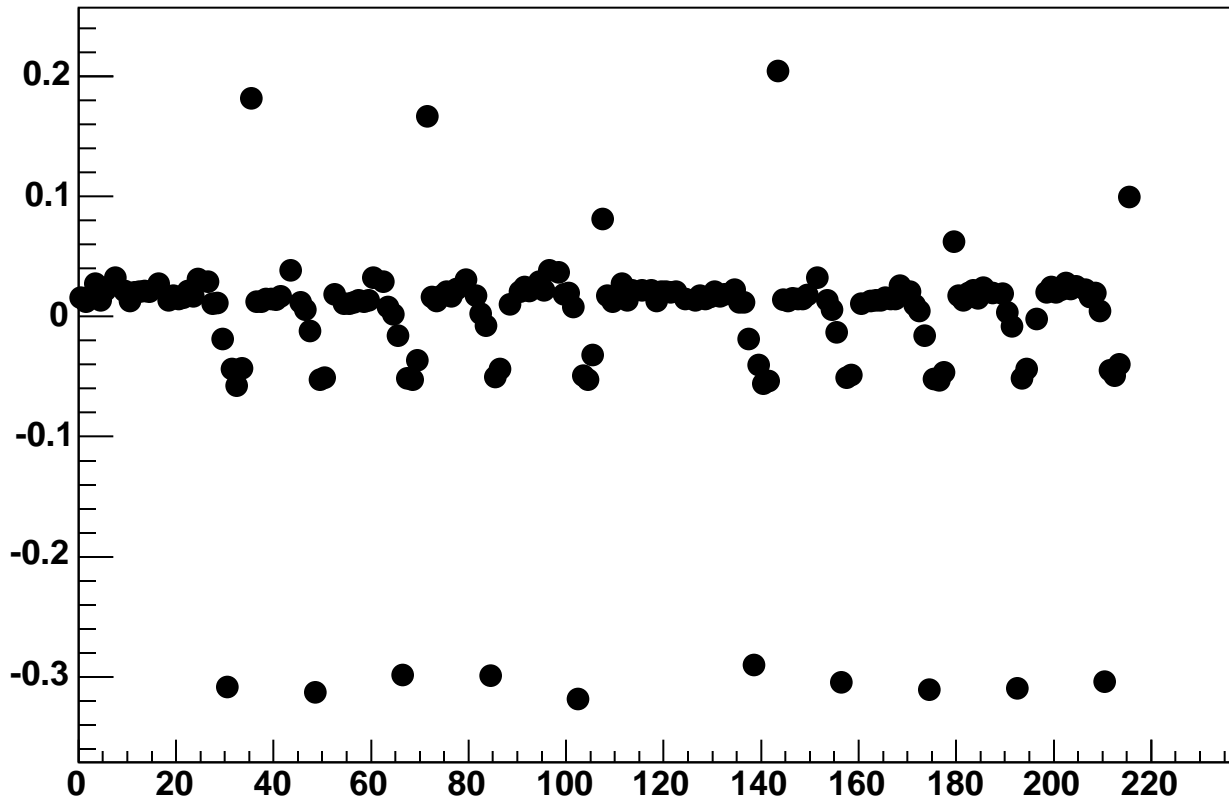
Disabled 1, Hold=35, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



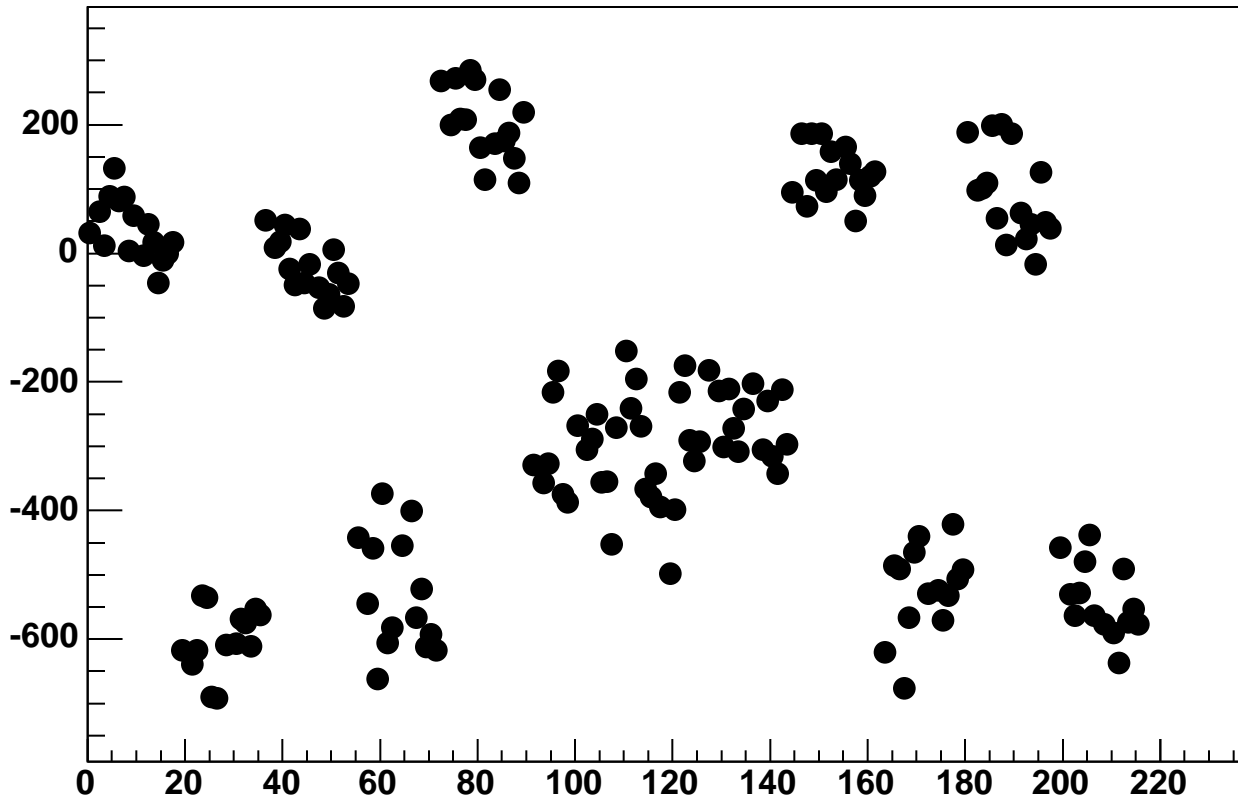
Disabled 2, Hold=35, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



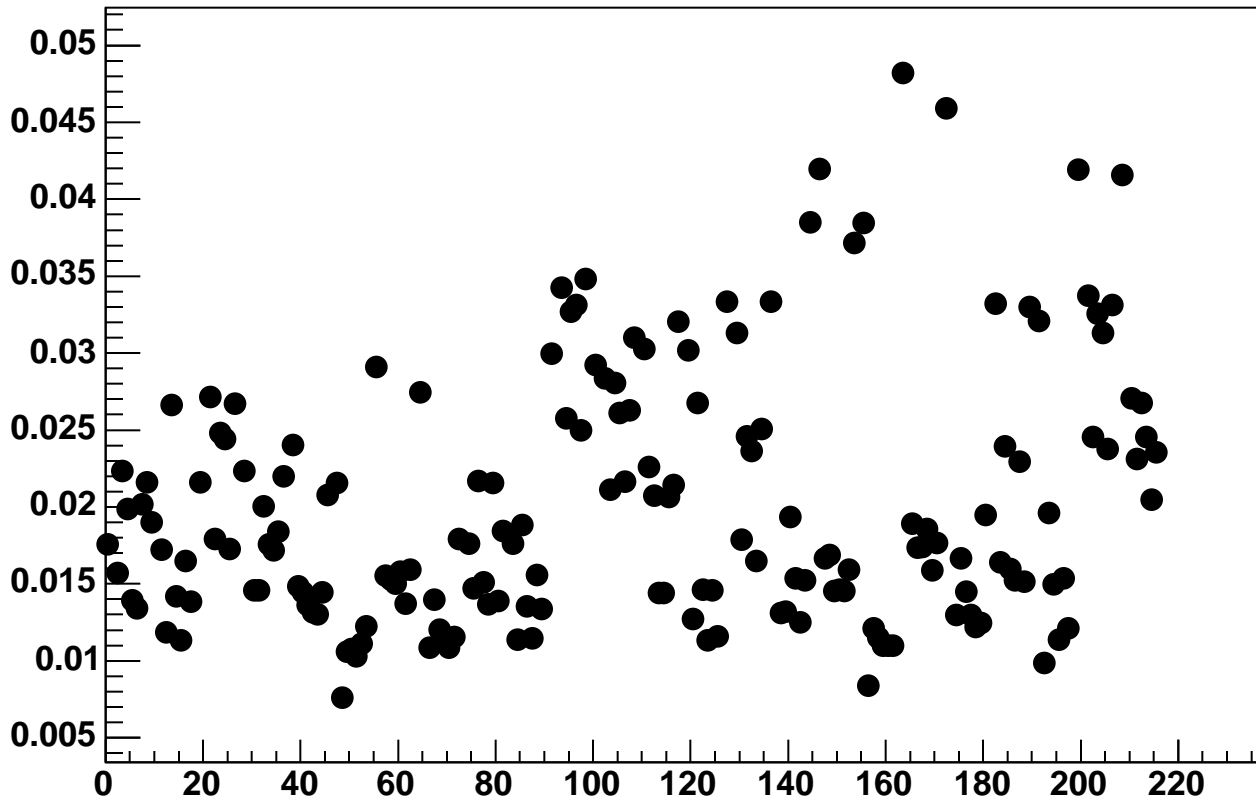
Disabled 2, Hold=35, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



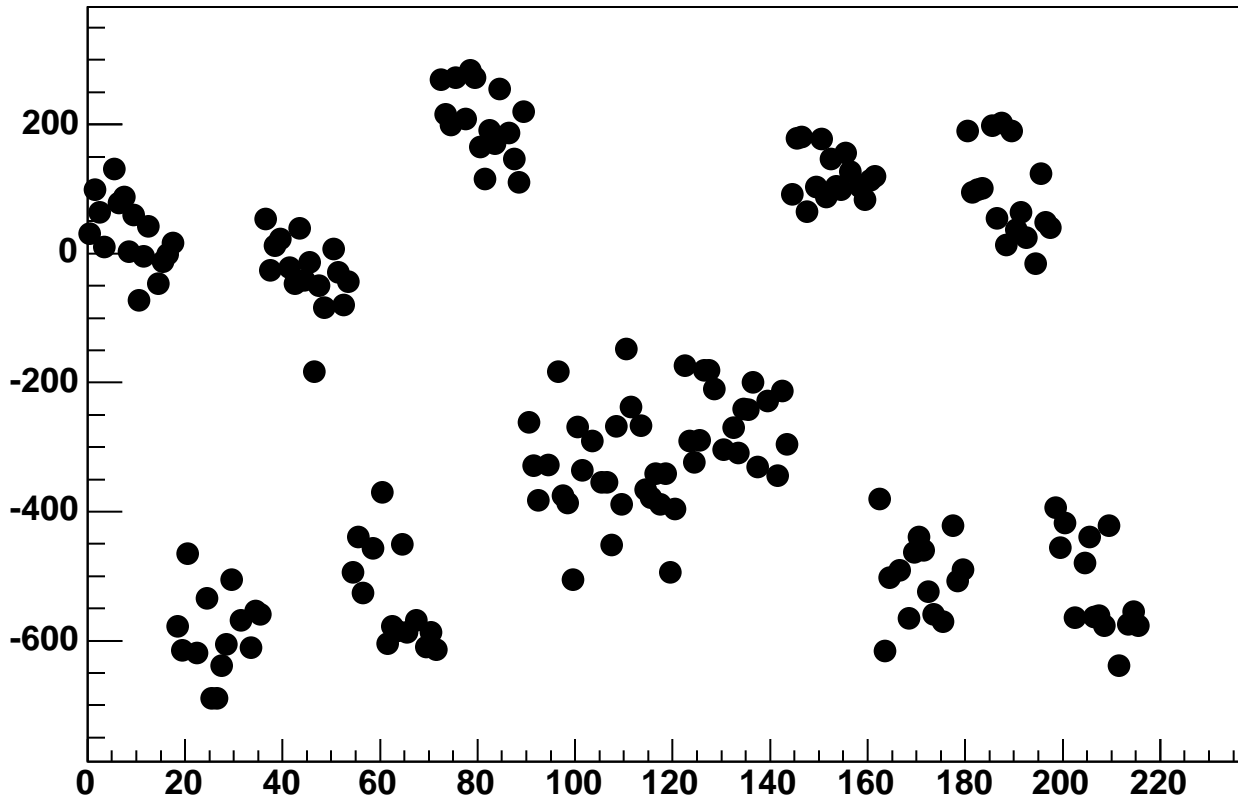
Disabled 3, Hold=35, Fit Intercept vs 18\*Chip+Chan



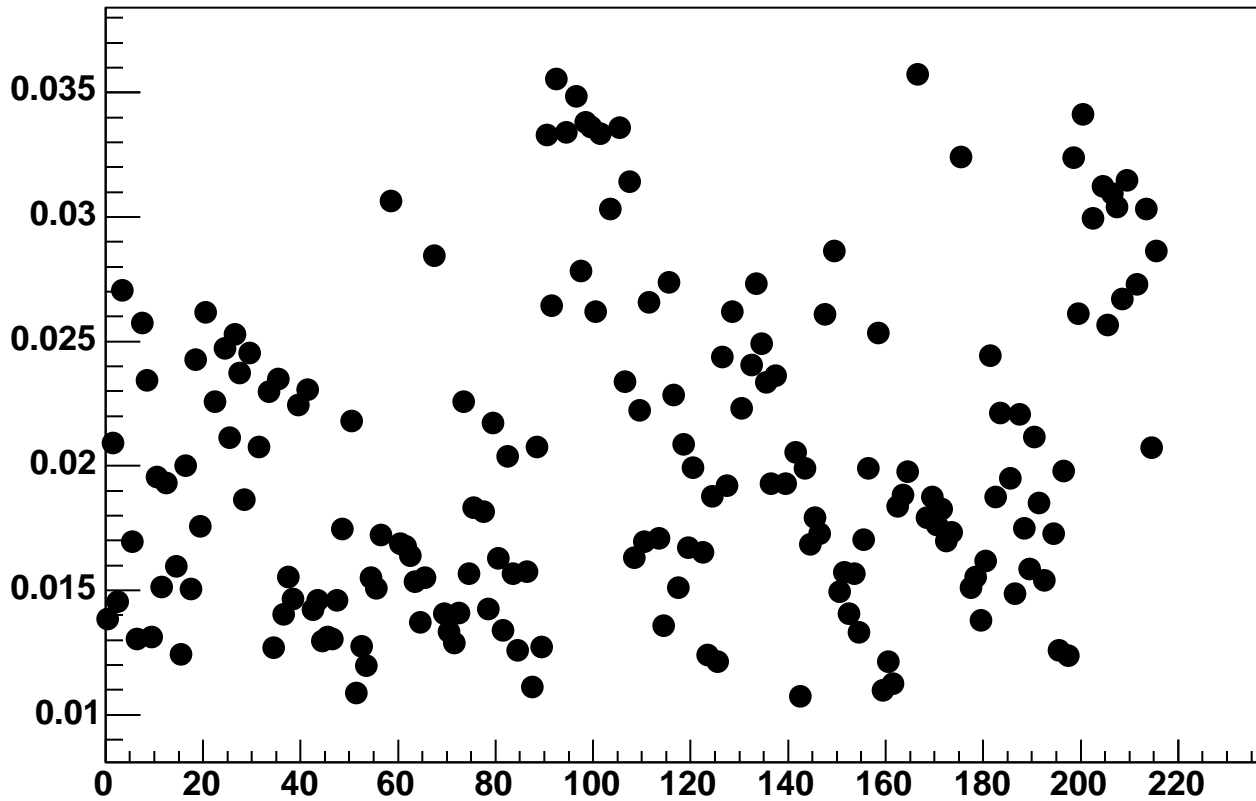
Disabled 3, Hold=35, Fit Slope vs 18\*Chip+Chan



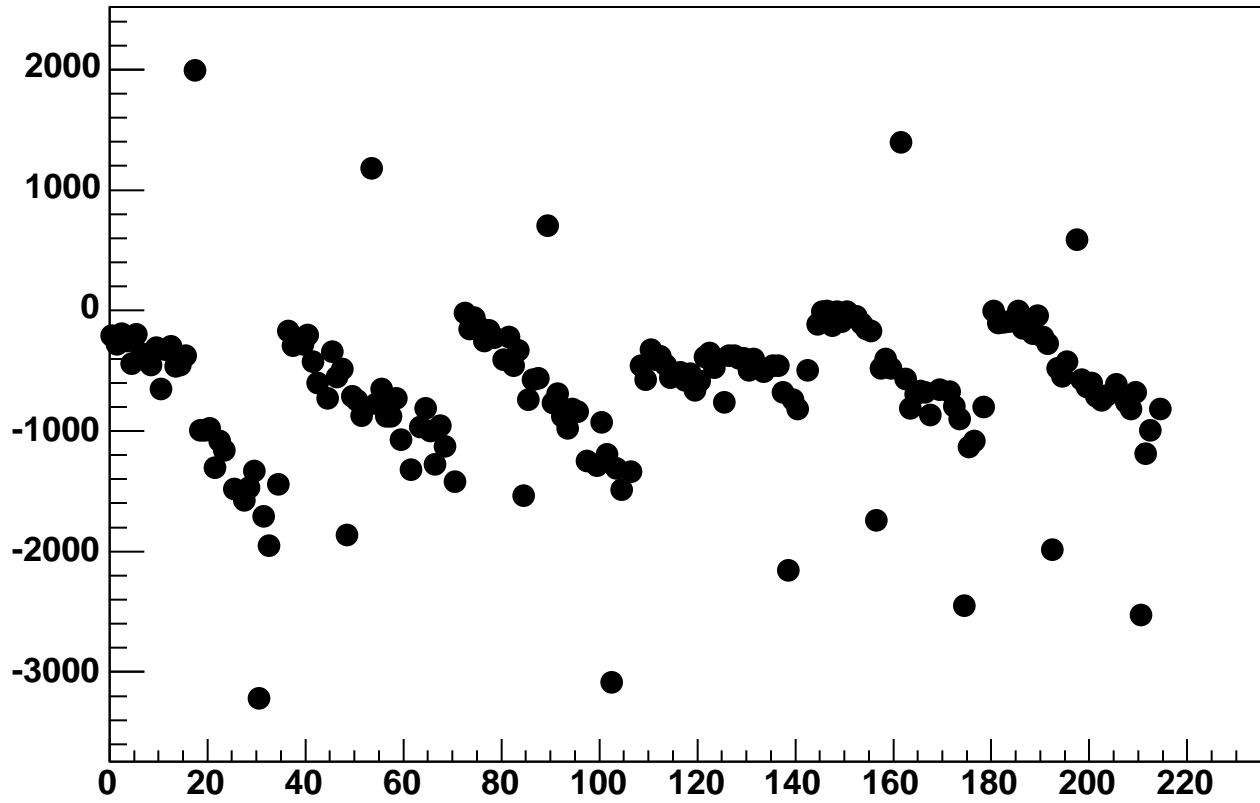
Disabled 4, Hold=35, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



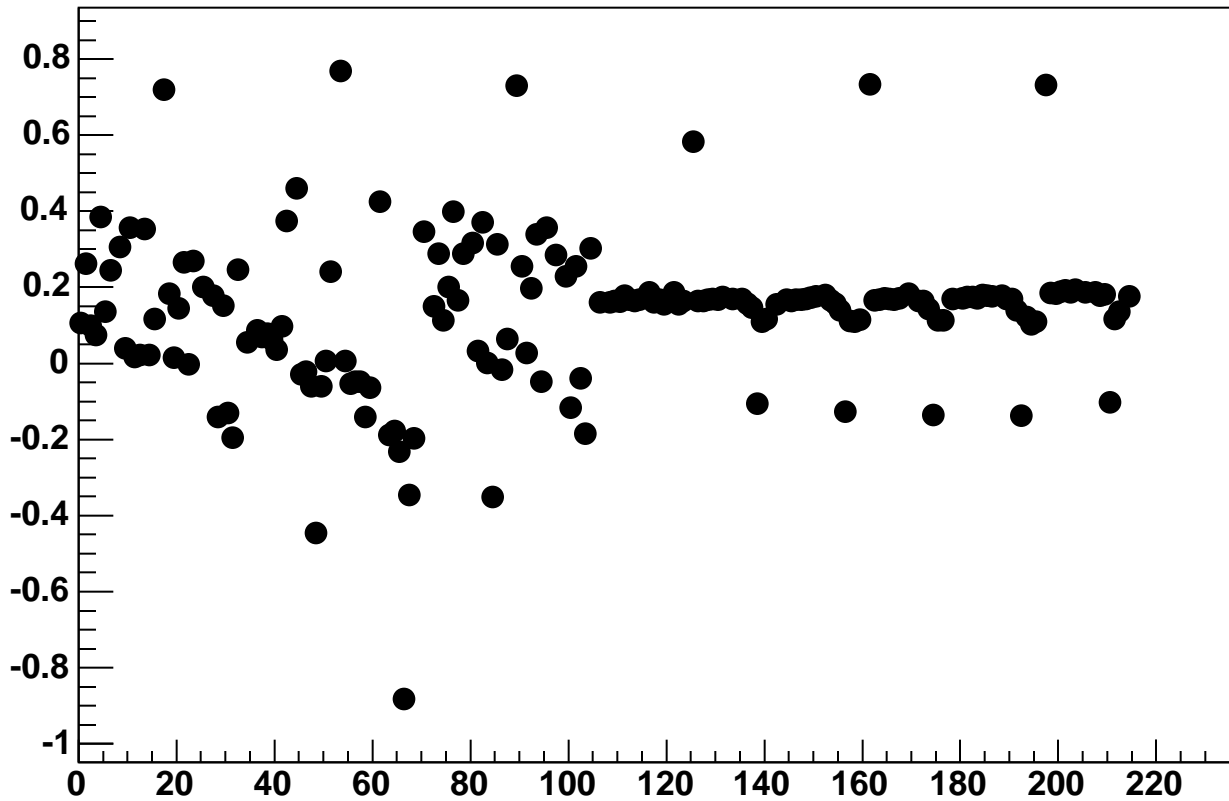
Disabled 4, Hold=35, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



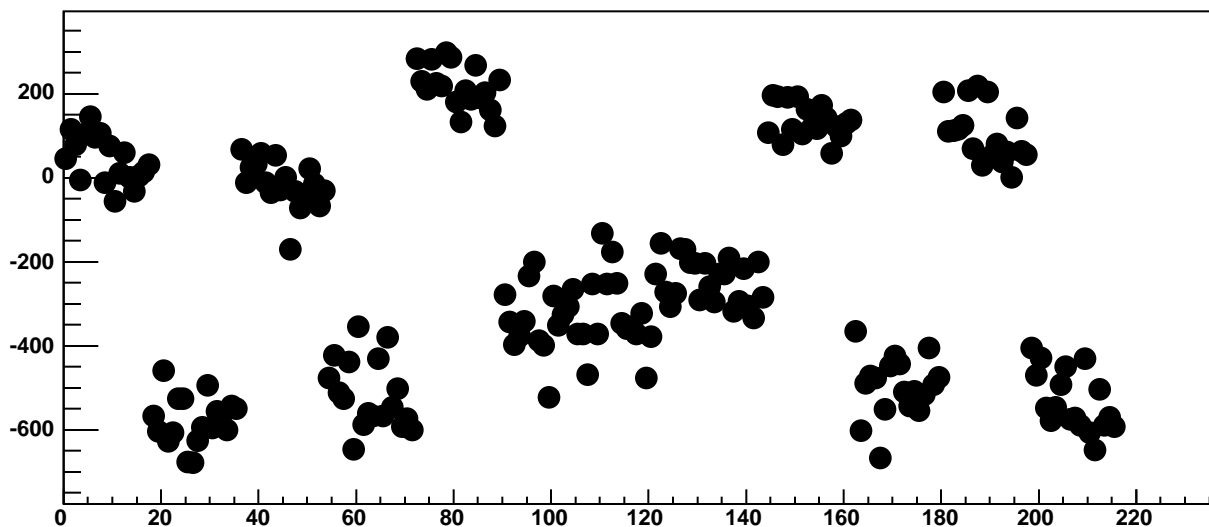
Disabled 5, Hold=35, Fit Intercept vs  $18 \cdot \text{Chip} + \text{Chan}$



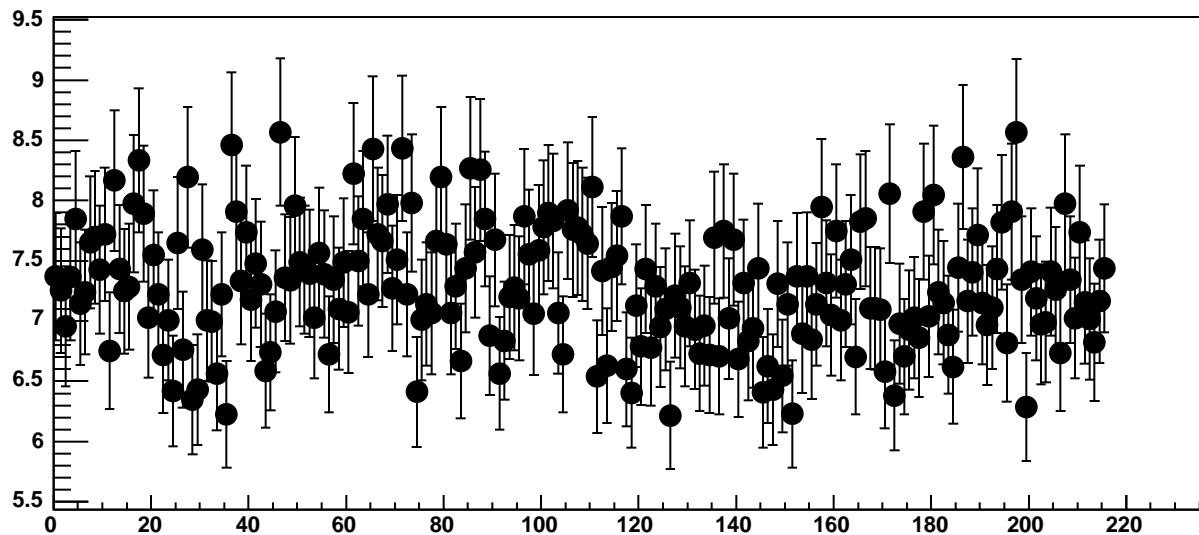
Disabled 5, Hold=35, Fit Slope vs  $18 \cdot \text{Chip} + \text{Chan}$



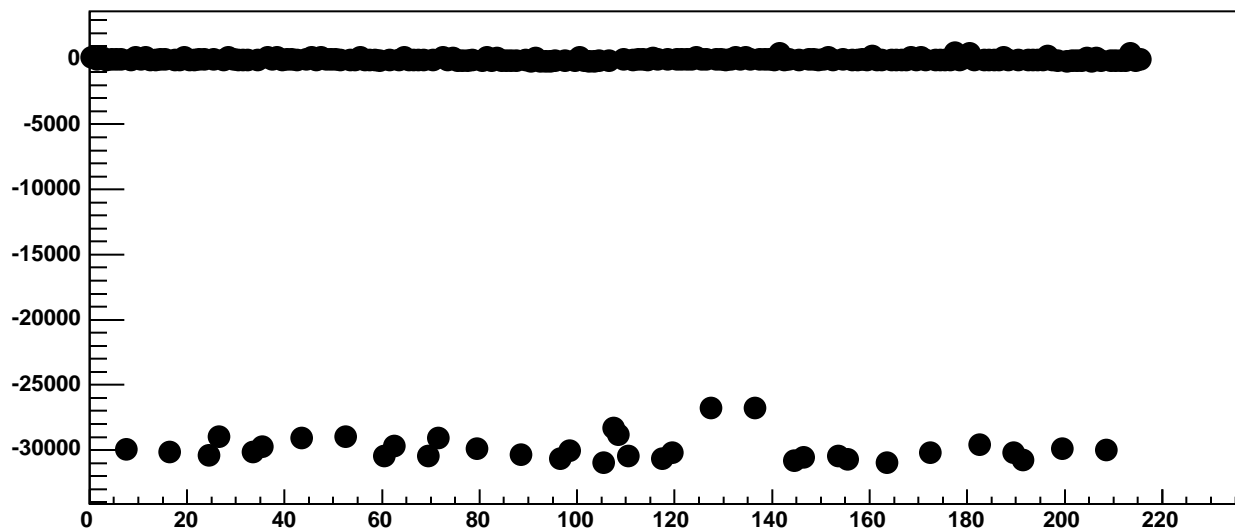
Enables off, ADC Mean vs 18\*Chip+Chan



Enables off, ADC Noise vs 18\*Chip+Chan

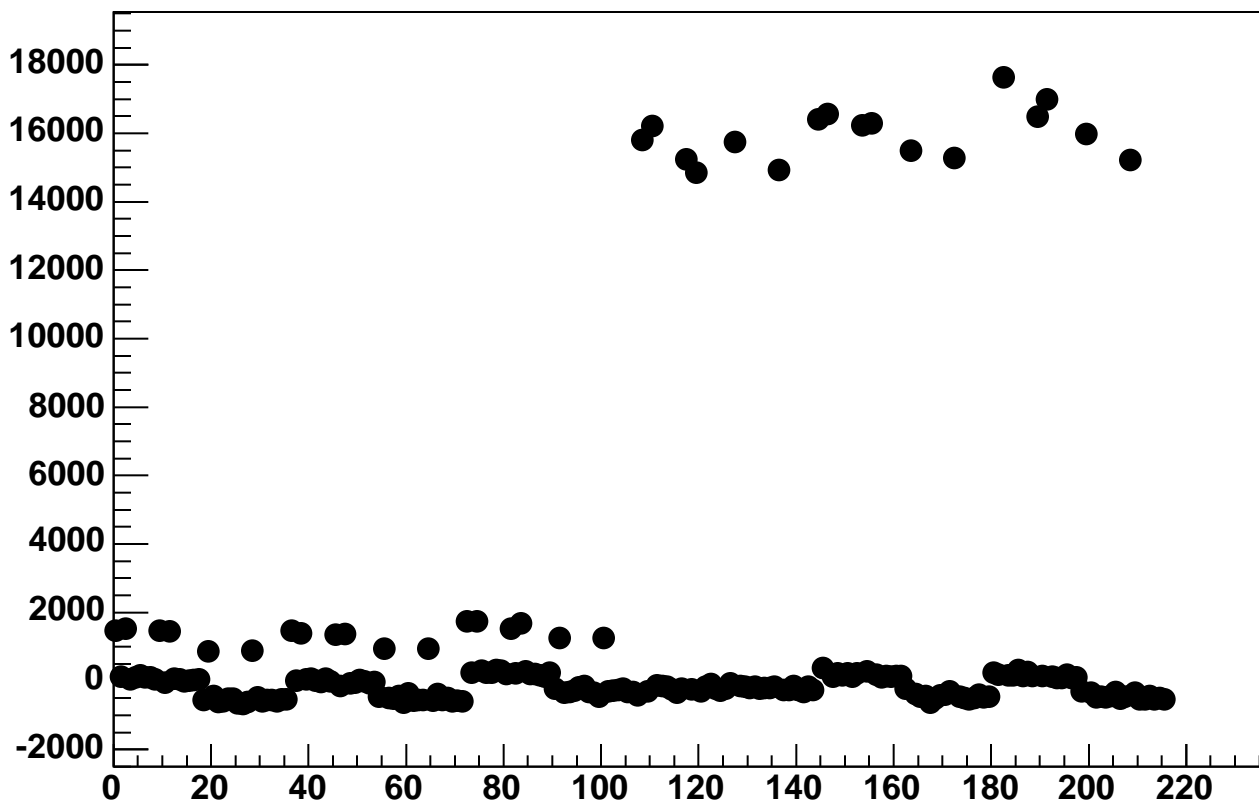


Enables off, ADC Residuals vs 18\*Chip+Chan

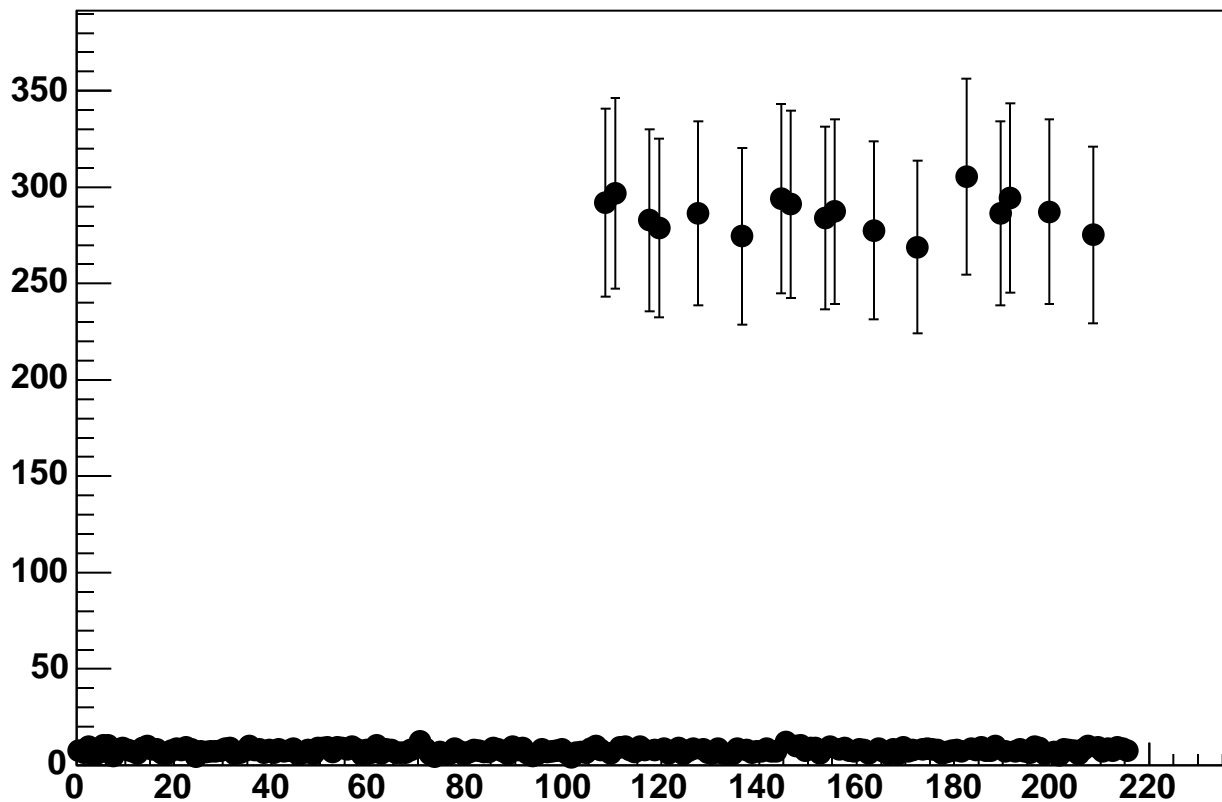




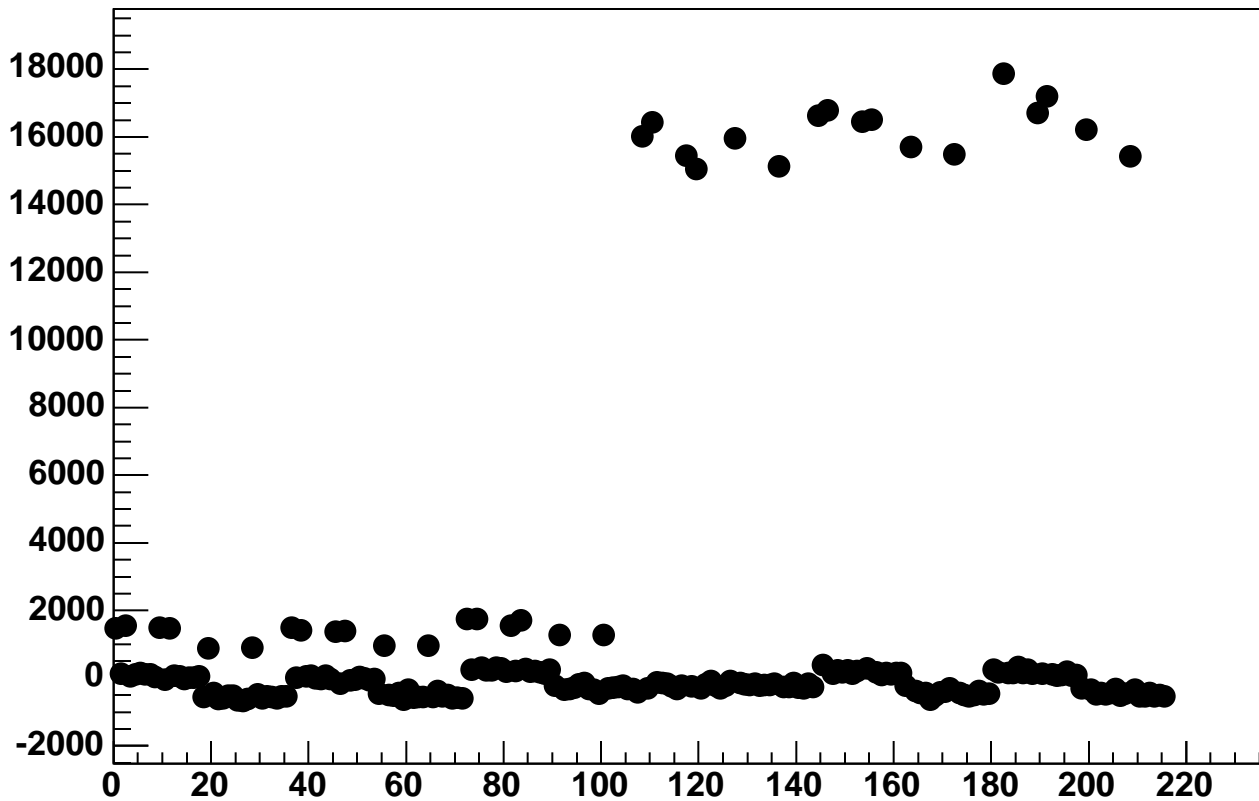
Enable 0, Hold=35, DAC=0, ADC Mean vs 18\*Chip+Chan



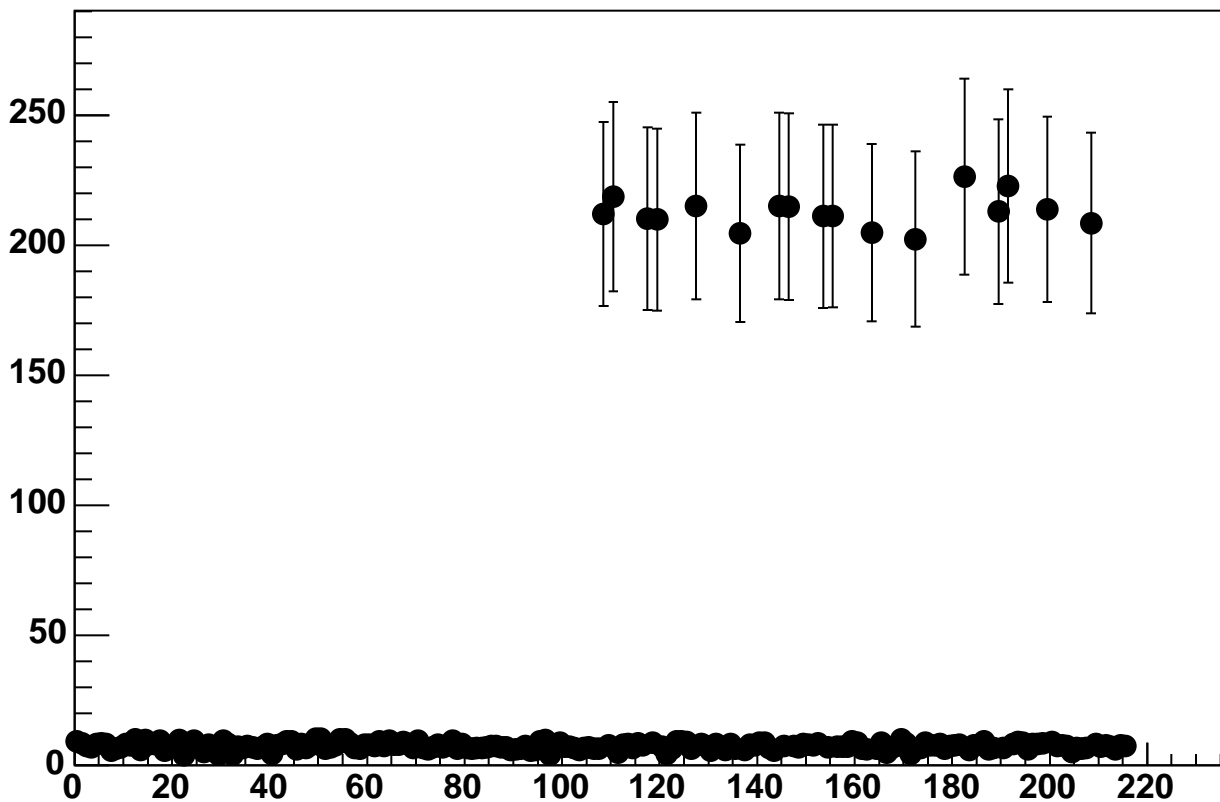
Enable 0, Hold=35, DAC=0, ADC Noise vs 18\*Chip+Chan



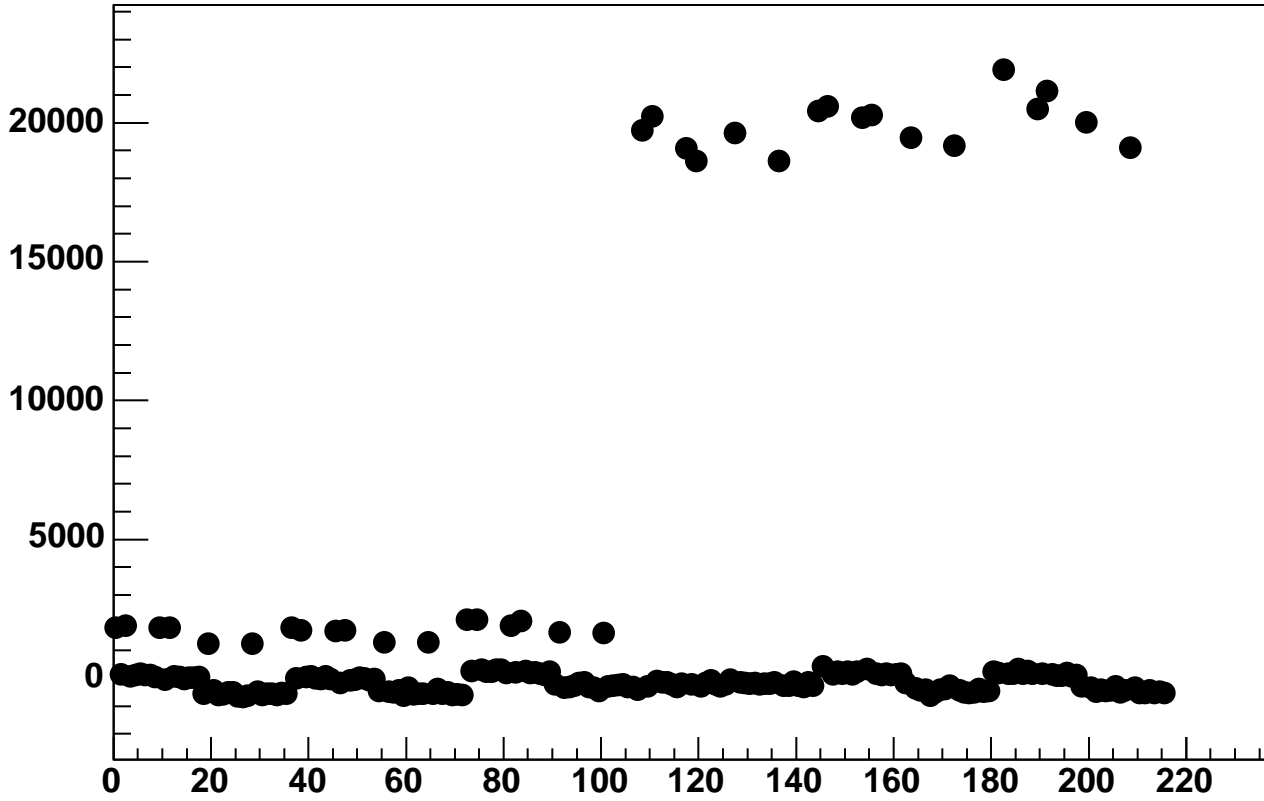
Enable 0, Hold=35, DAC=400, ADC Mean vs 18\*Chip+Chan



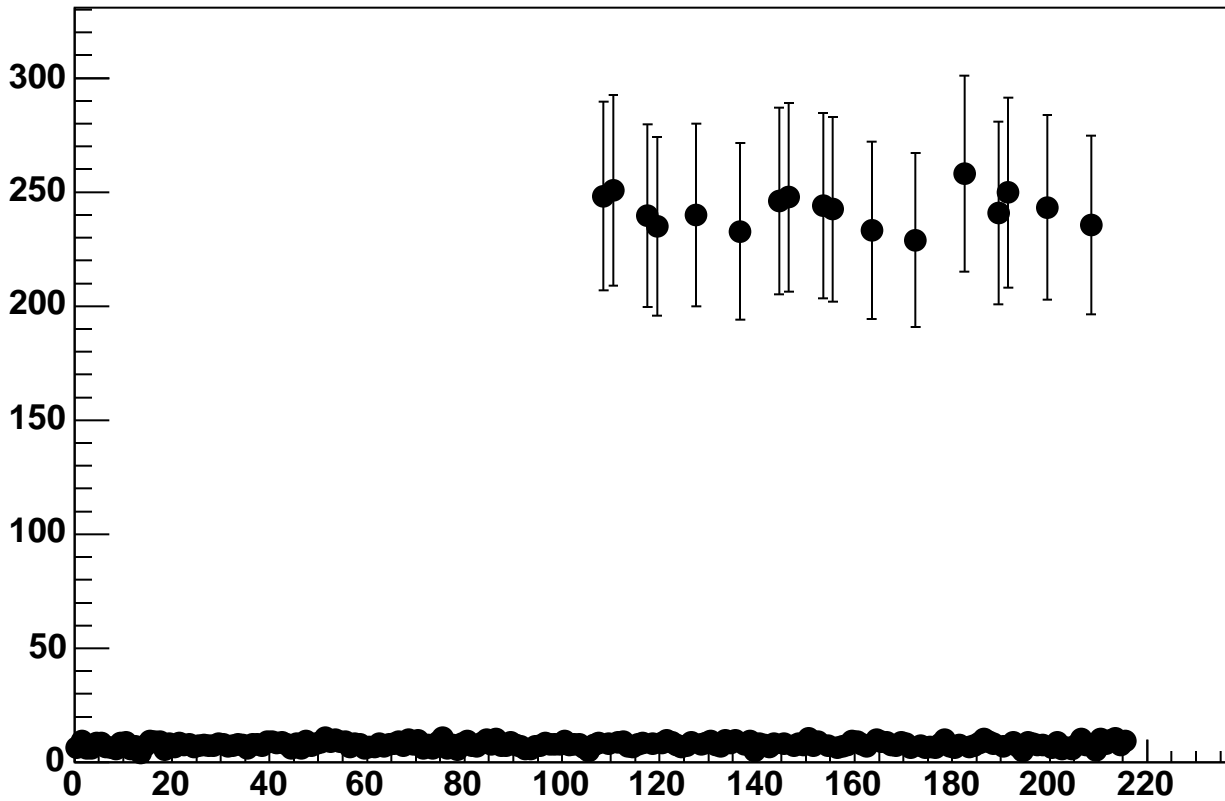
Enable 0, Hold=35, DAC=400, ADC Noise vs 18\*Chip+Chan



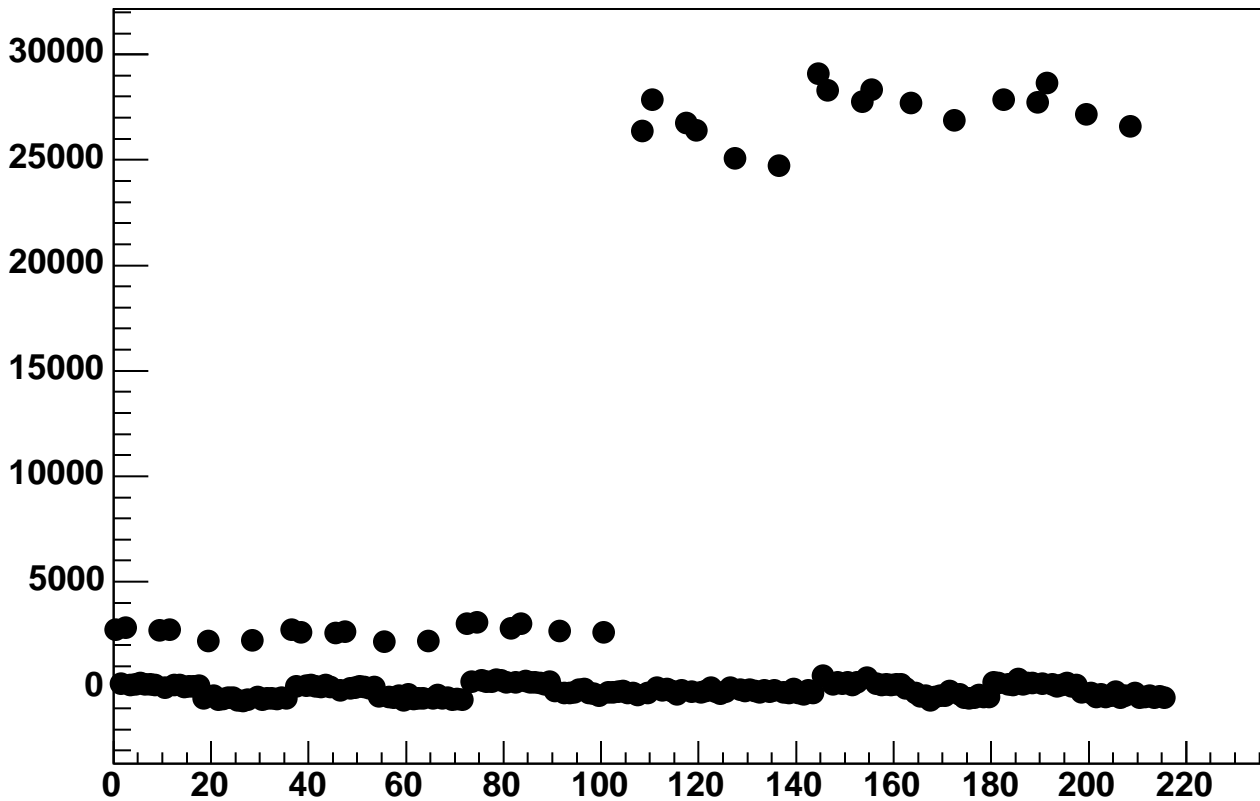
Enable 0, Hold=35, DAC=800, ADC Mean vs 18\*Chip+Chan



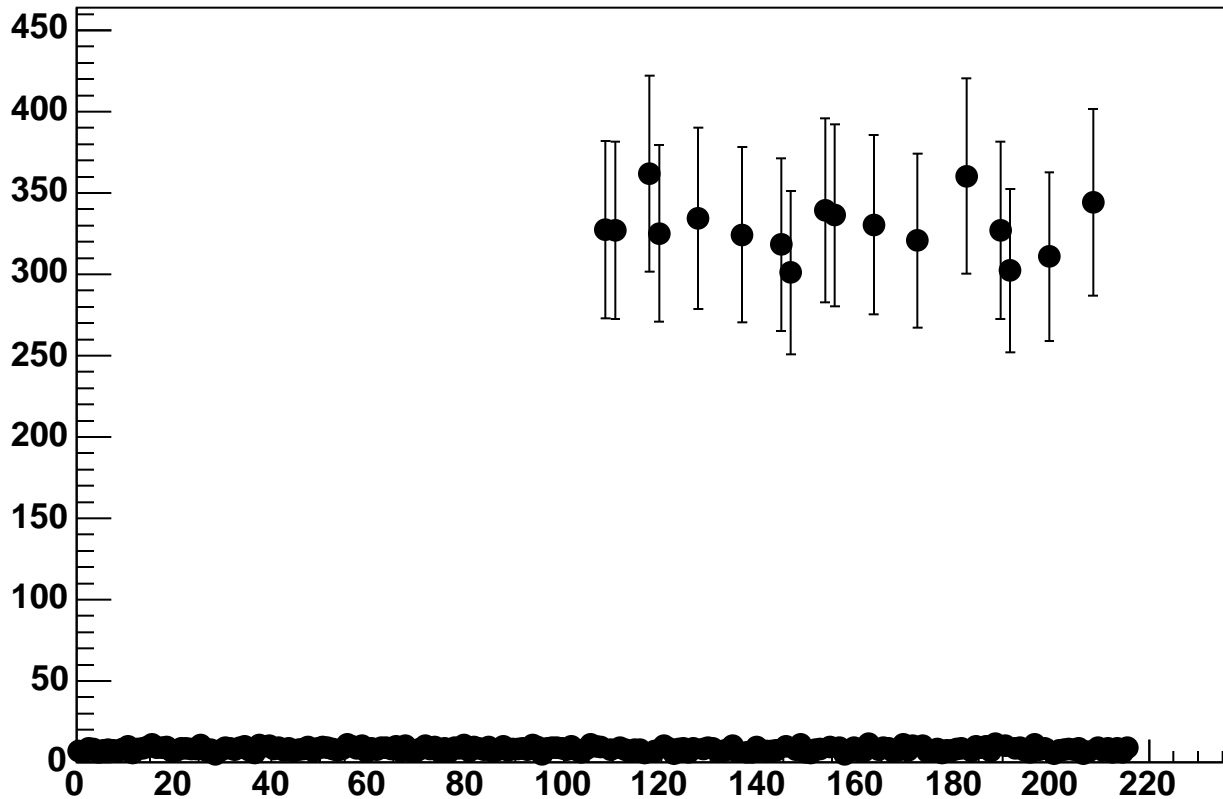
Enable 0, Hold=35, DAC=800, ADC Noise vs 18\*Chip+Chan



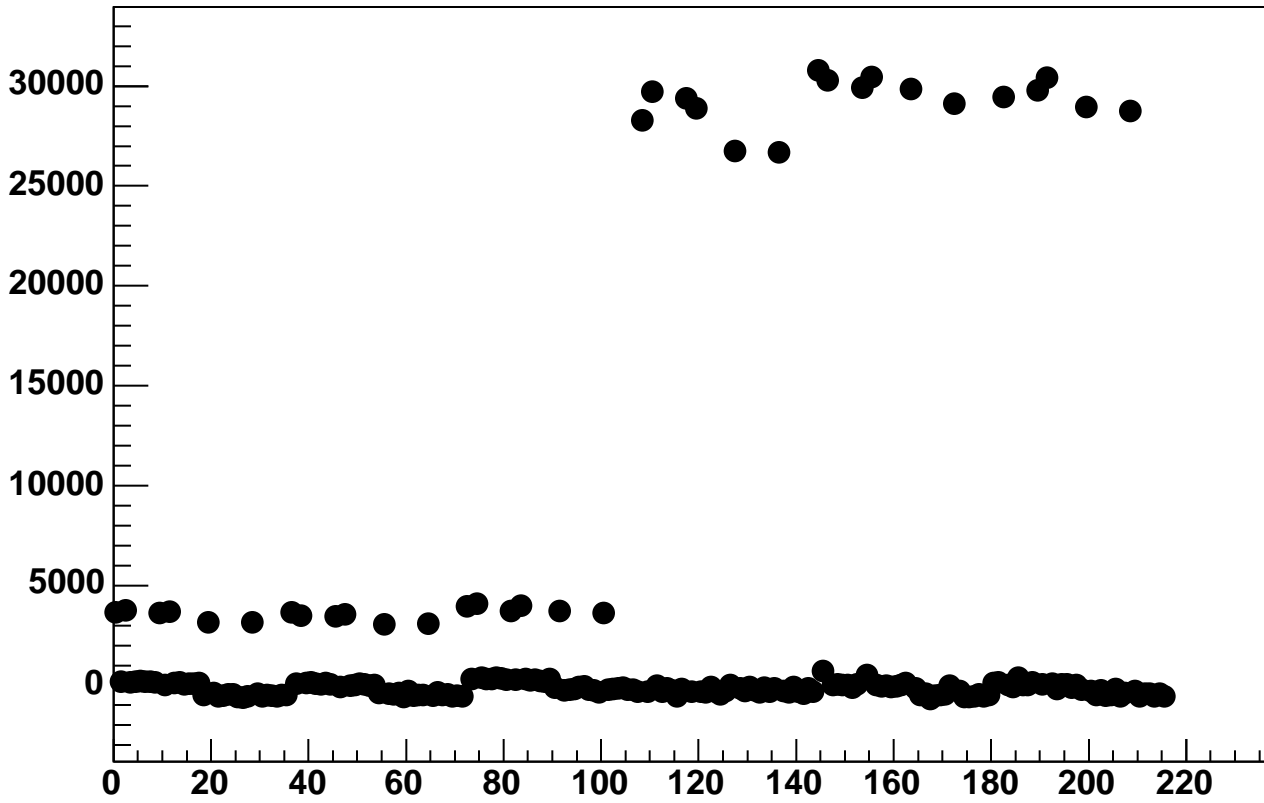
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18\*Chip+Chan



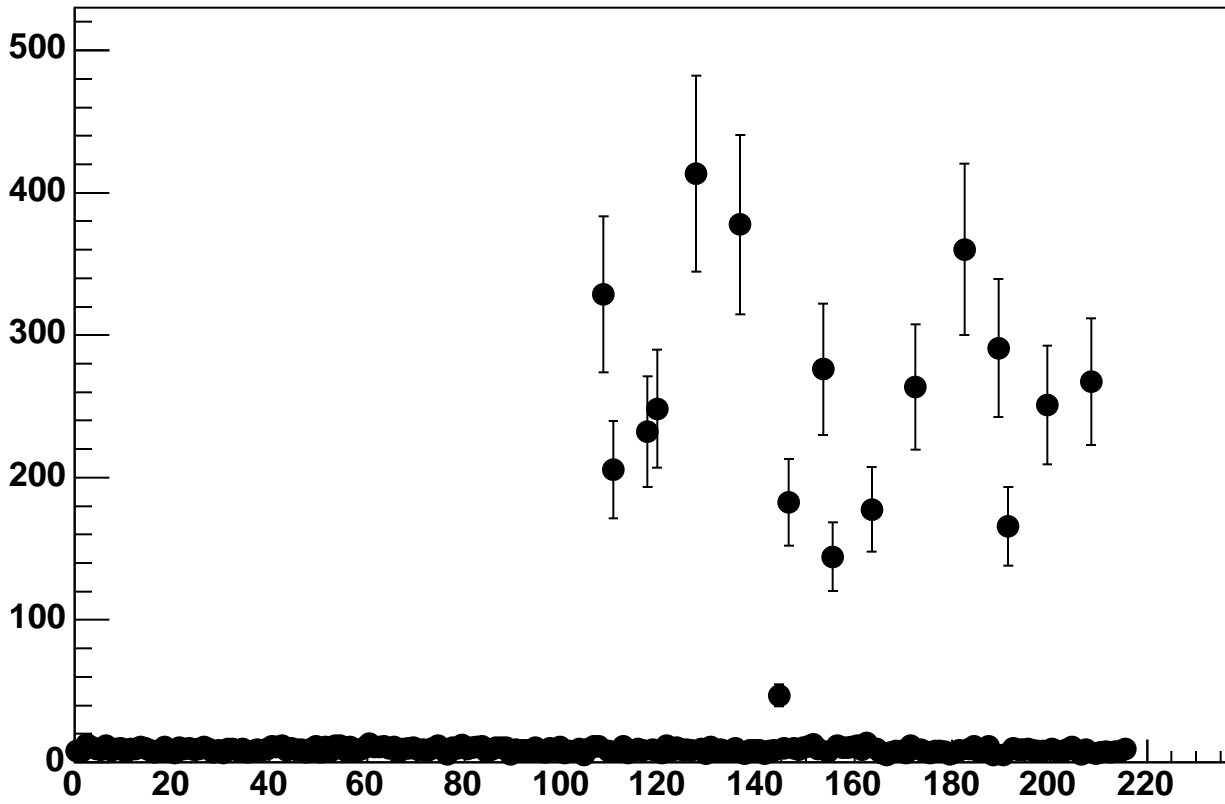
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18\*Chip+Chan



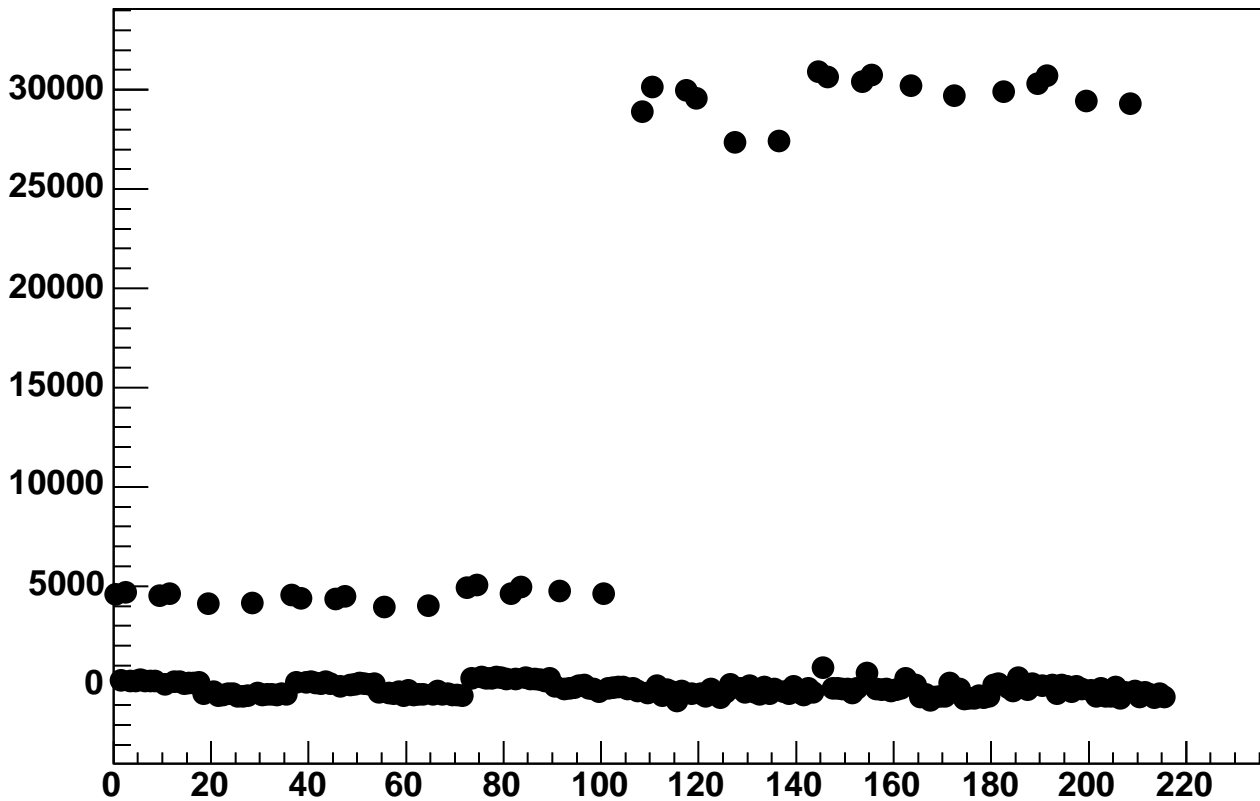
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18\*Chip+Chan



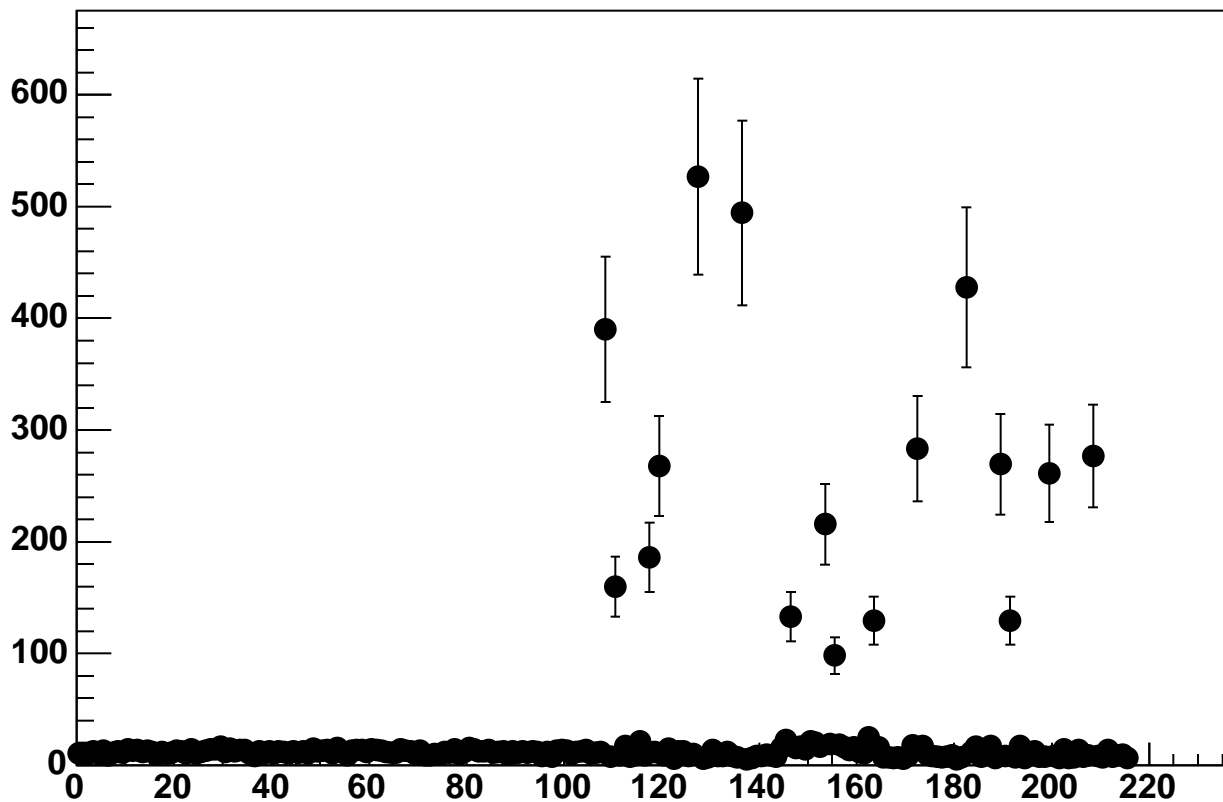
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18\*Chip+Chan



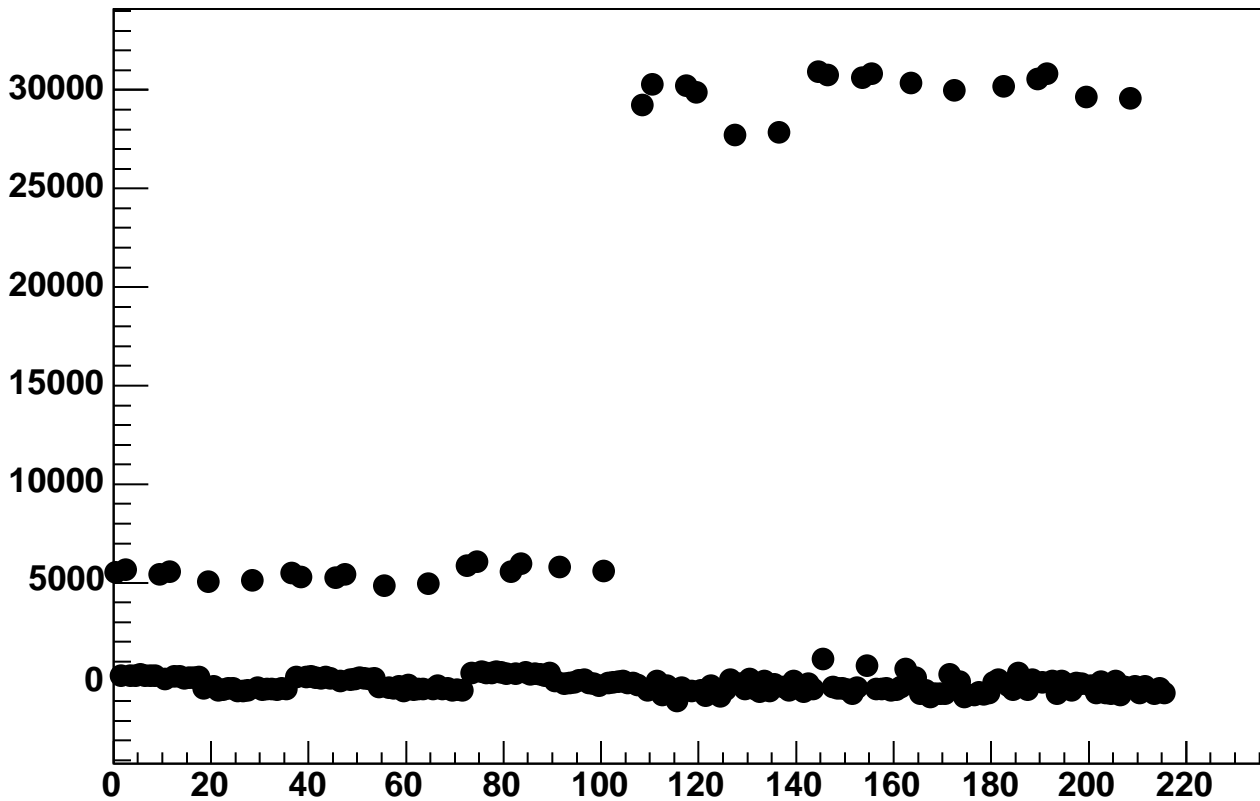
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18\*Chip+Chan



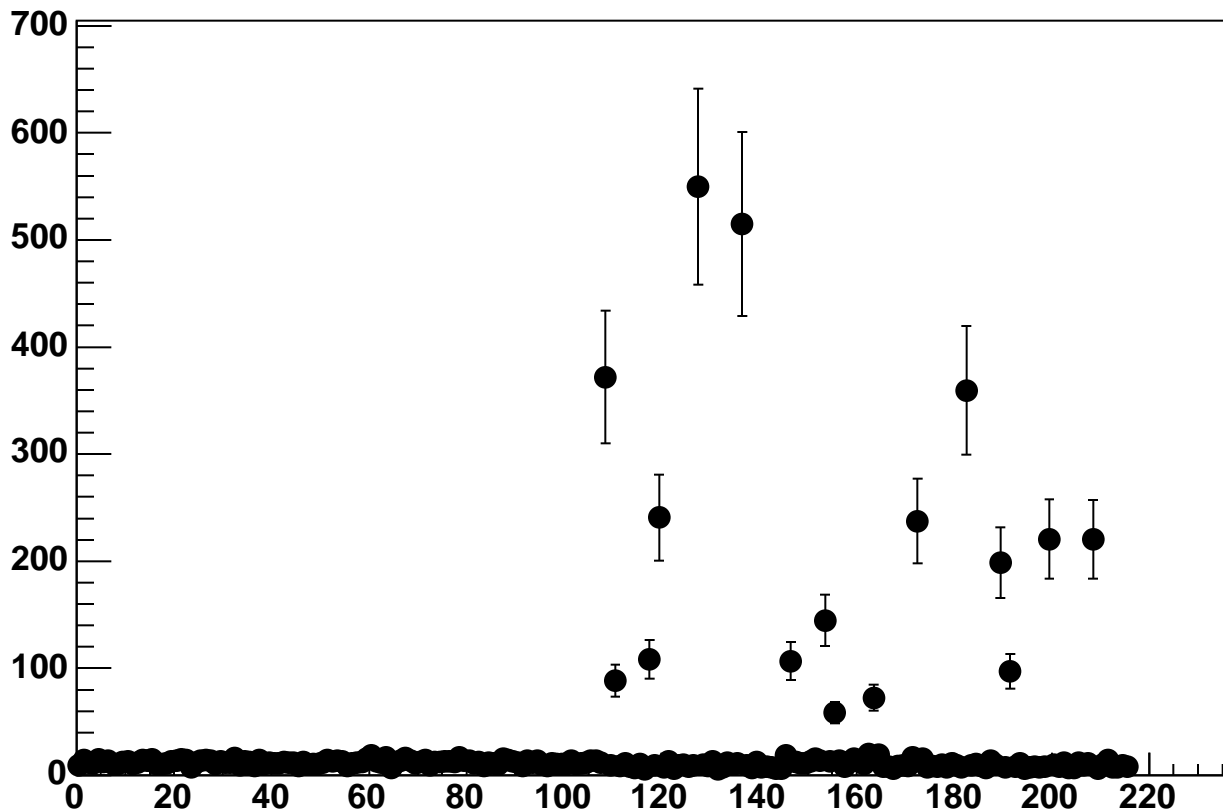
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18\*Chip+Chan



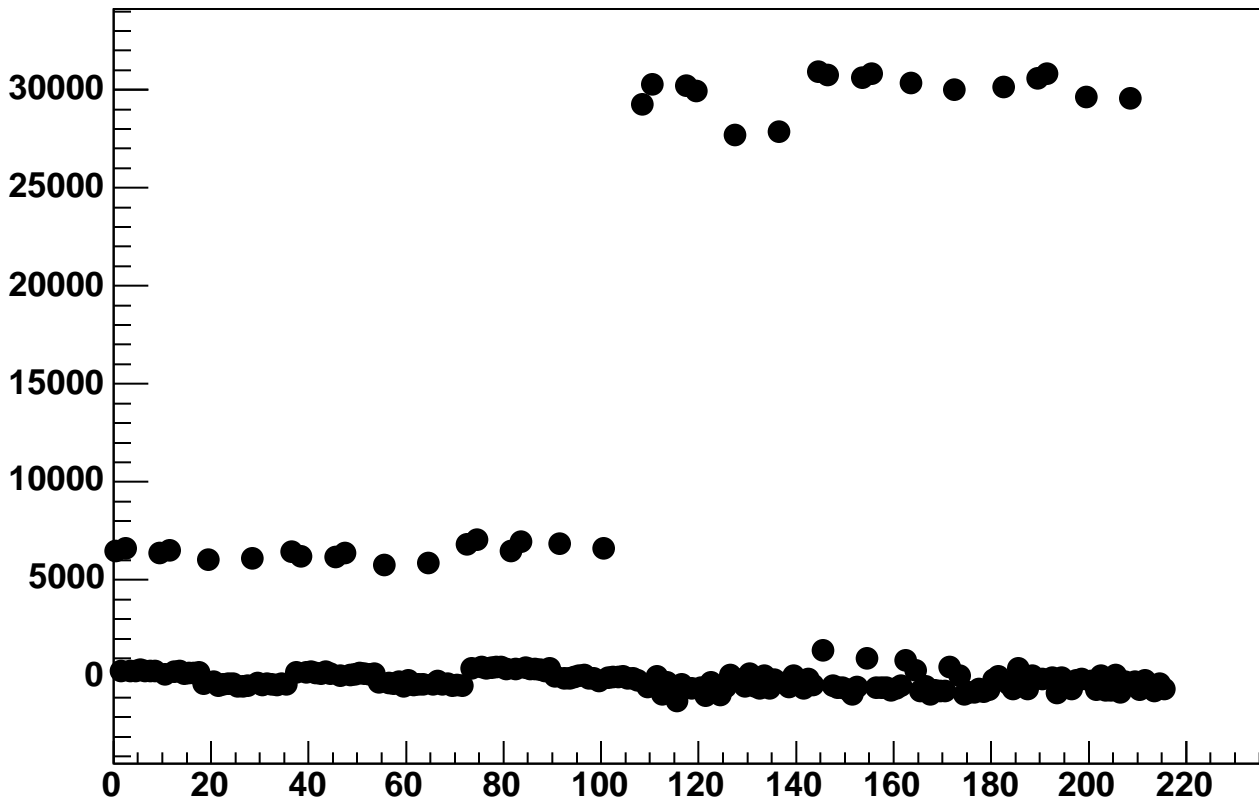
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18\*Chip+Chan



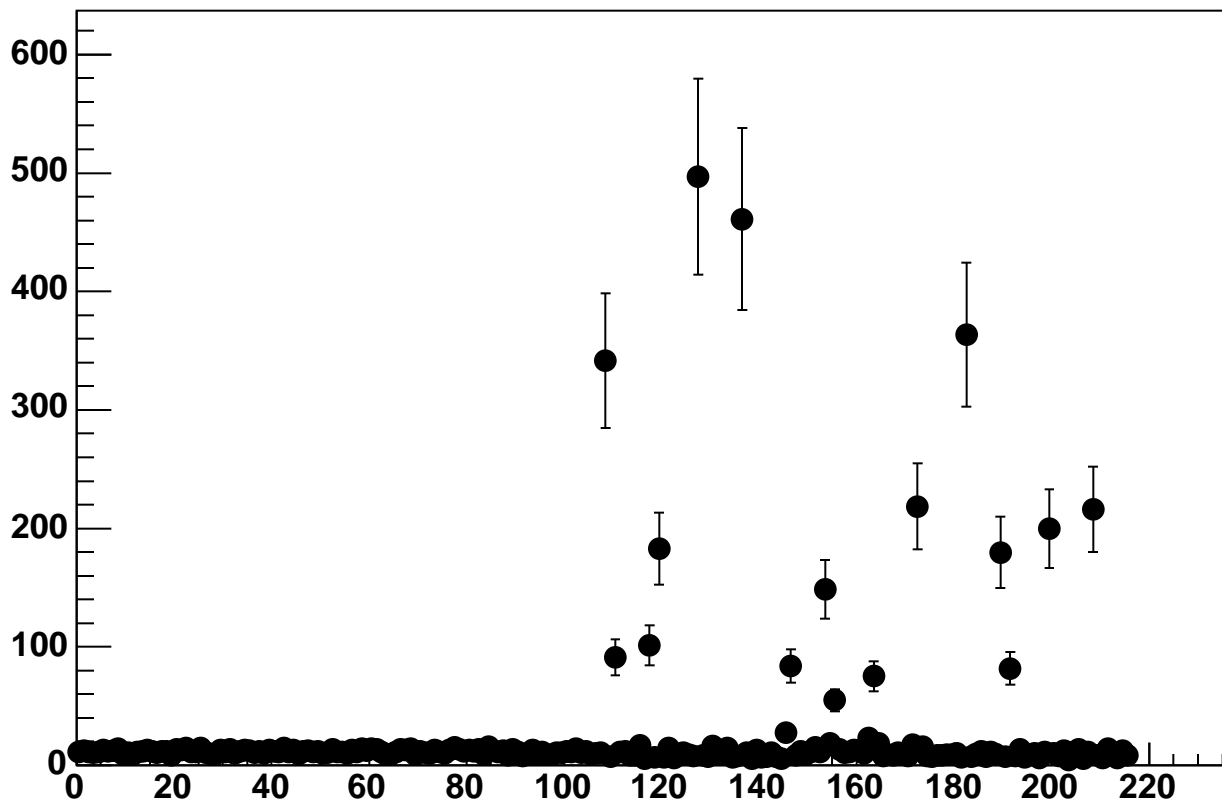
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=35, DAC=2800, ADC Mean vs 18\*Chip+Chan

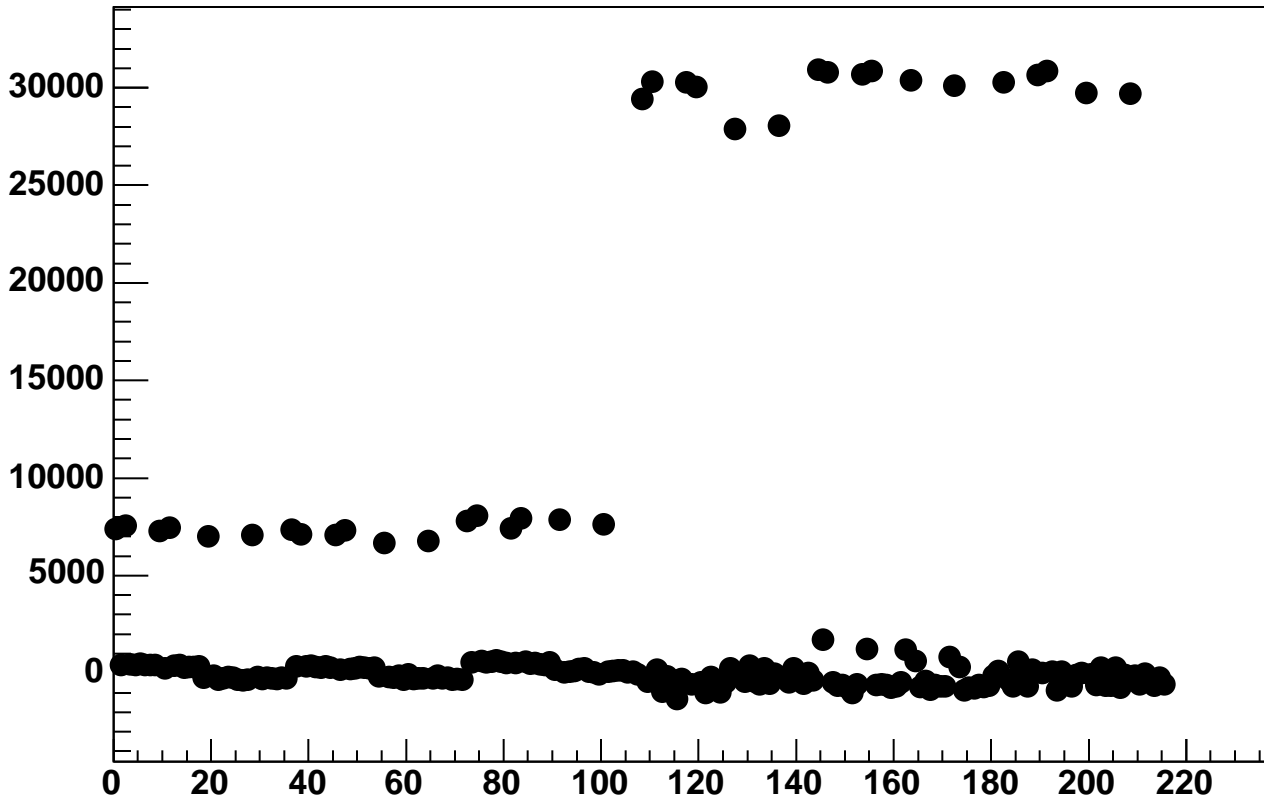


Enable 0, Hold=35, DAC=2800, ADC Noise vs 18\*Chip+Chan

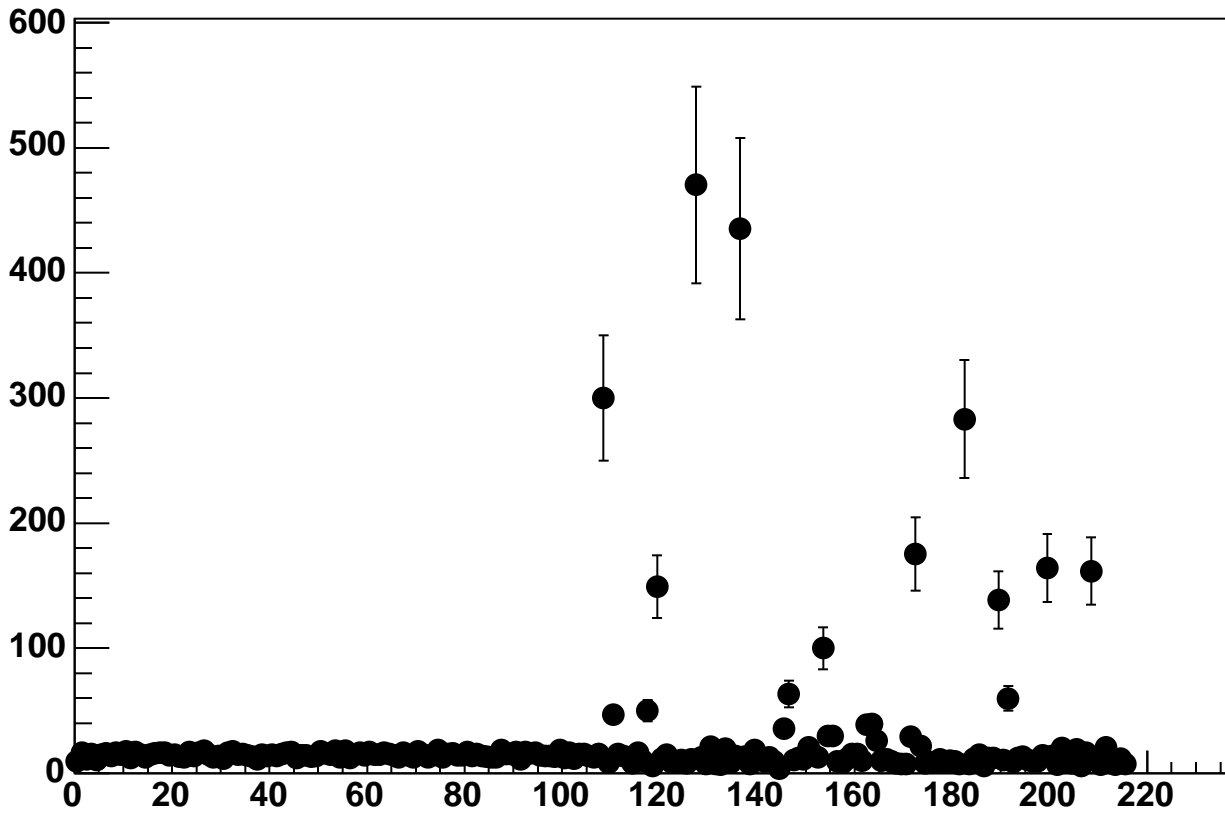




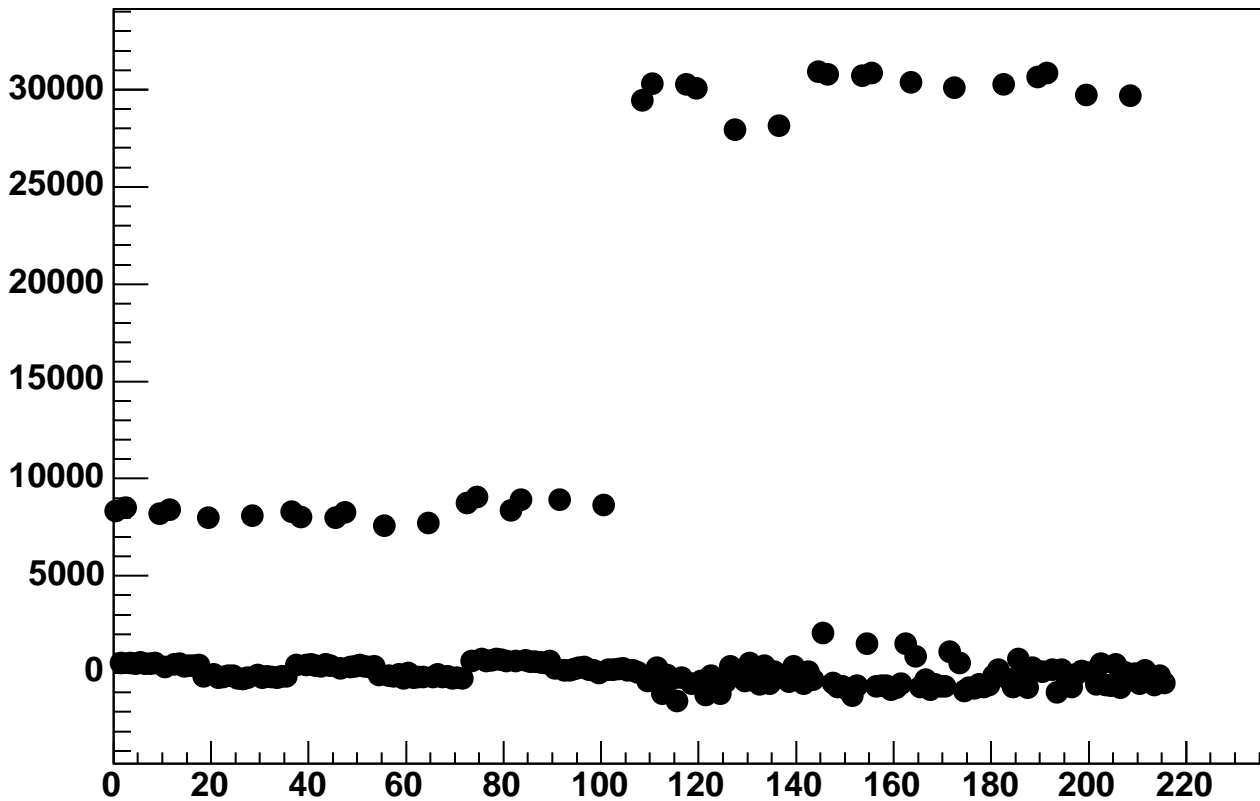
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18\*Chip+Chan



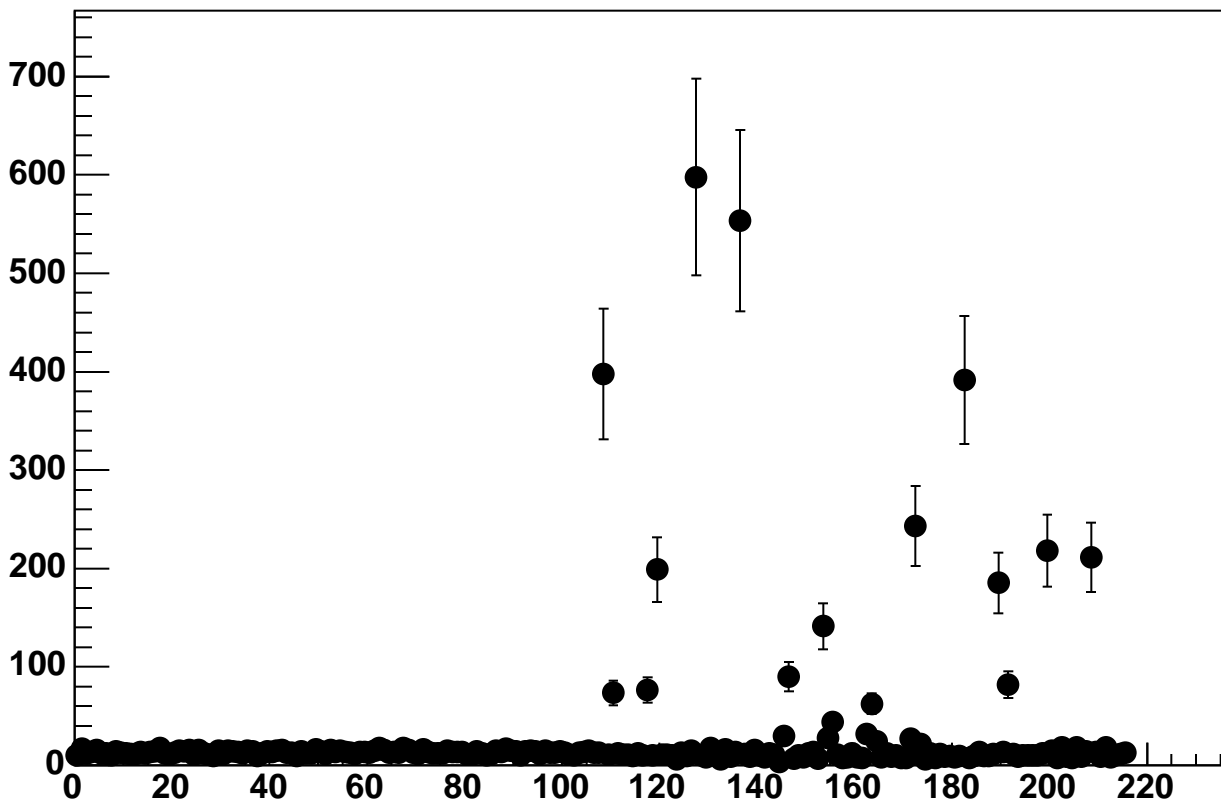
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18\*Chip+Chan



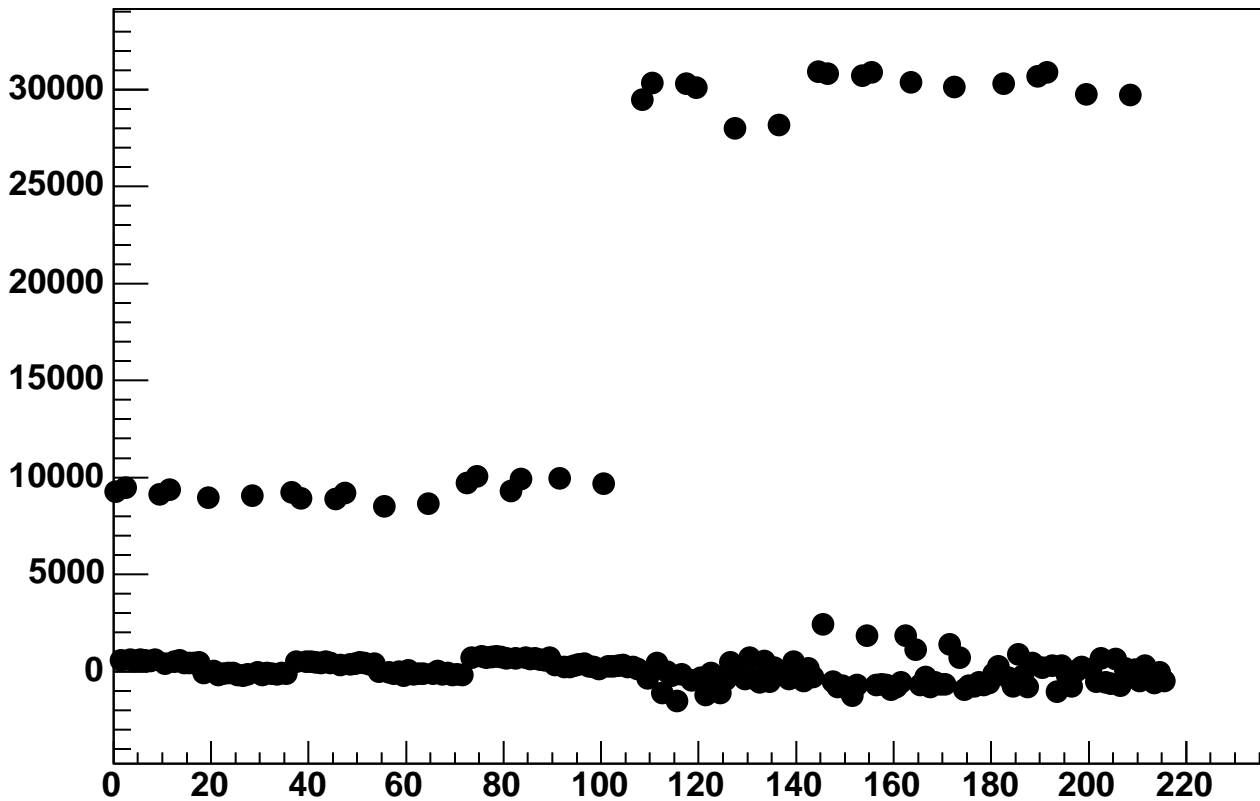
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18\*Chip+Chan



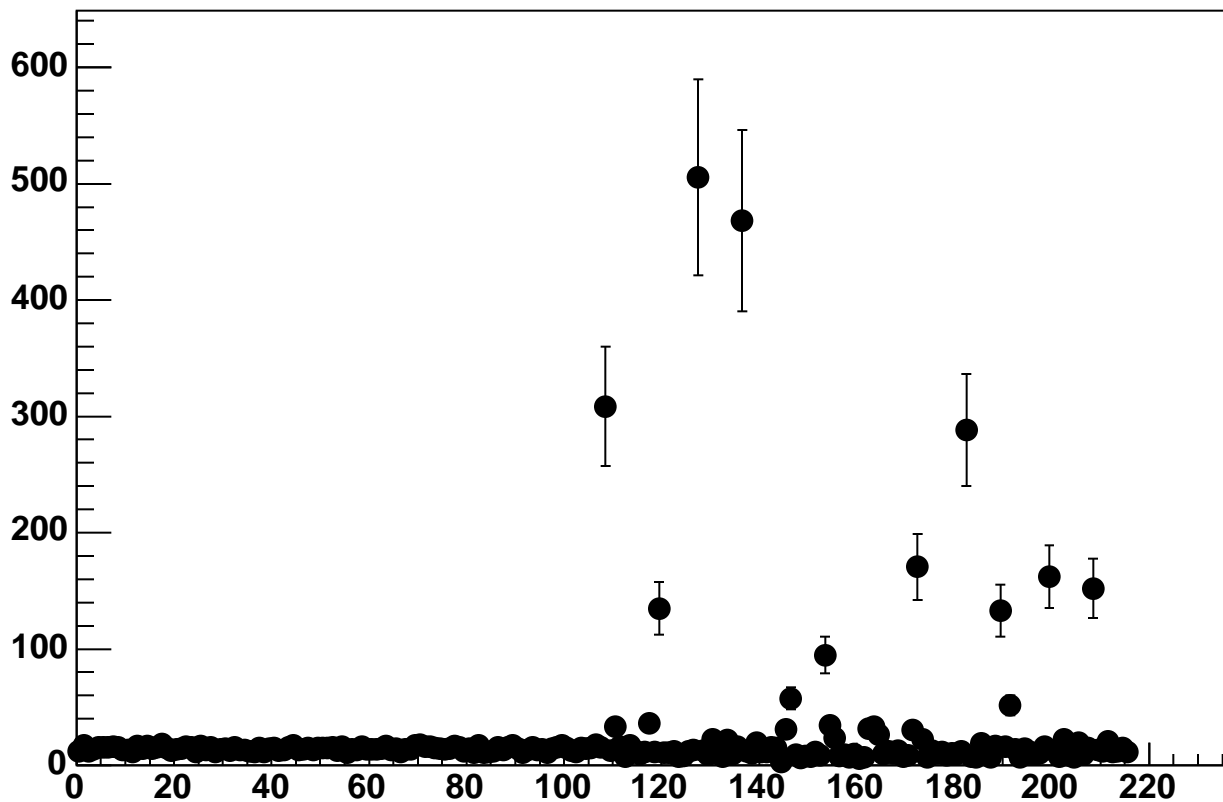
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18\*Chip+Chan



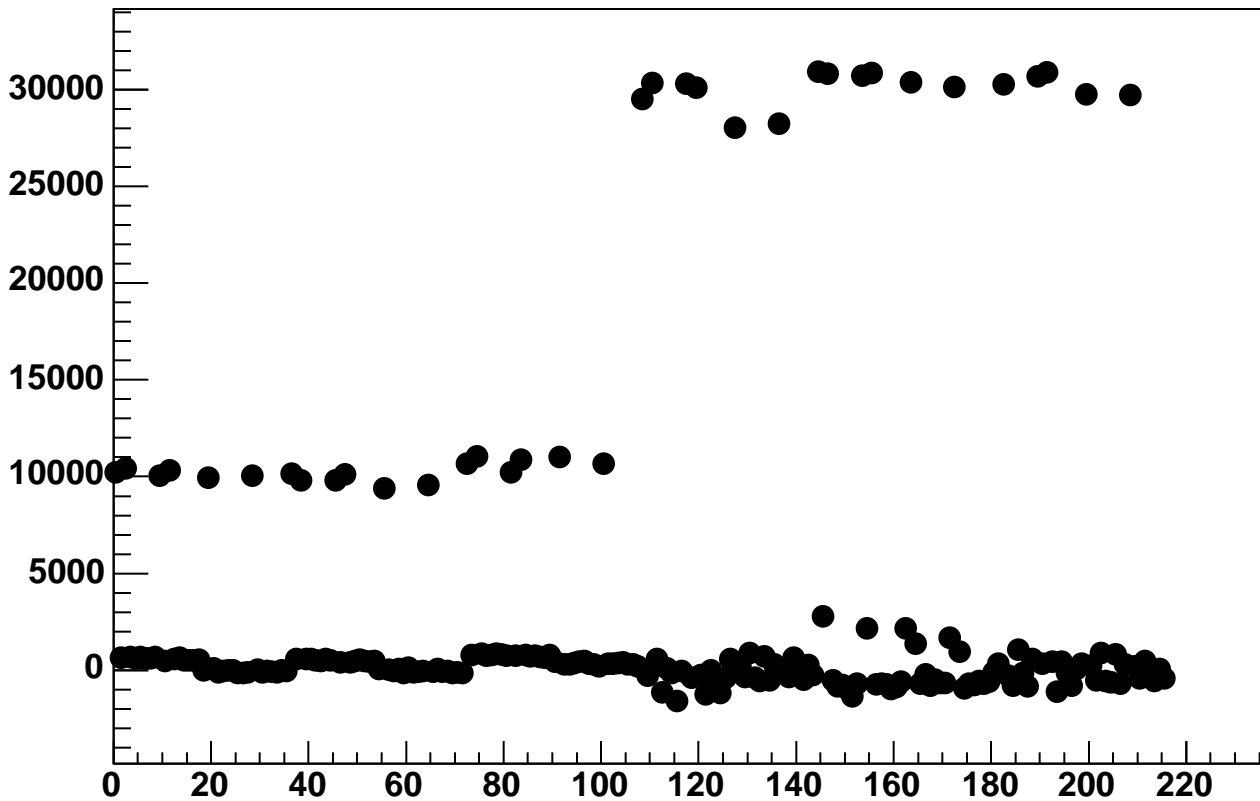
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18\*Chip+Chan



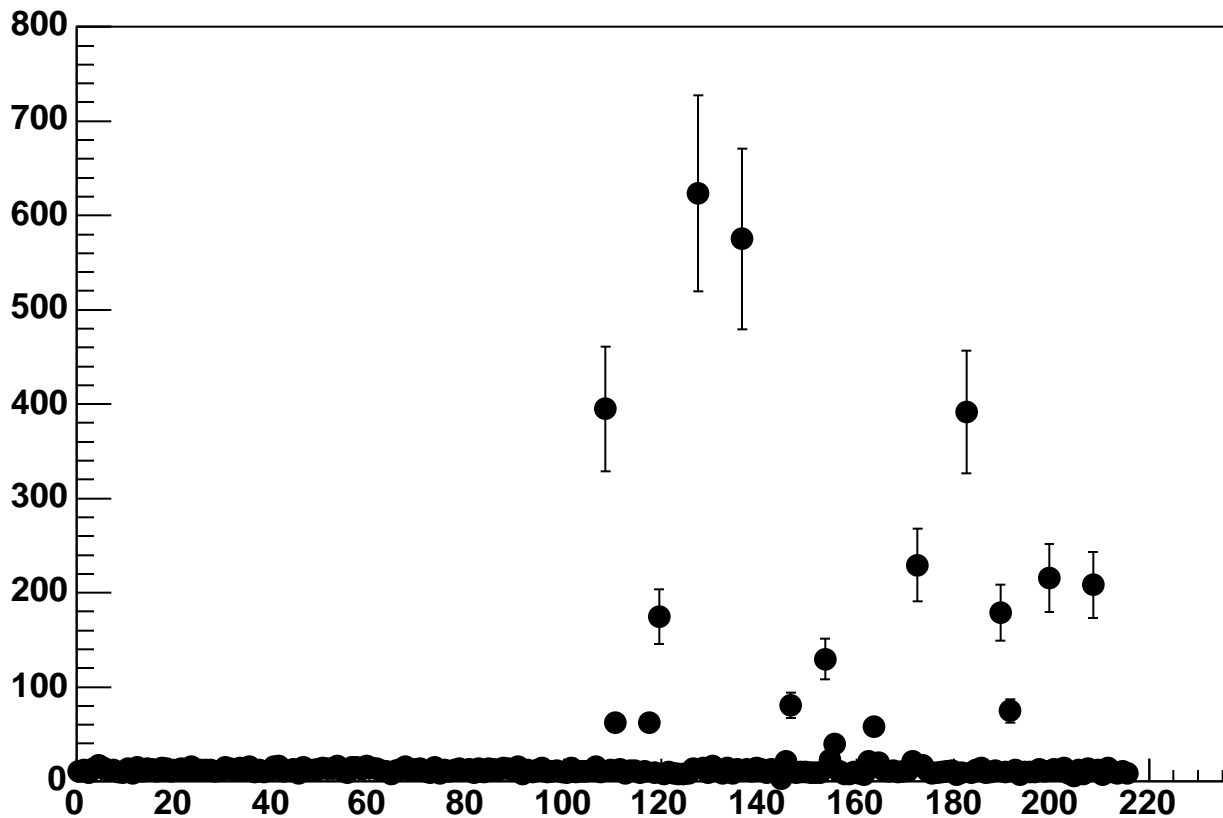
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18\*Chip+Chan



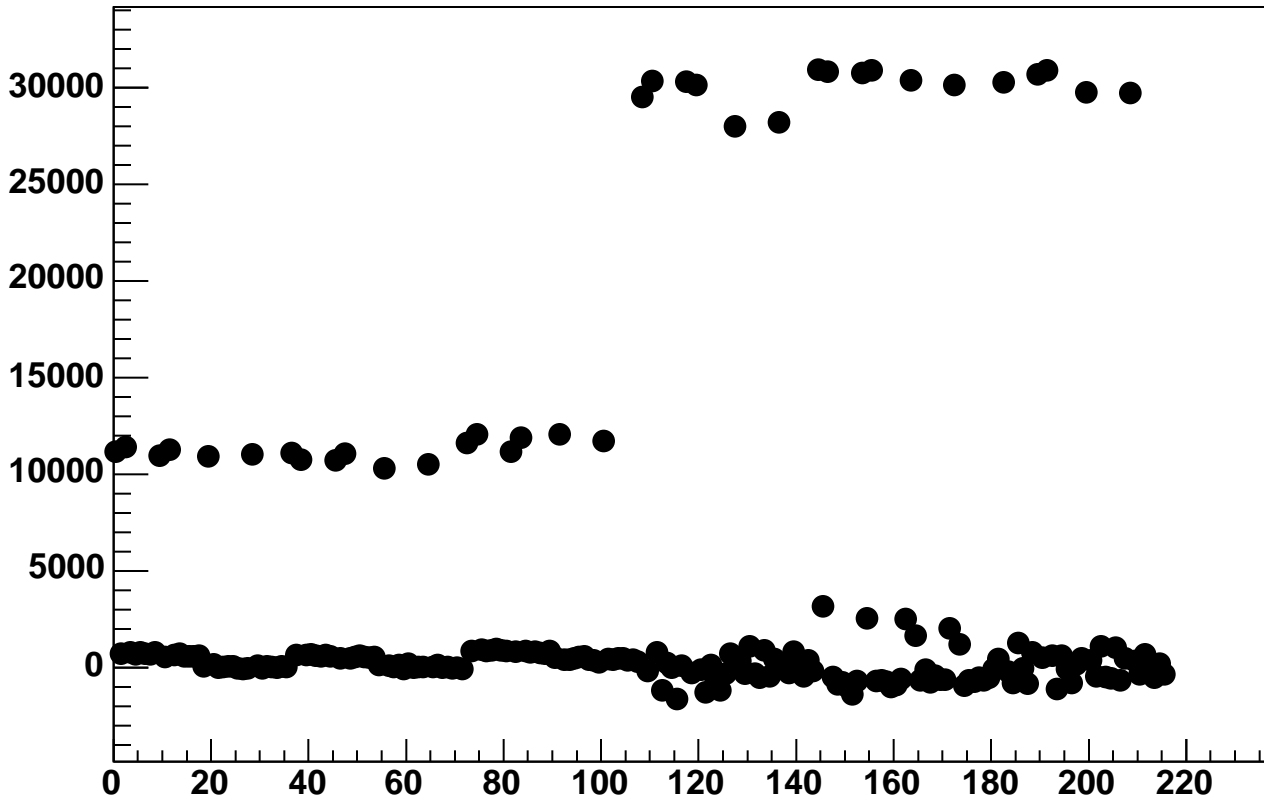
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18\*Chip+Chan



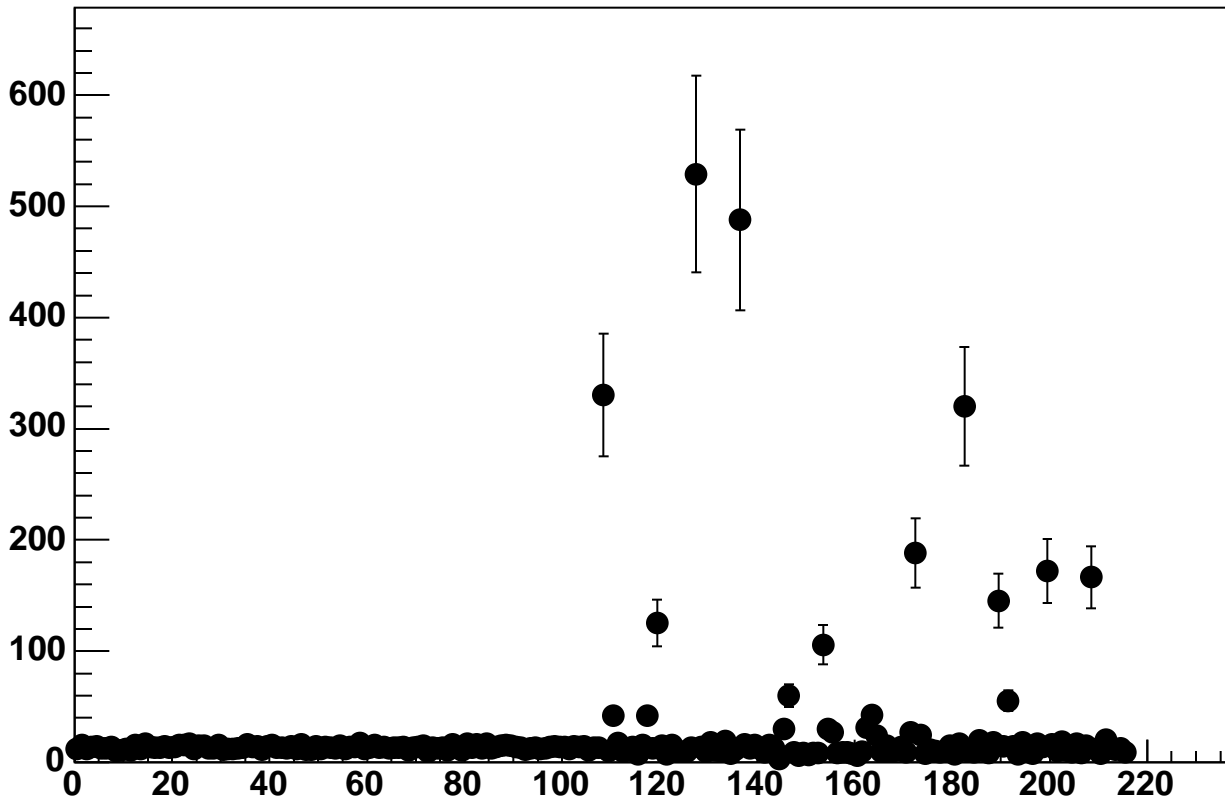
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18\*Chip+Chan



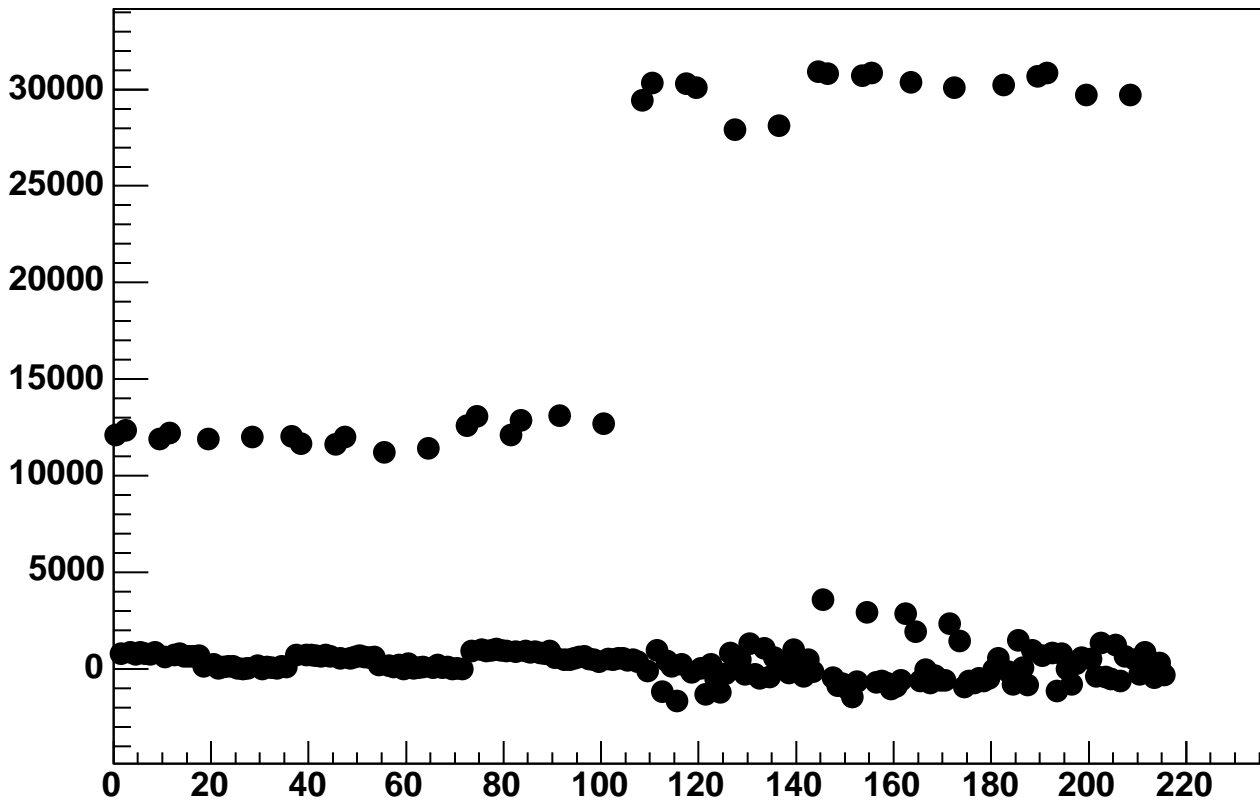
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18\*Chip+Chan



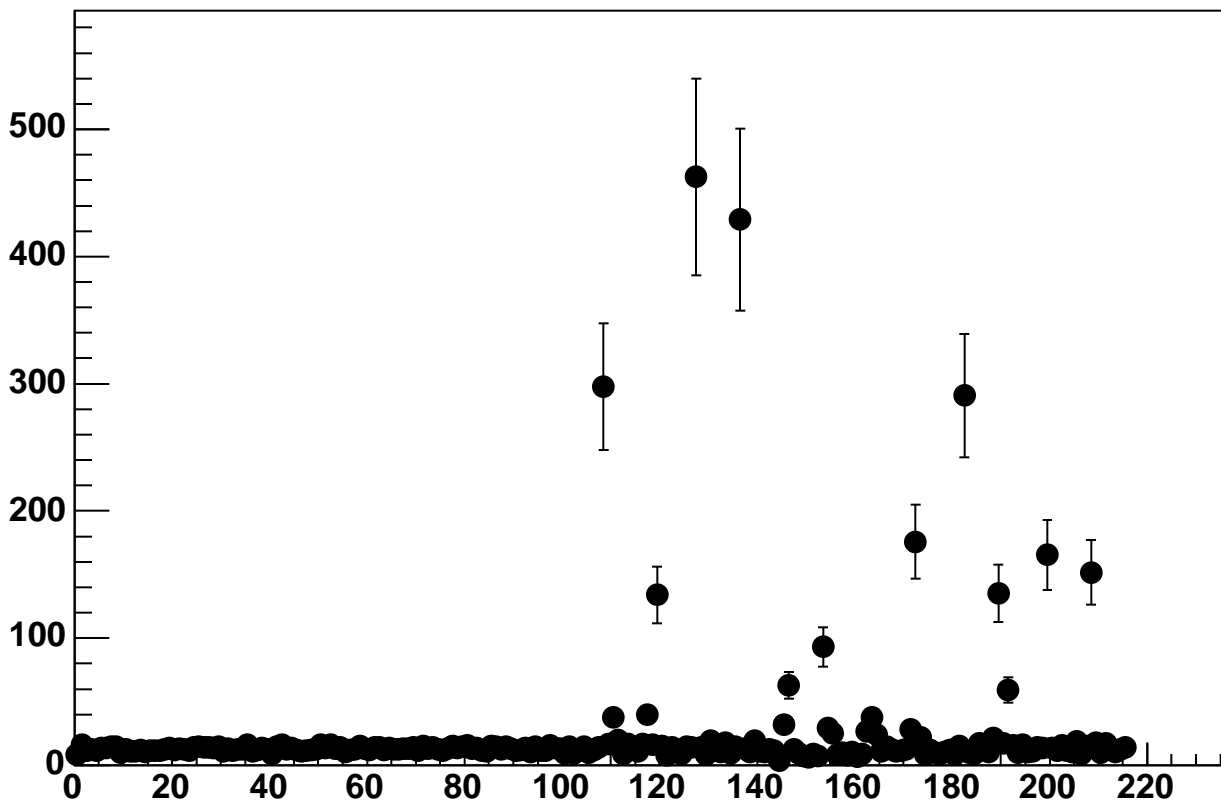
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18\*Chip+Chan



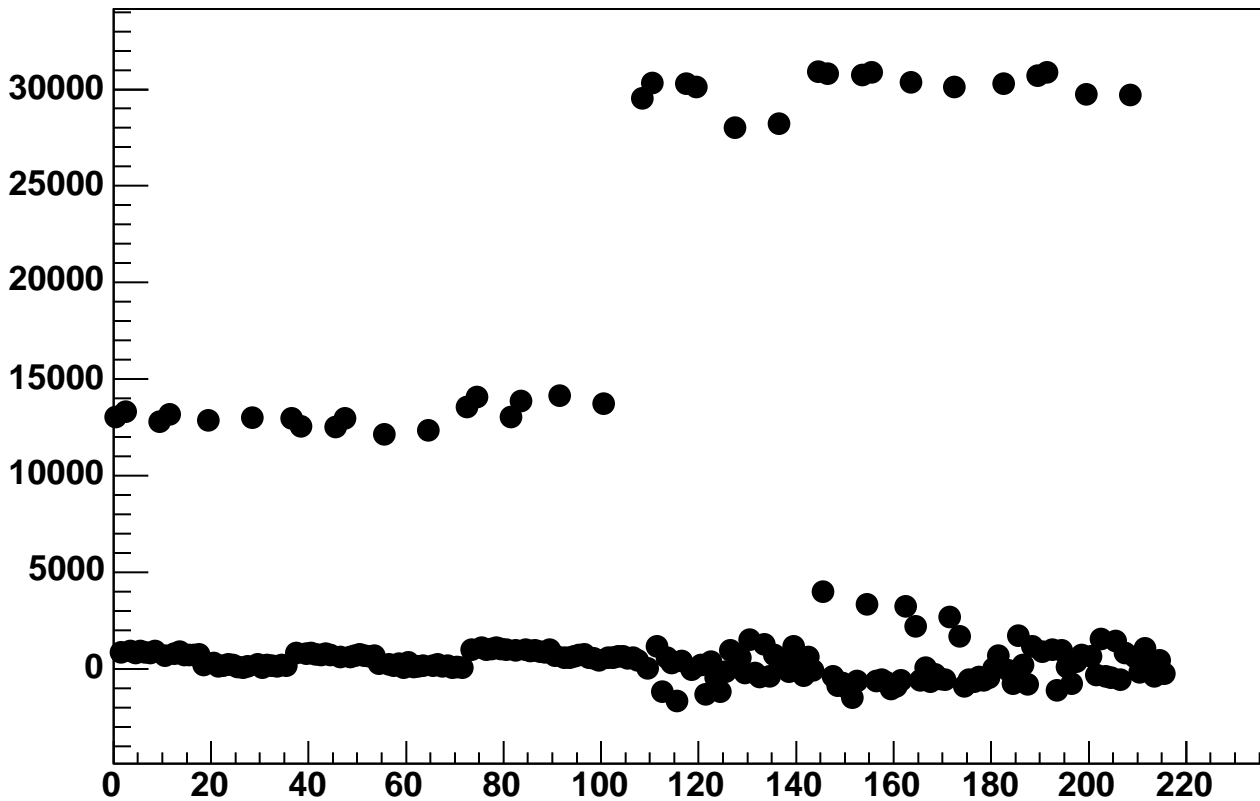
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18\*Chip+Chan



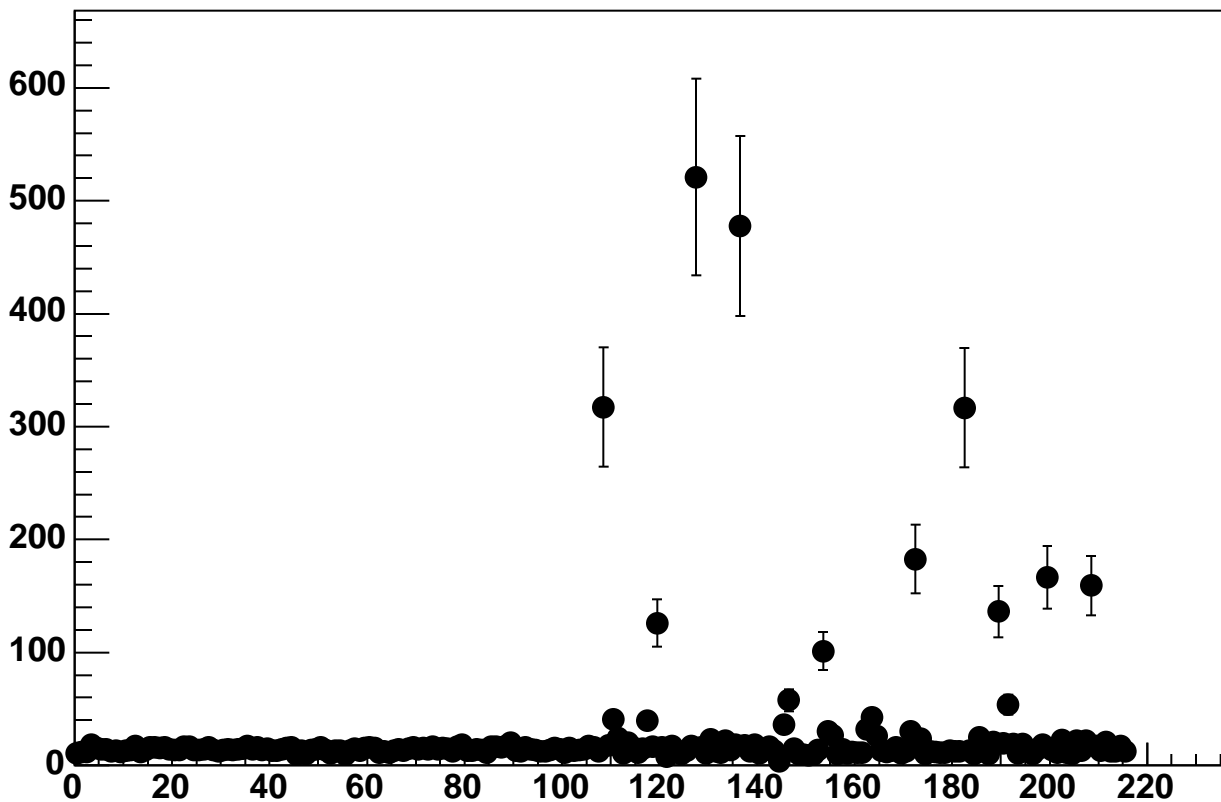
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18\*Chip+Chan



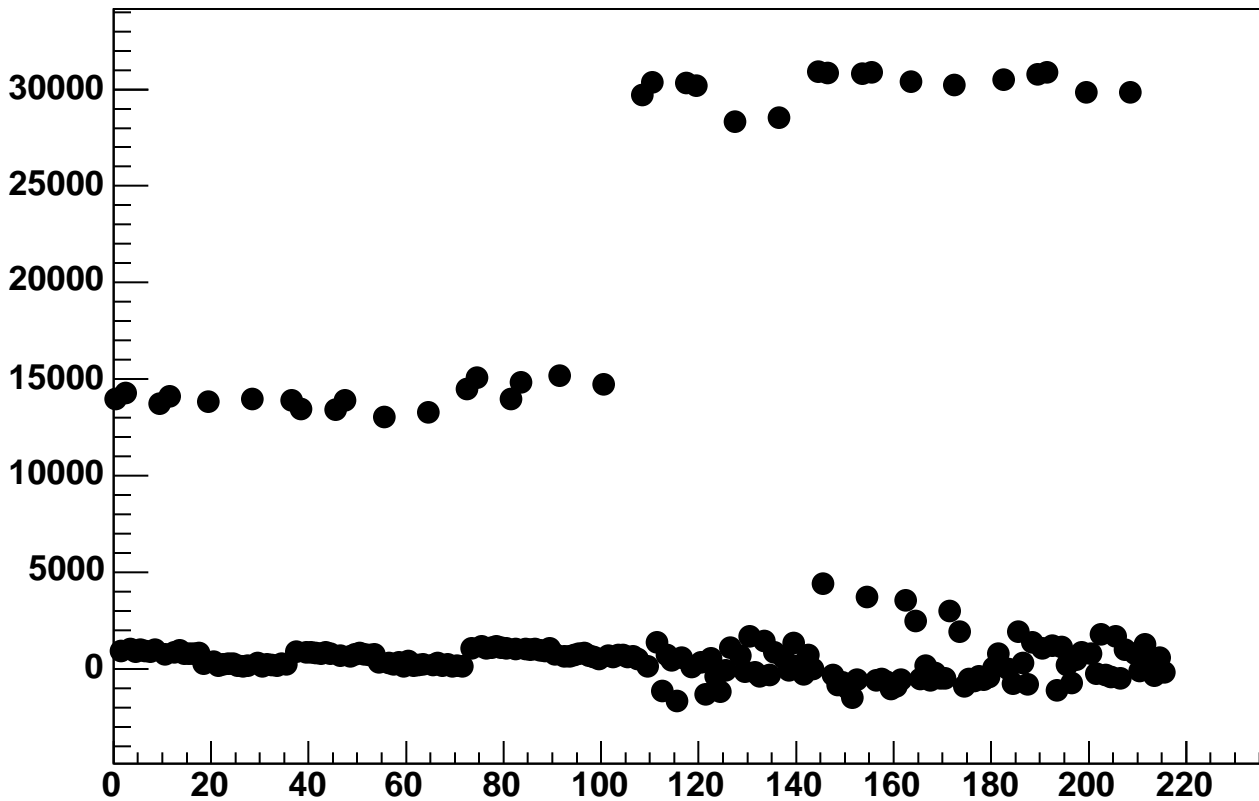
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18\*Chip+Chan



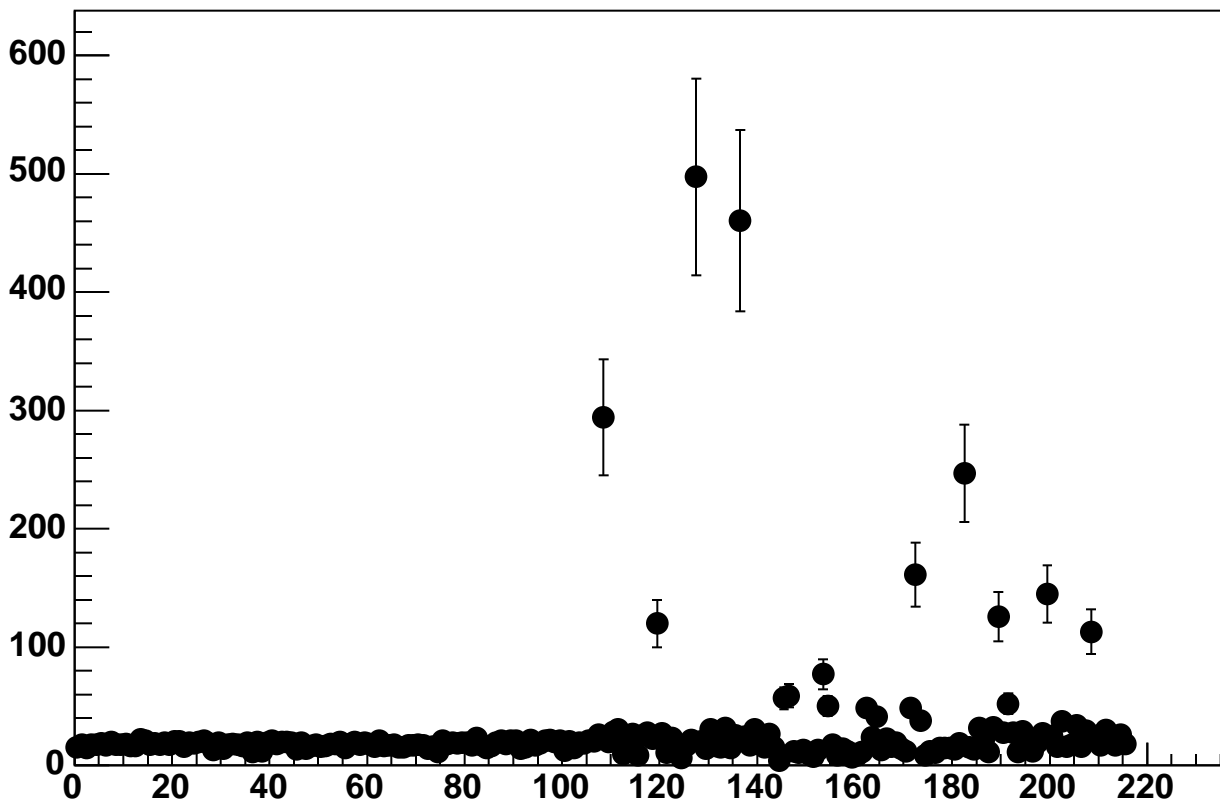
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=35, DAC=6000, ADC Mean vs 18\*Chip+Chan

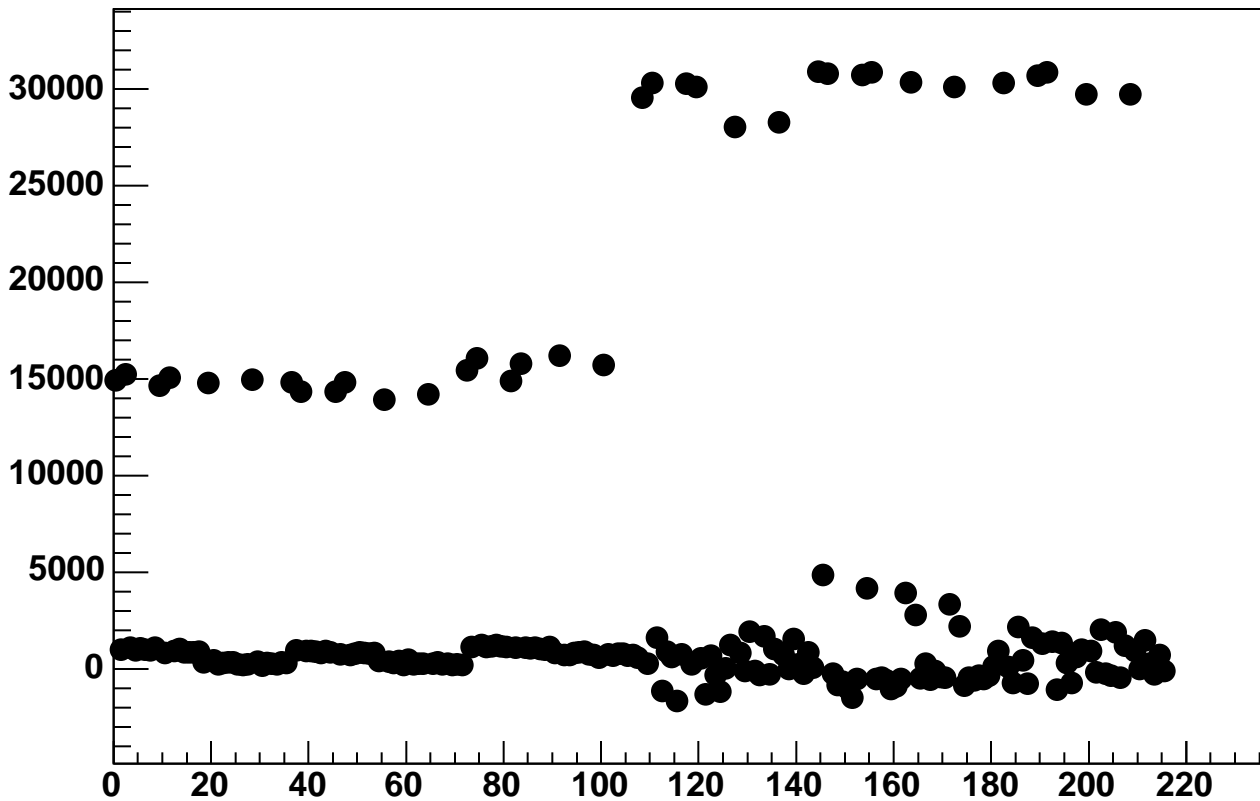


Enable 0, Hold=35, DAC=6000, ADC Noise vs 18\*Chip+Chan

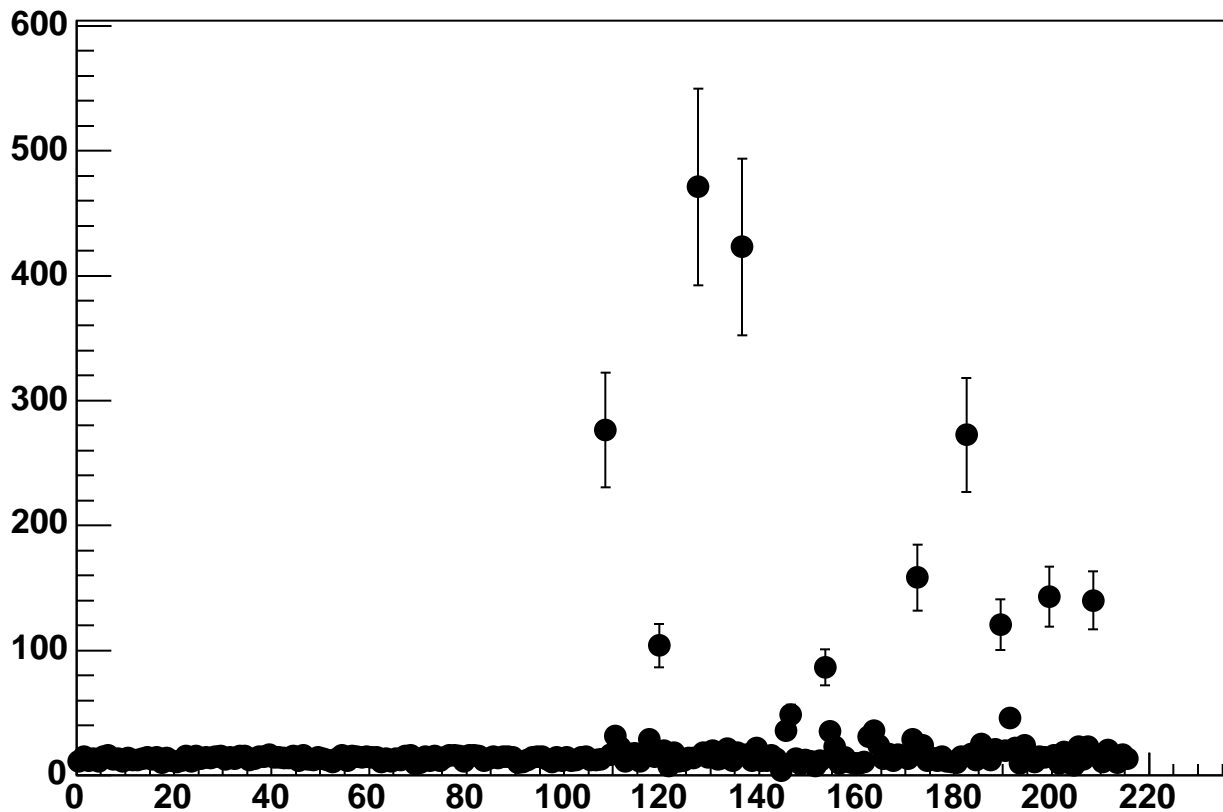




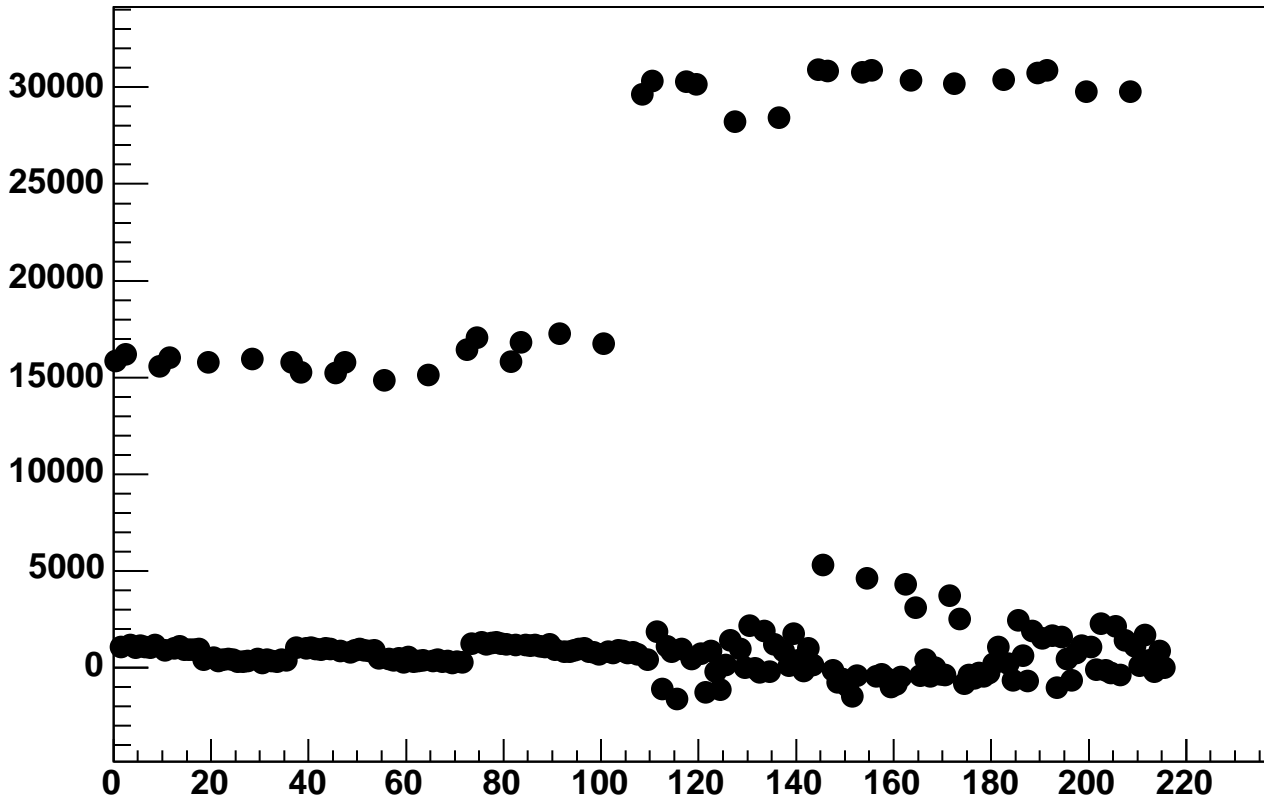
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18\*Chip+Chan



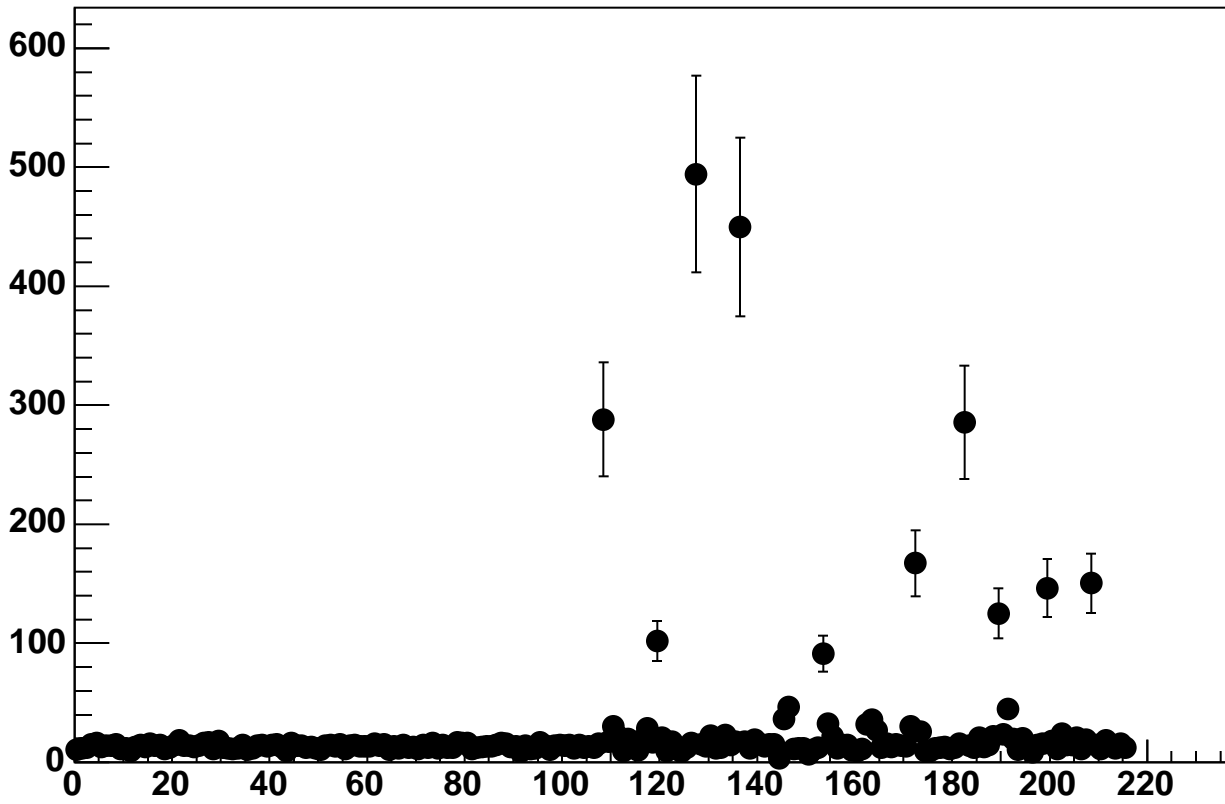
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18\*Chip+Chan



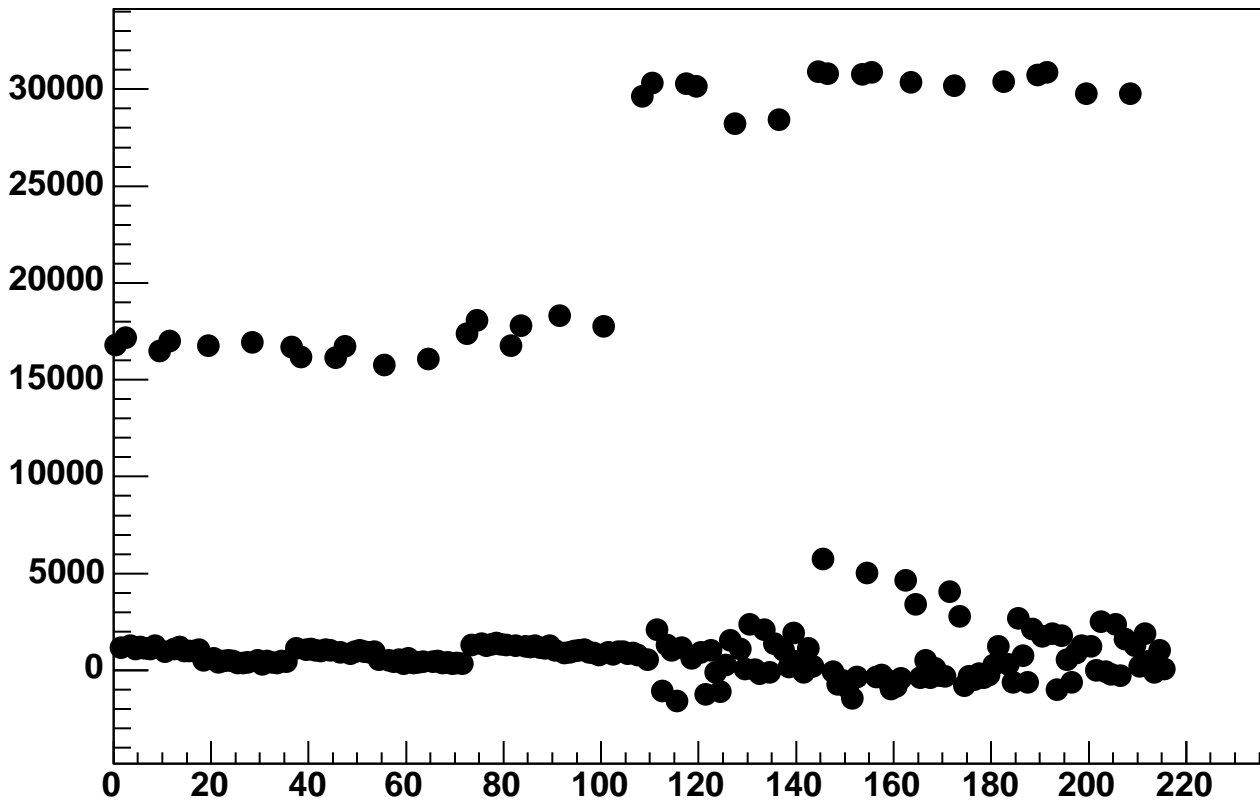
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18\*Chip+Chan



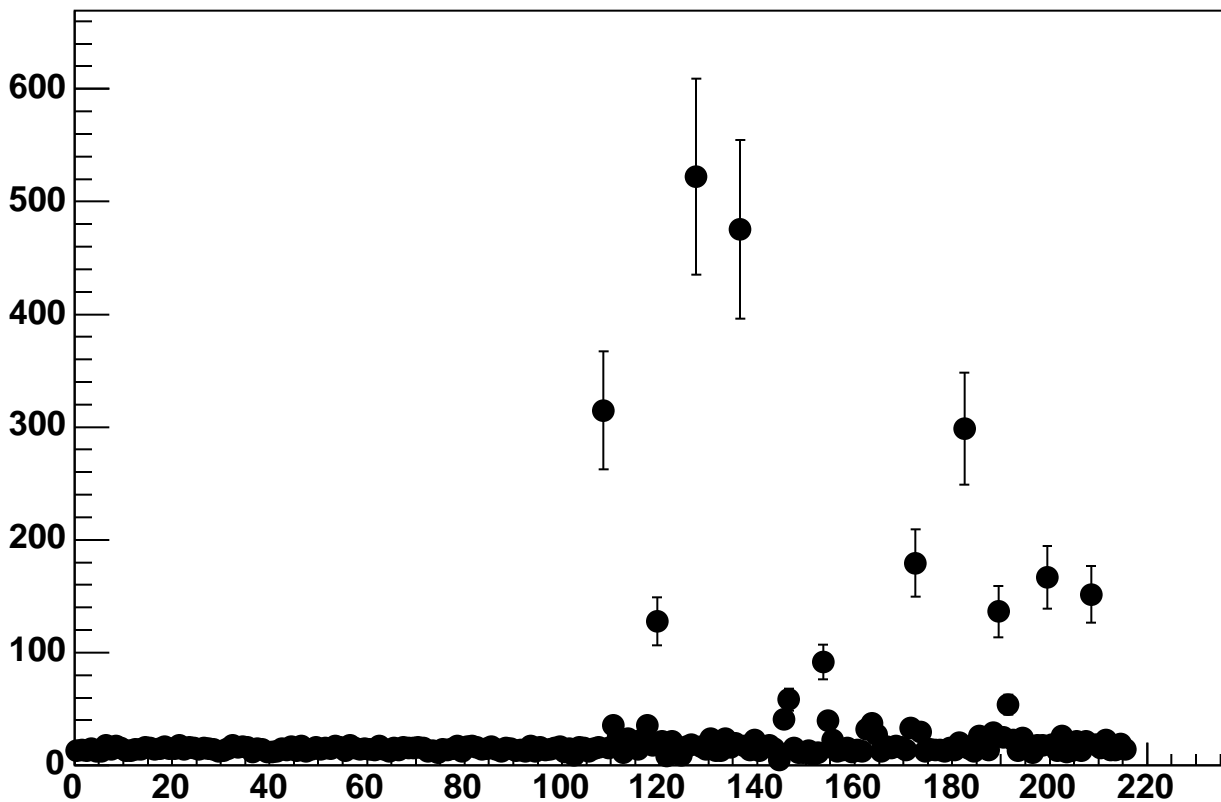
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18\*Chip+Chan



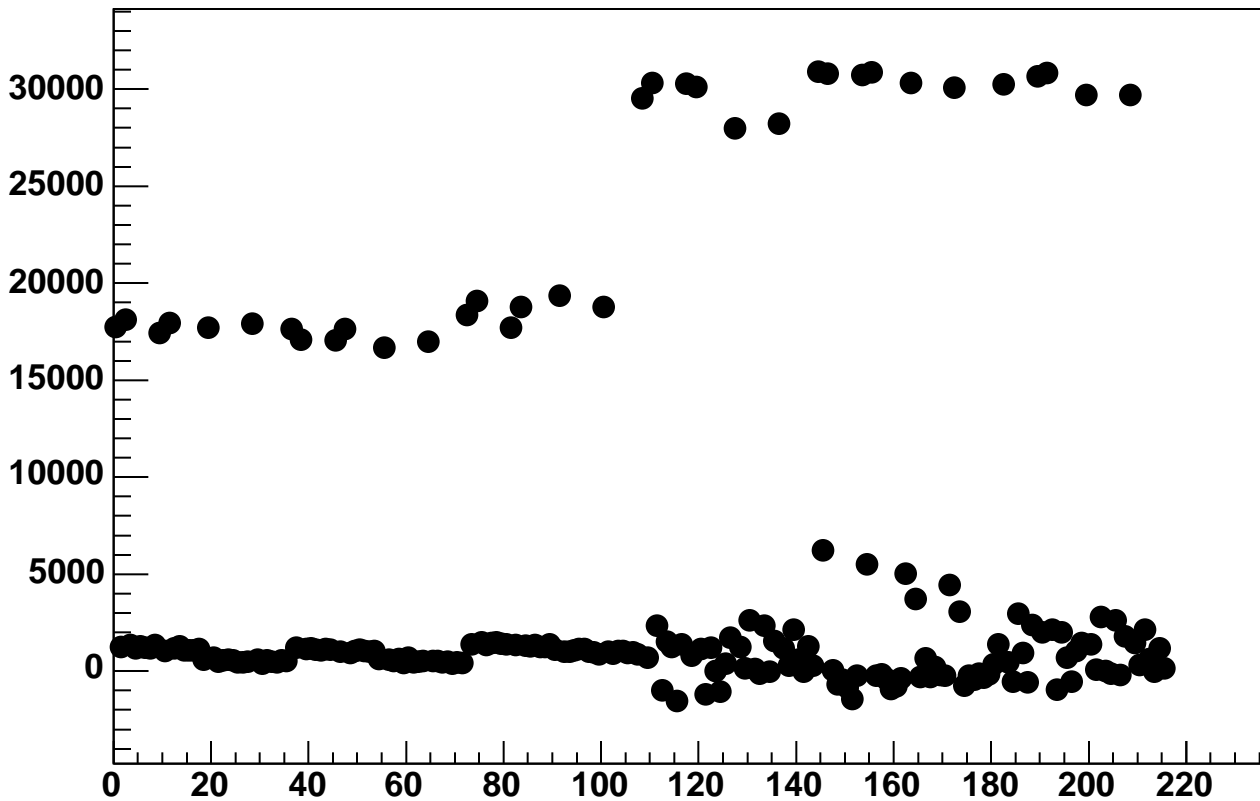
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18\*Chip+Chan



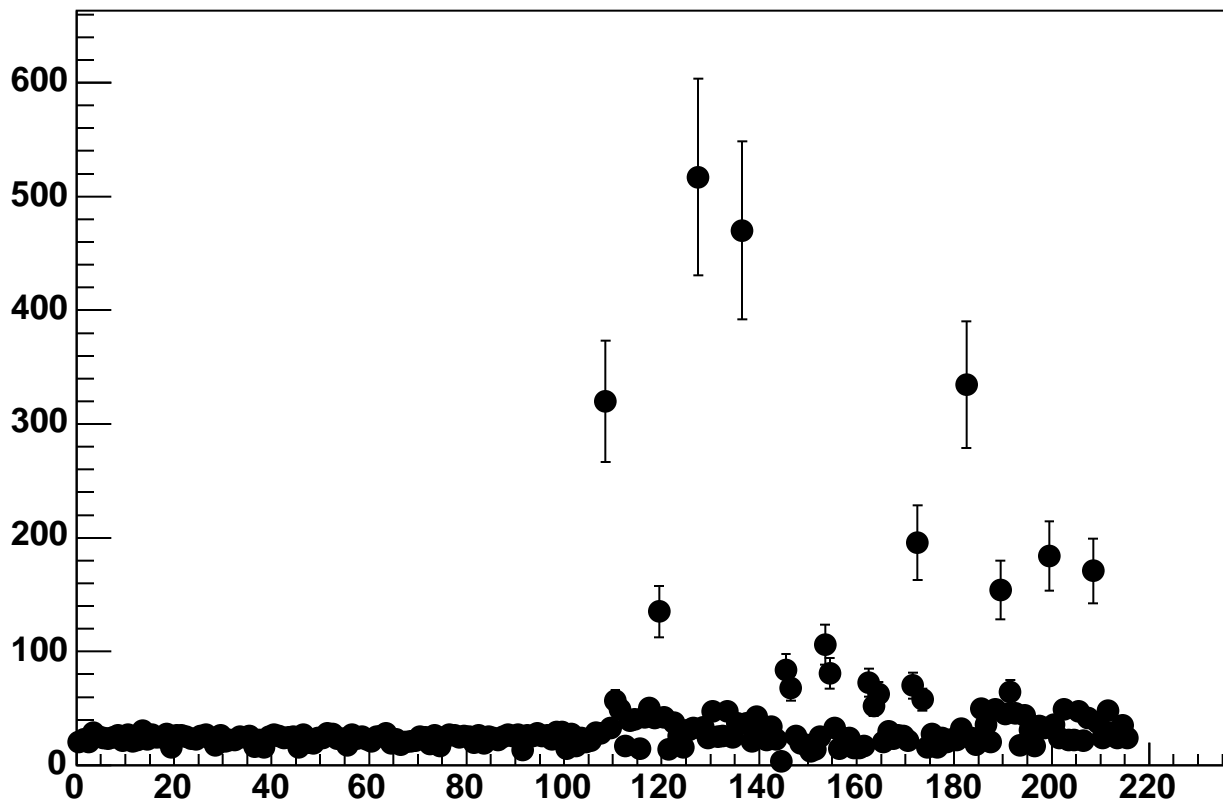
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18\*Chip+Chan



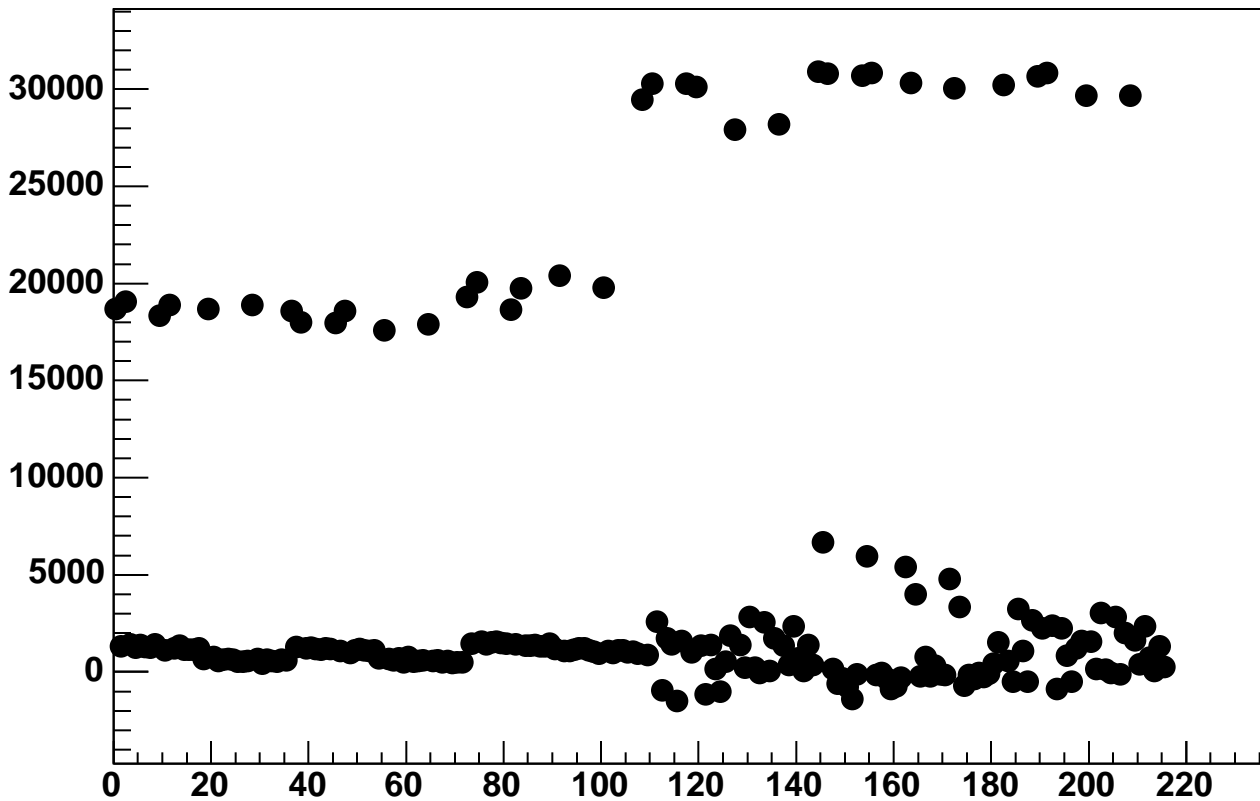
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18\*Chip+Chan



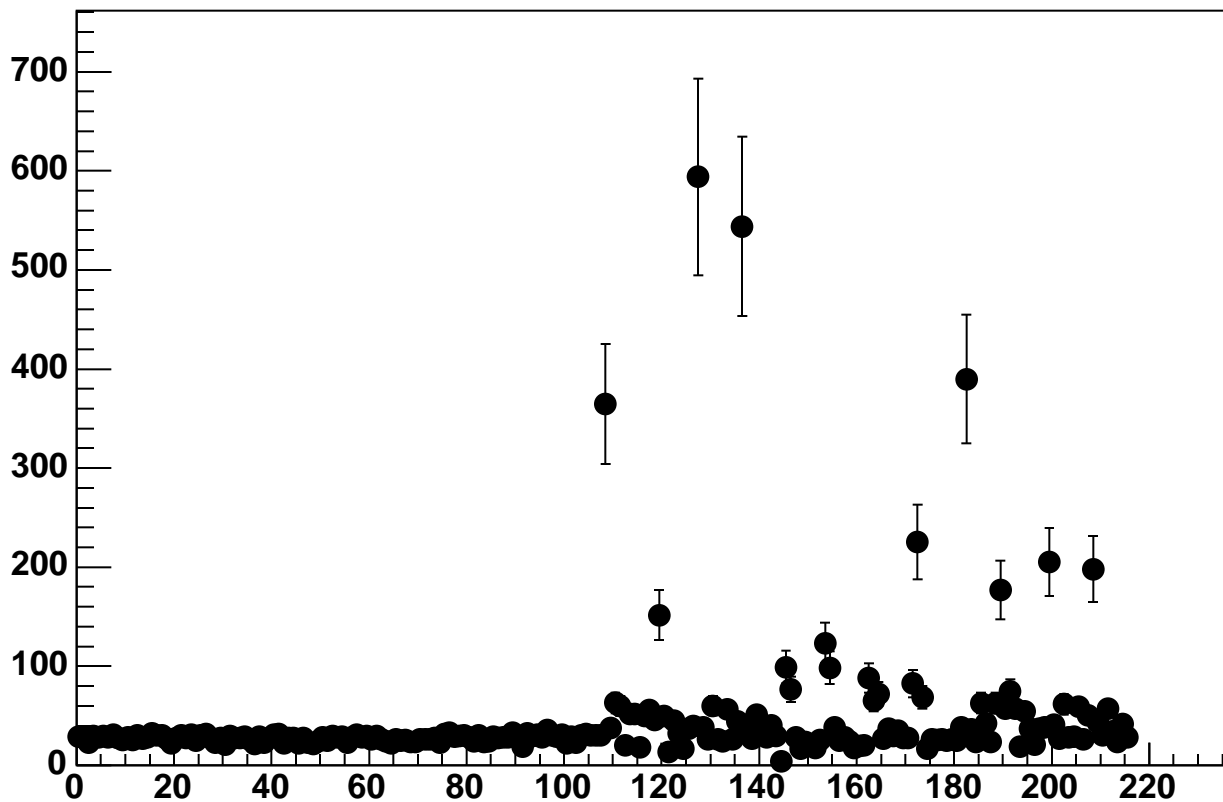
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18\*Chip+Chan



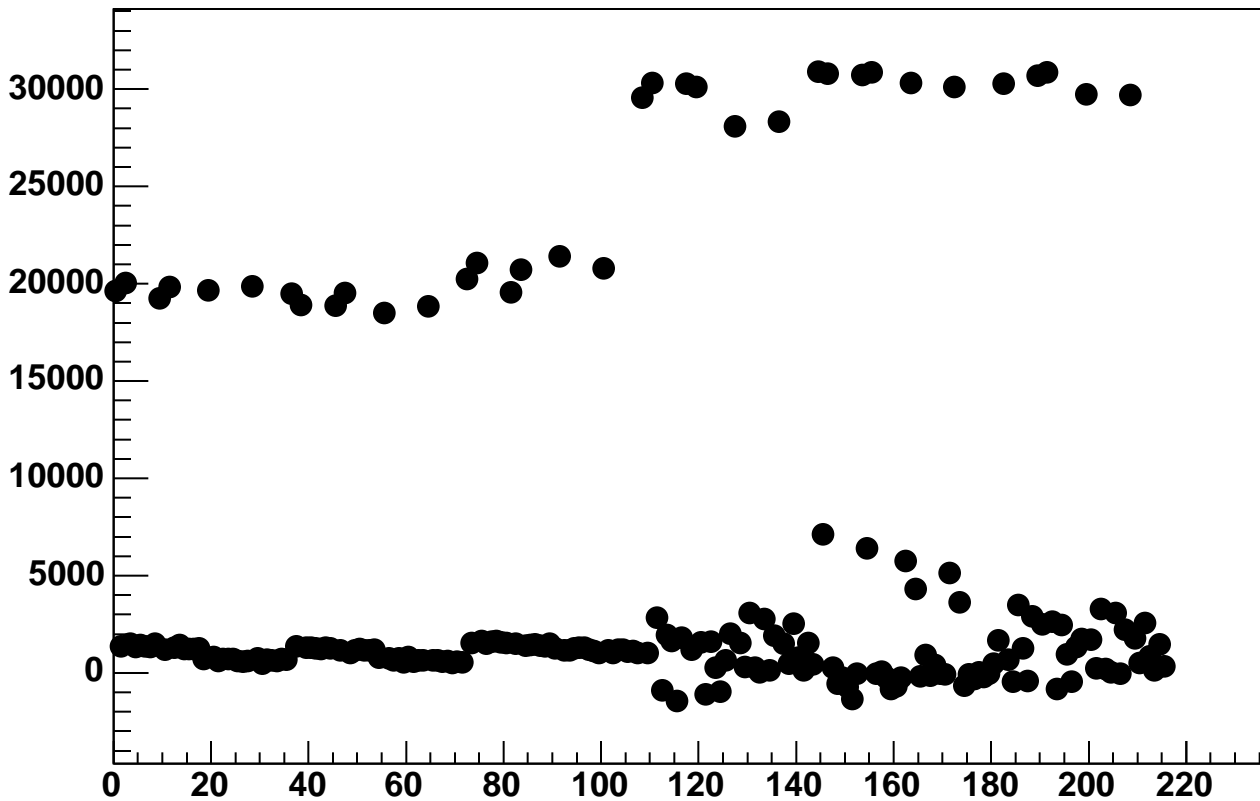
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18\*Chip+Chan



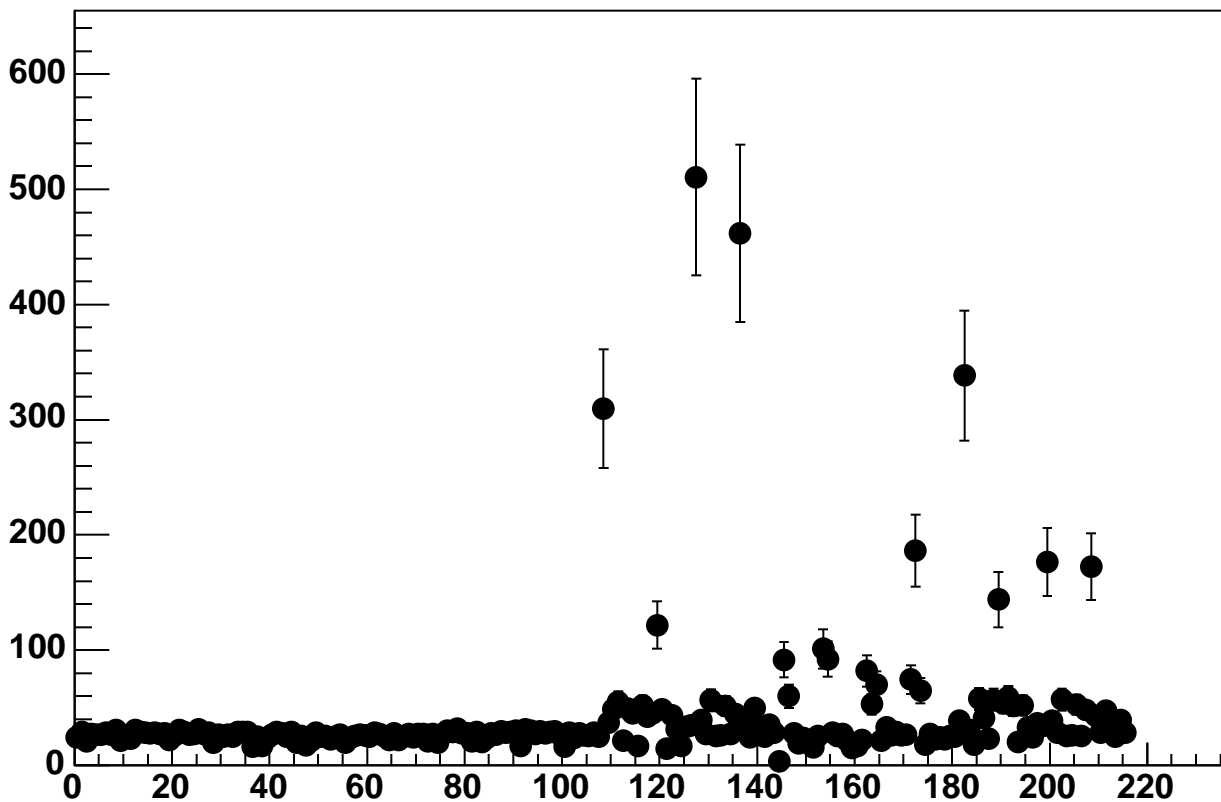
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18\*Chip+Chan



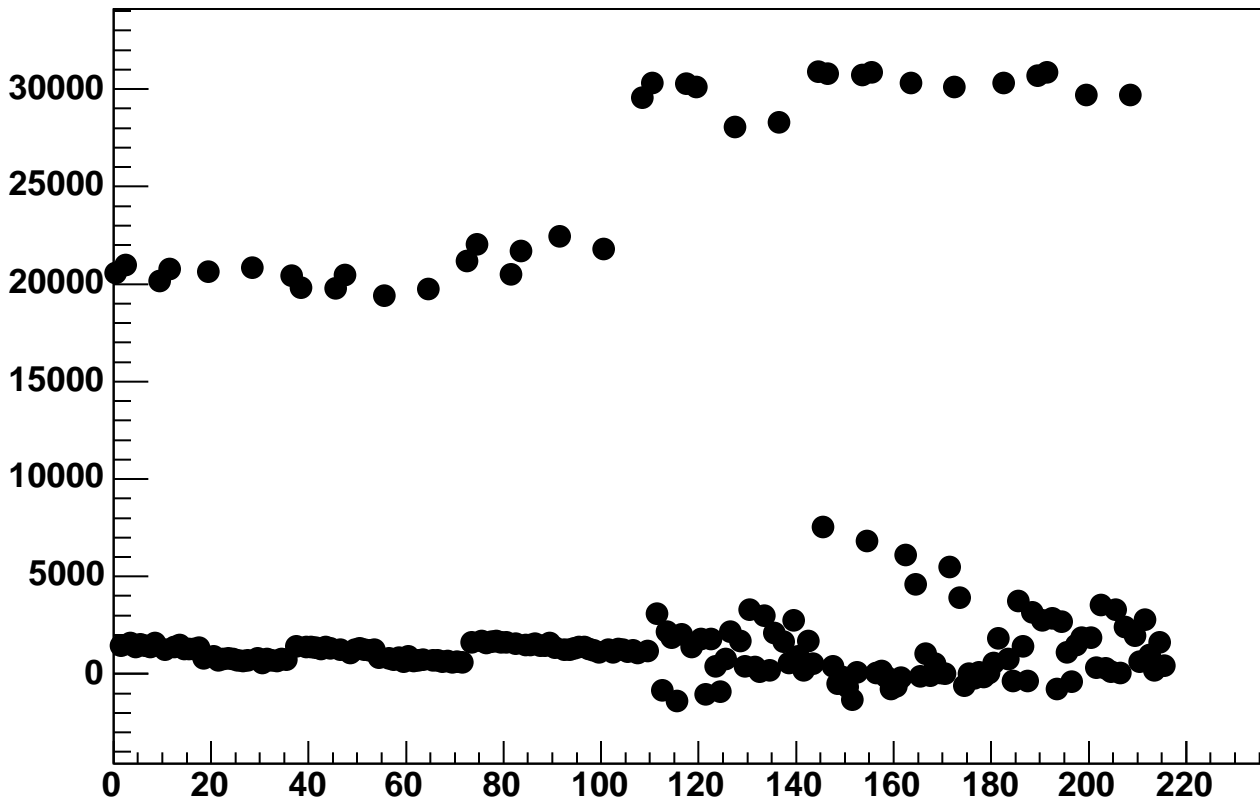
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18\*Chip+Chan



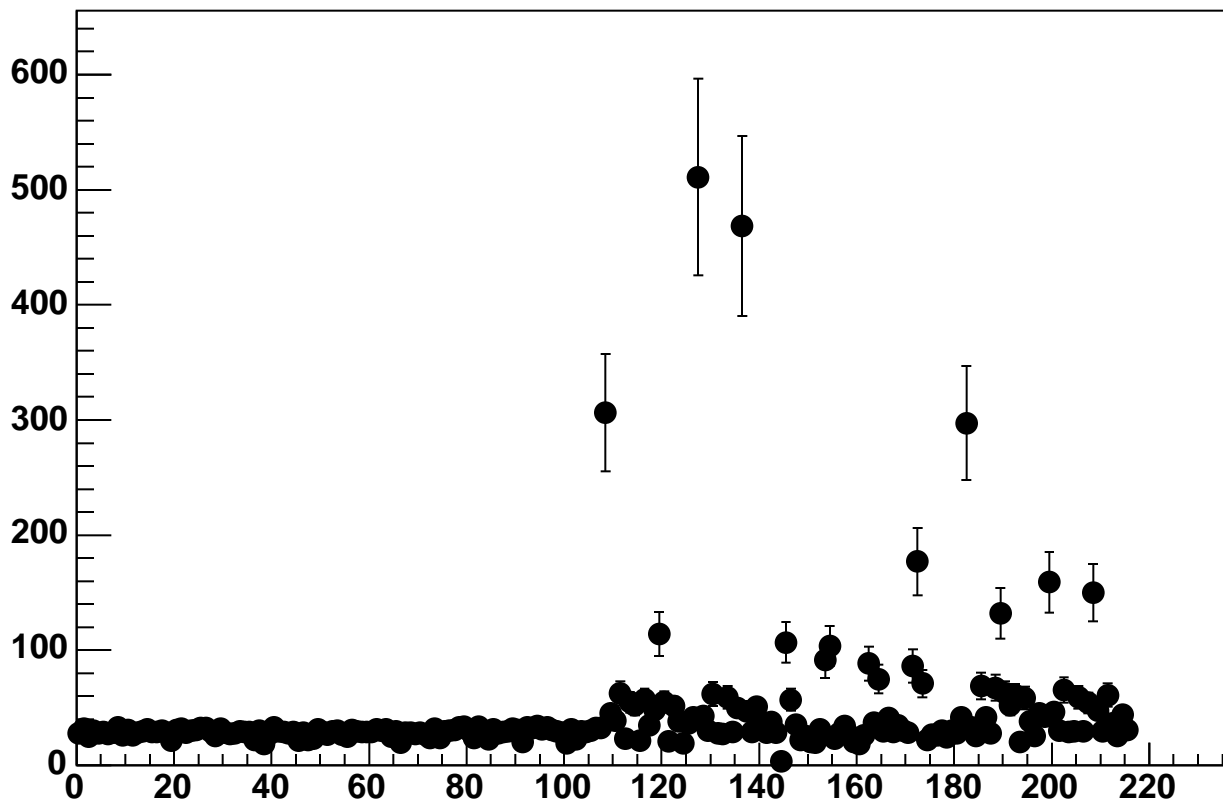
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18\*Chip+Chan



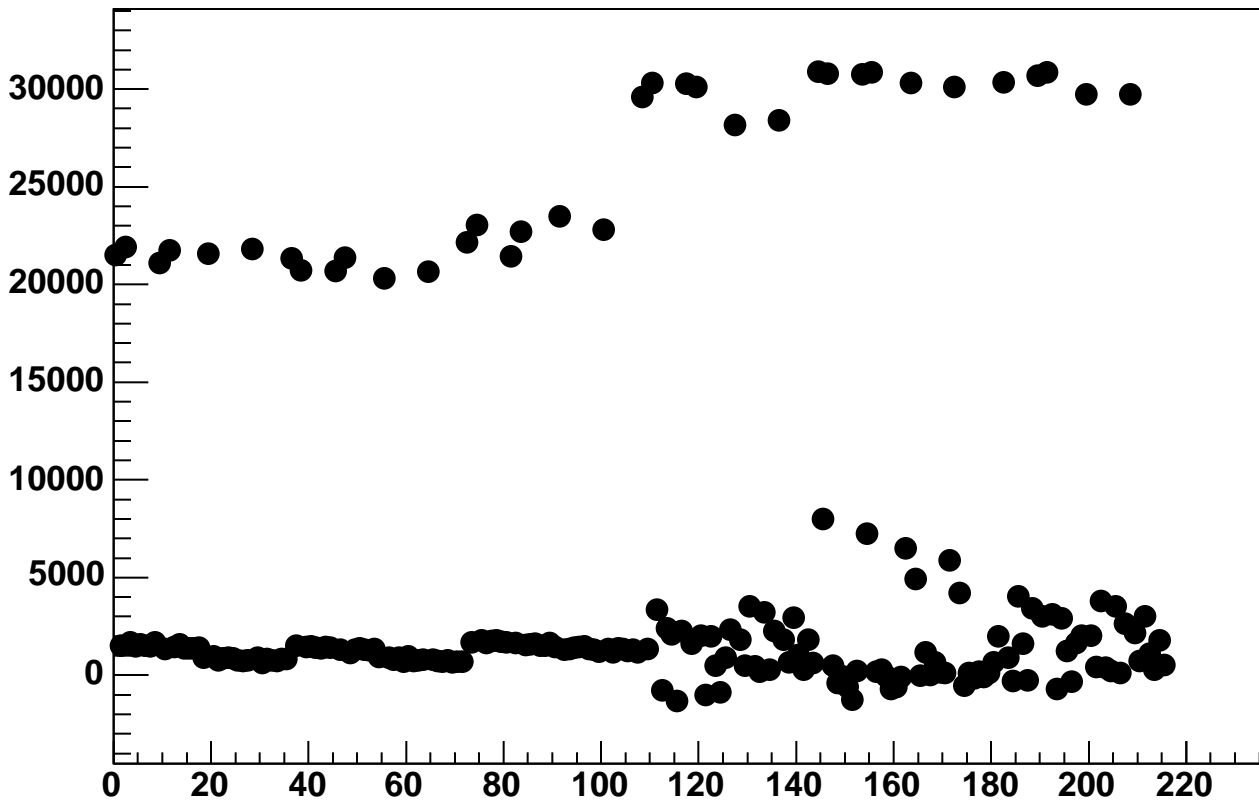
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18\*Chip+Chan



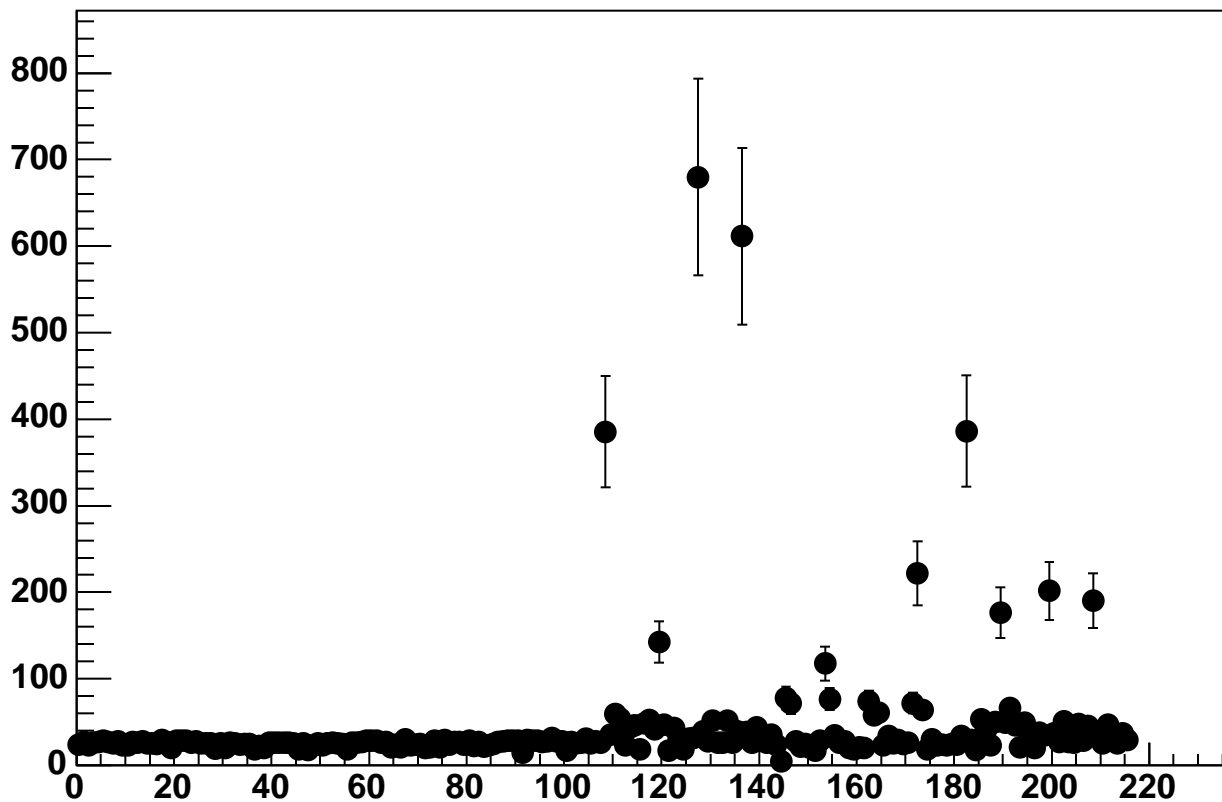
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=35, DAC=9200, ADC Mean vs 18\*Chip+Chan

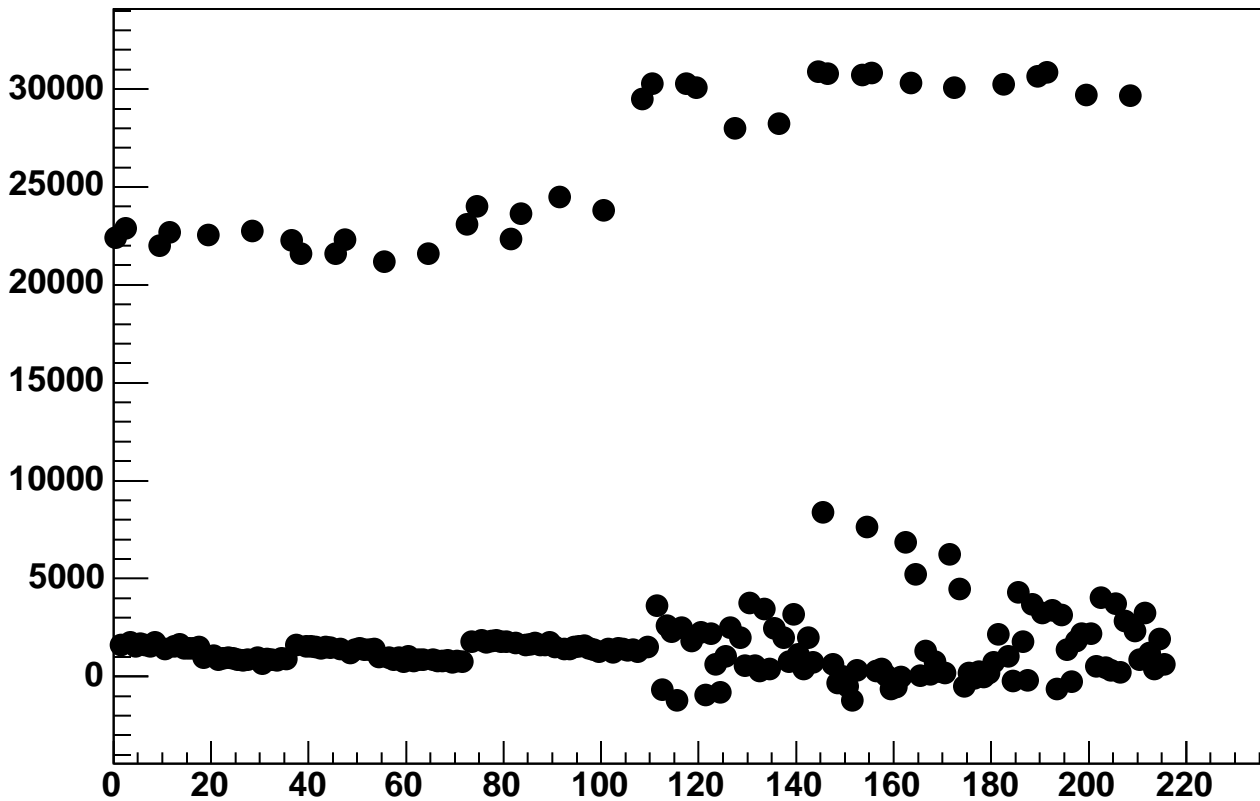


Enable 0, Hold=35, DAC=9200, ADC Noise vs 18\*Chip+Chan

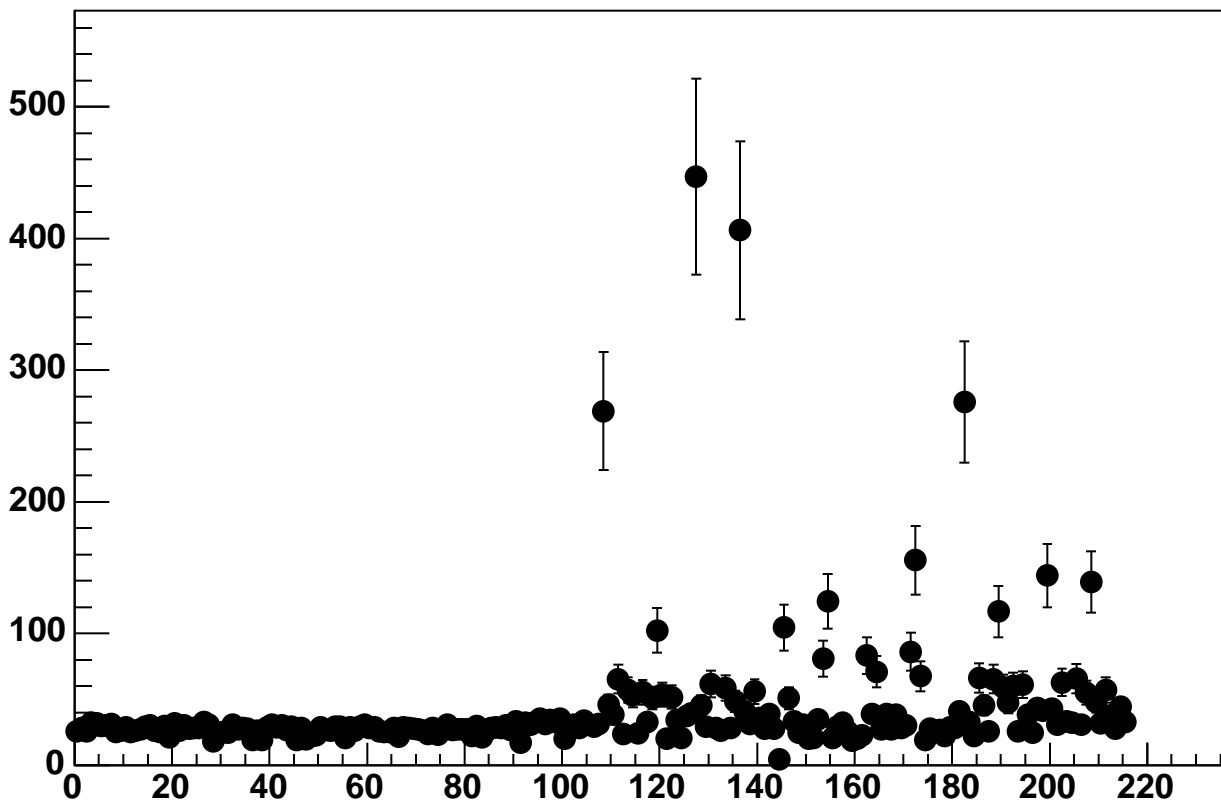




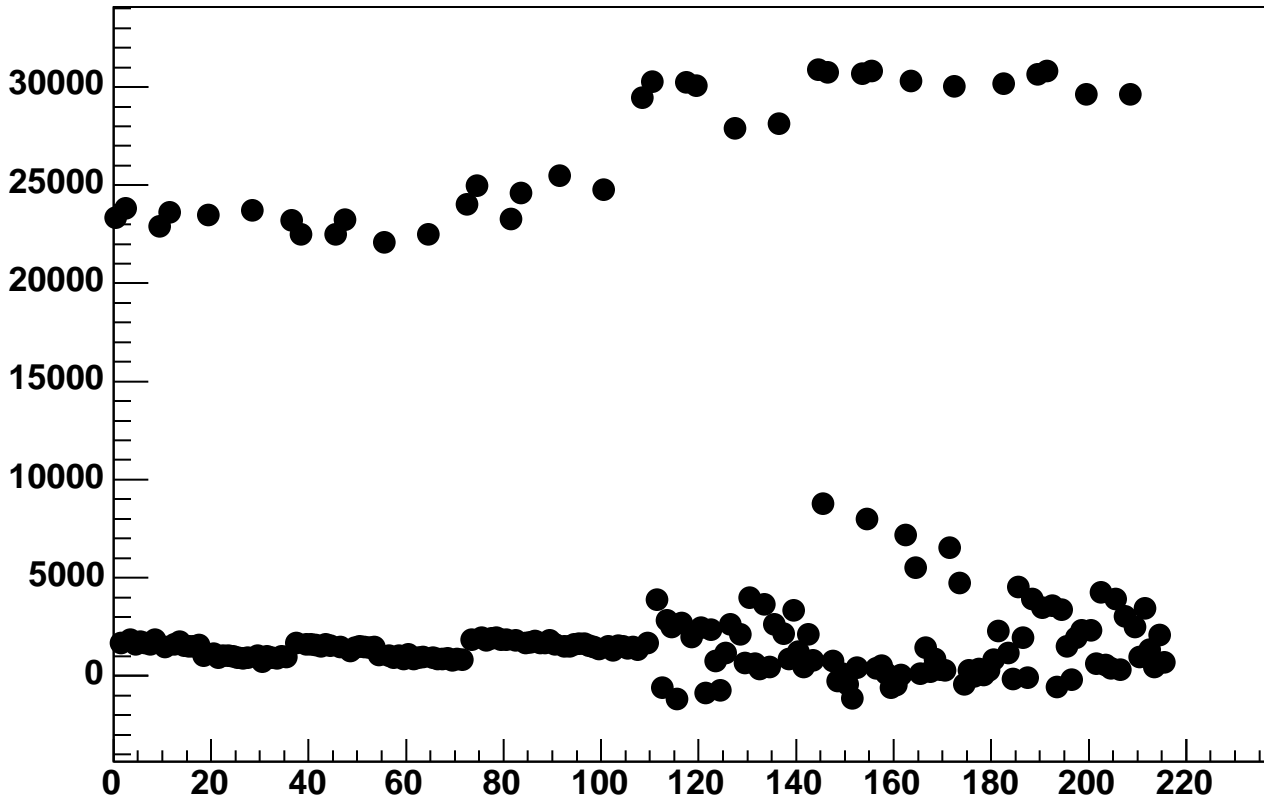
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18\*Chip+Chan



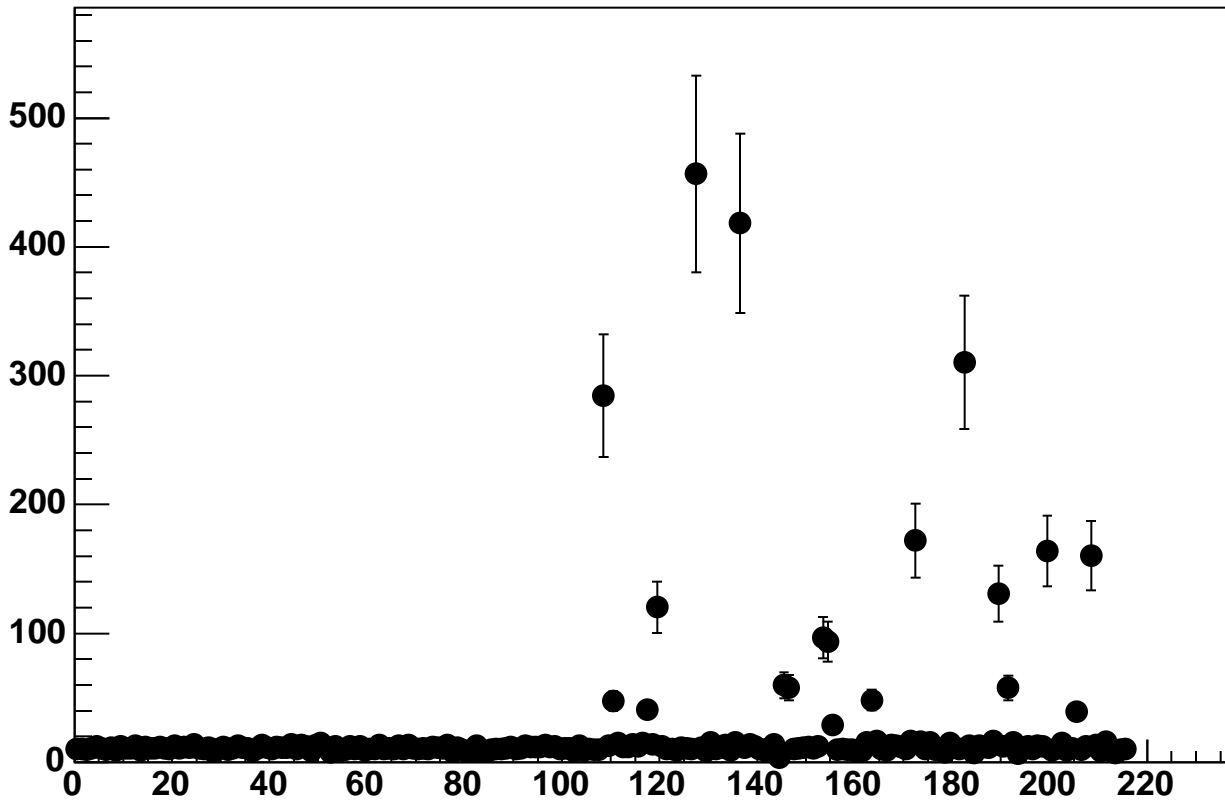
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18\*Chip+Chan



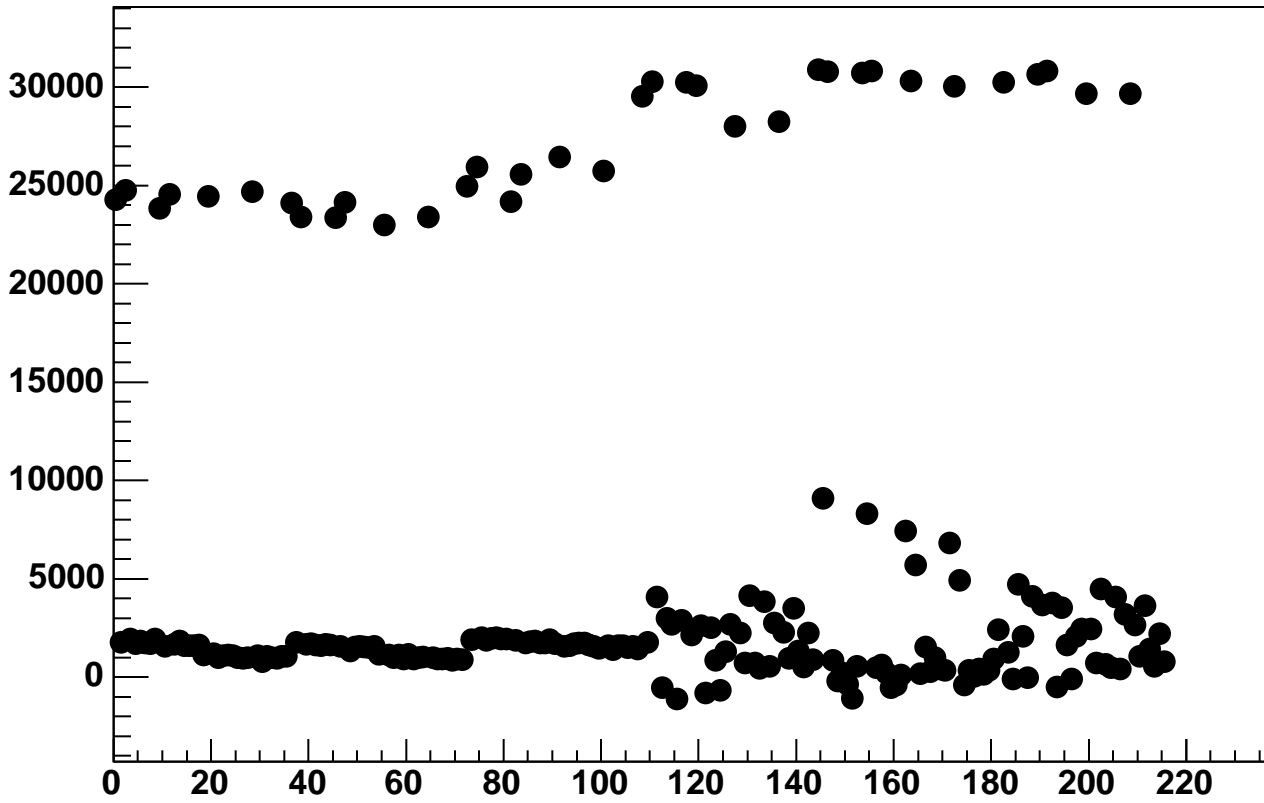
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18\*Chip+Chan



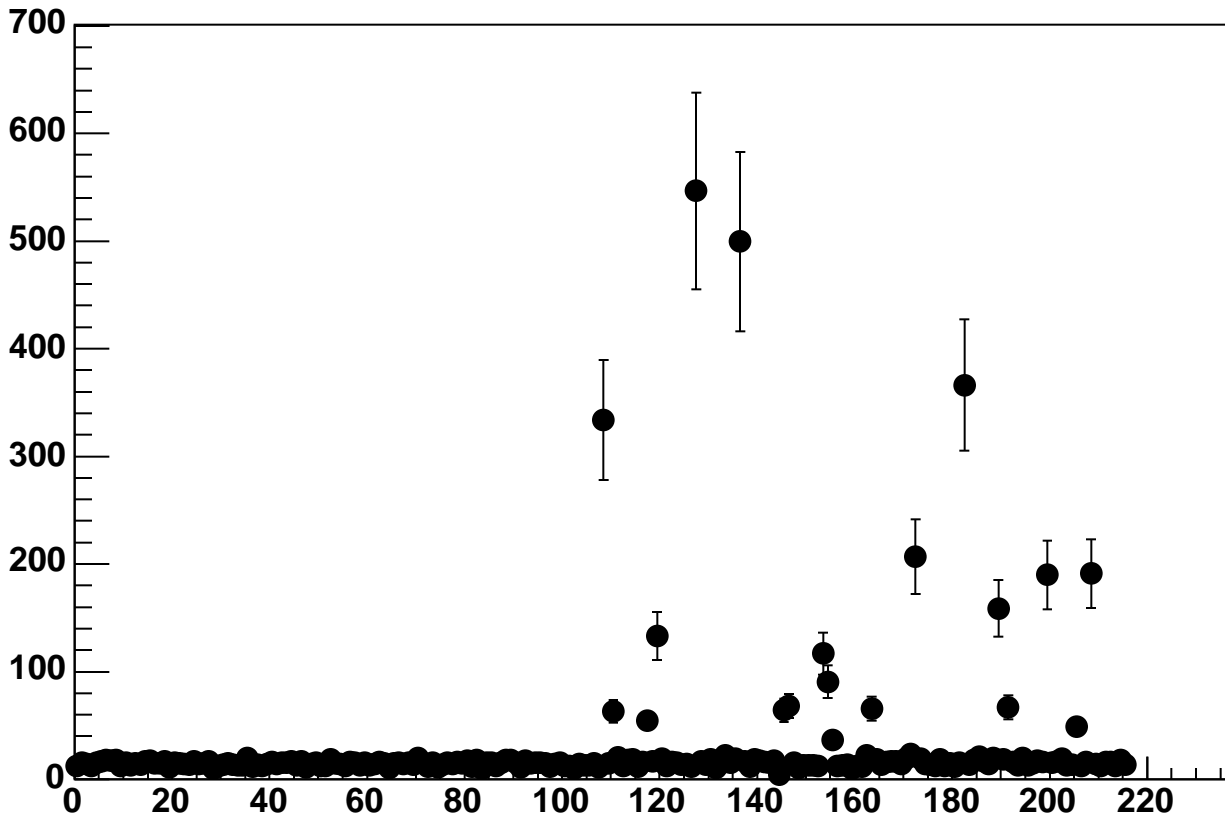
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18\*Chip+Chan



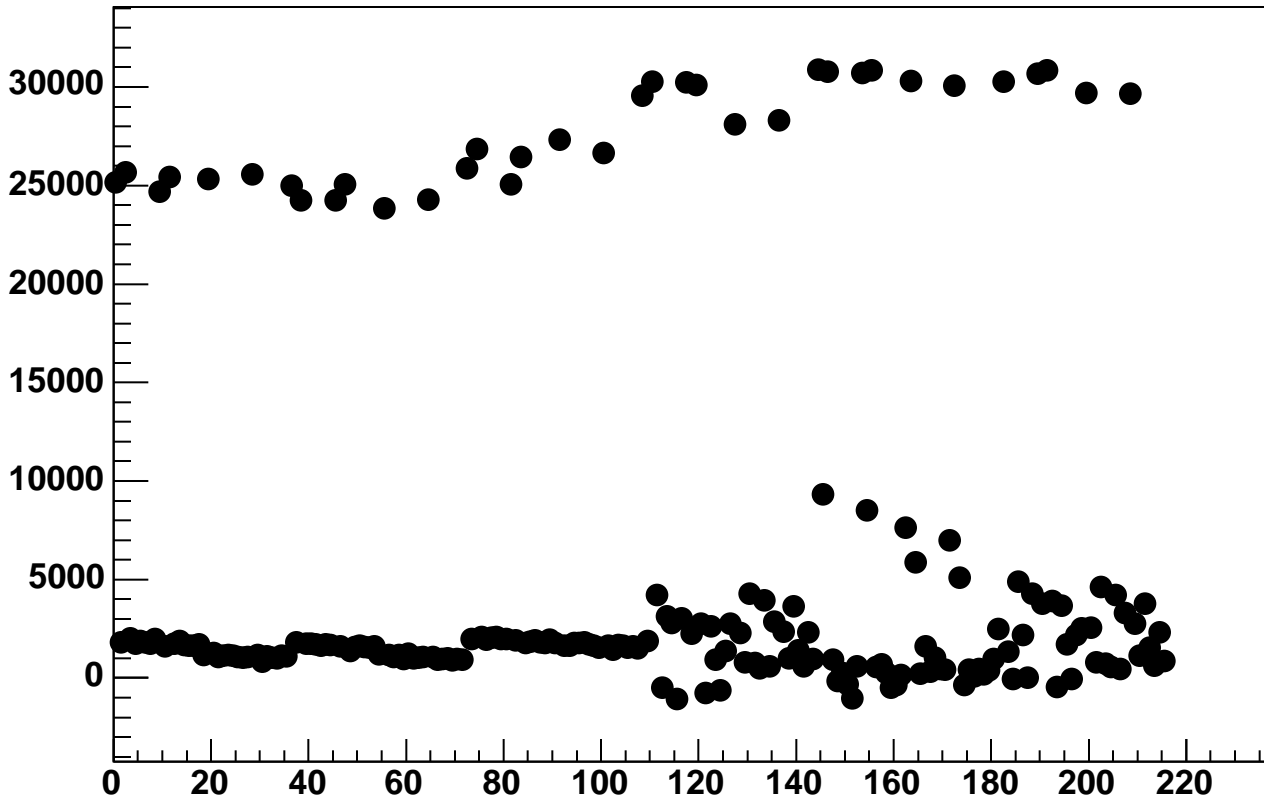
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18\*Chip+Chan



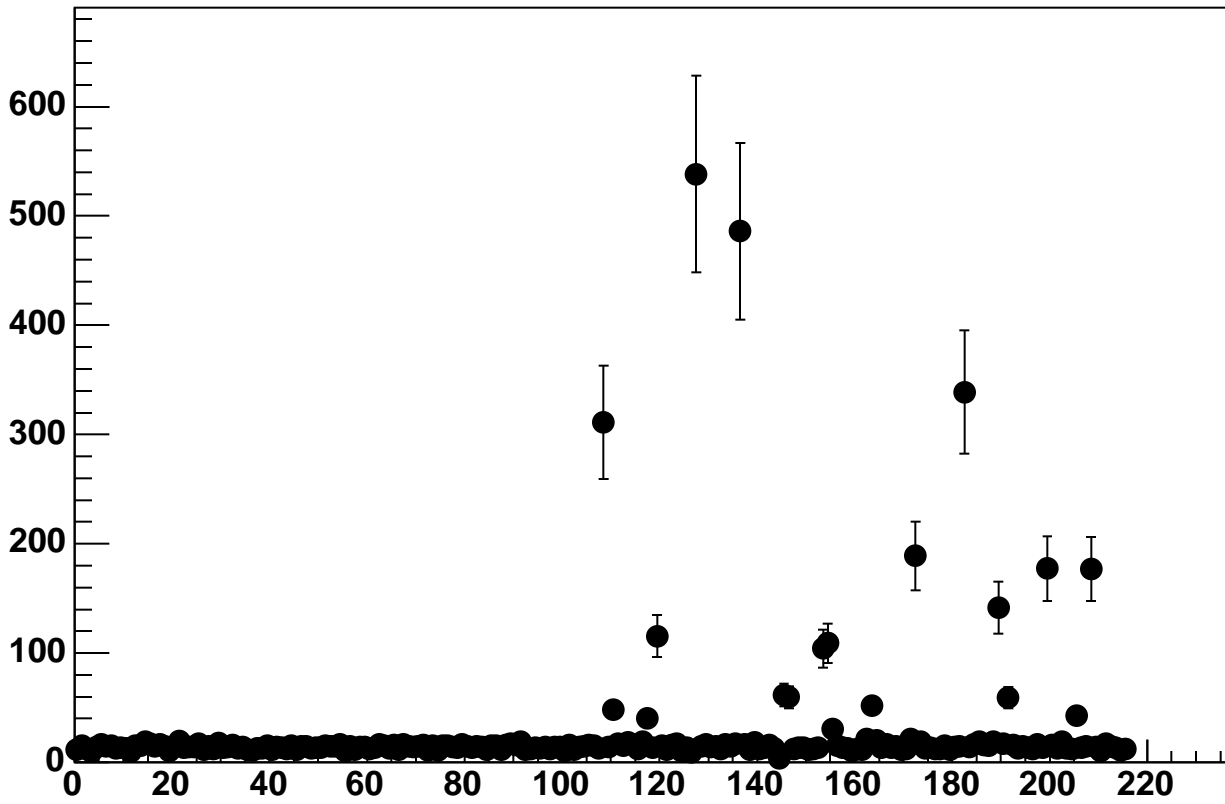
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18\*Chip+Chan



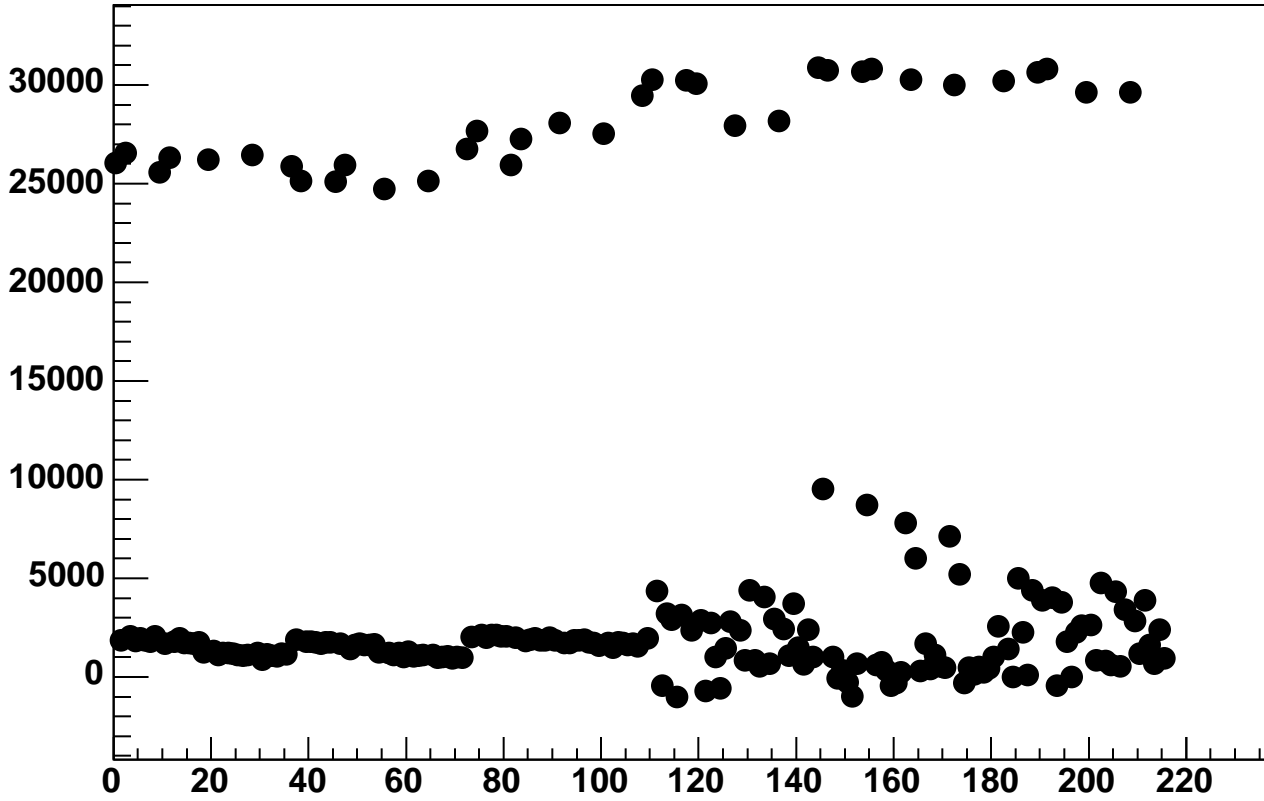
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18\*Chip+Chan



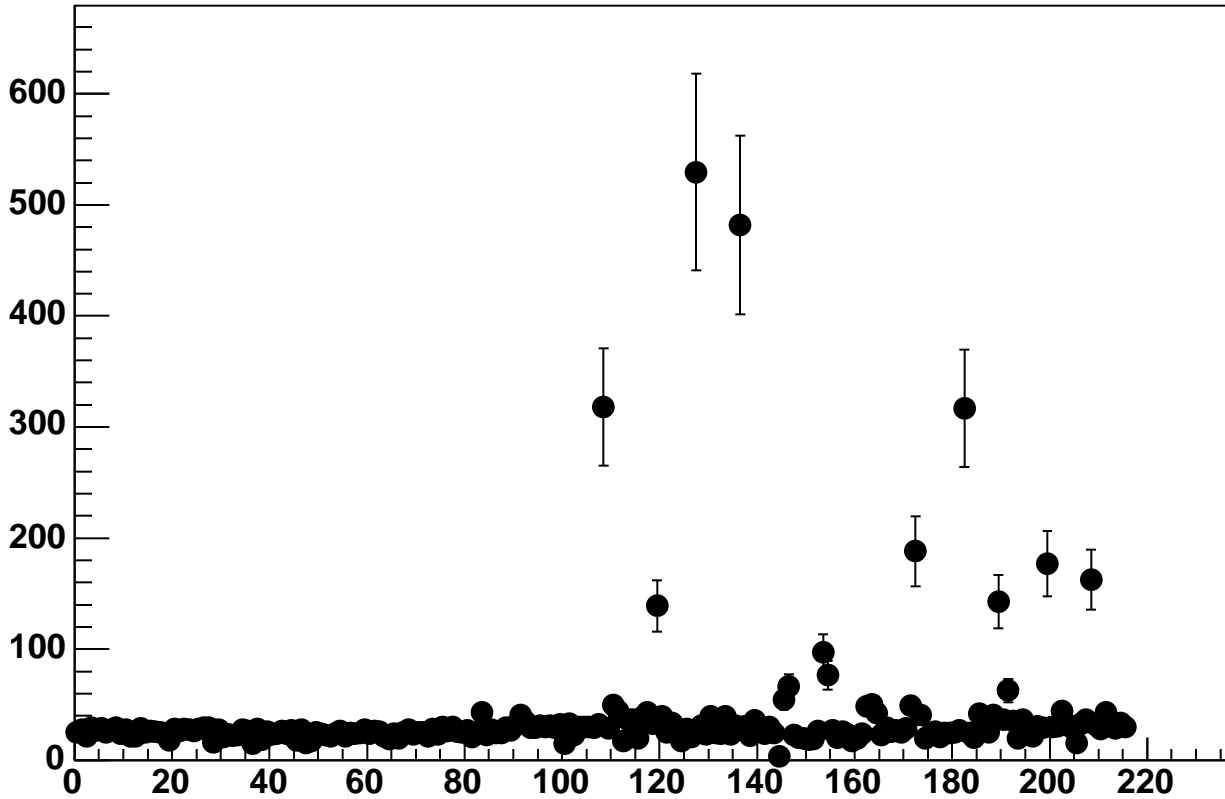
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18\*Chip+Chan



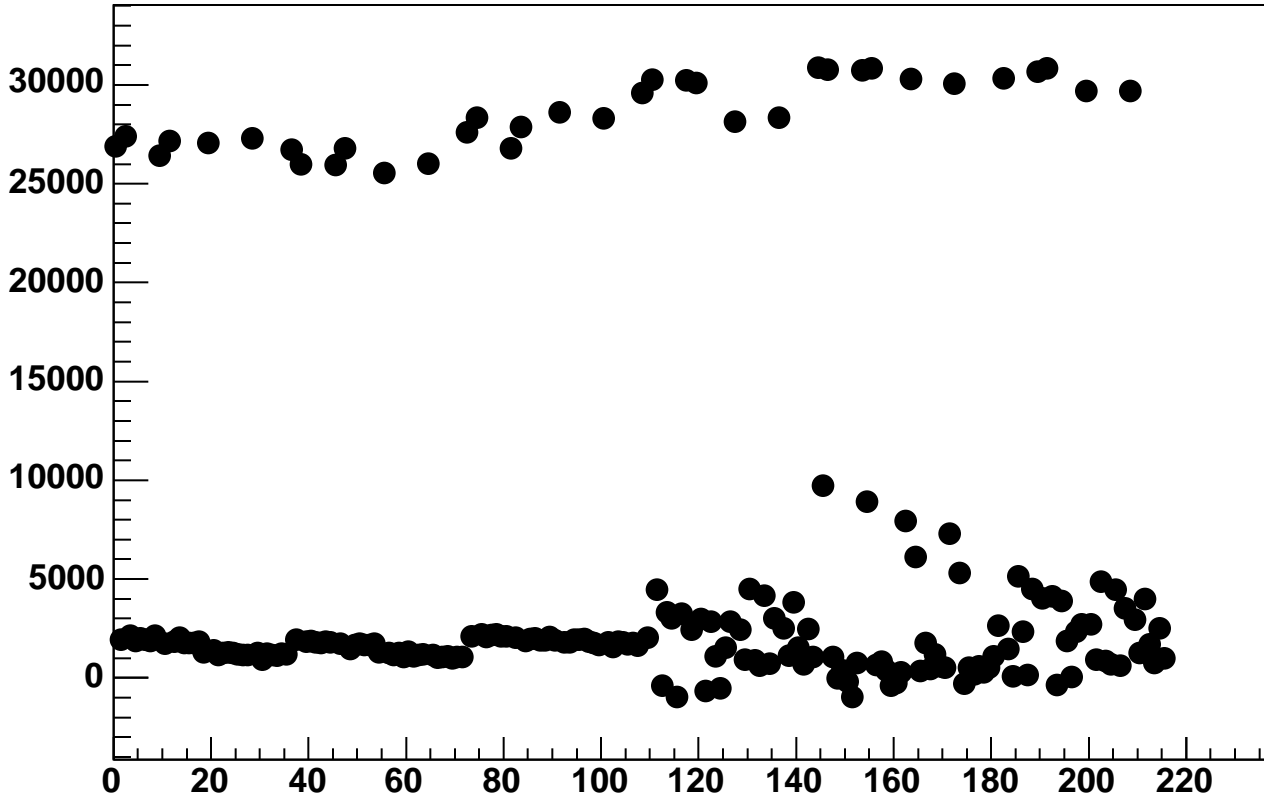
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18\*Chip+Chan



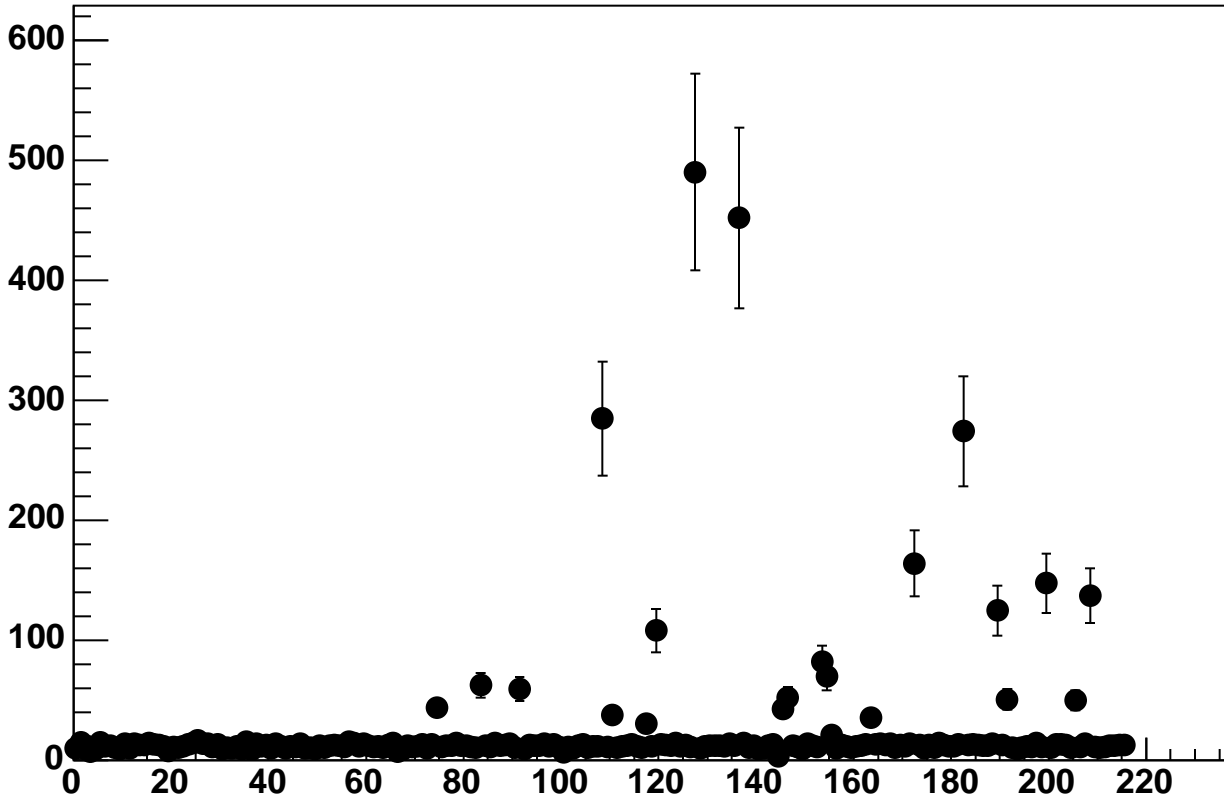
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18\*Chip+Chan



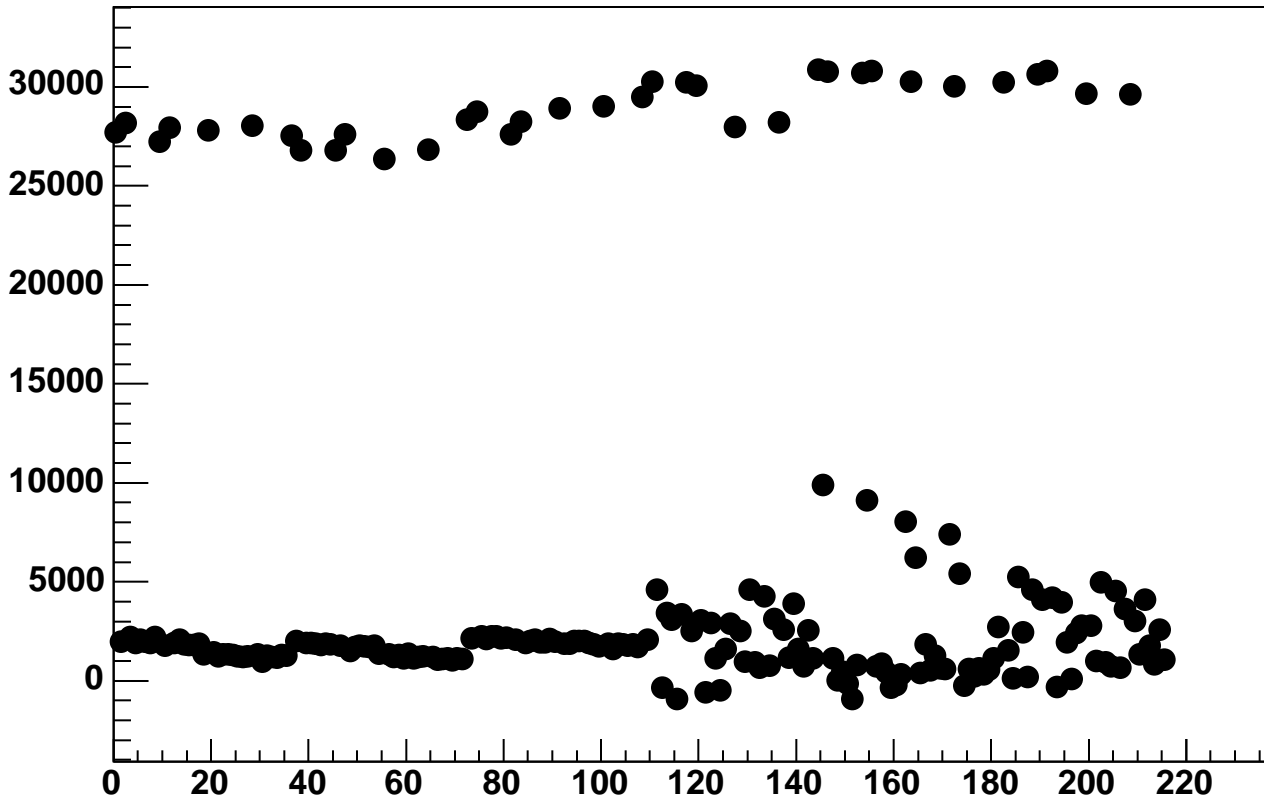
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18\*Chip+Chan



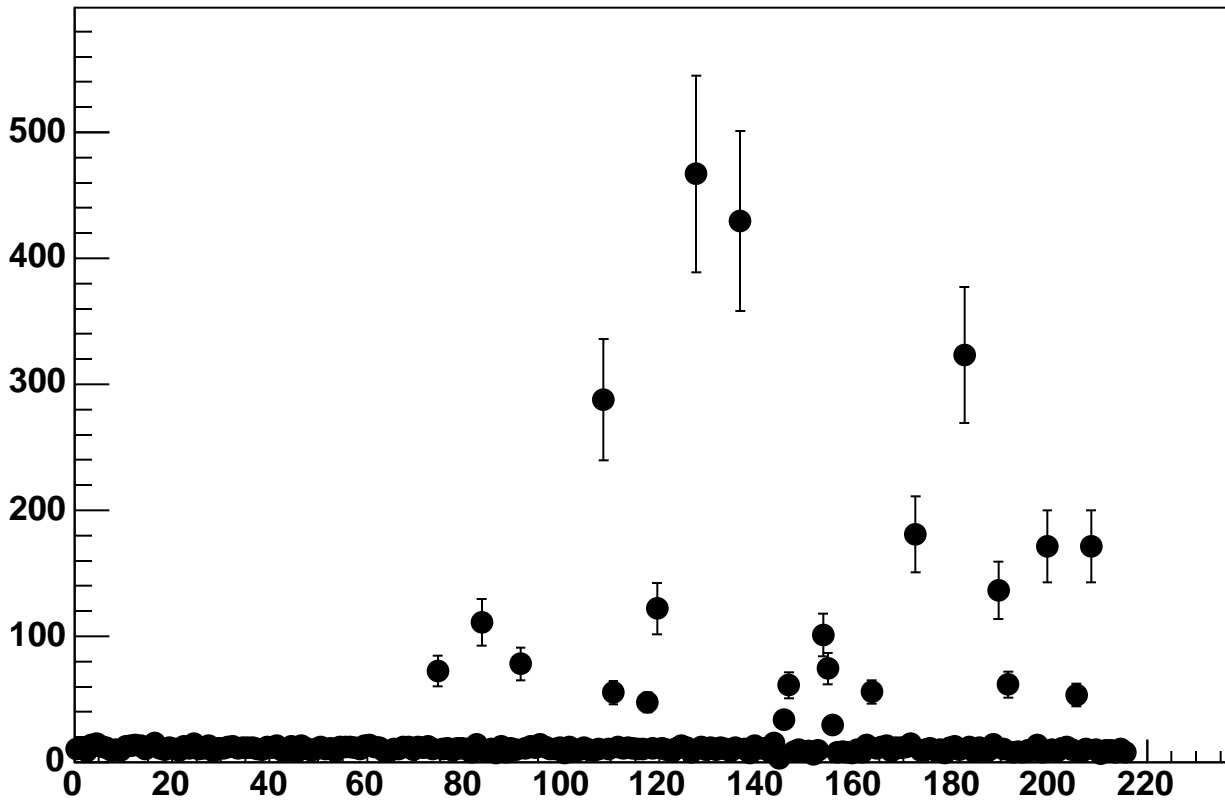
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18\*Chip+Chan



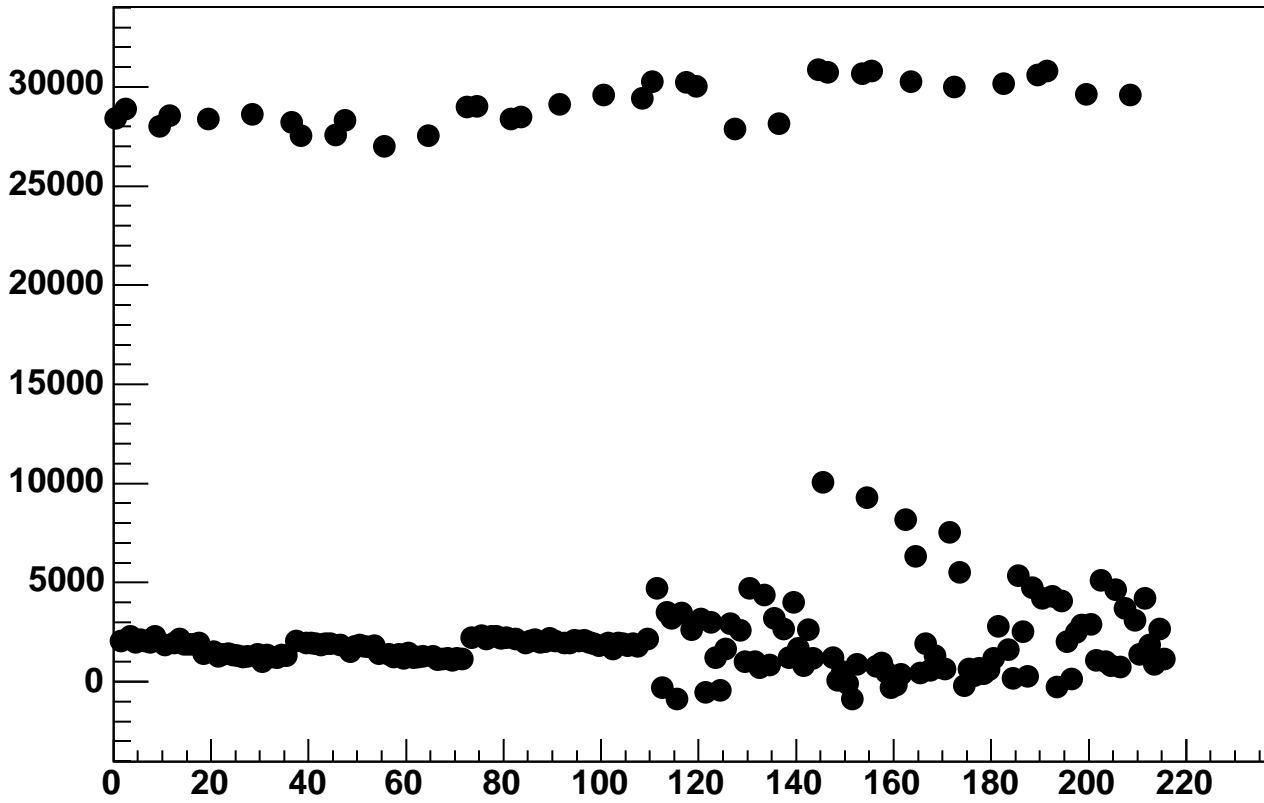
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18\*Chip+Chan



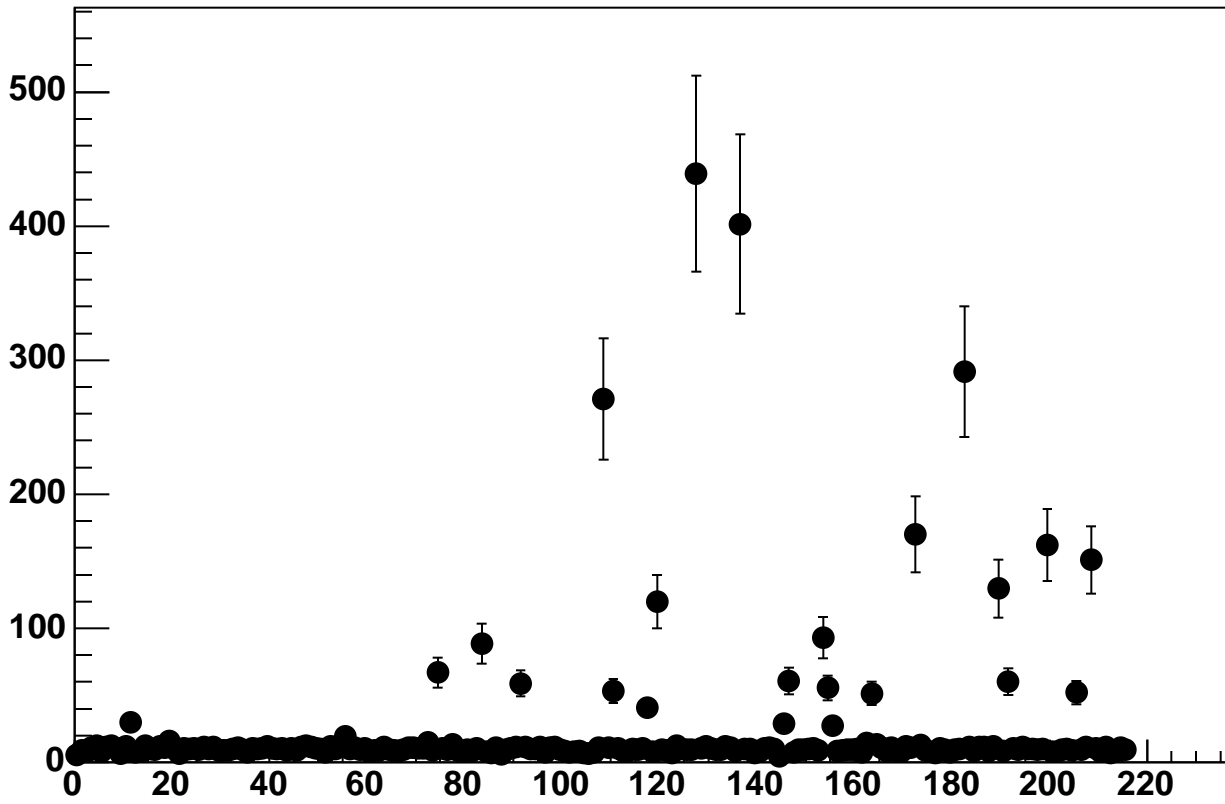
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18\*Chip+Chan



Enable 0, Hold=35, DAC=12400, ADC Mean vs 18\*Chip+Chan

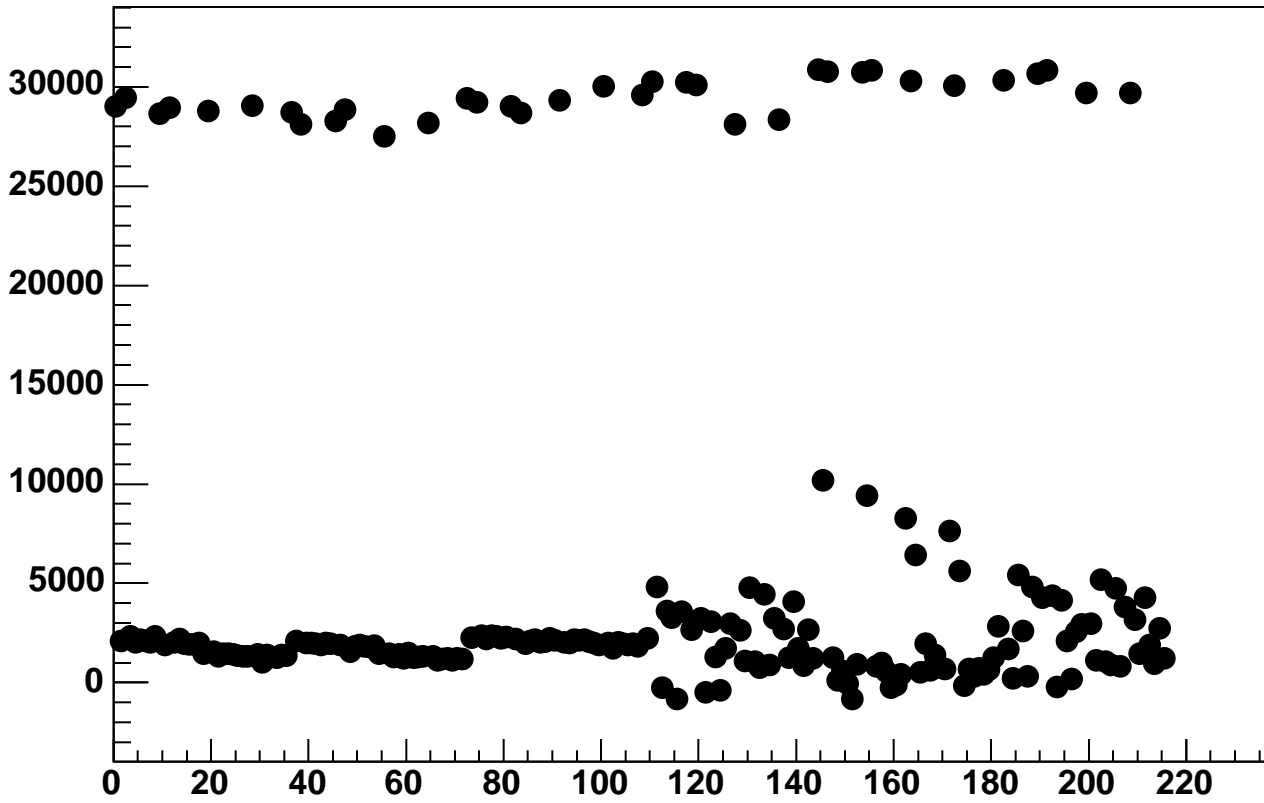


Enable 0, Hold=35, DAC=12400, ADC Noise vs 18\*Chip+Chan

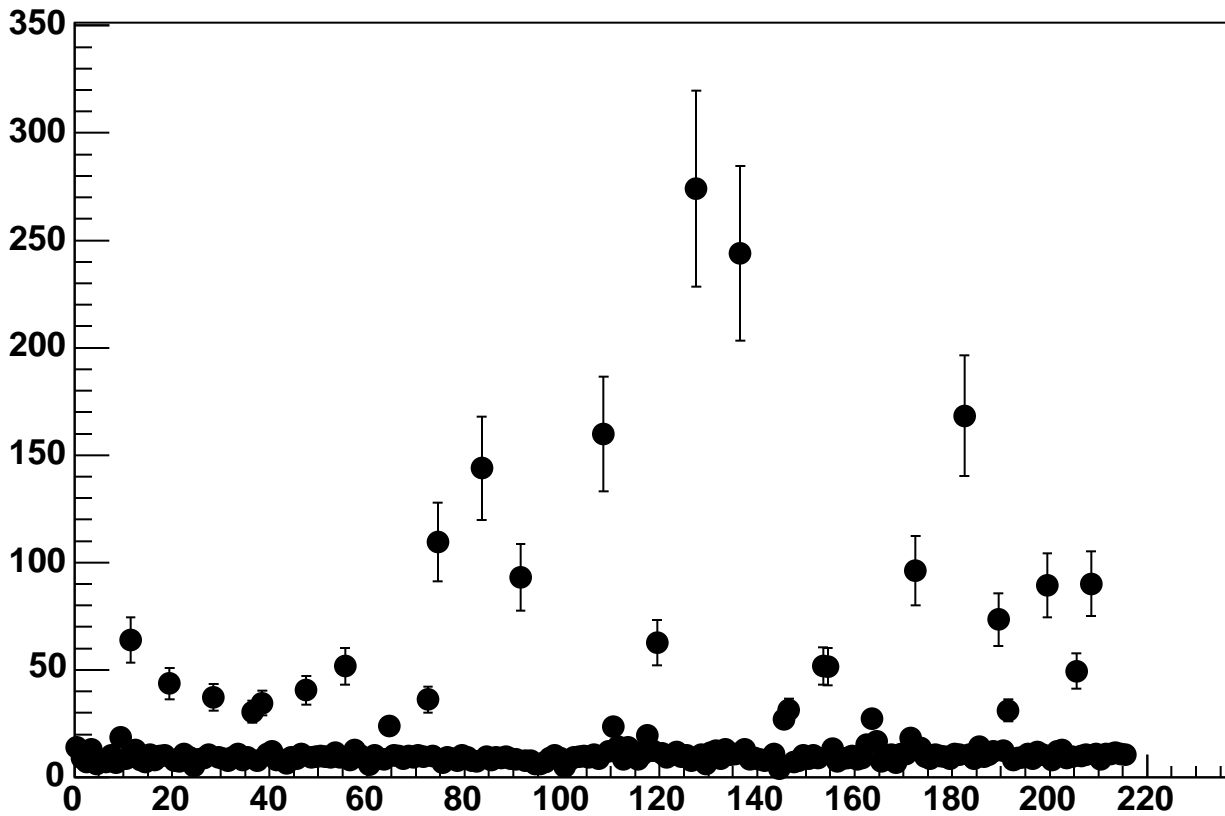




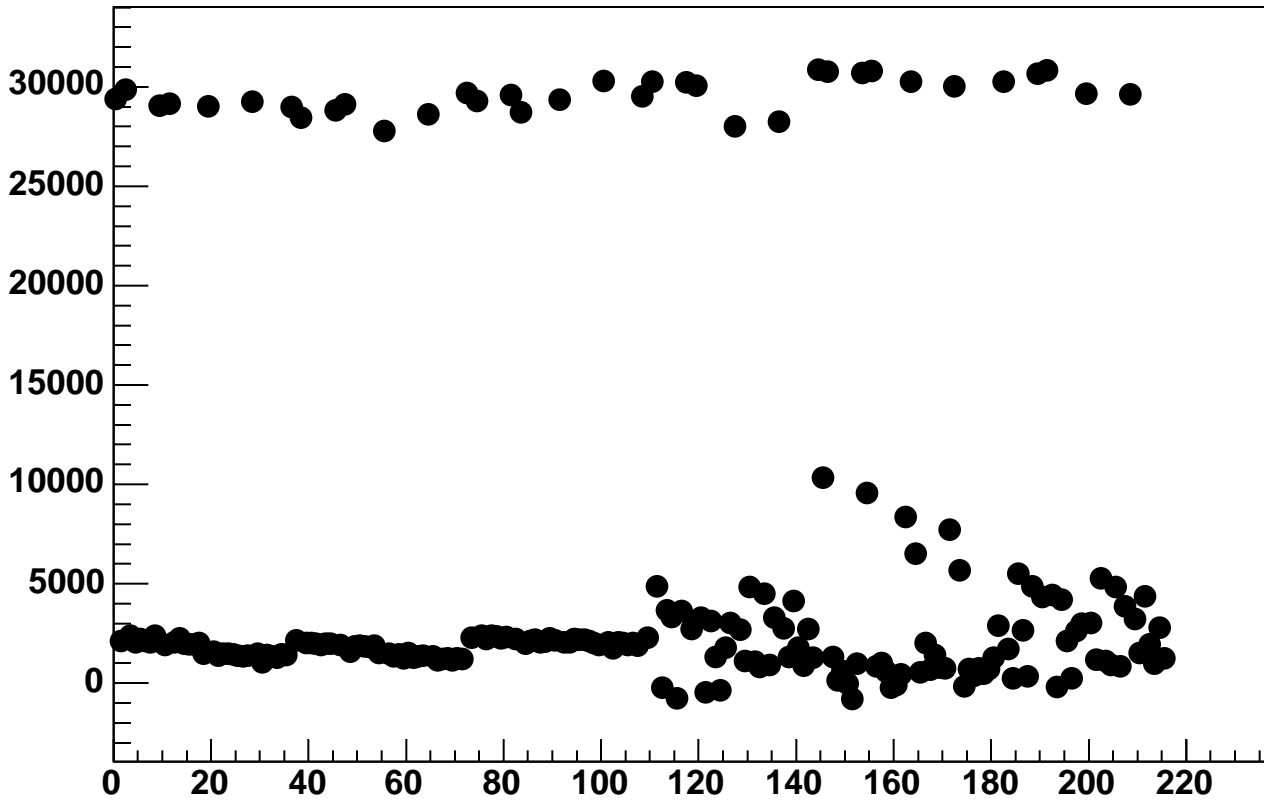
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18\*Chip+Chan



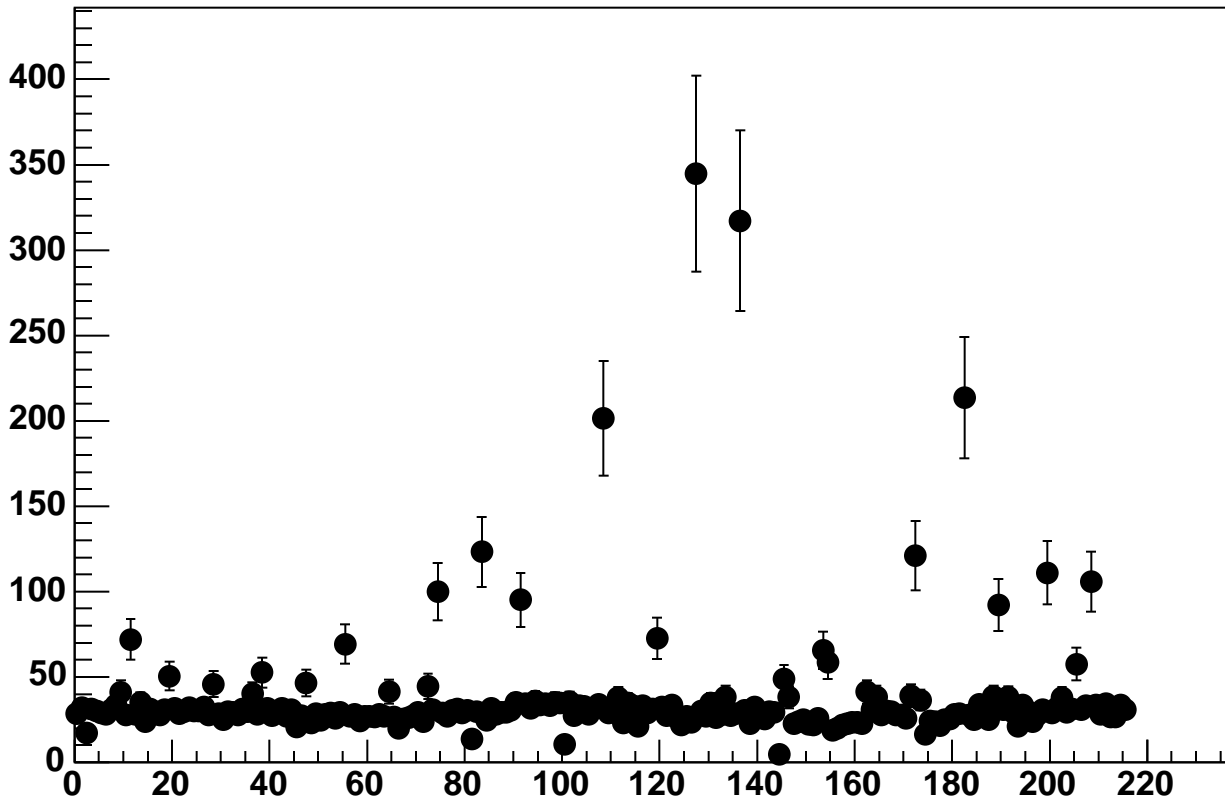
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18\*Chip+Chan



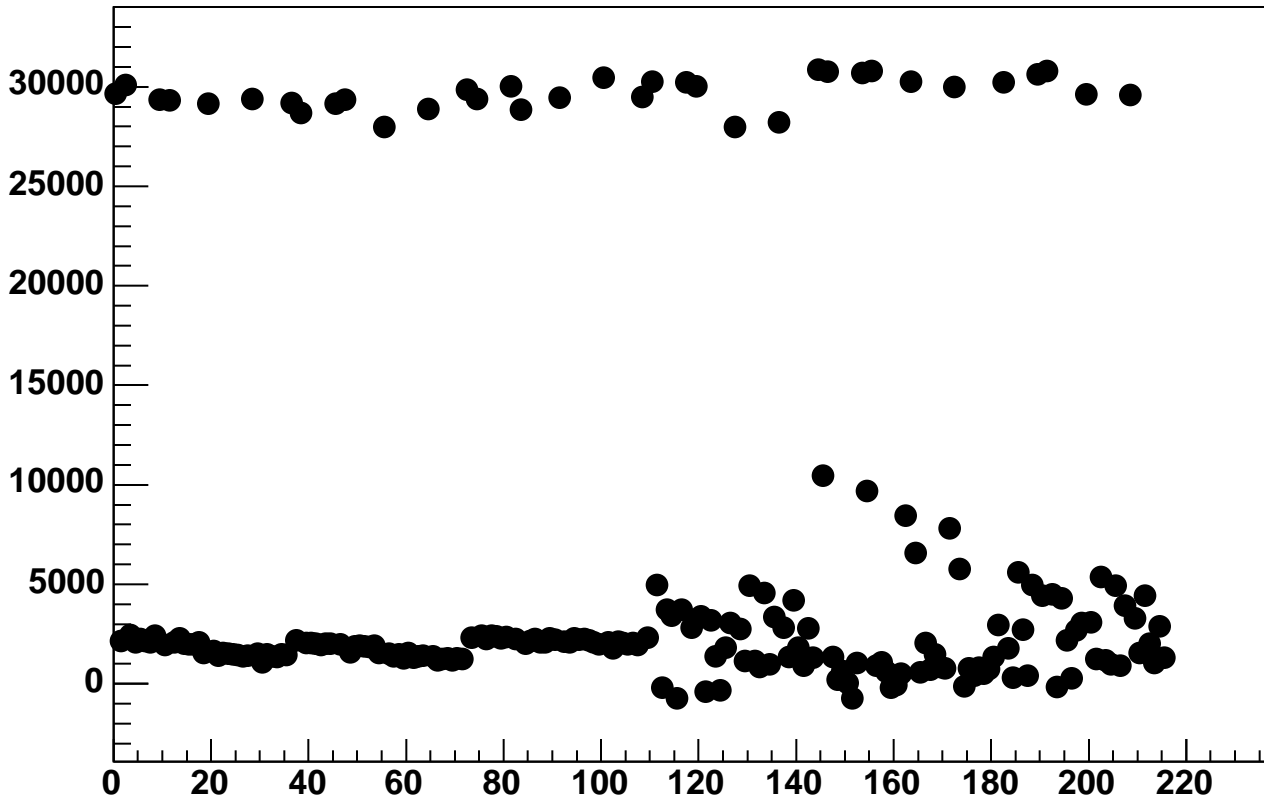
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18\*Chip+Chan



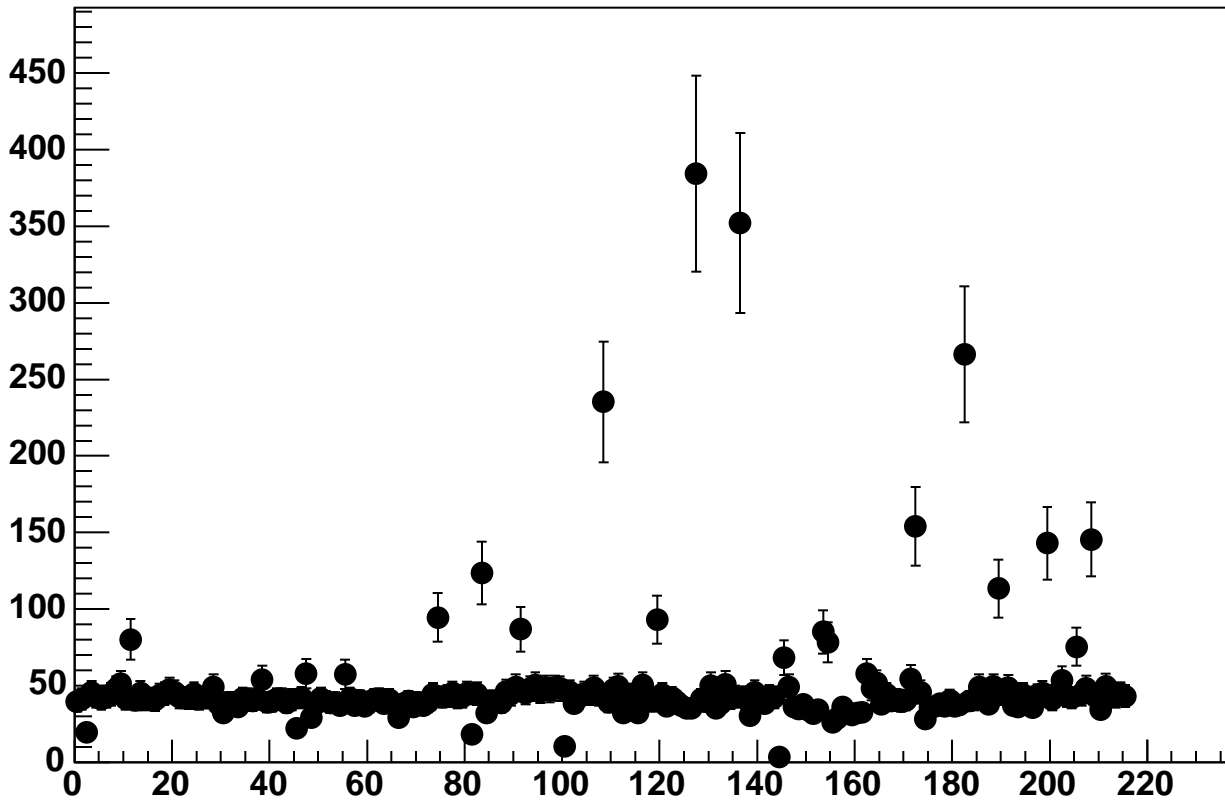
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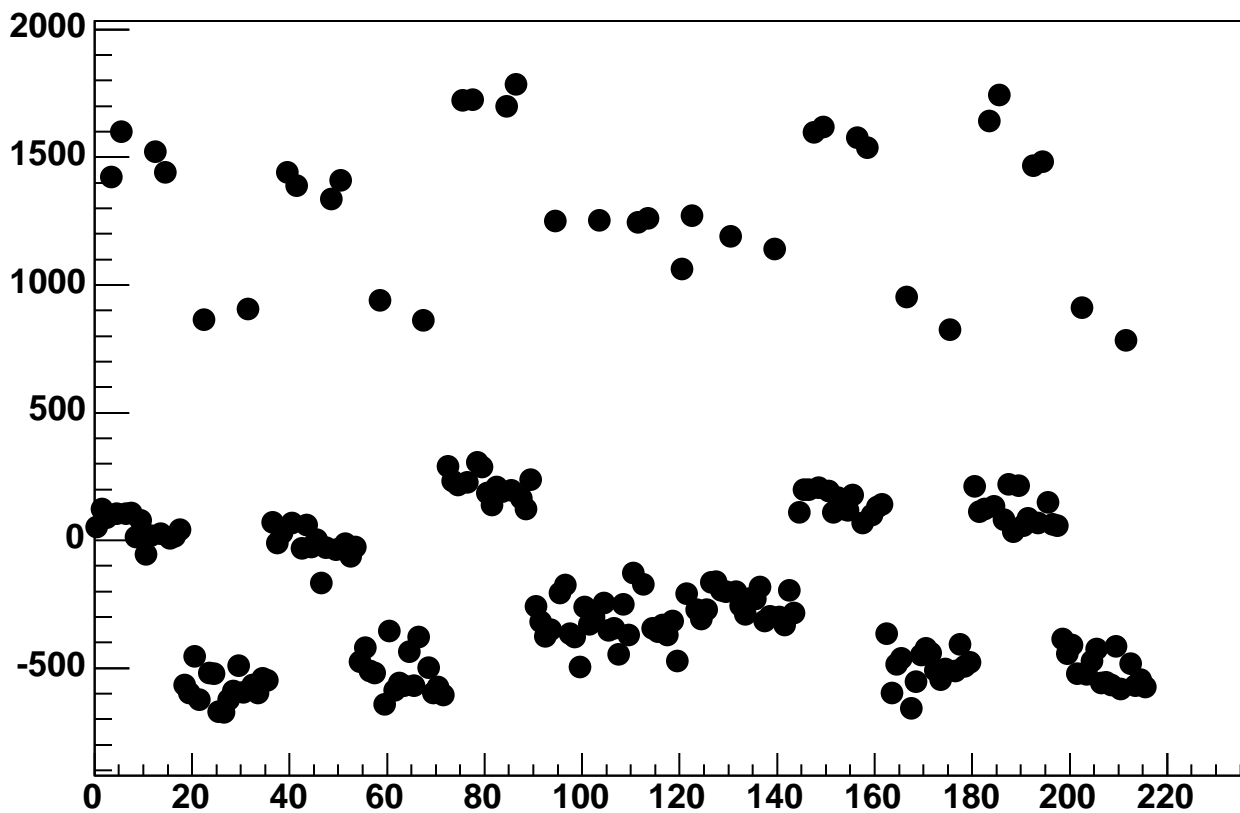
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18\*Chip+Chan



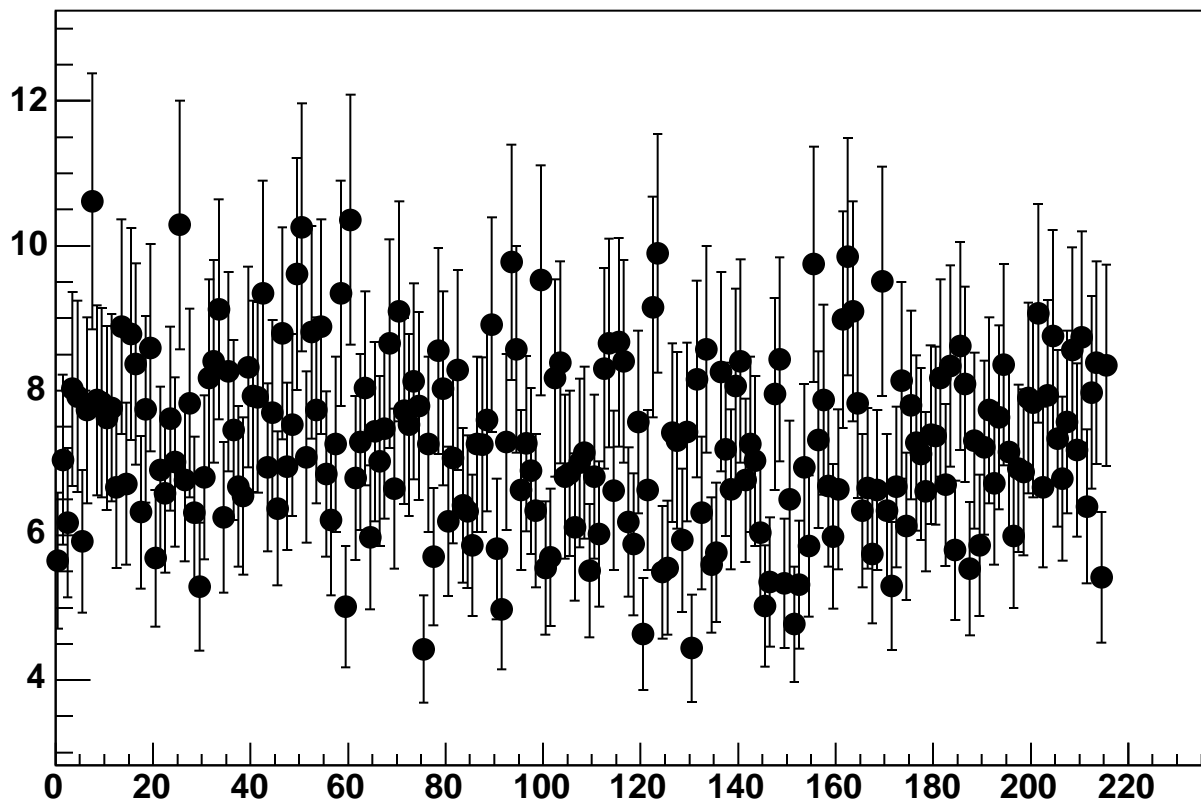
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18\*Chip+Chan



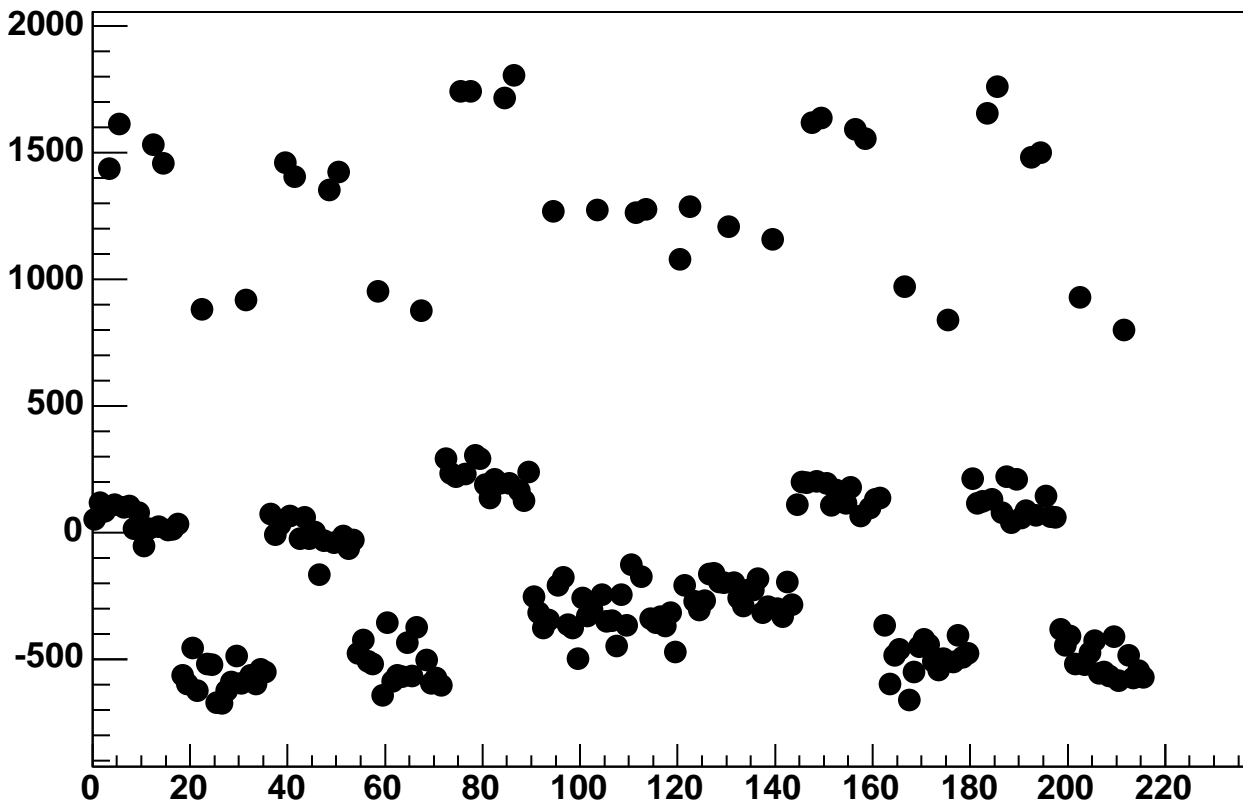
Enable 1, Hold=35, DAC=0, ADC Mean vs 18\*Chip+Chan



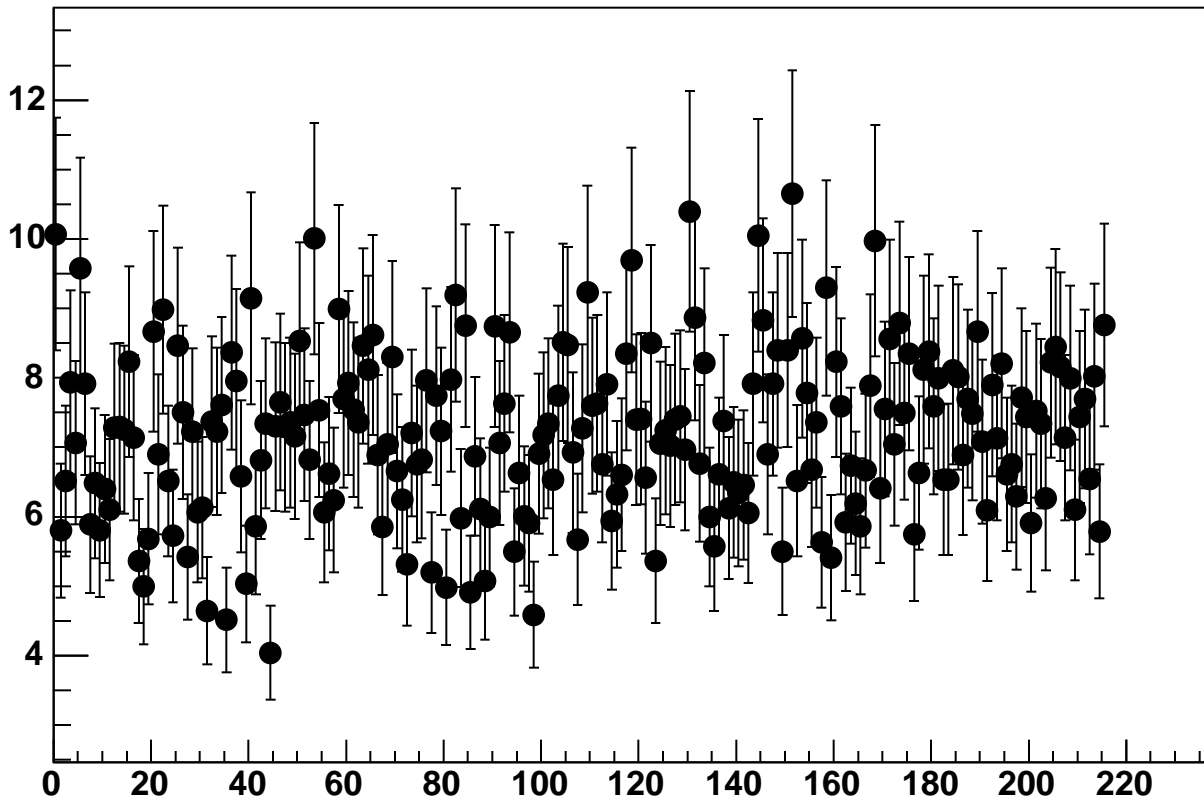
Enable 1, Hold=35, DAC=0, ADC Noise vs 18\*Chip+Chan



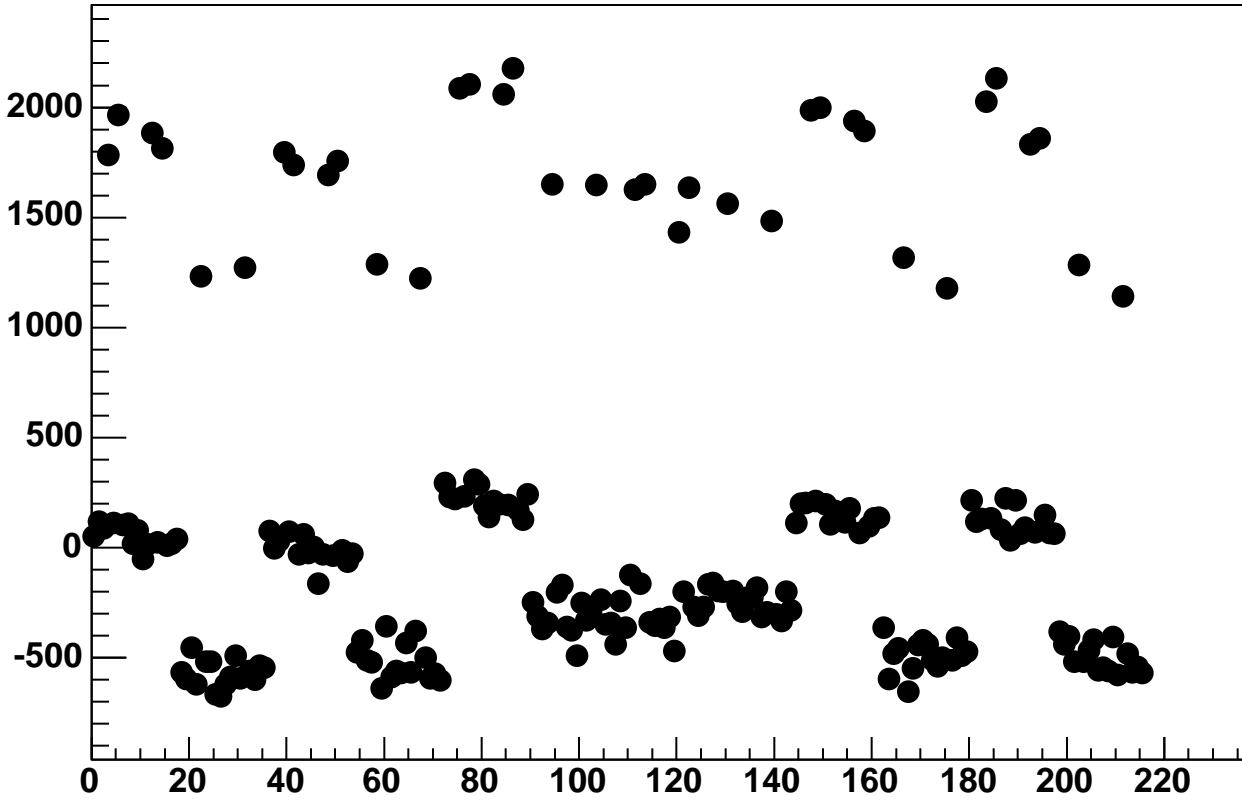
Enable 1, Hold=35, DAC=400, ADC Mean vs 18\*Chip+Chan



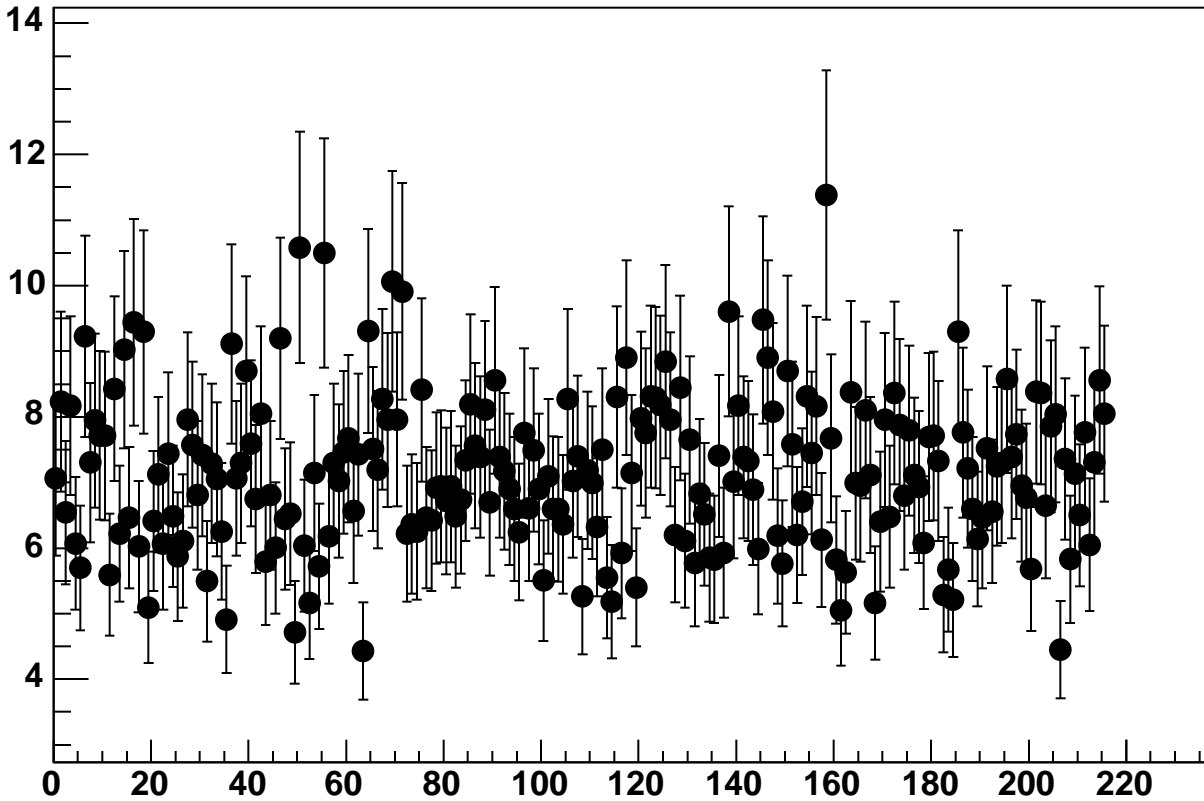
Enable 1, Hold=35, DAC=400, ADC Noise vs 18\*Chip+Chan



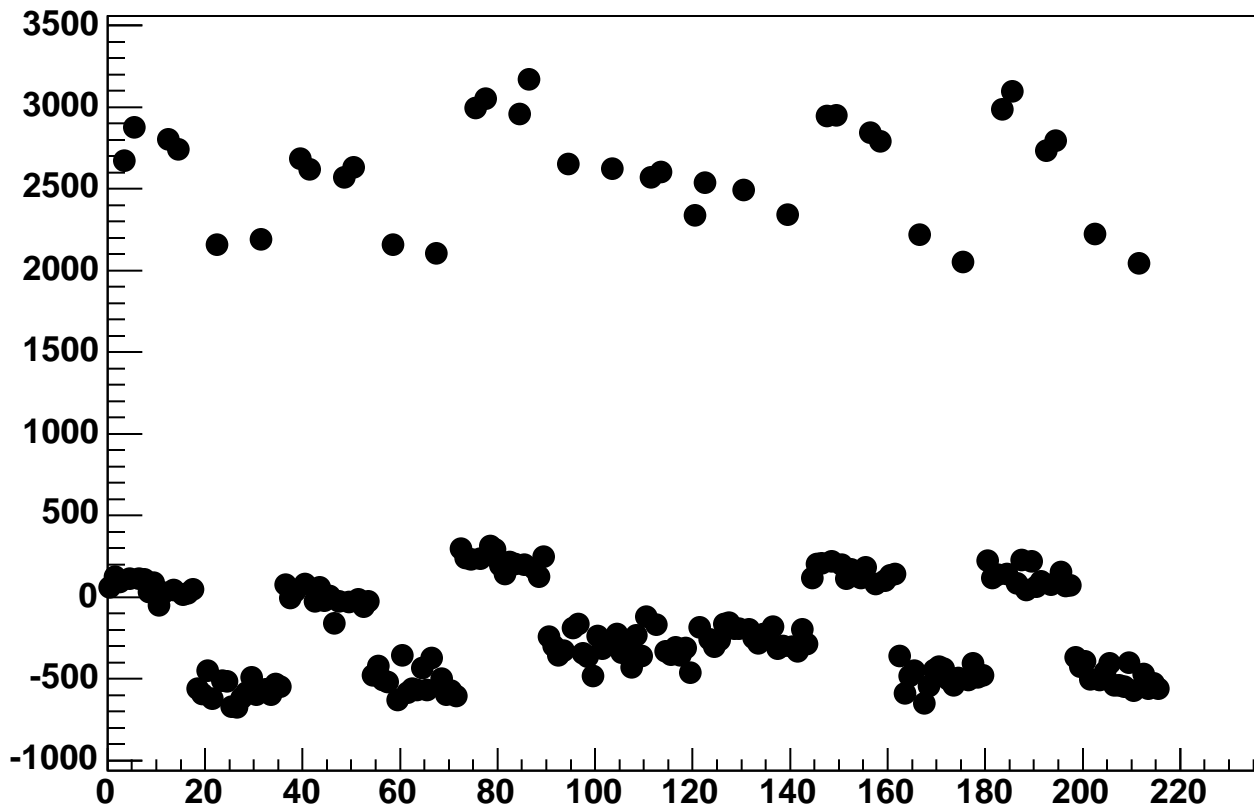
Enable 1, Hold=35, DAC=800, ADC Mean vs 18\*Chip+Chan



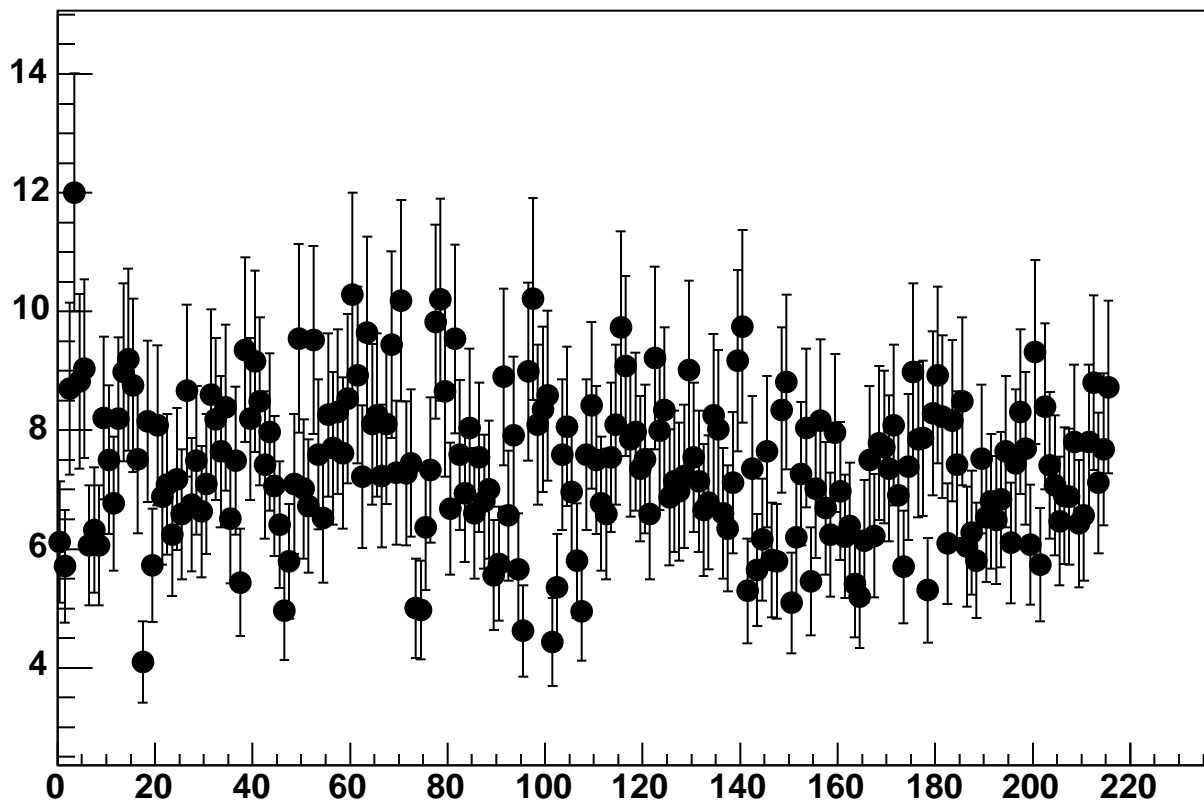
Enable 1, Hold=35, DAC=800, ADC Noise vs 18\*Chip+Chan



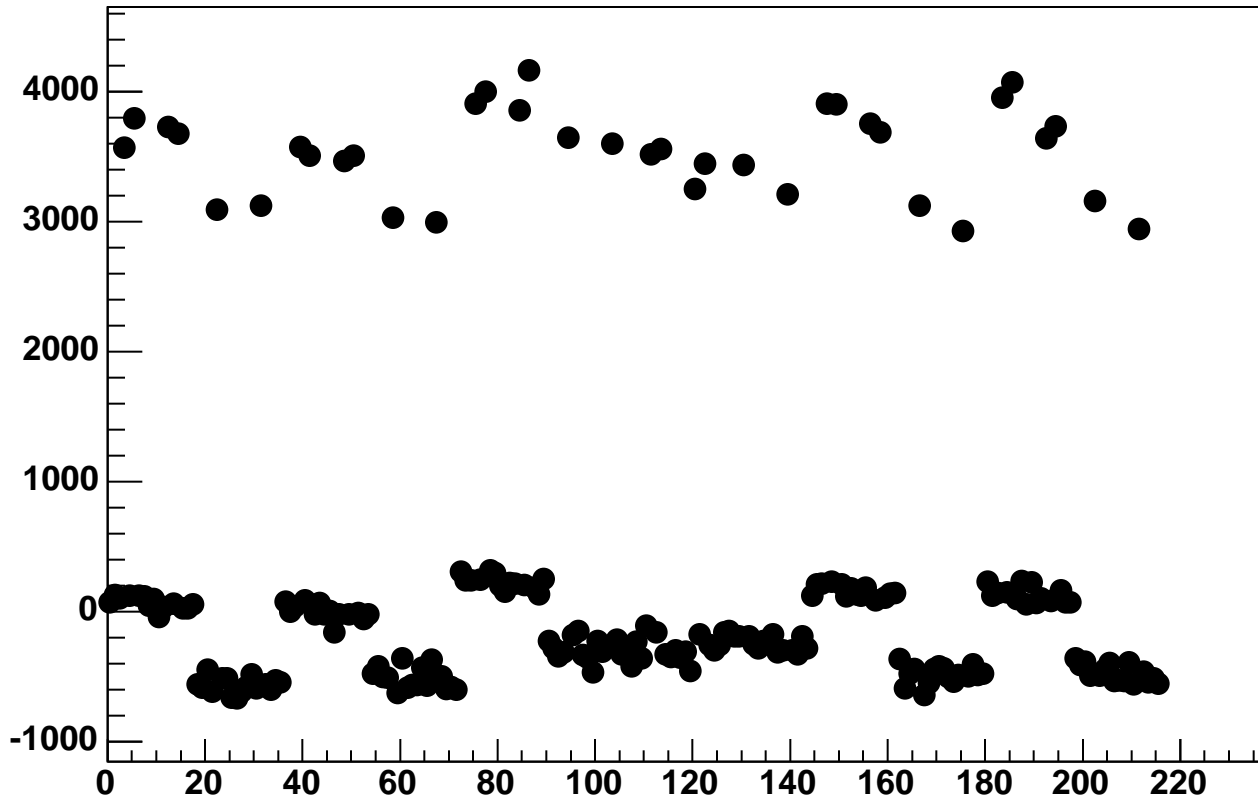
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18\*Chip+Chan



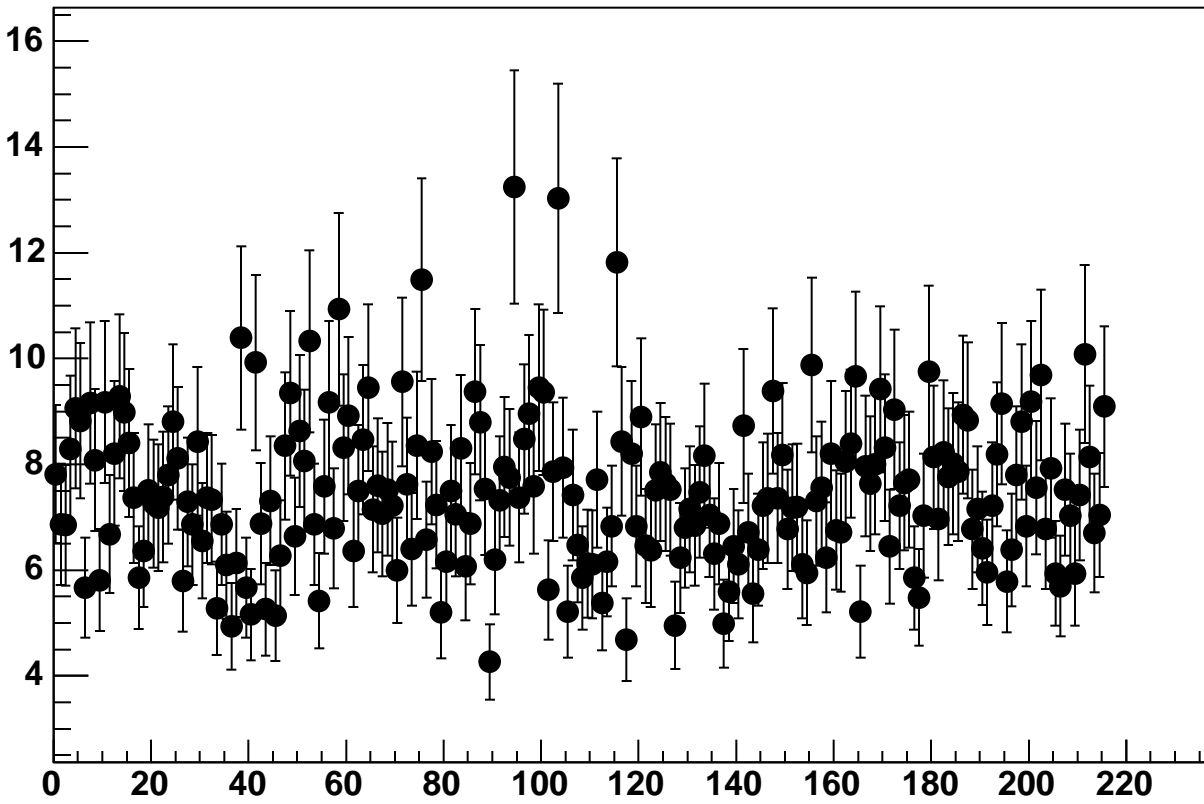
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=35, DAC=1600, ADC Mean vs 18\*Chip+Chan

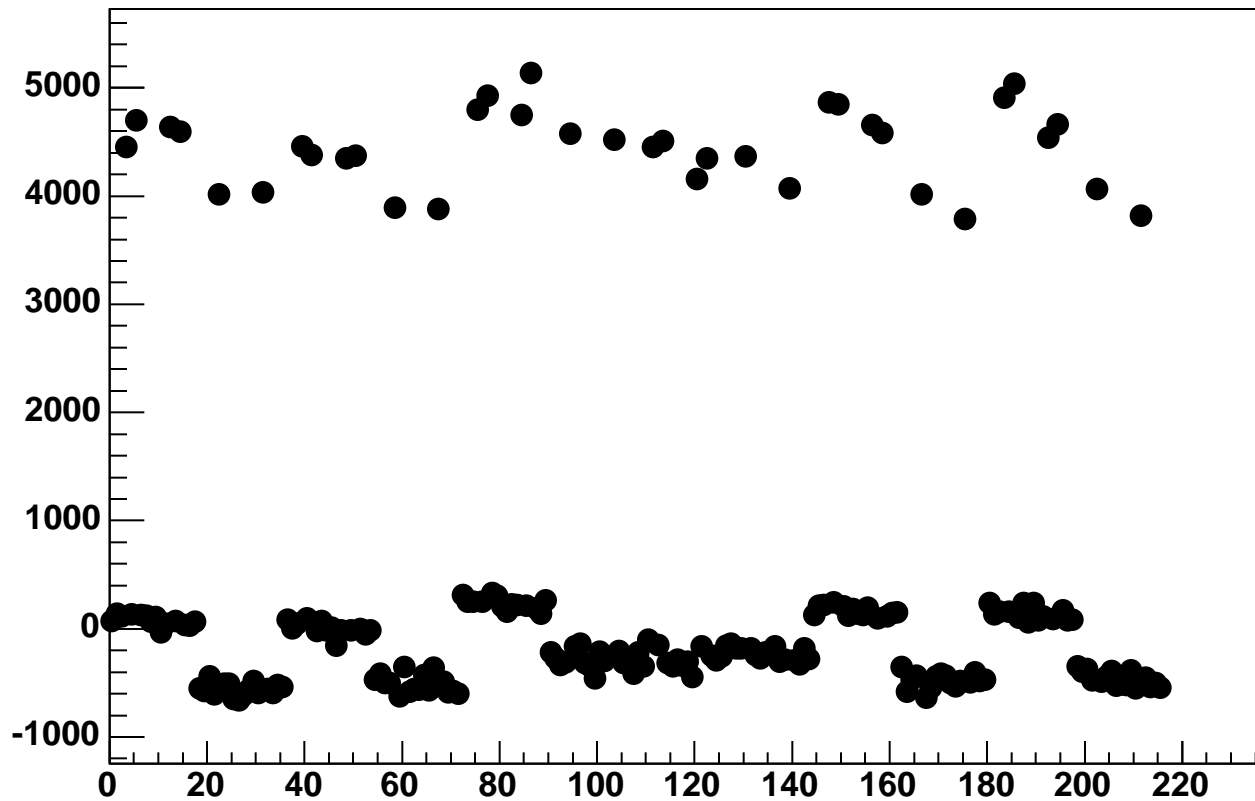


Enable 1, Hold=35, DAC=1600, ADC Noise vs 18\*Chip+Chan

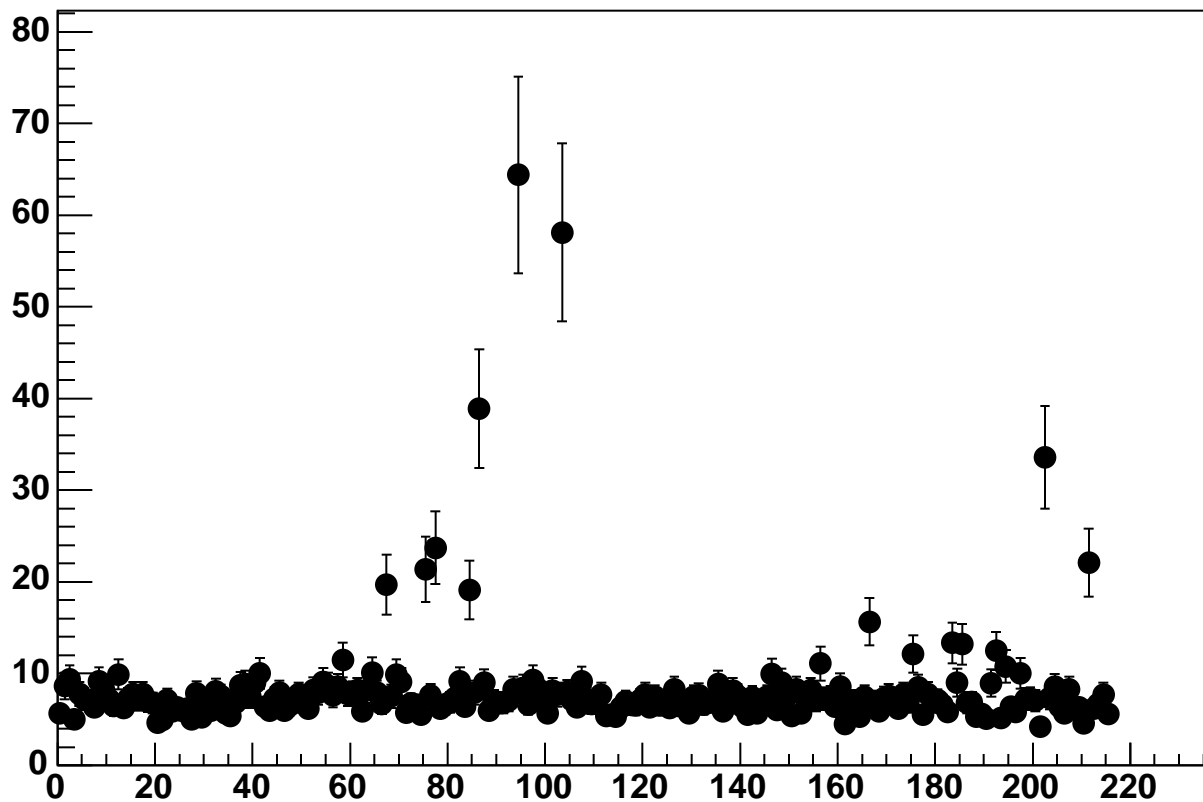




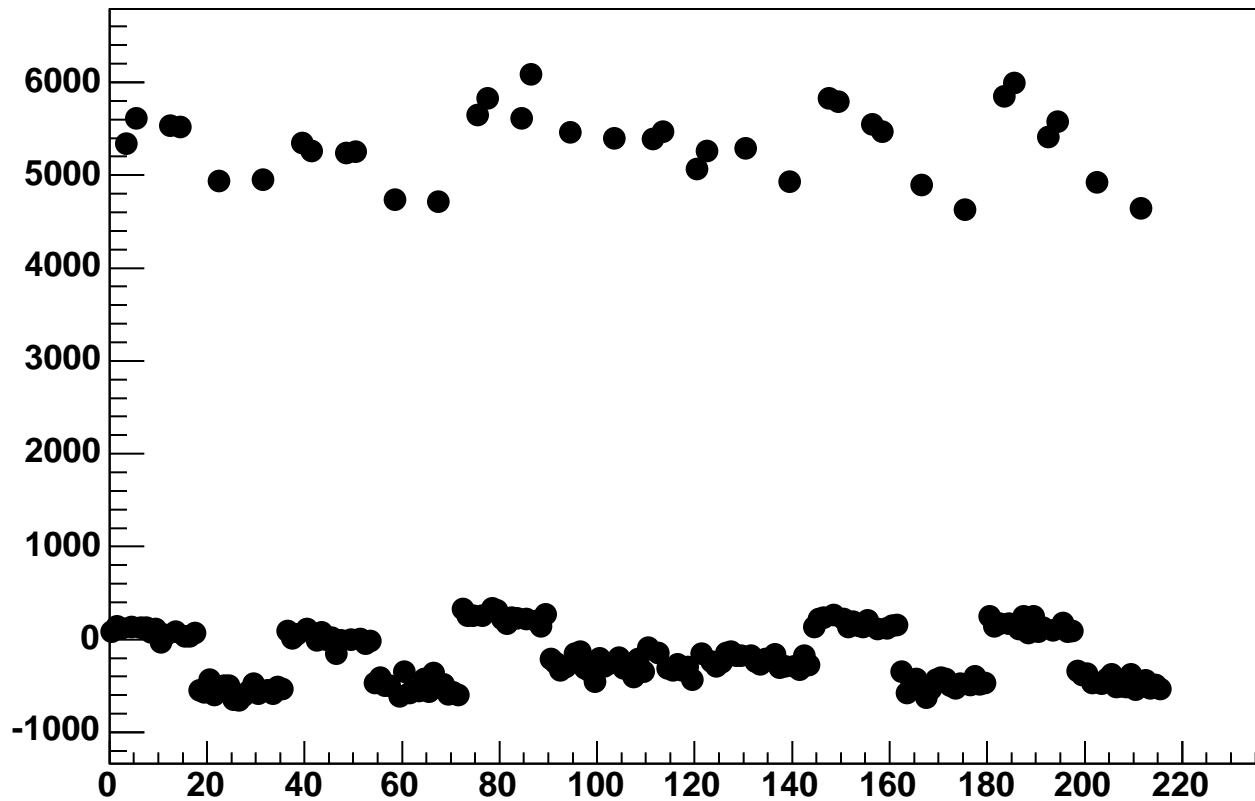
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18\*Chip+Chan



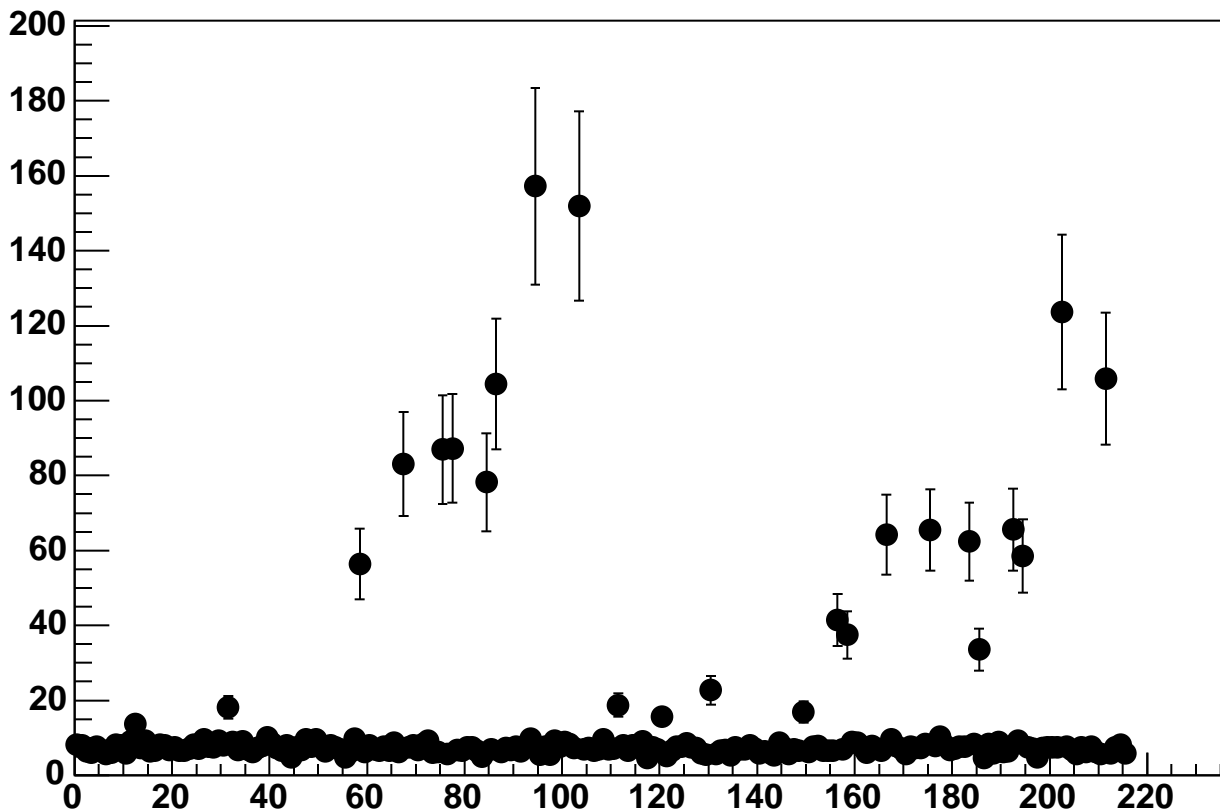
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18\*Chip+Chan



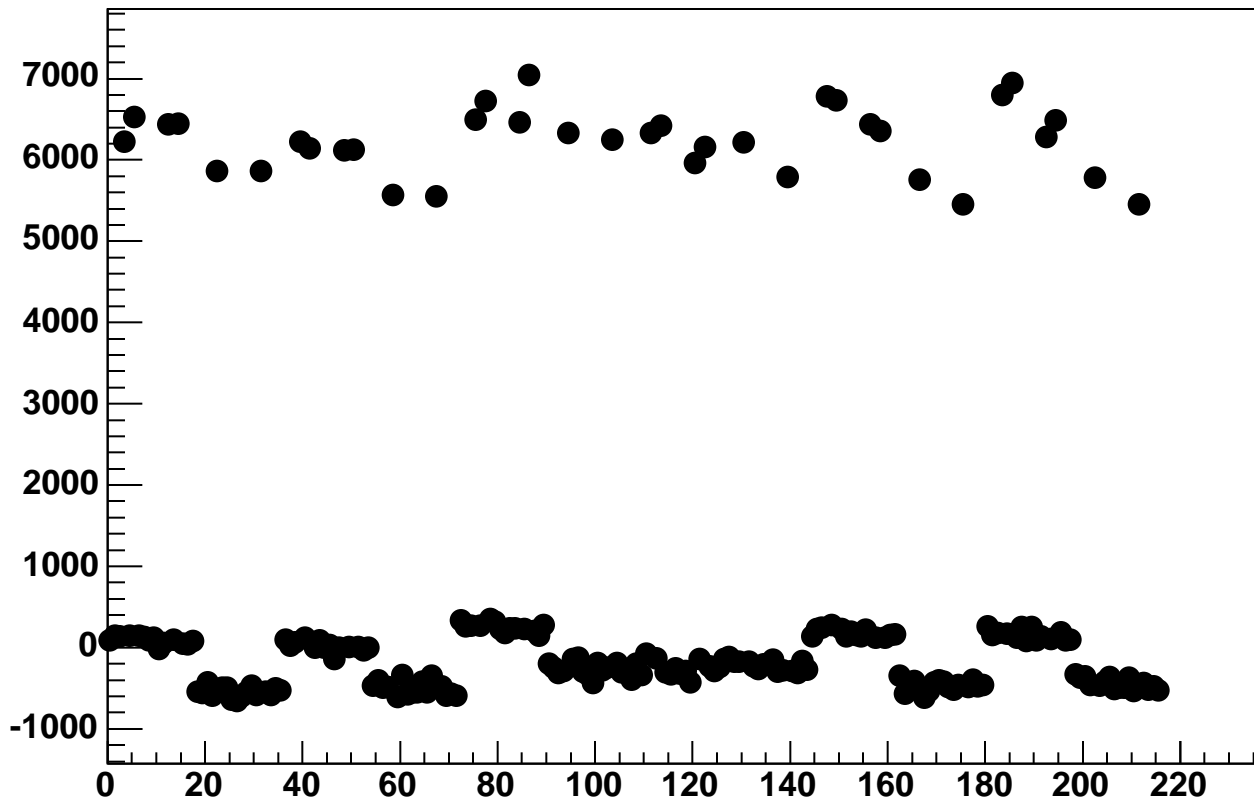
Enable 1, Hold=35, DAC=2400, ADC Mean vs 18\*Chip+Chan



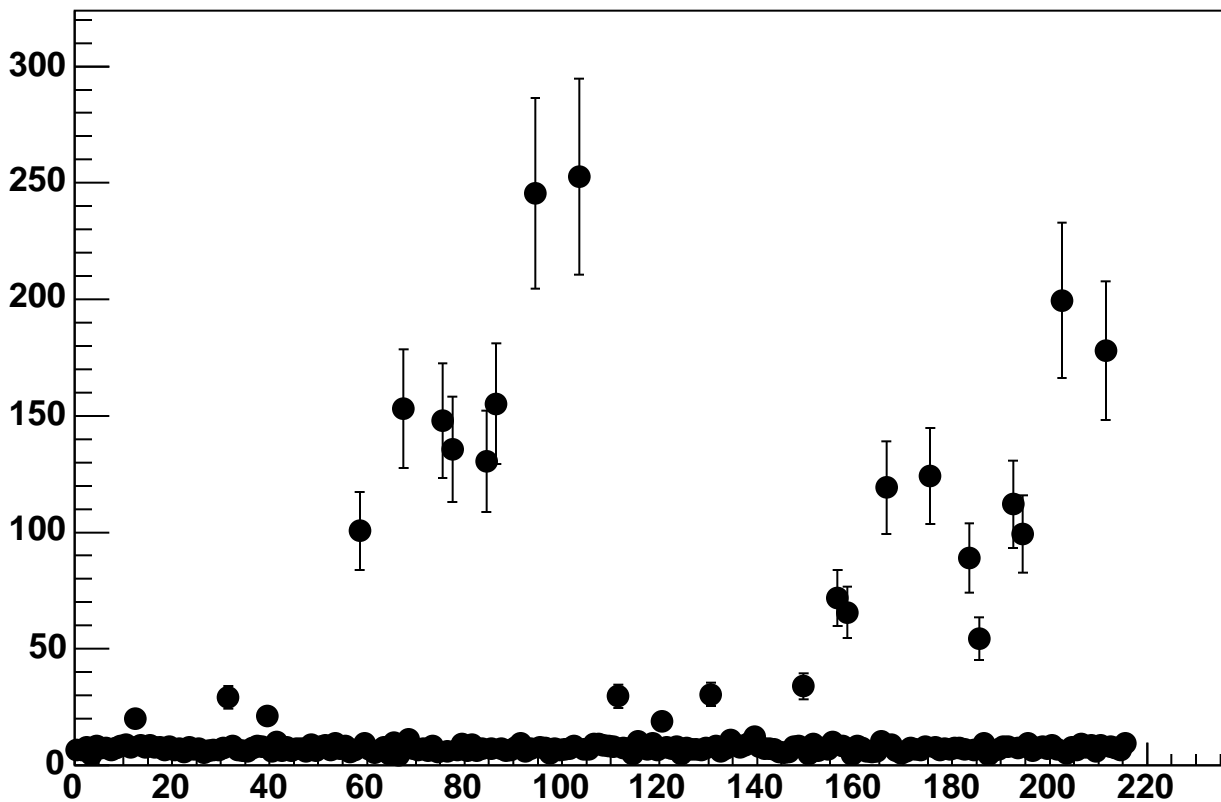
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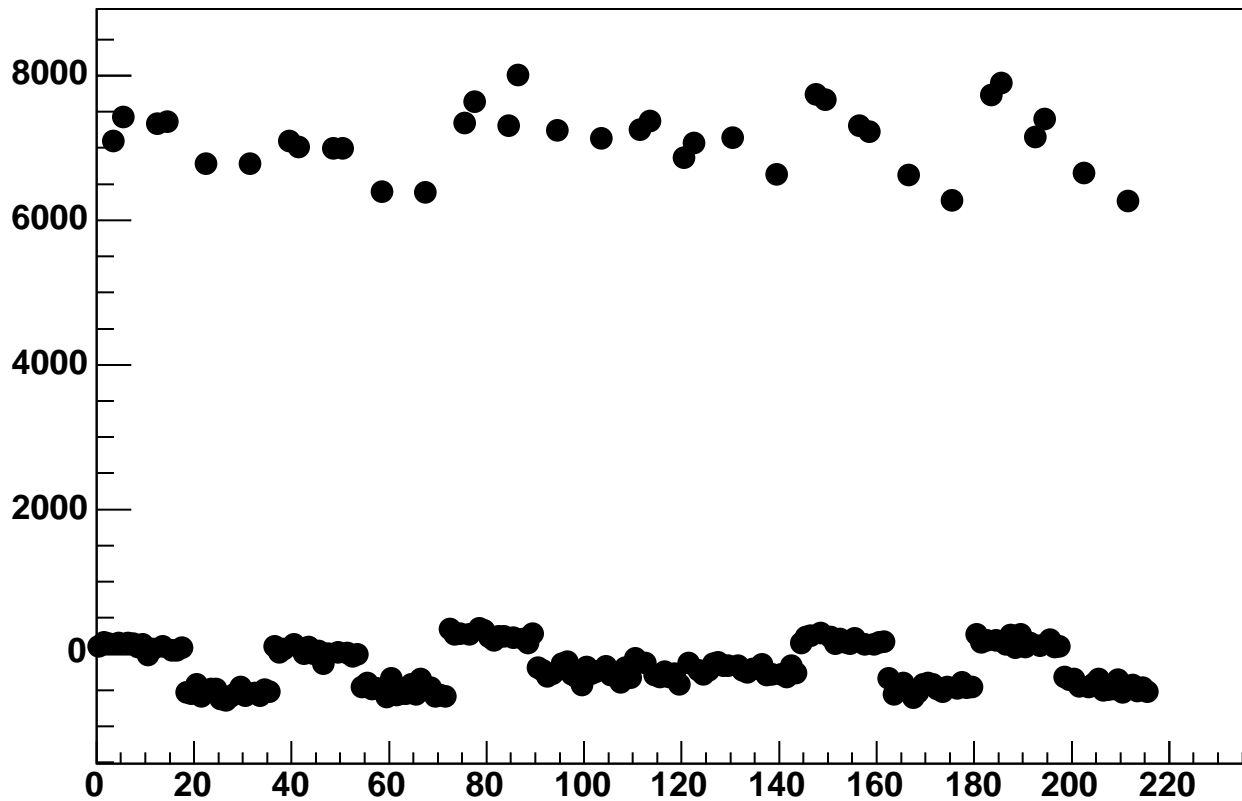
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18\*Chip+Chan



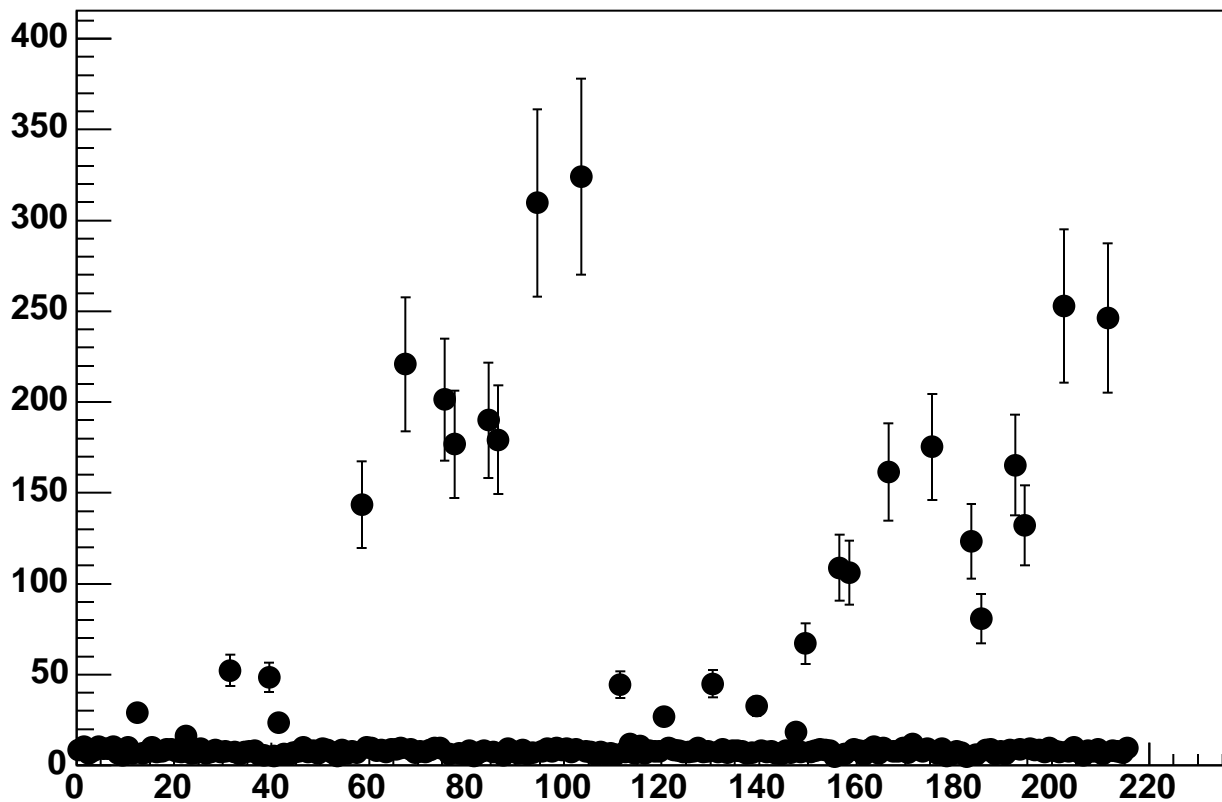
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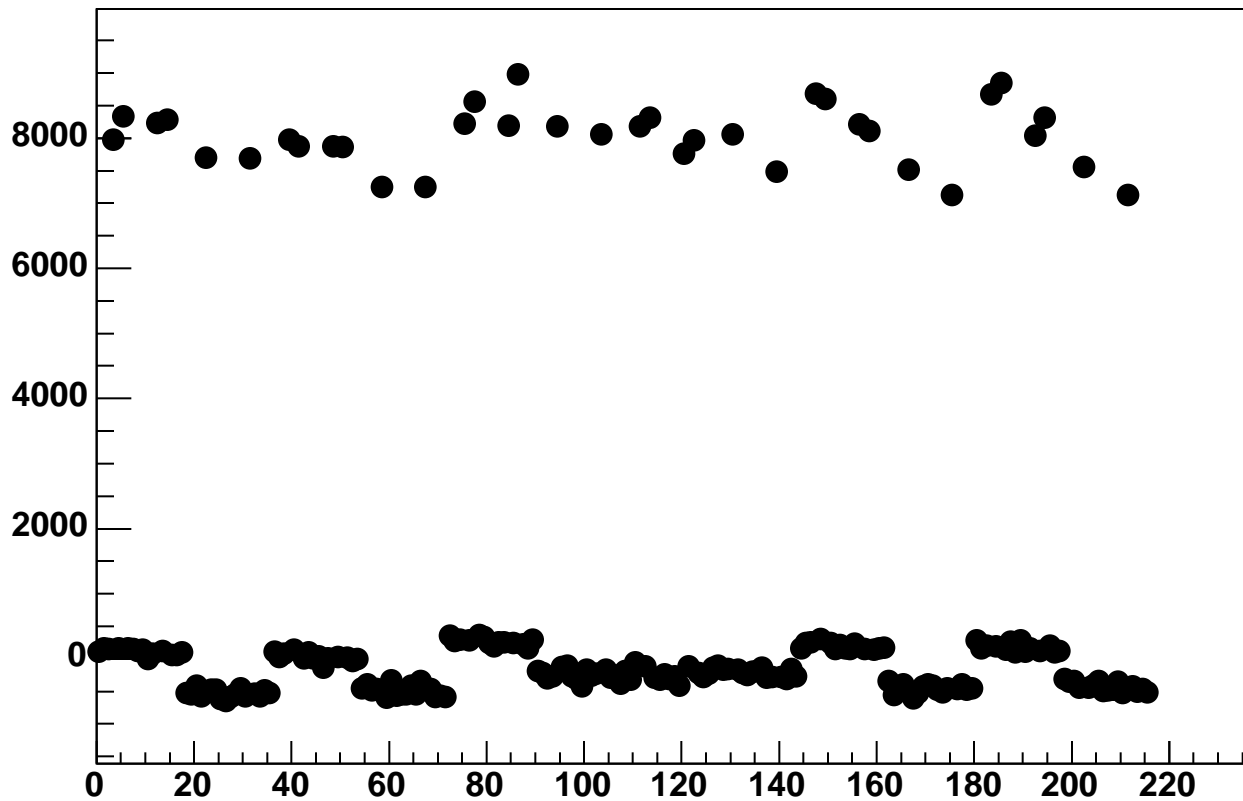
Enable 1, Hold=35, DAC=3200, ADC Mean vs 18\*Chip+Chan



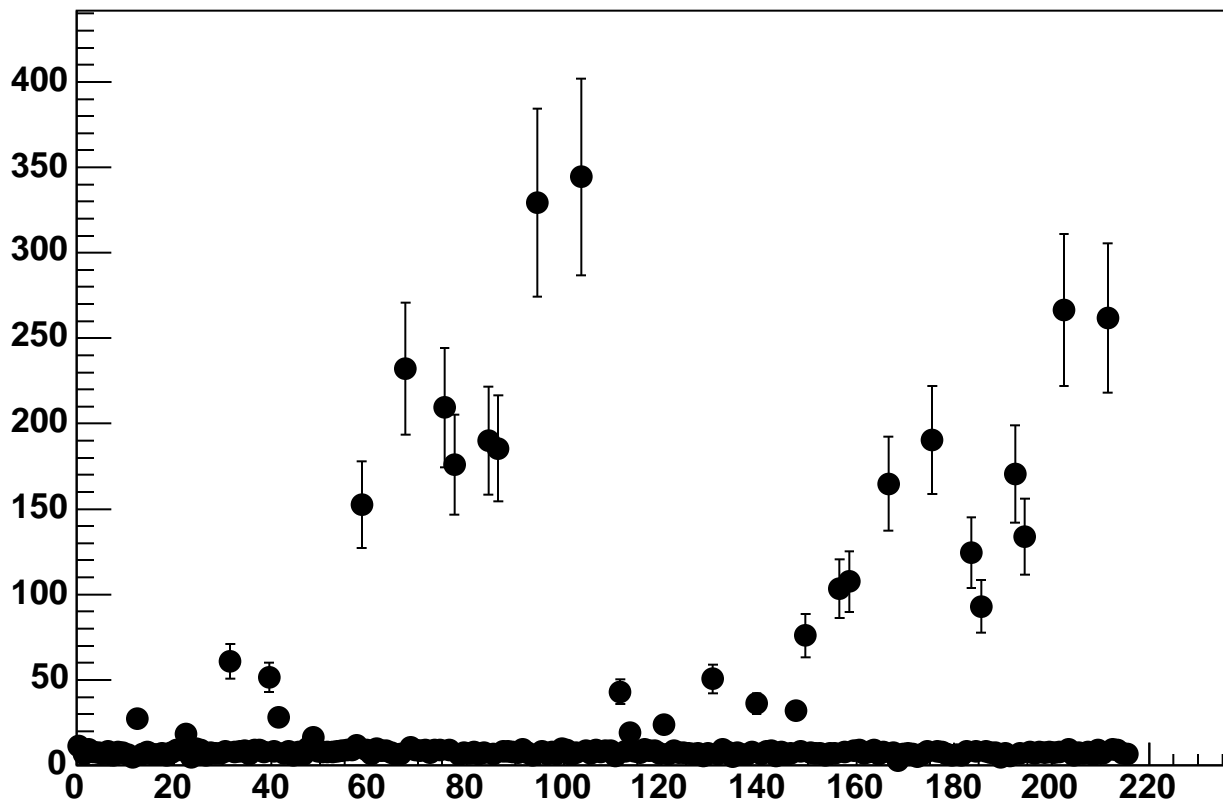
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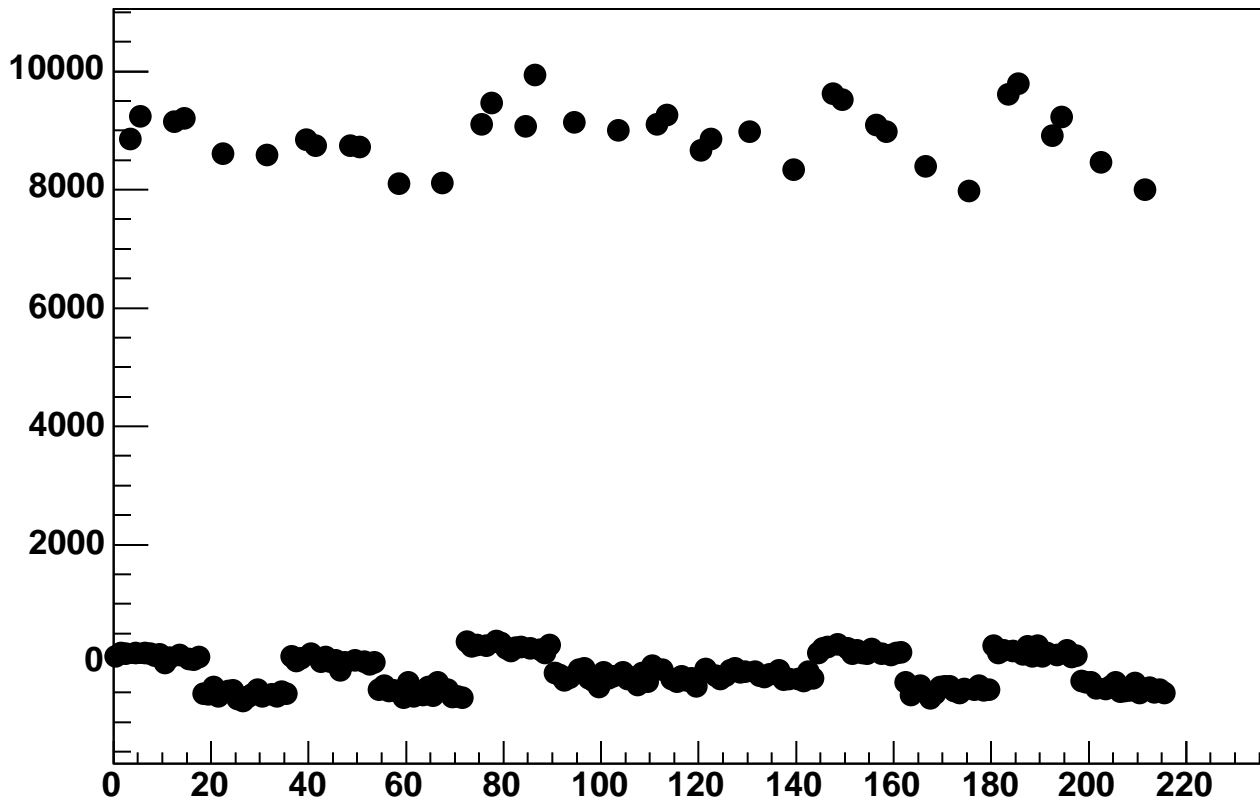
Enable 1, Hold=35, DAC=3600, ADC Mean vs 18\*Chip+Chan



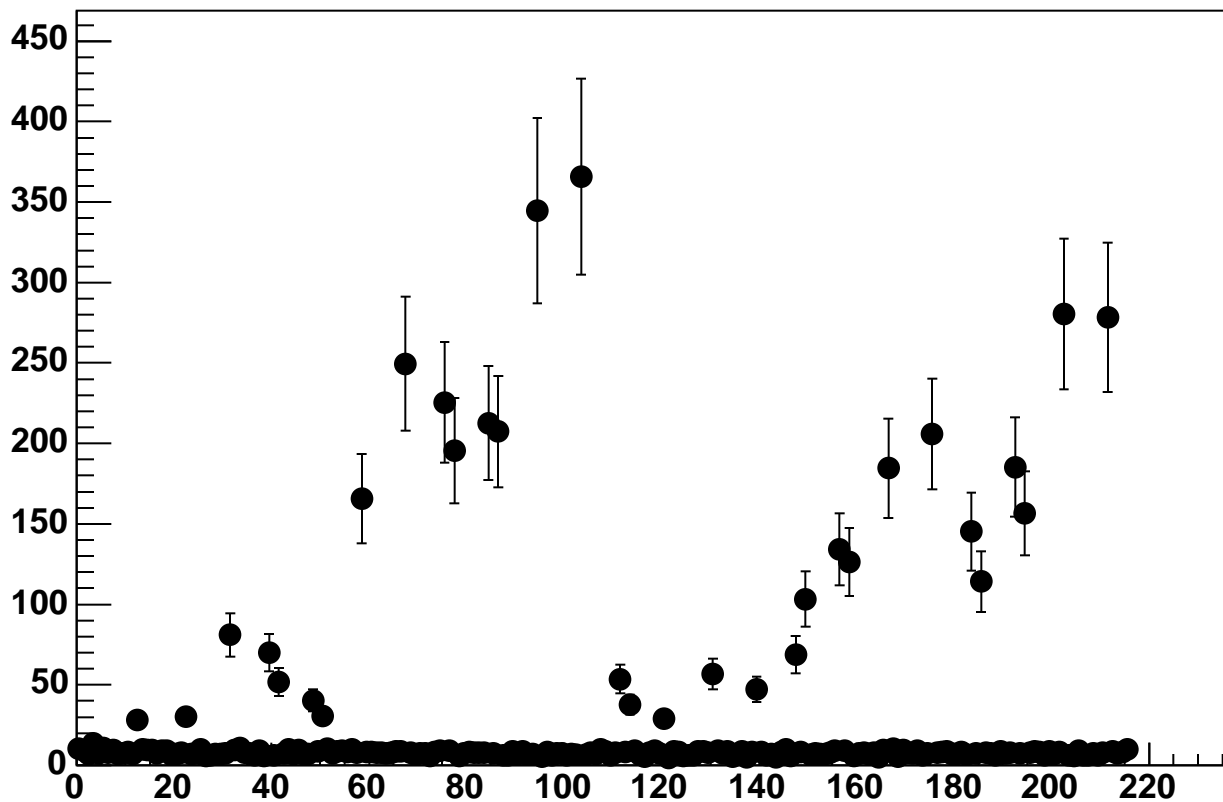
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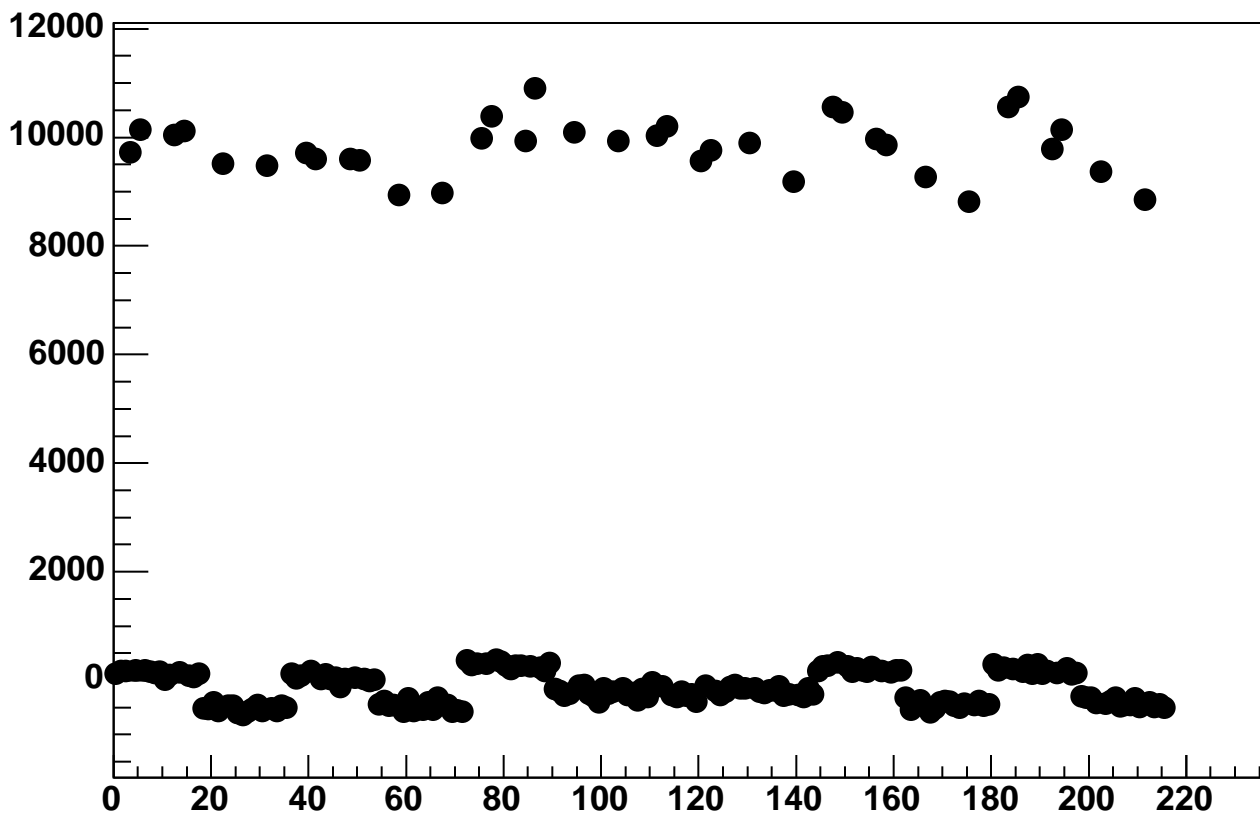
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18\*Chip+Chan



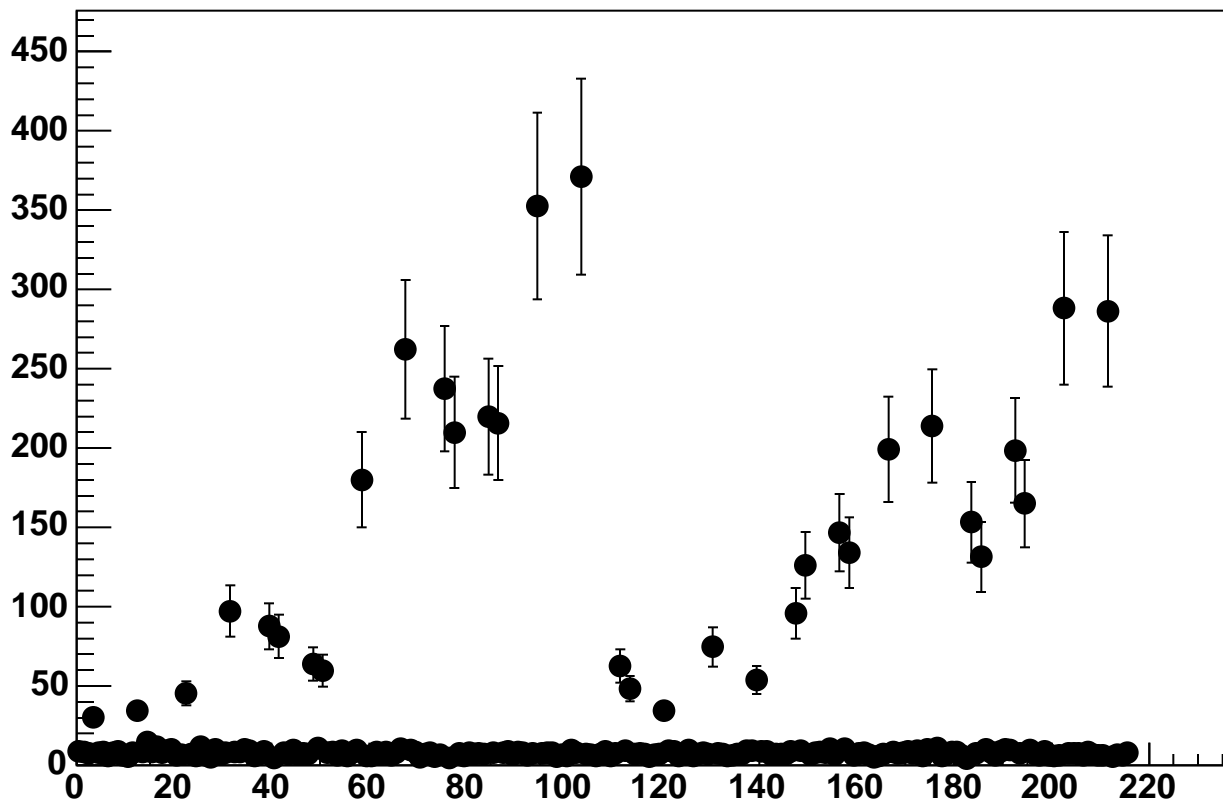
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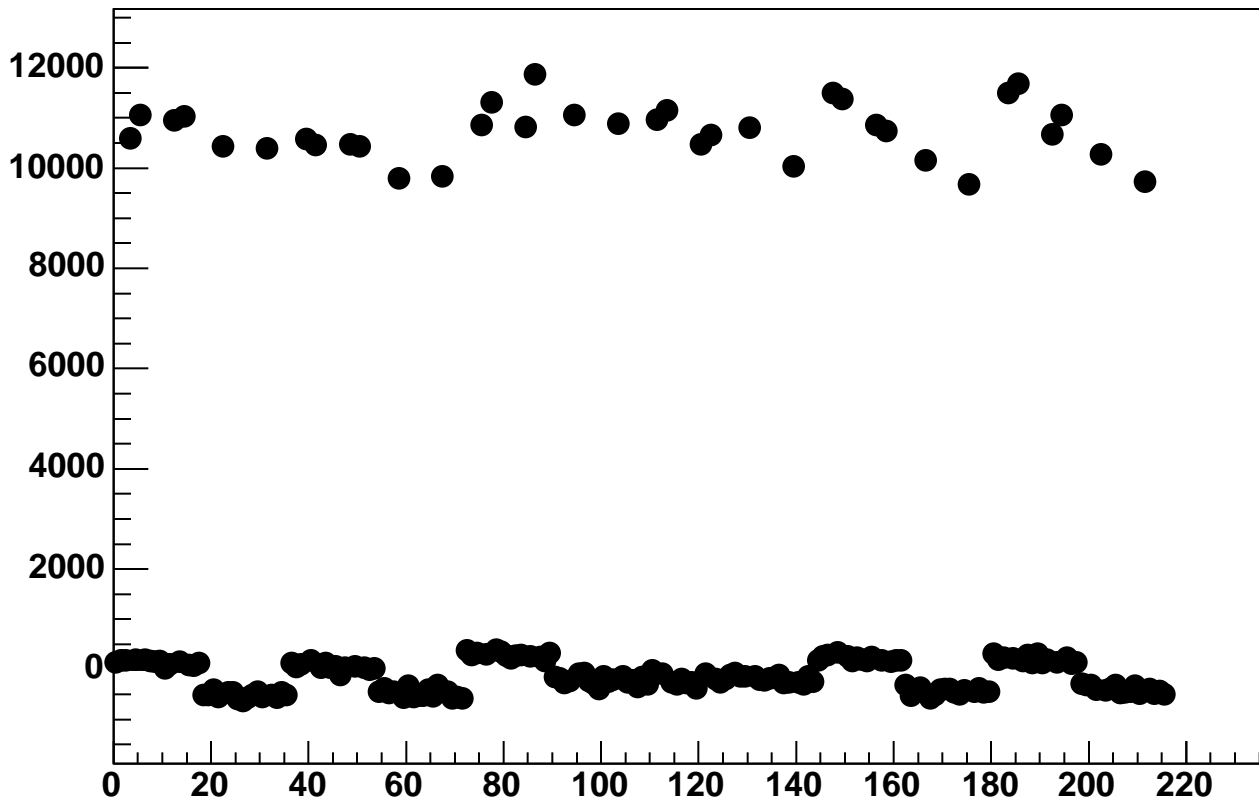
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18\*Chip+Chan



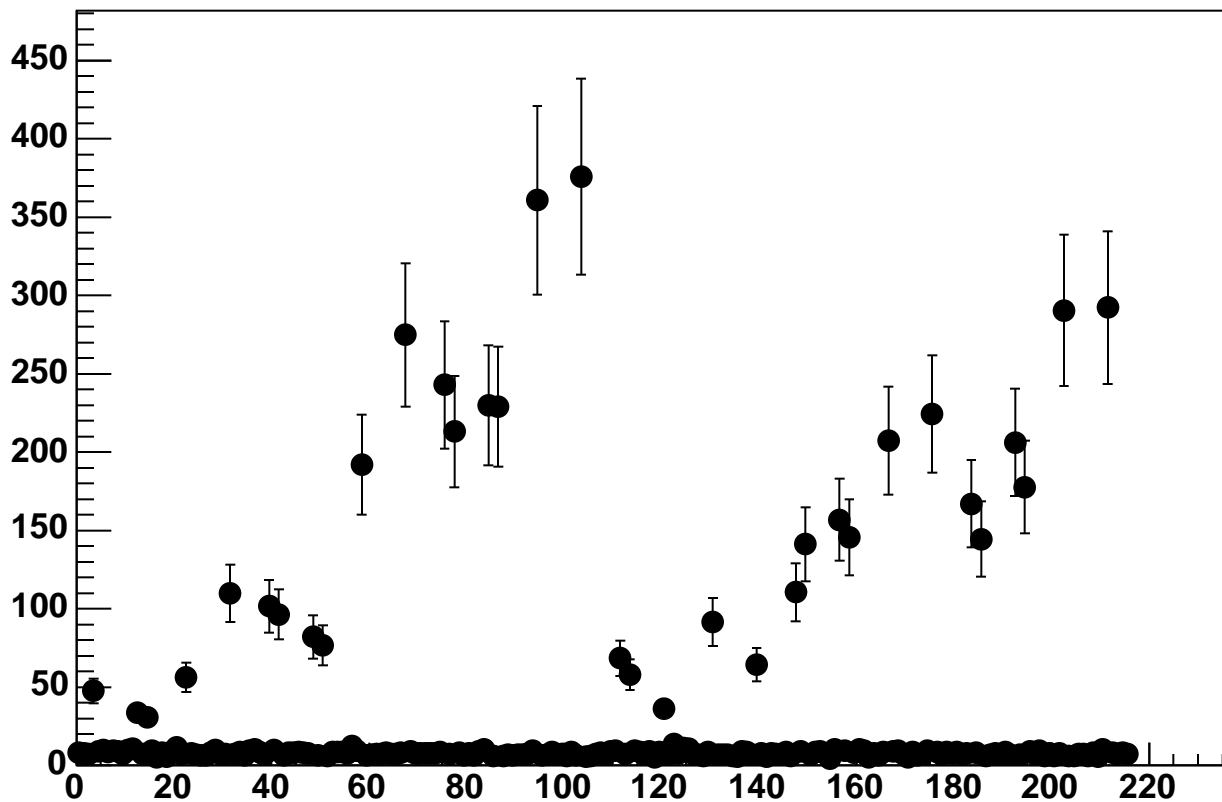
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=35, DAC=4800, ADC Mean vs 18\*Chip+Chan

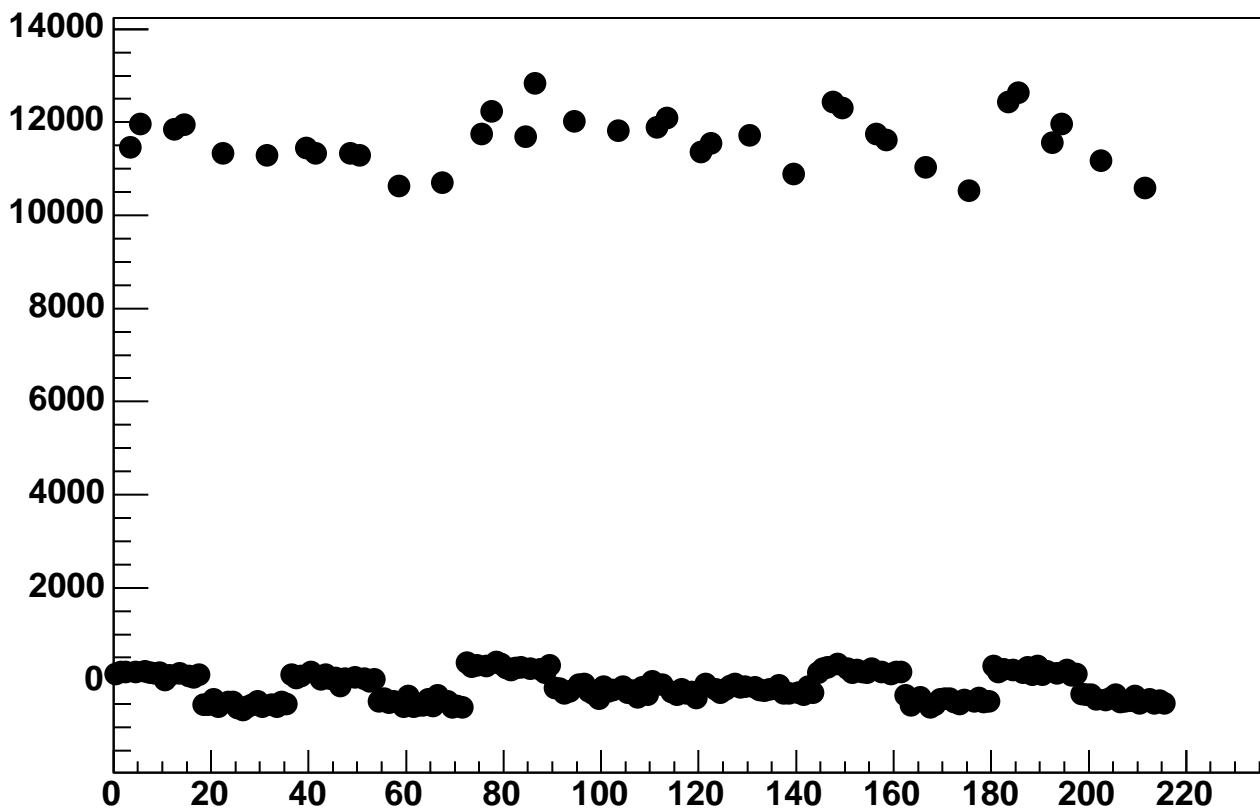


Enable 1, Hold=35, DAC=4800, ADC Noise vs 18\*Chip+Chan

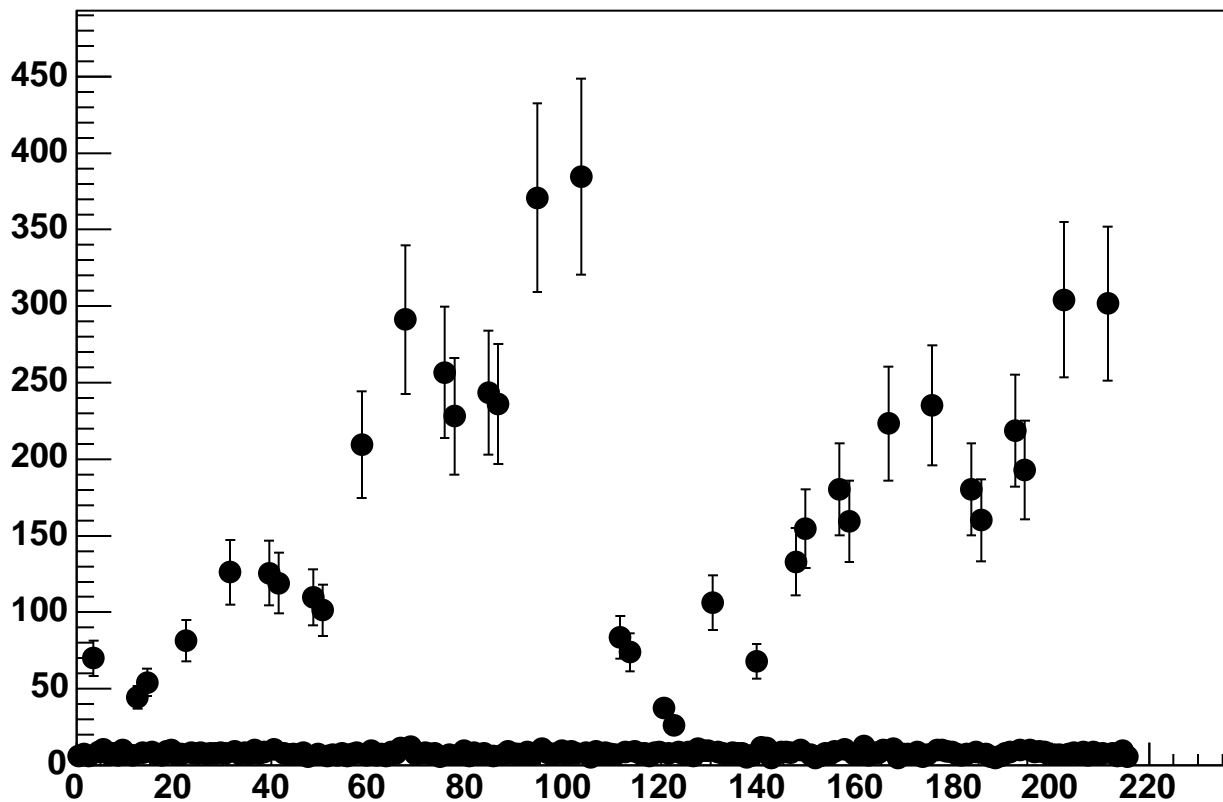




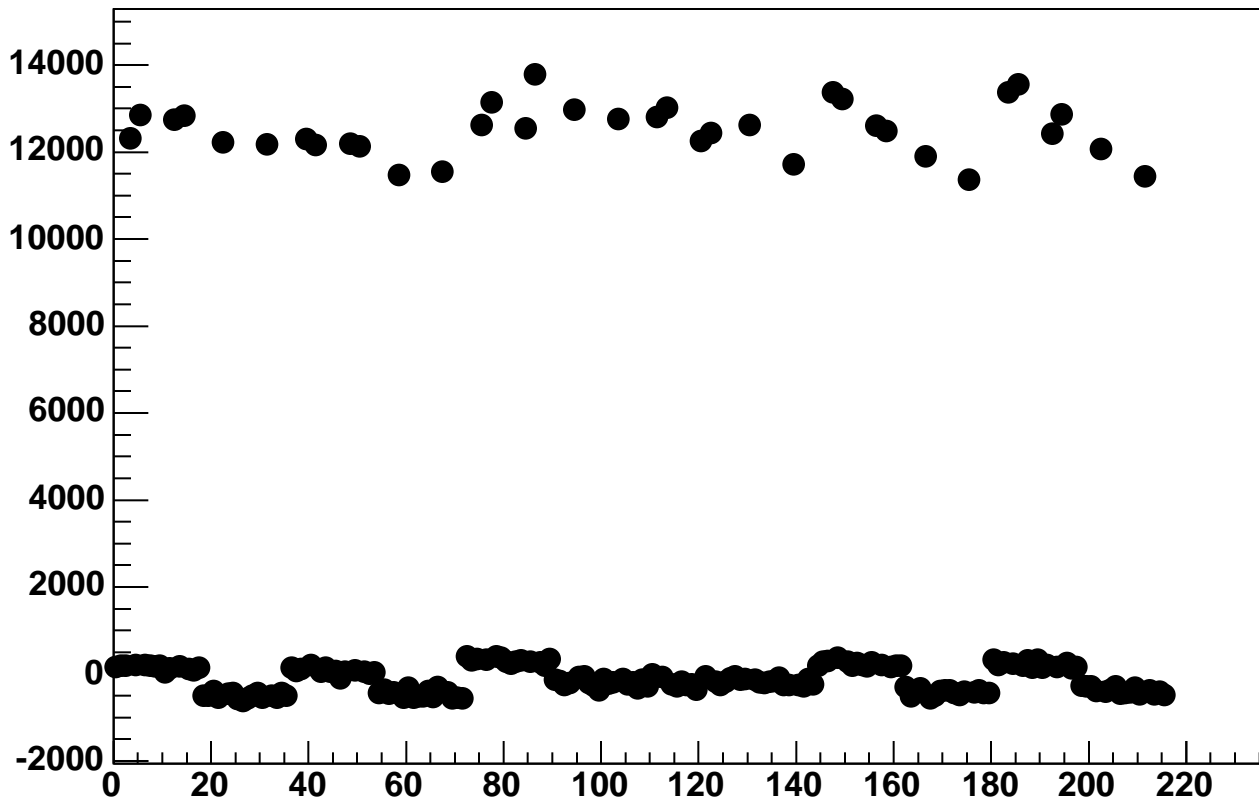
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18\*Chip+Chan



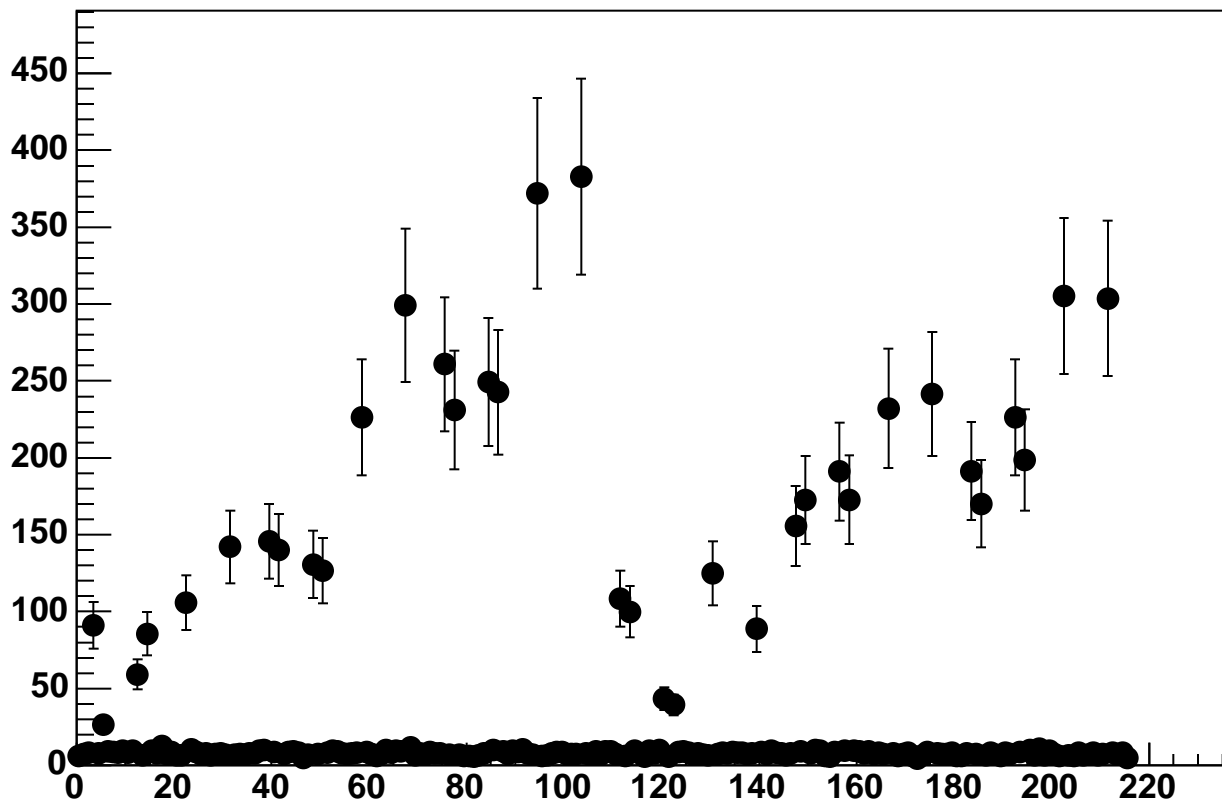
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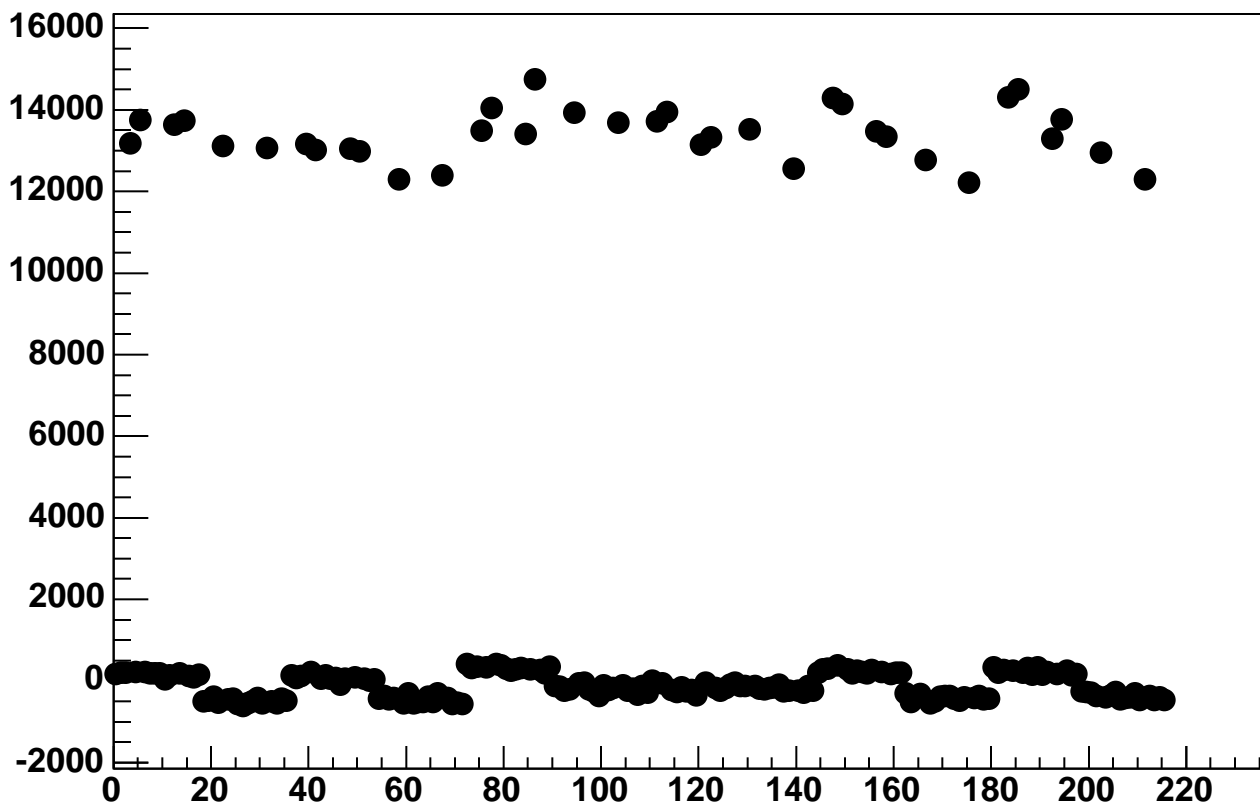
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18\*Chip+Chan



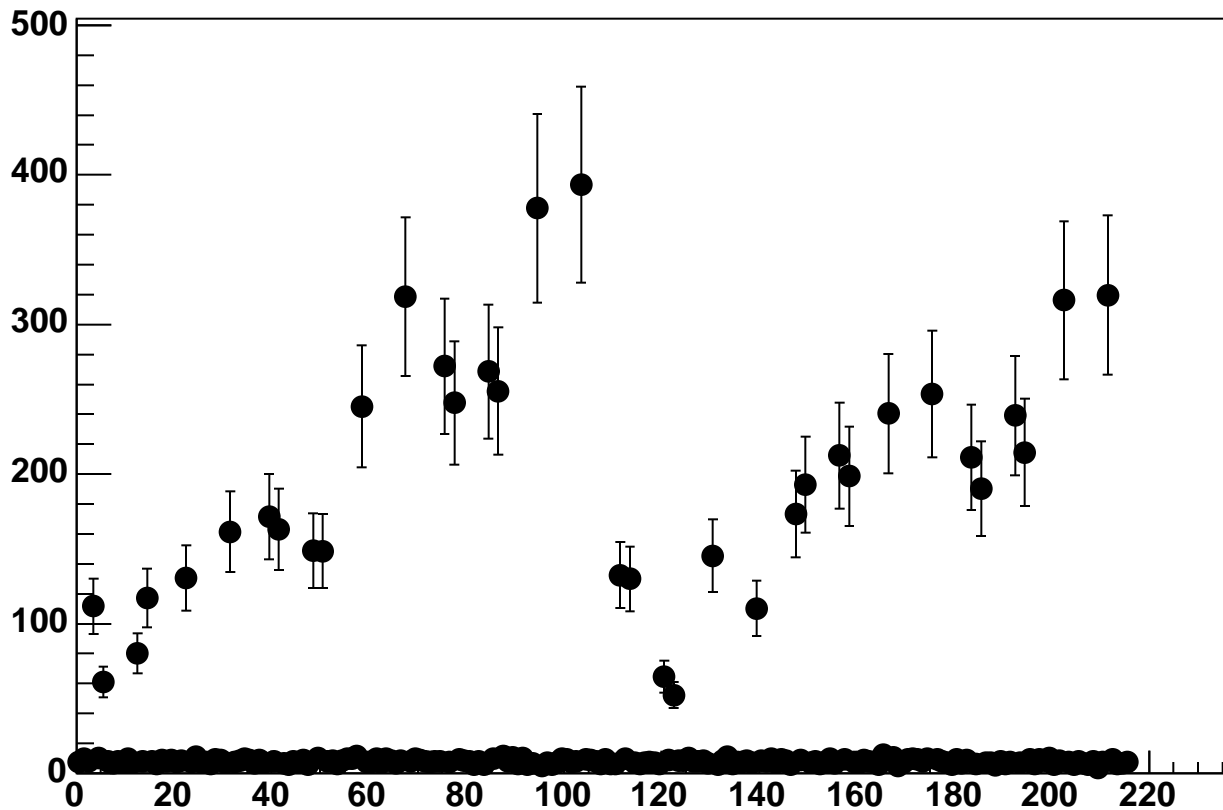
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18\*Chip+Chan



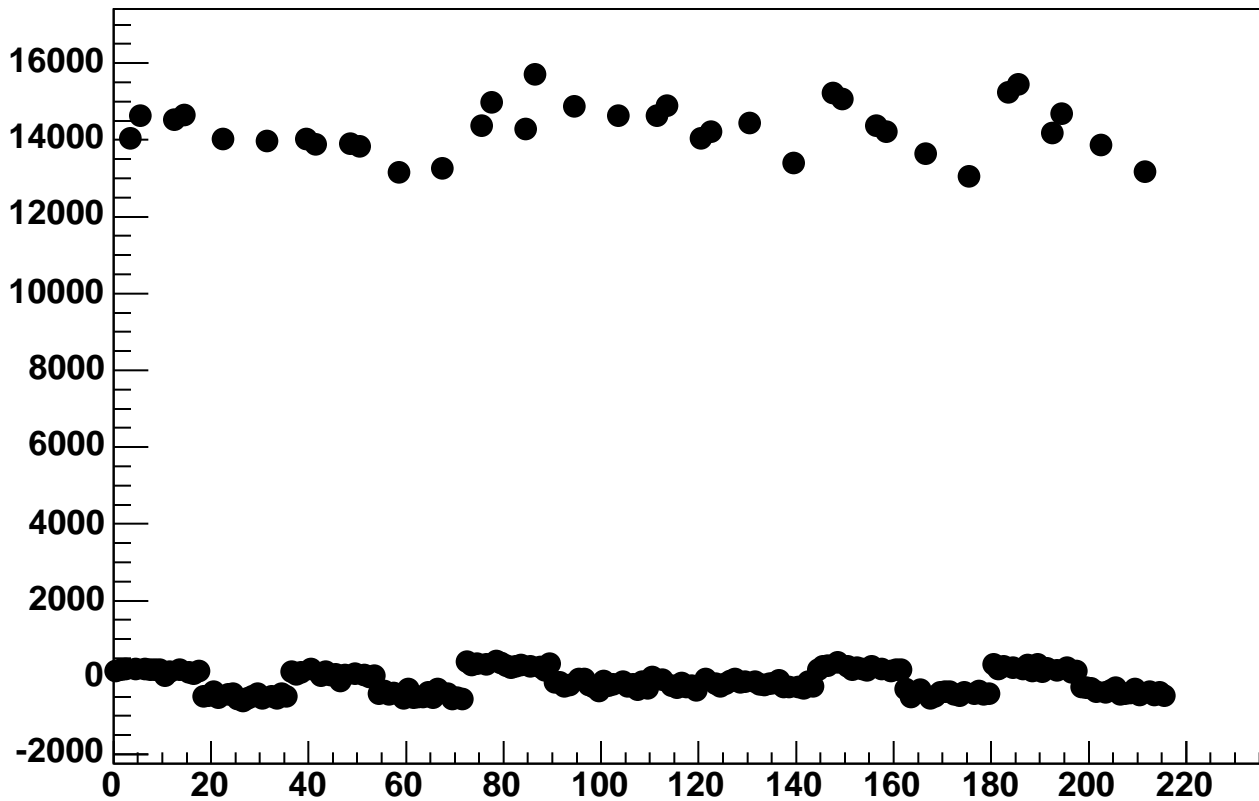
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18\*Chip+Chan



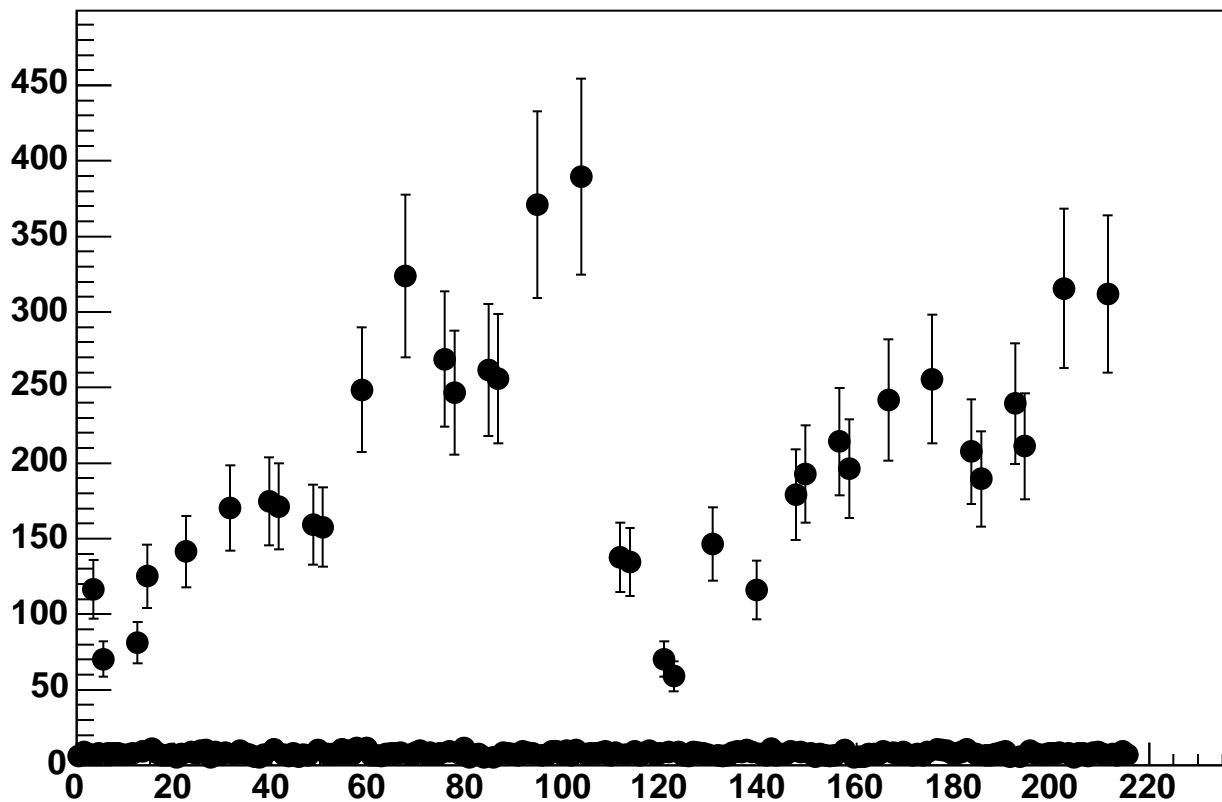
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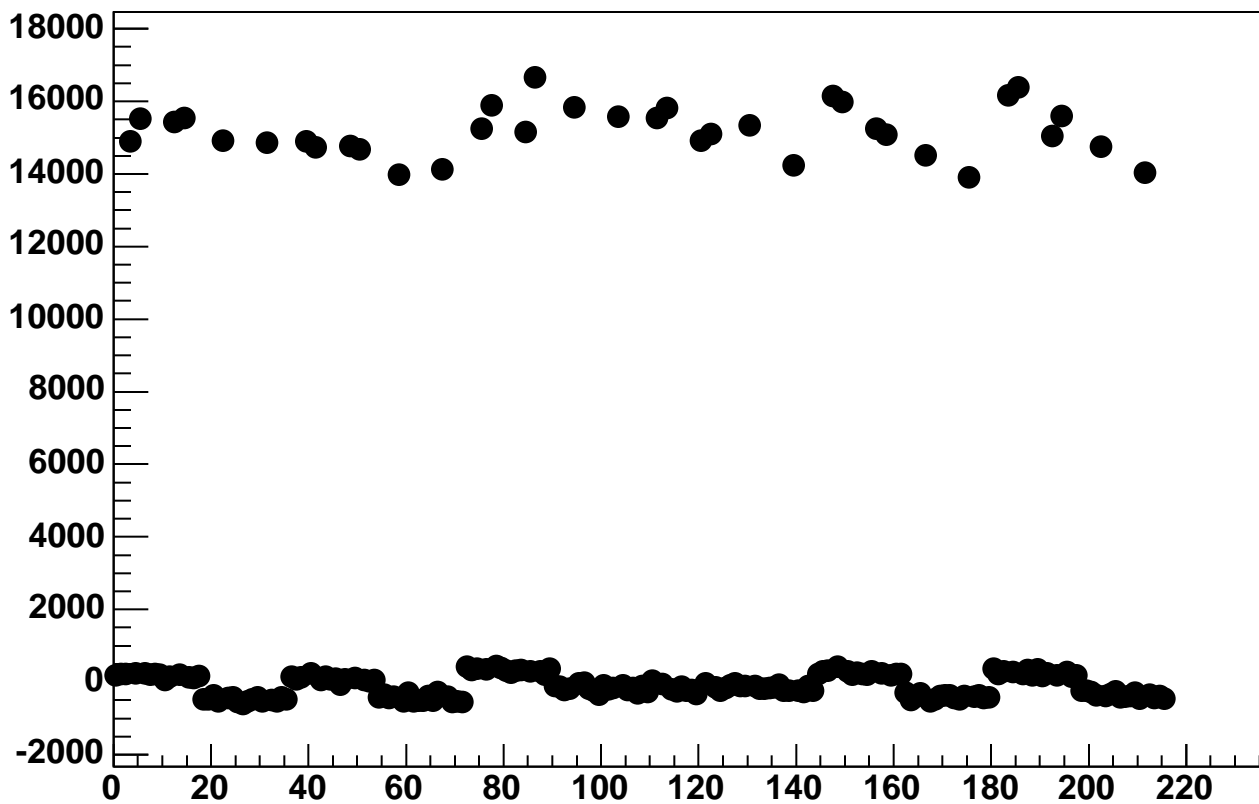
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18\*Chip+Chan



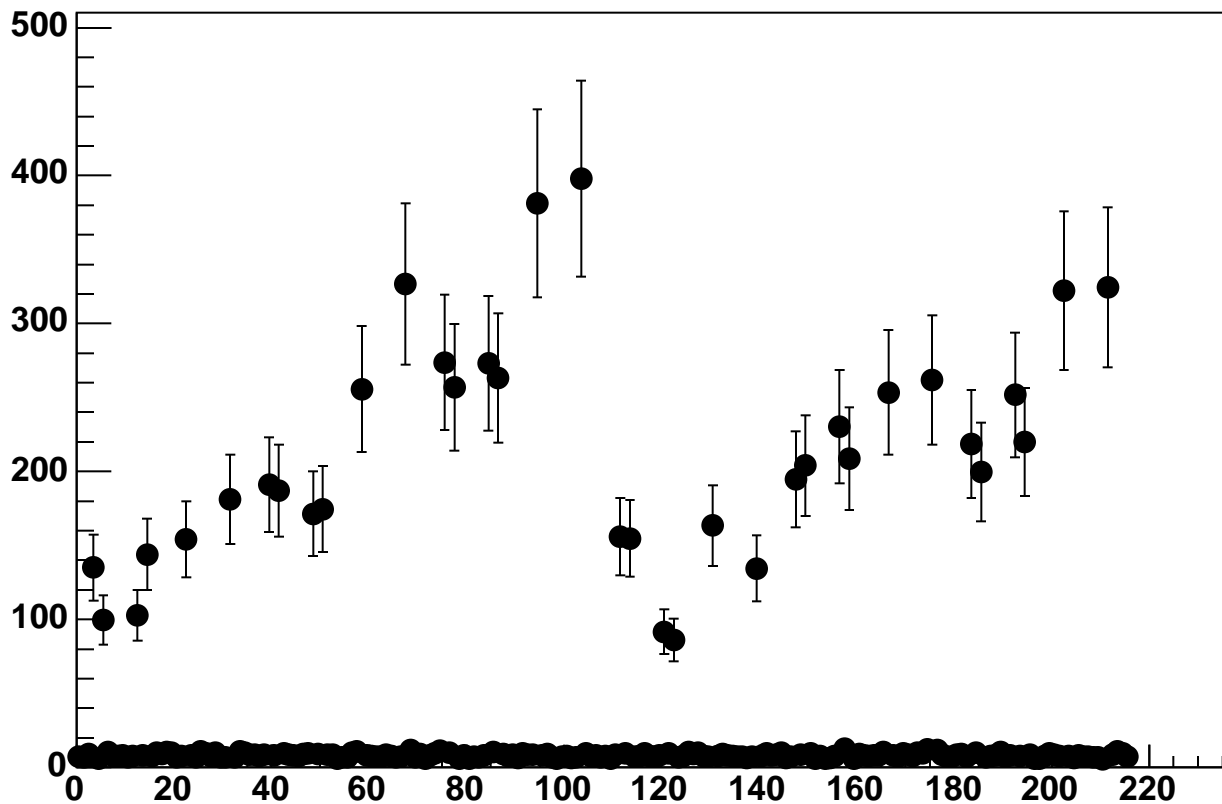
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18\*Chip+Chan



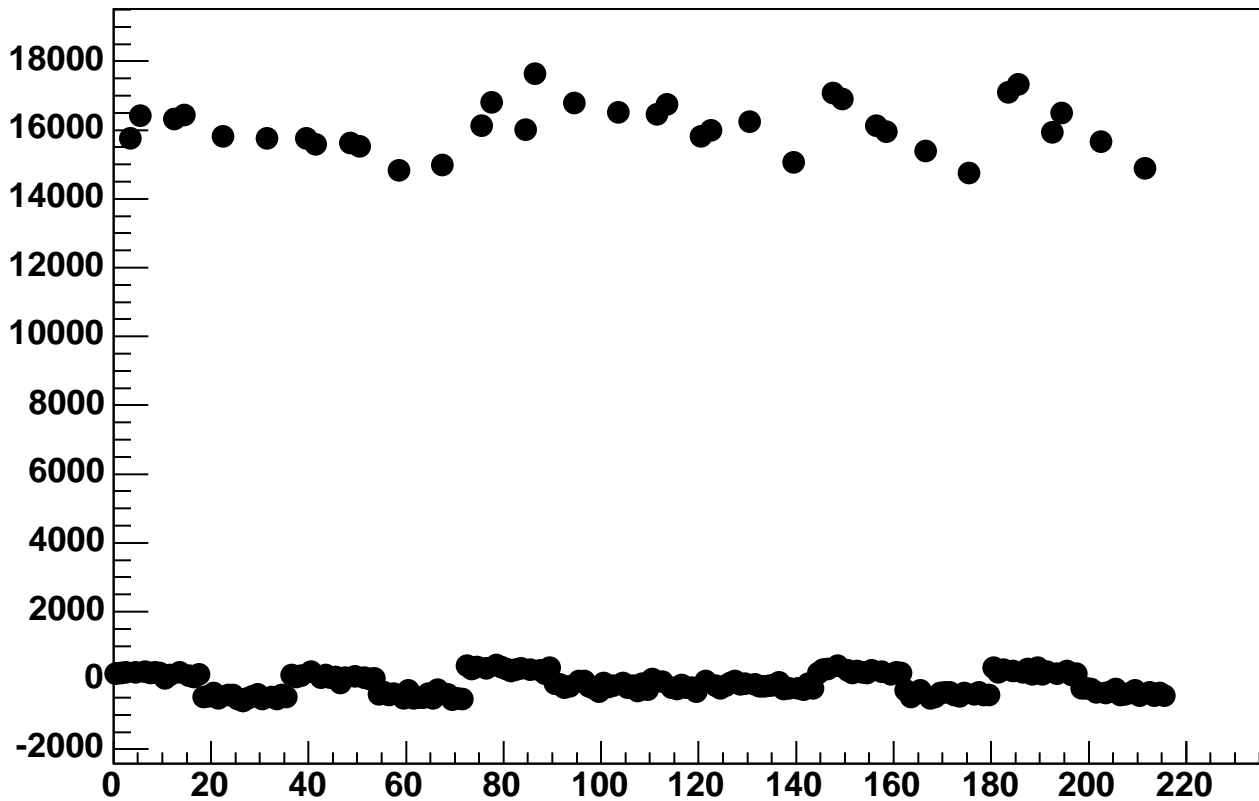
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18\*Chip+Chan



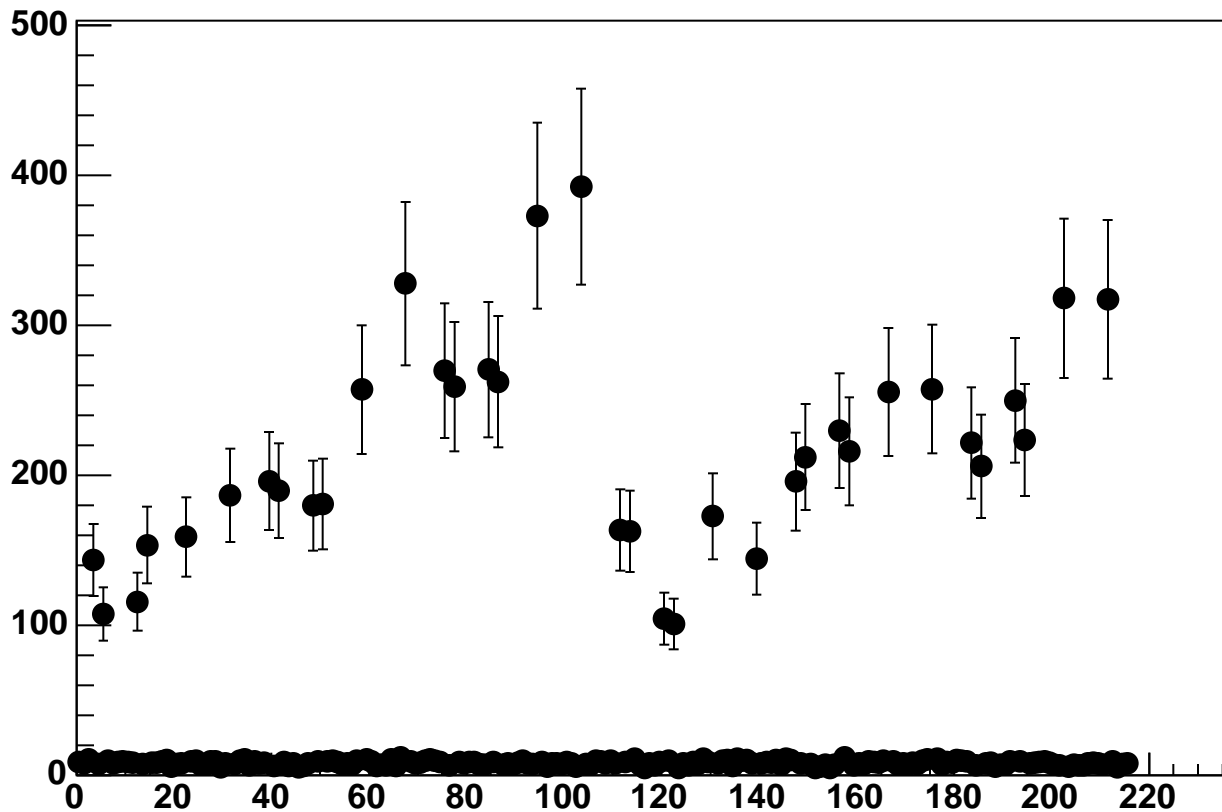
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18\*Chip+Chan



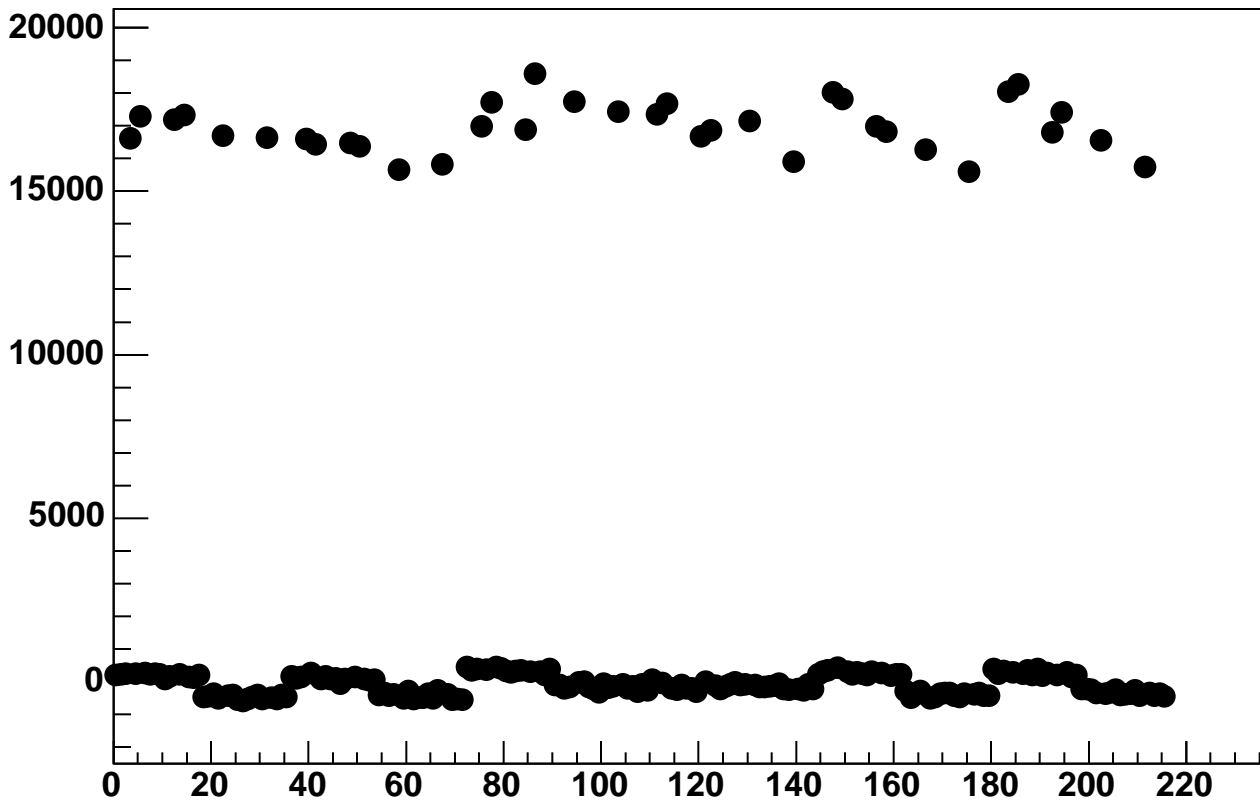
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18\*Chip+Chan



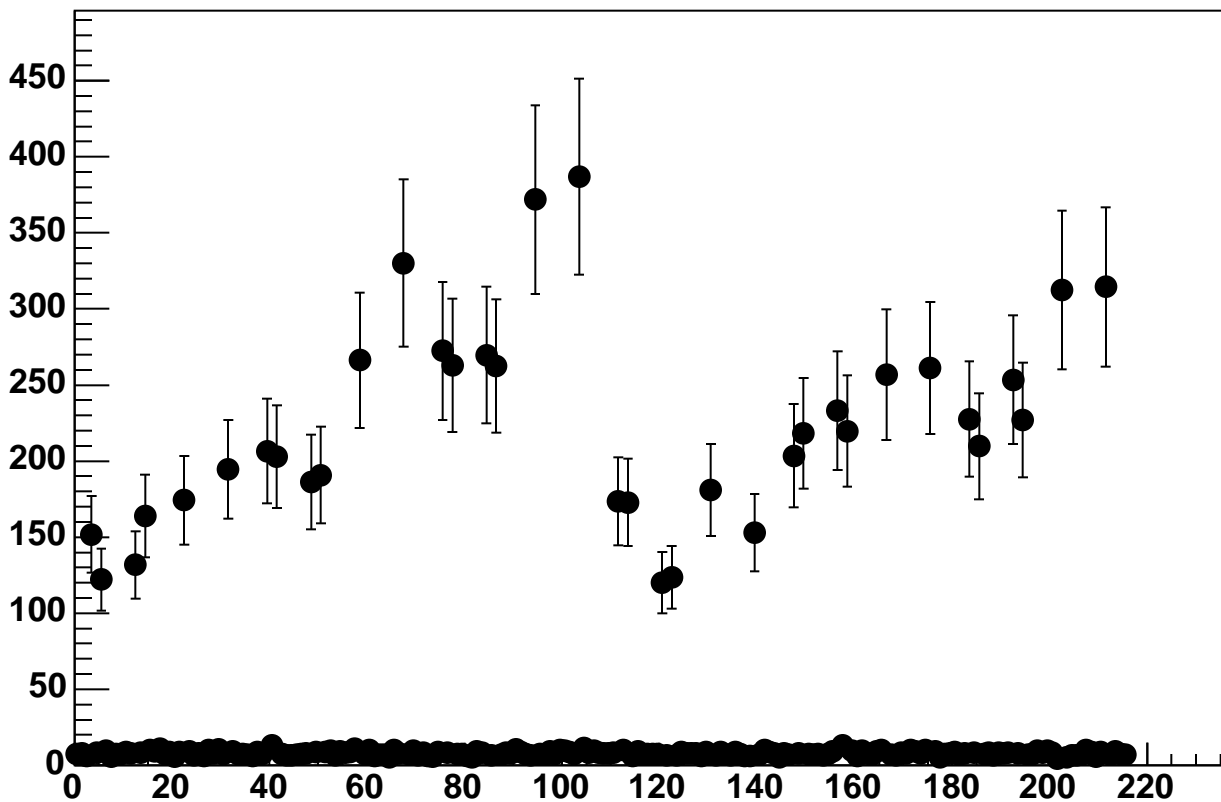
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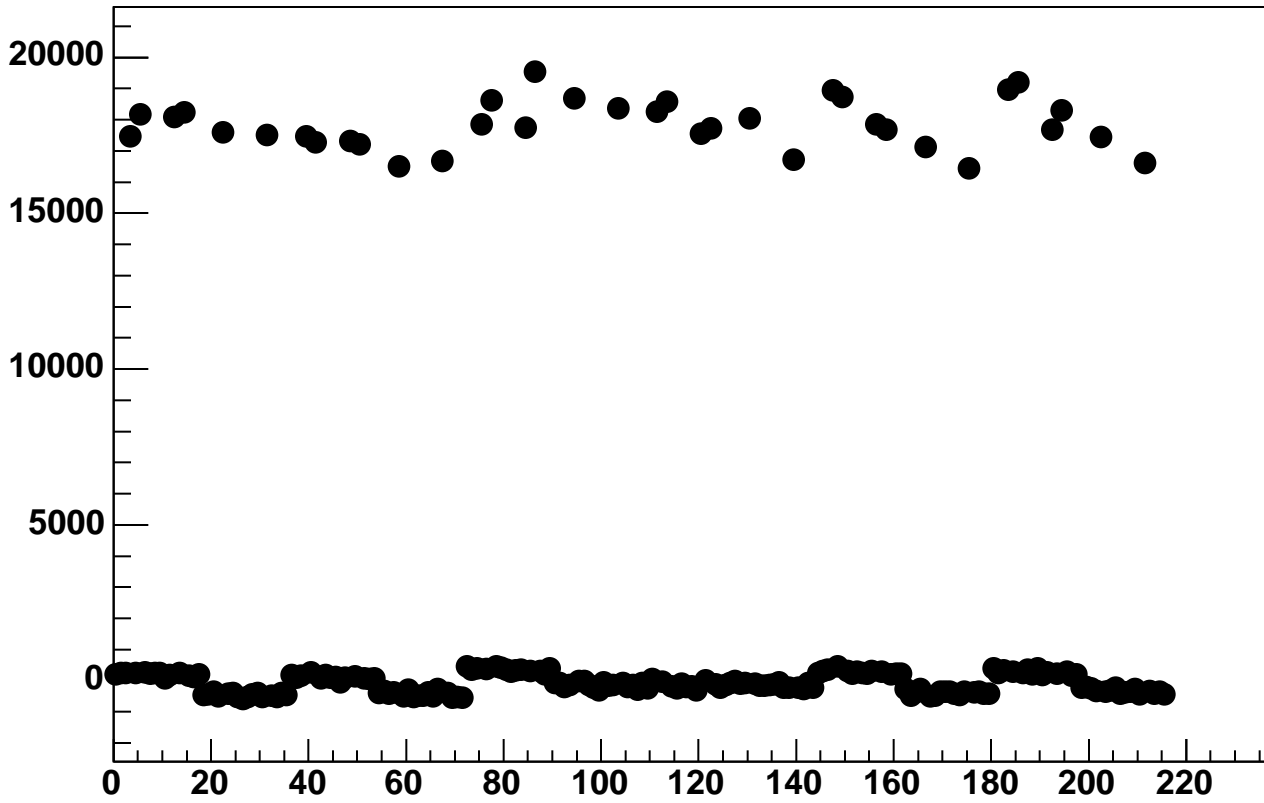
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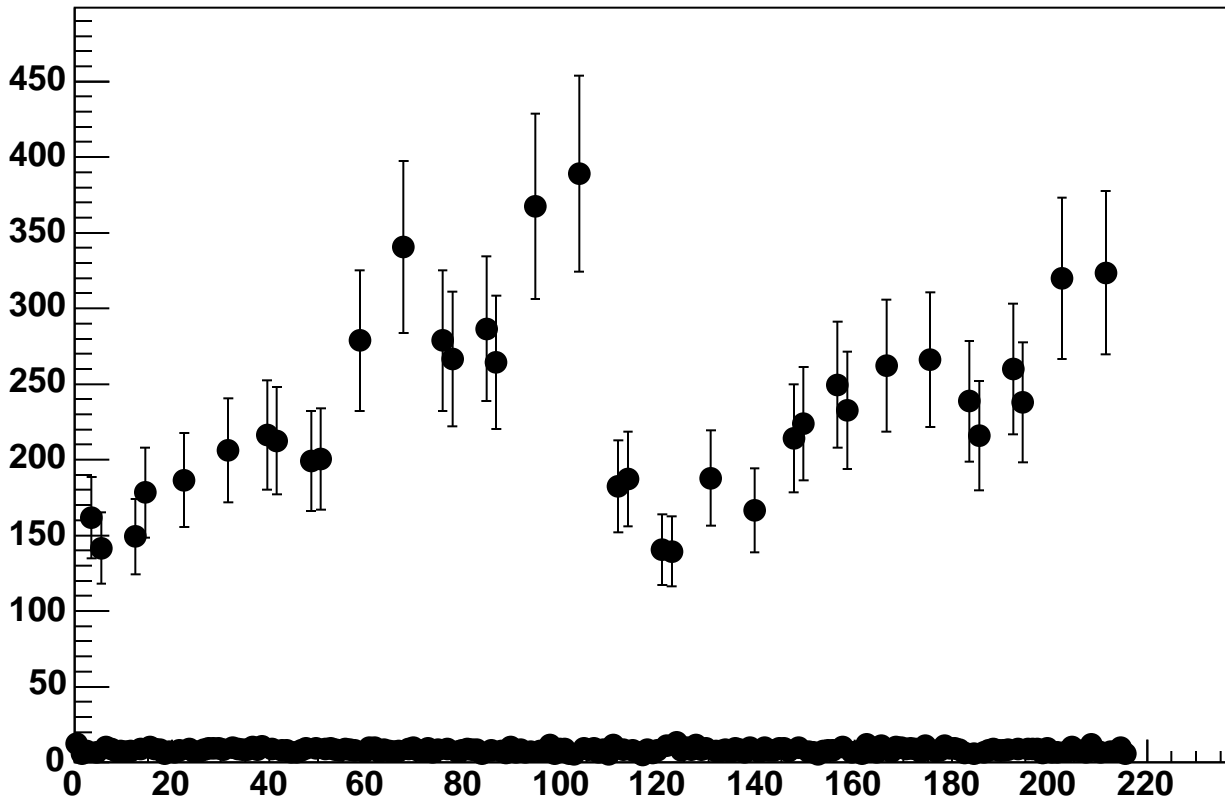
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18\*Chip+Chan



Enable 1, Hold=35, DAC=8000, ADC Mean vs 18\*Chip+Chan

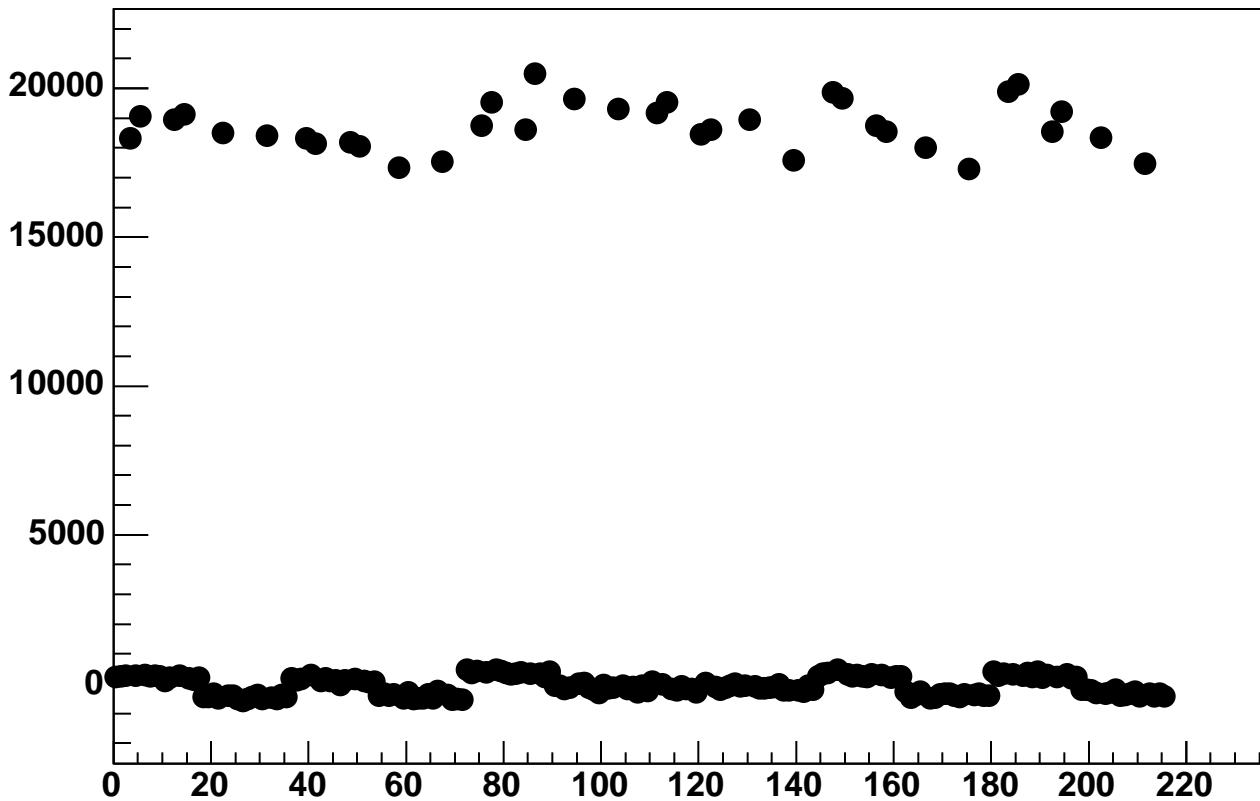


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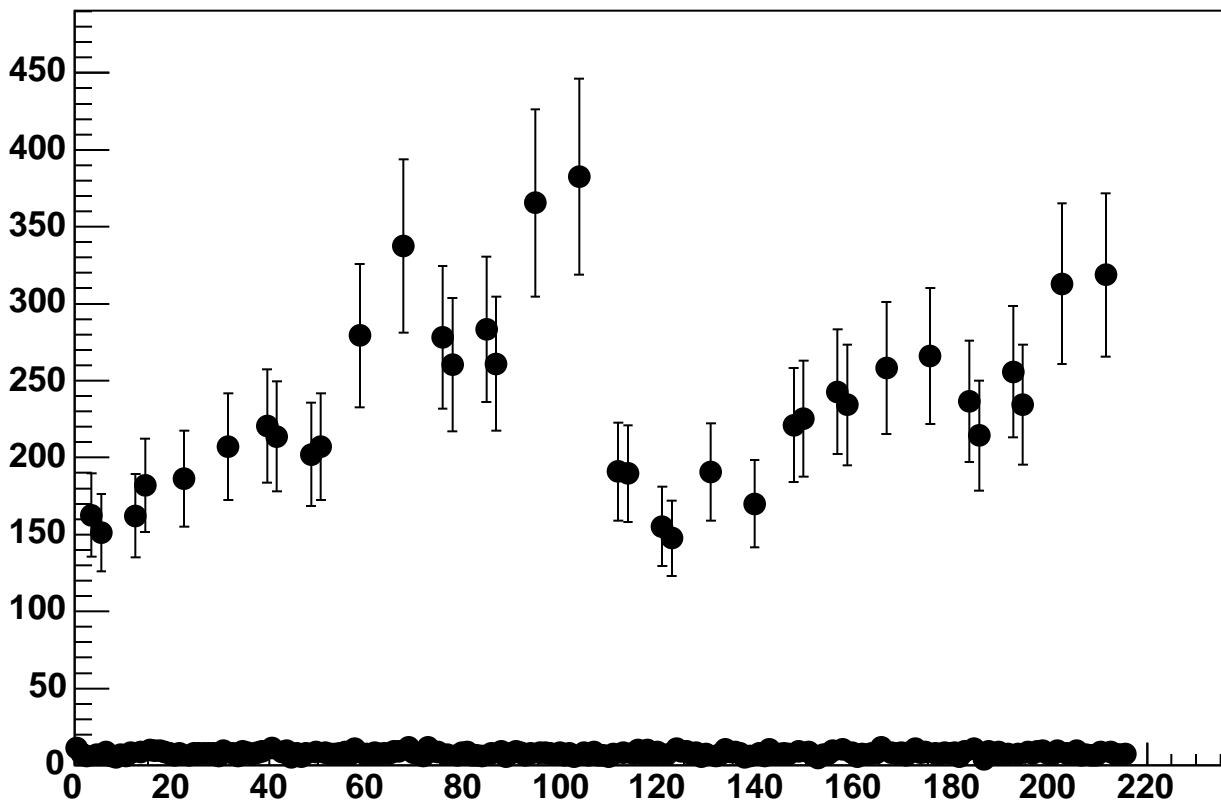




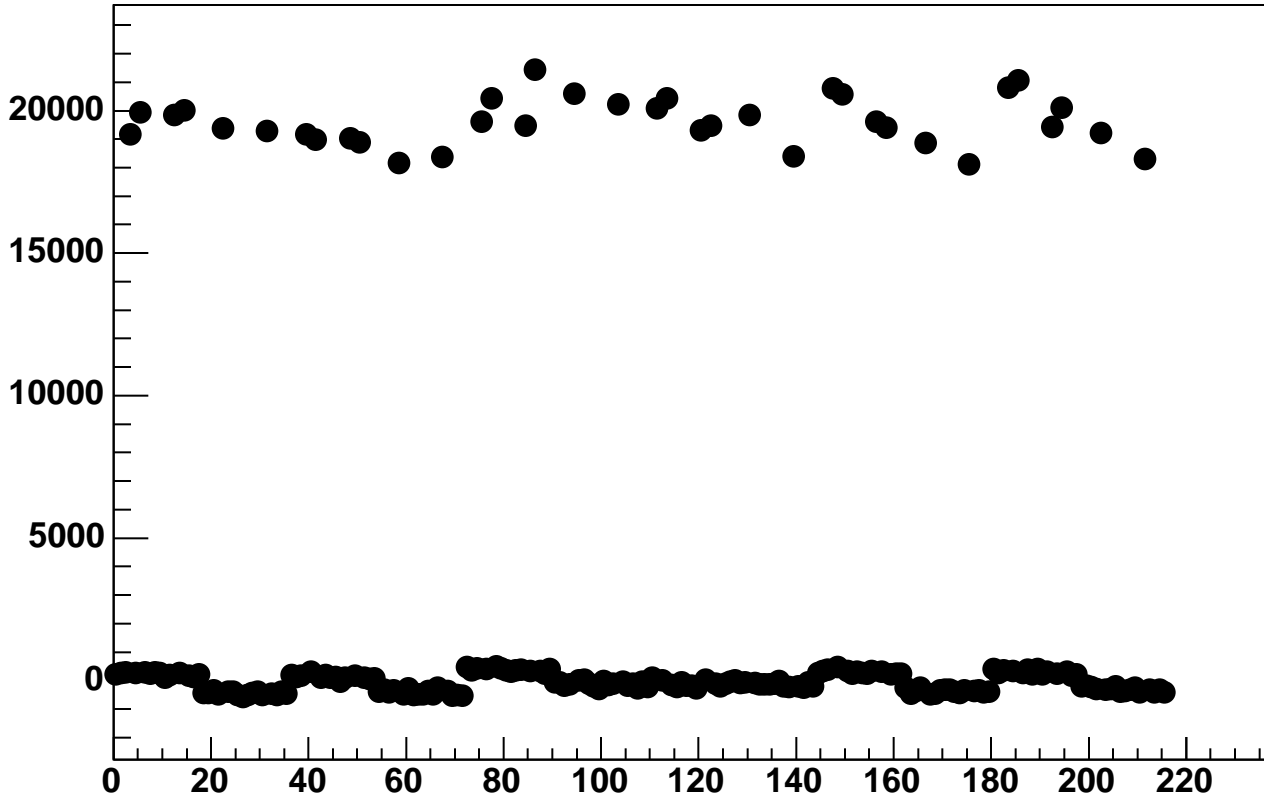
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18\*Chip+Chan



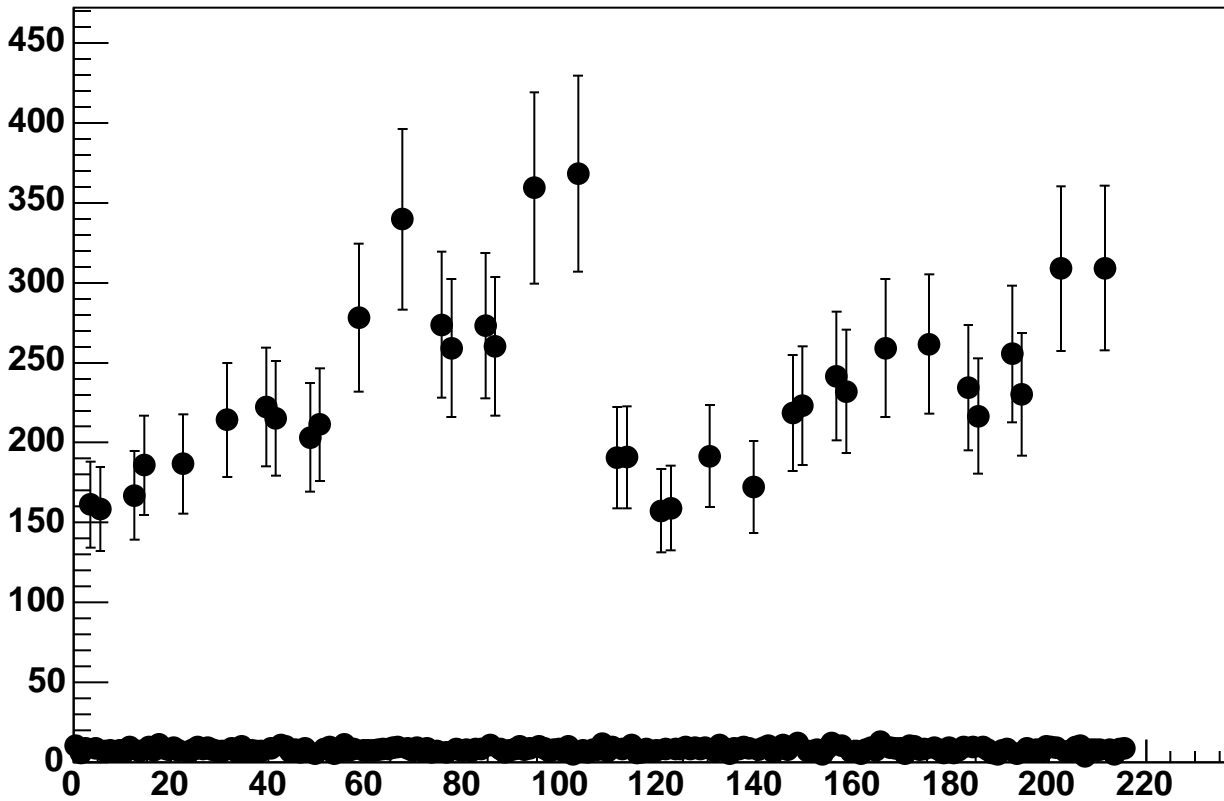
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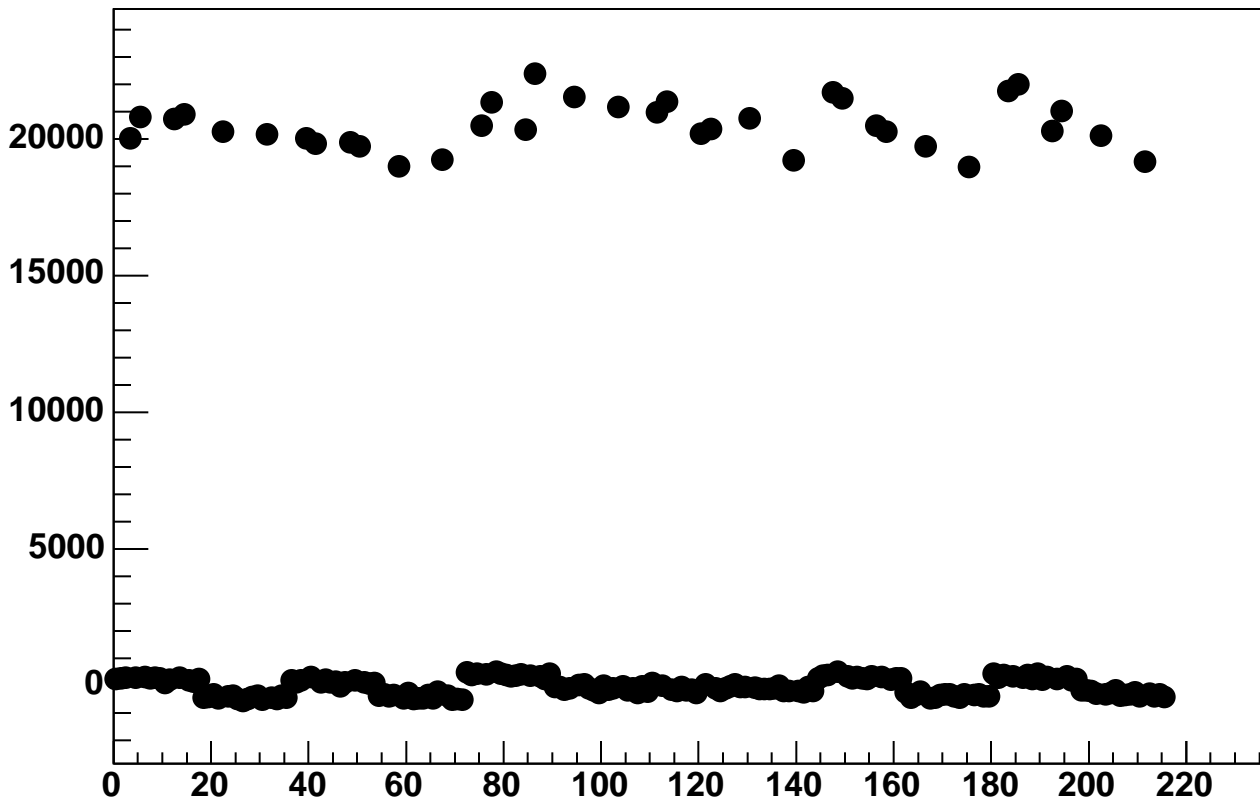
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18\*Chip+Chan



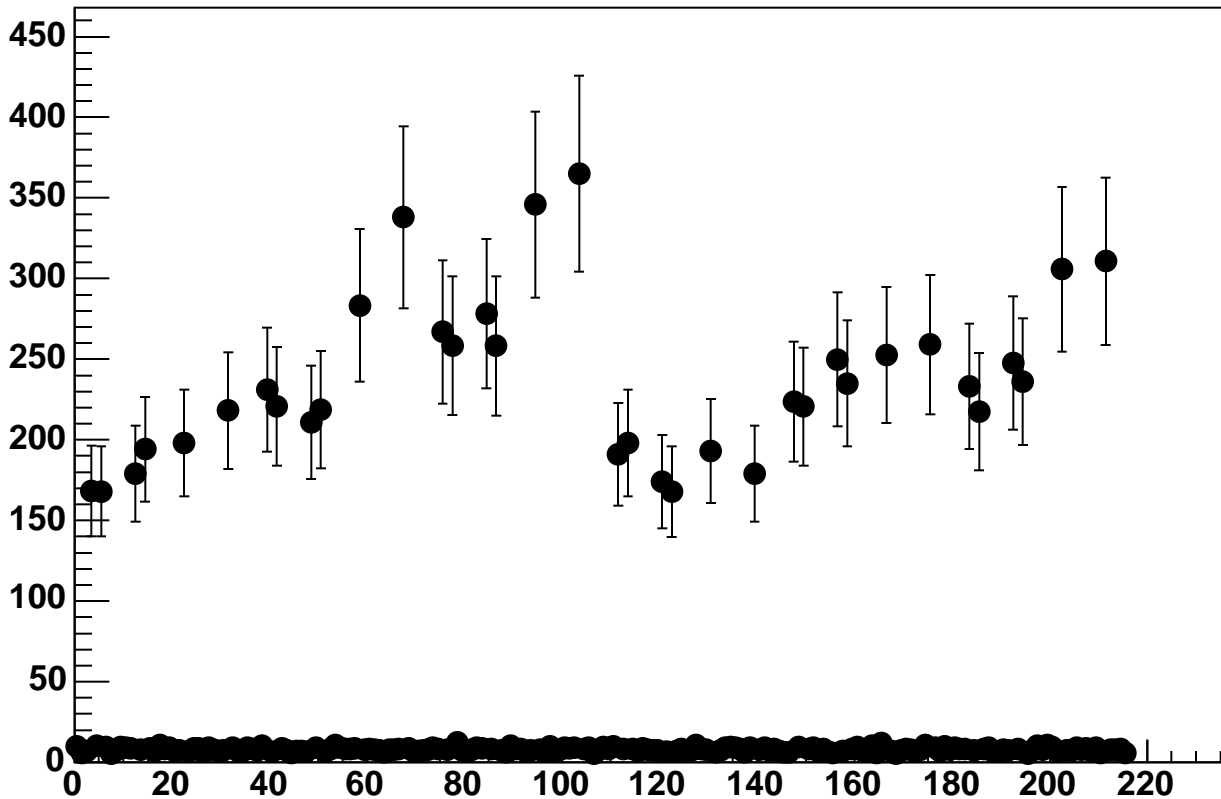
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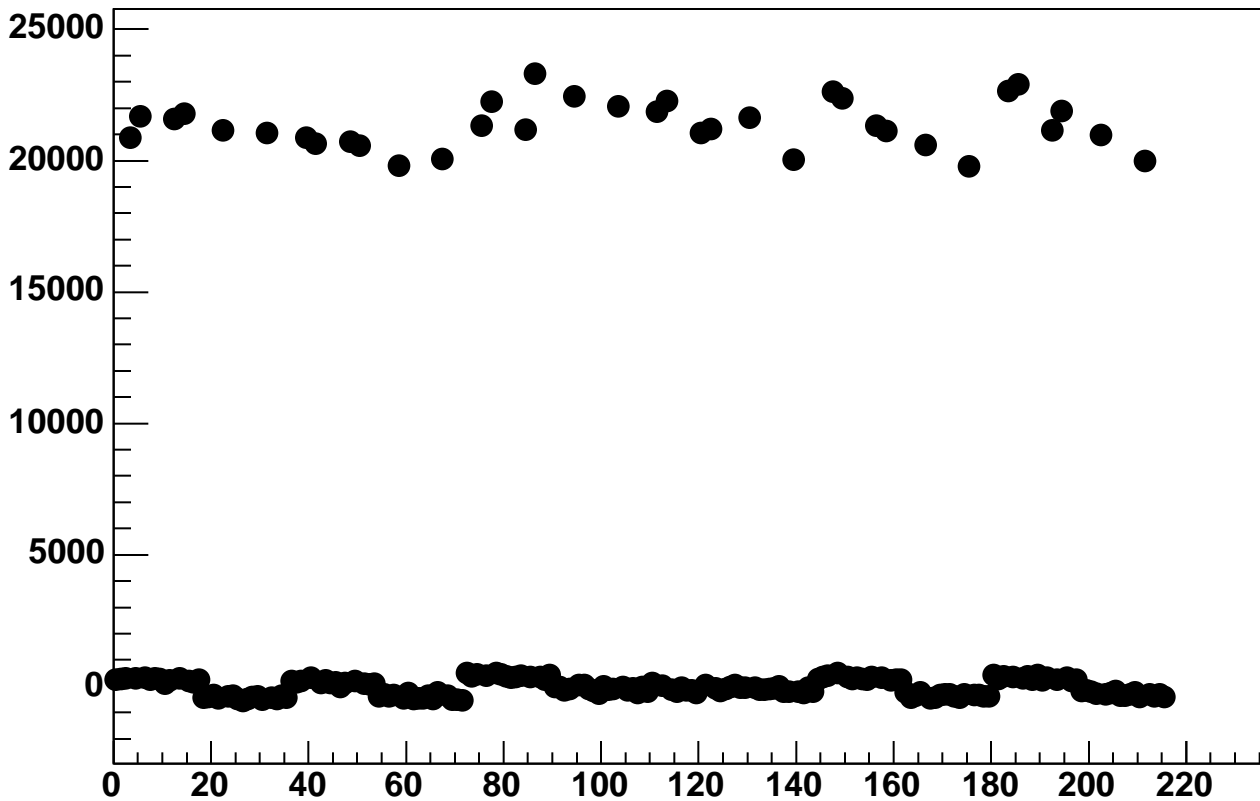
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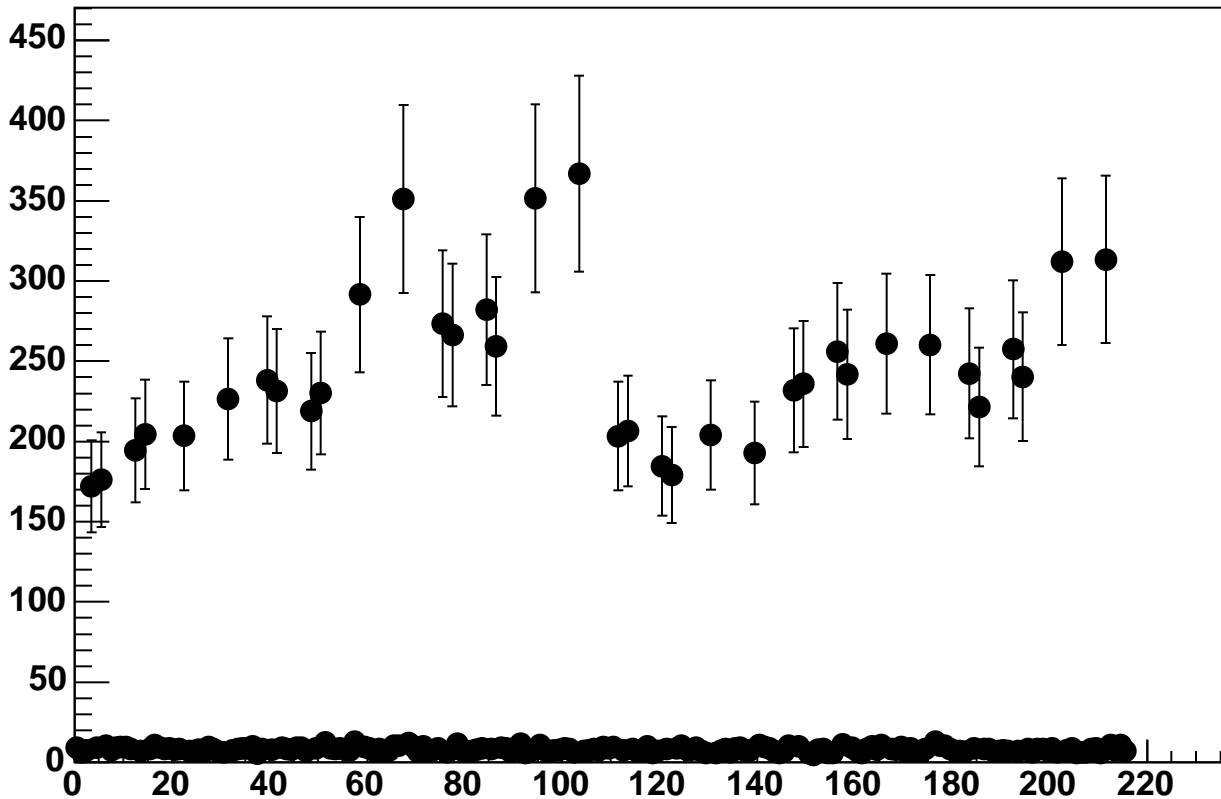
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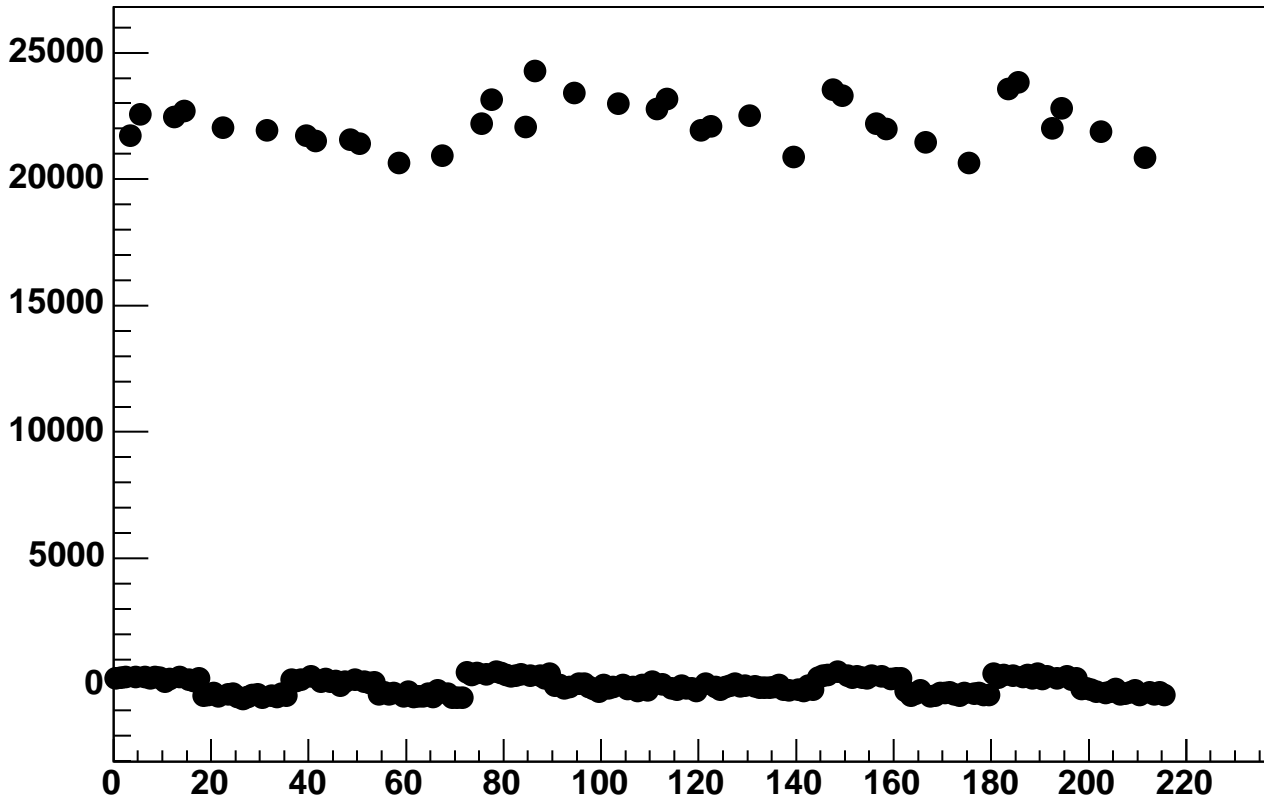
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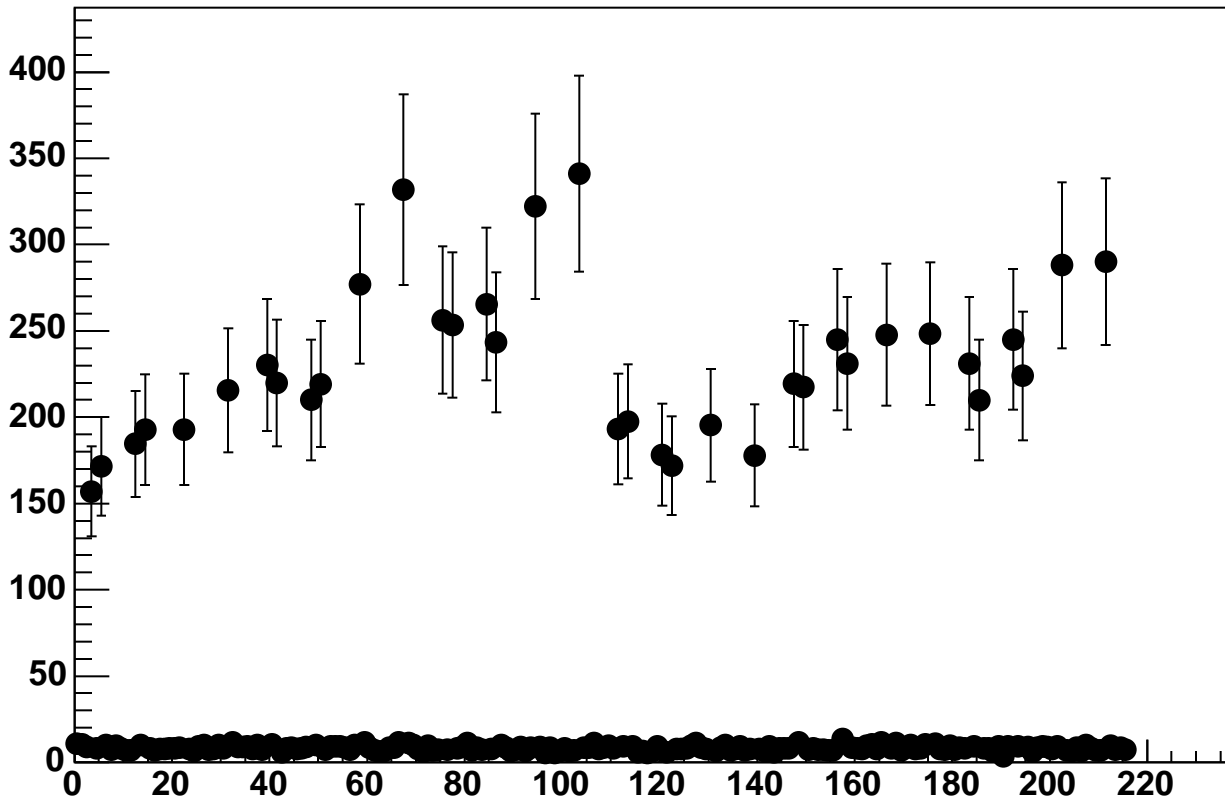
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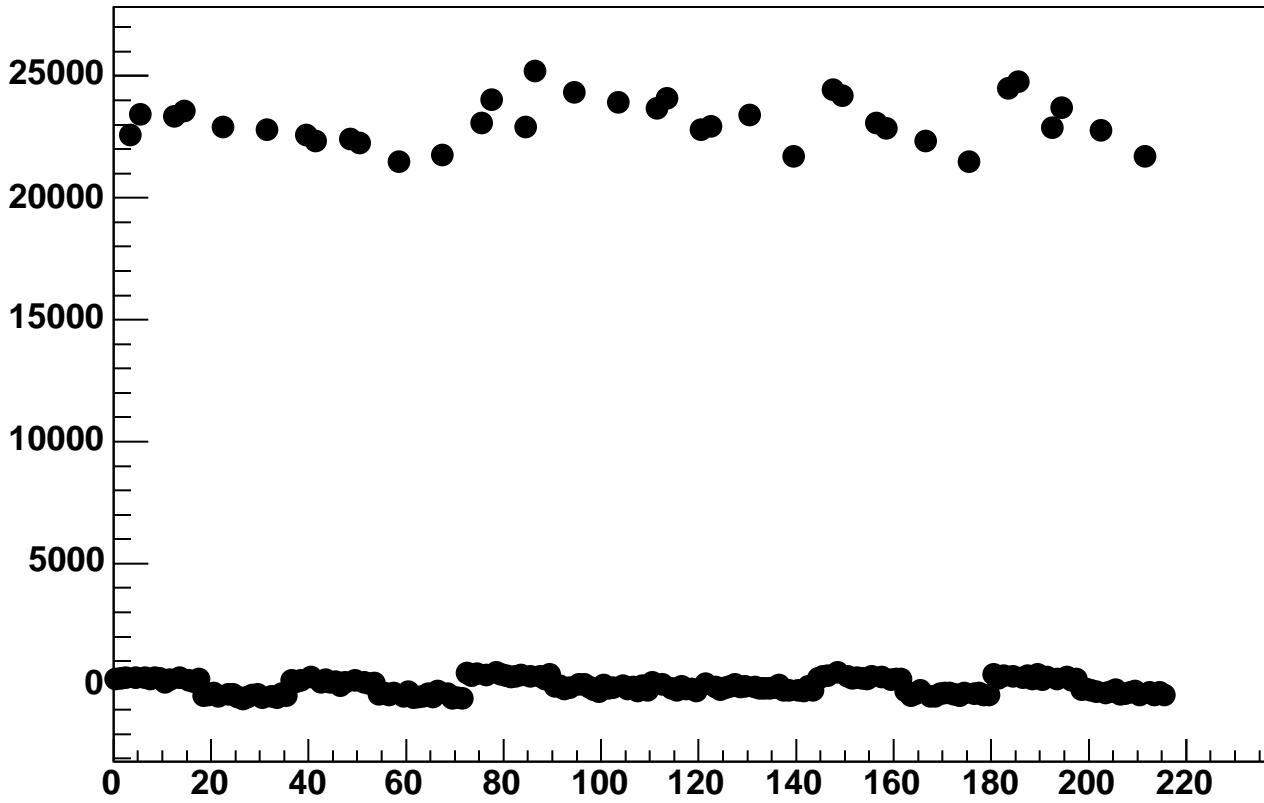
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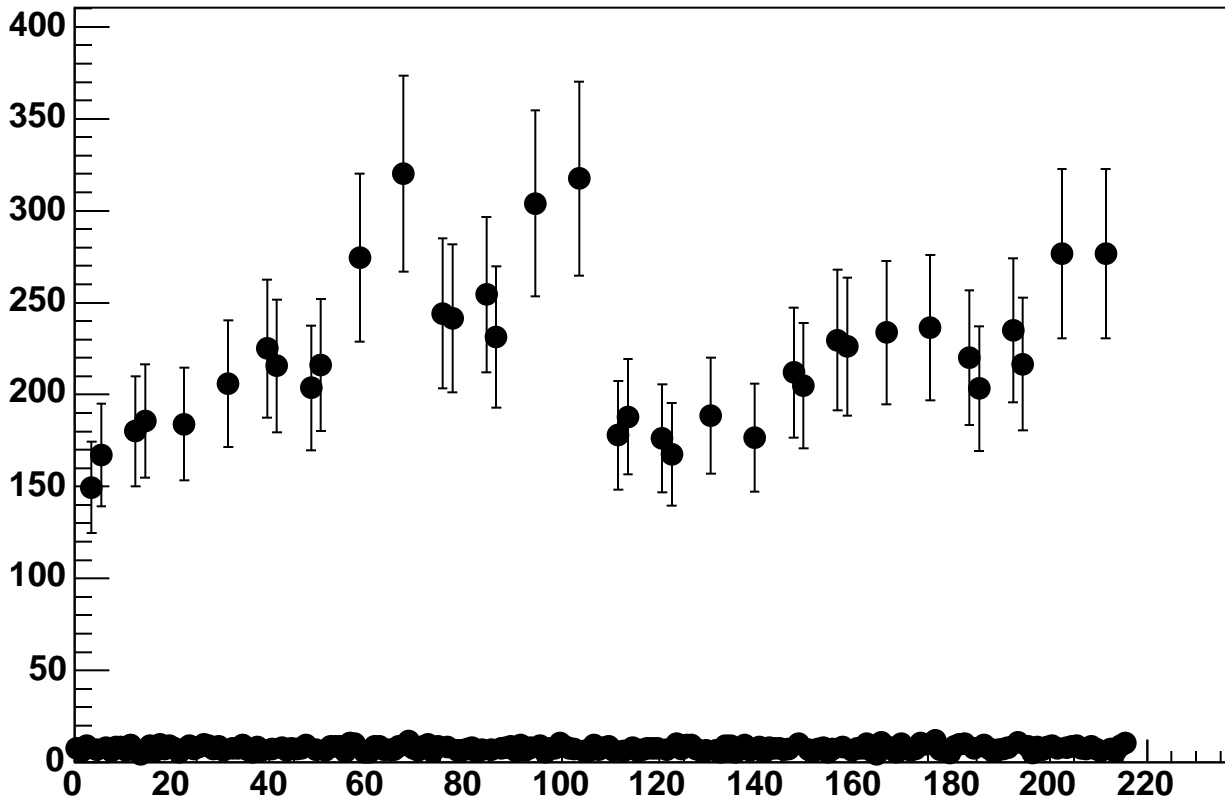
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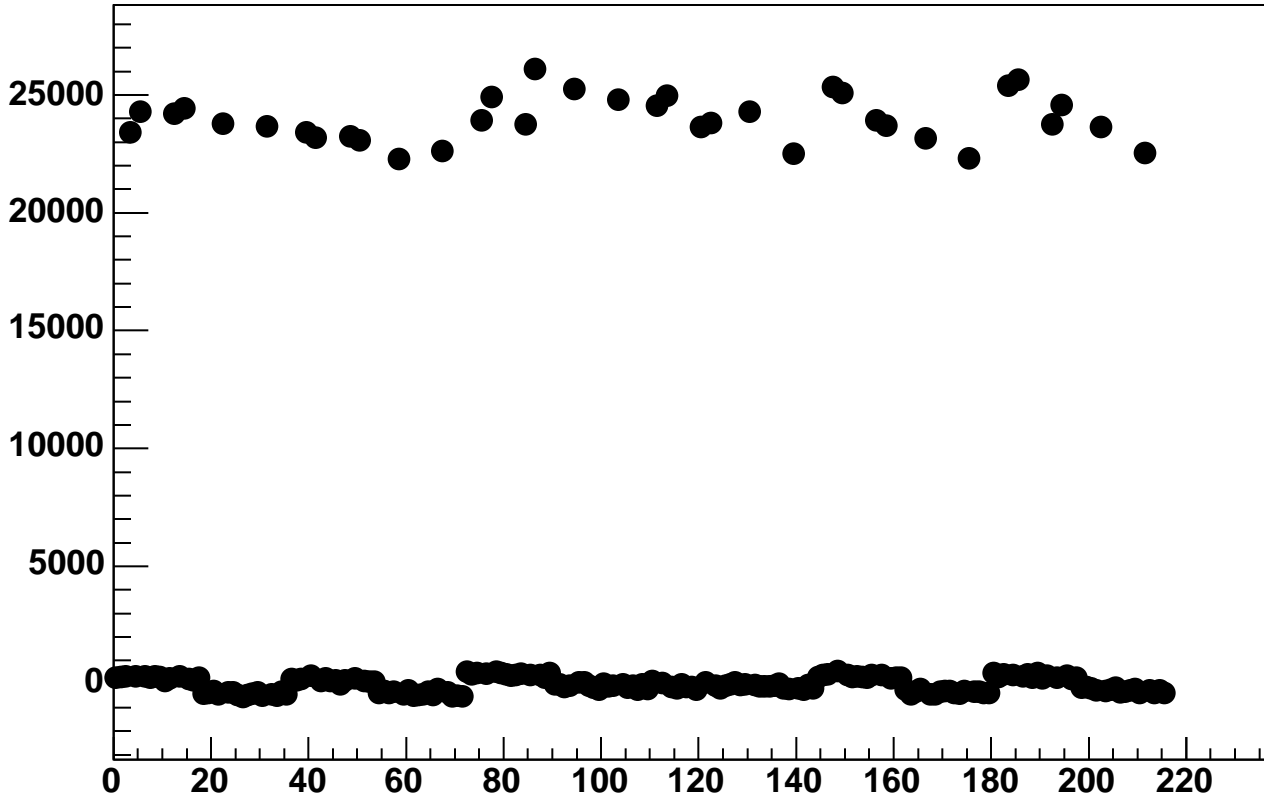
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18\*Chip+Chan



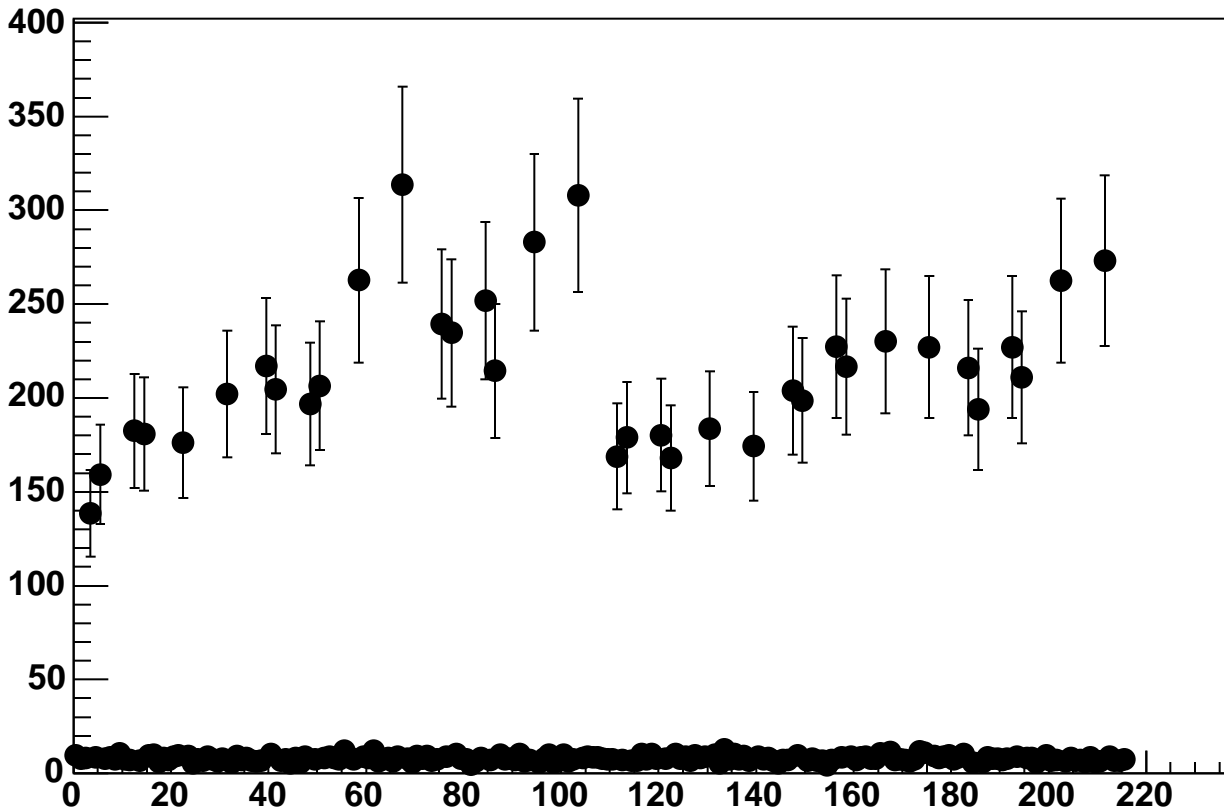
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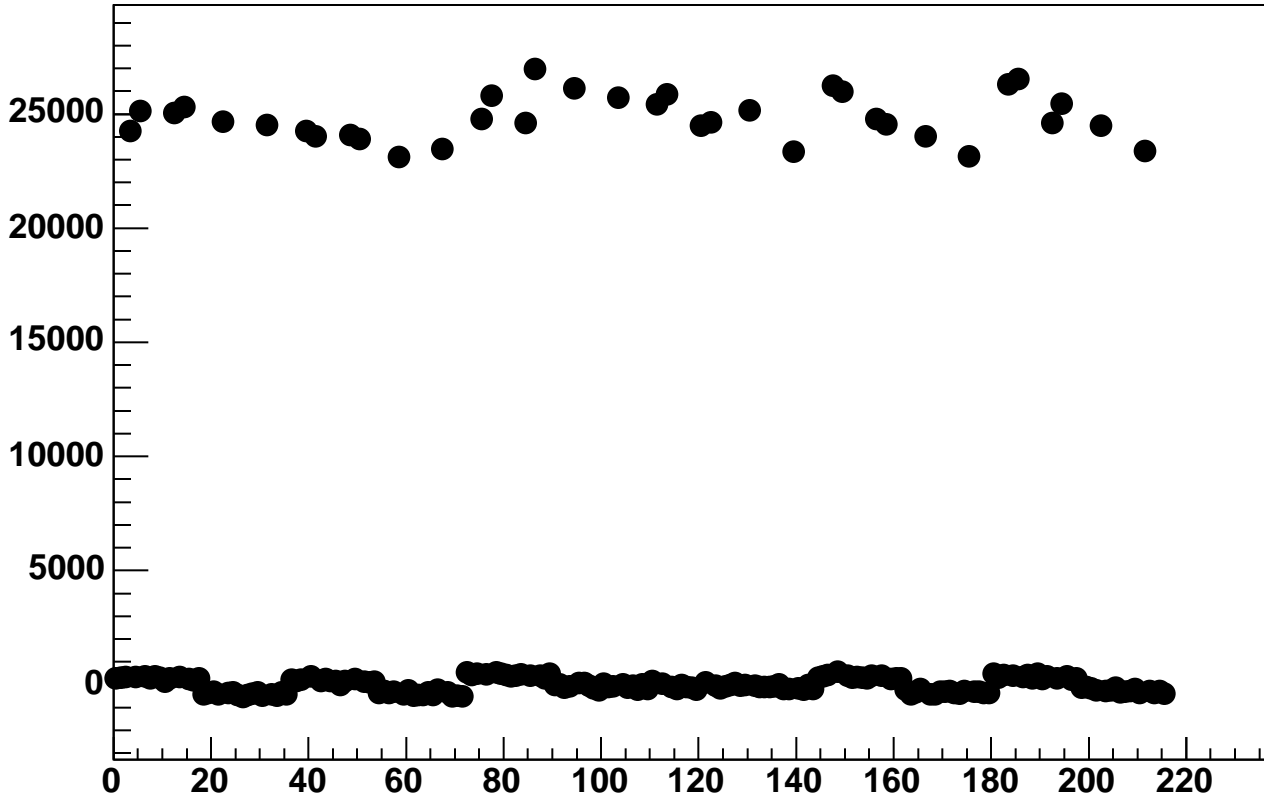
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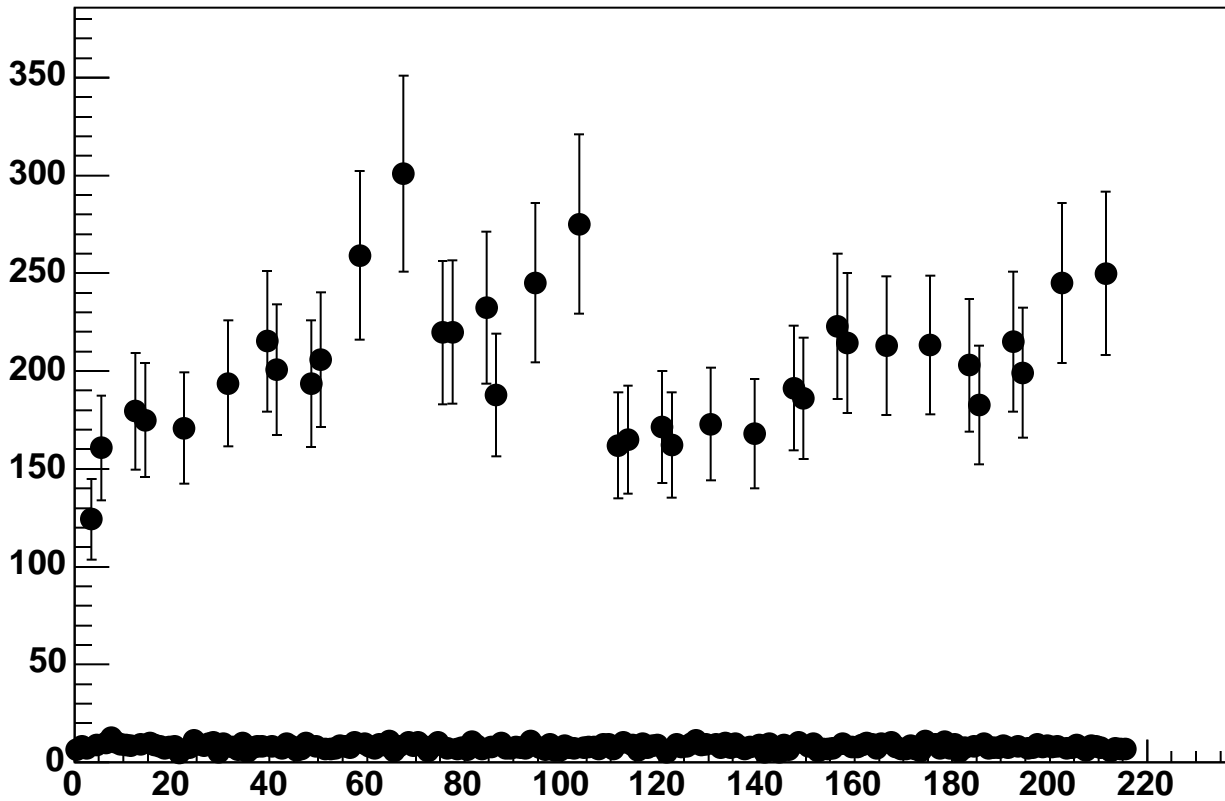
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Enable 1, Hold=35, DAC=11200, ADC Mean vs 18\*Chip+Chan

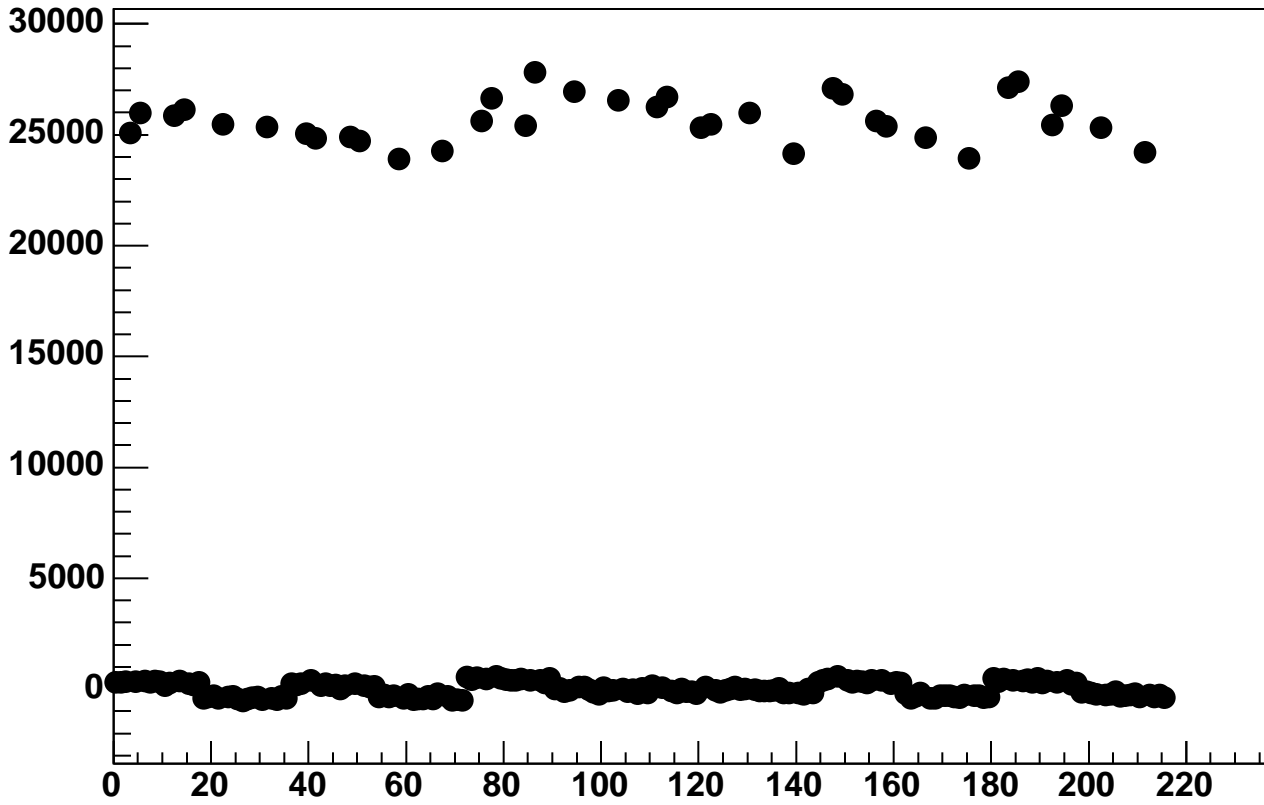


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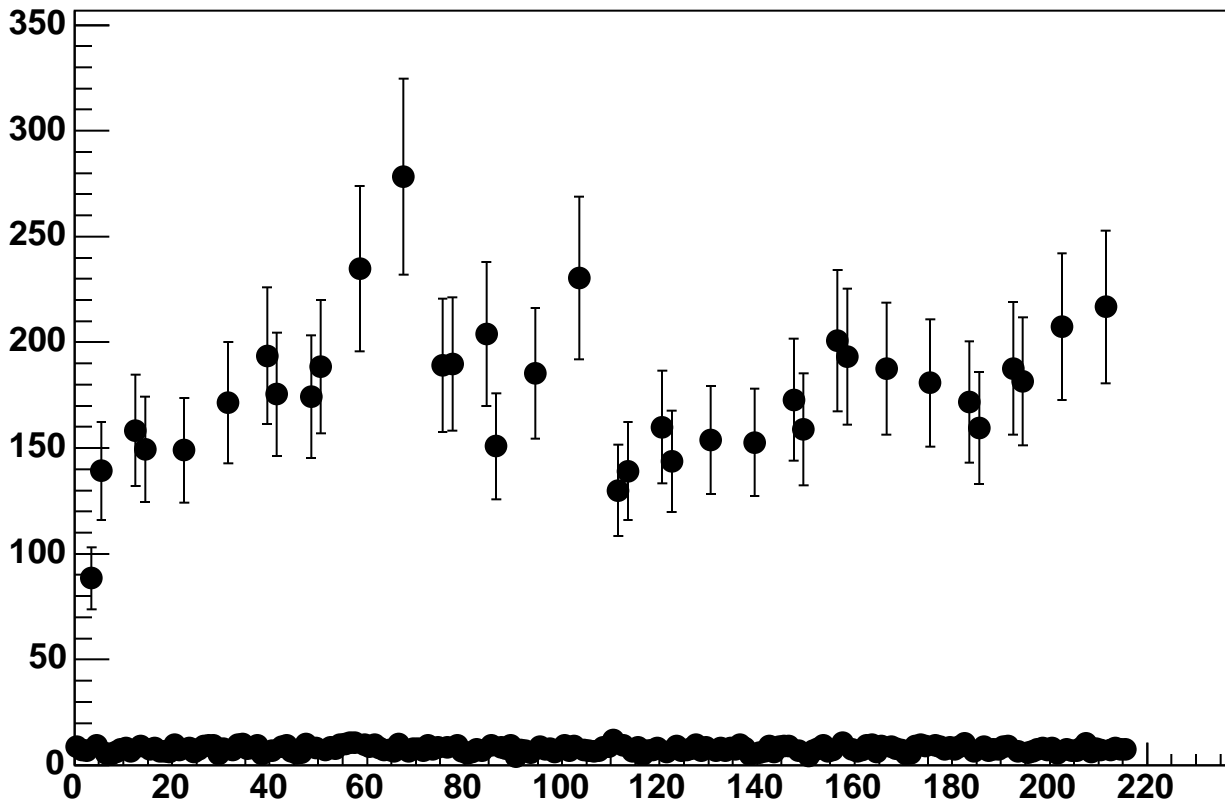




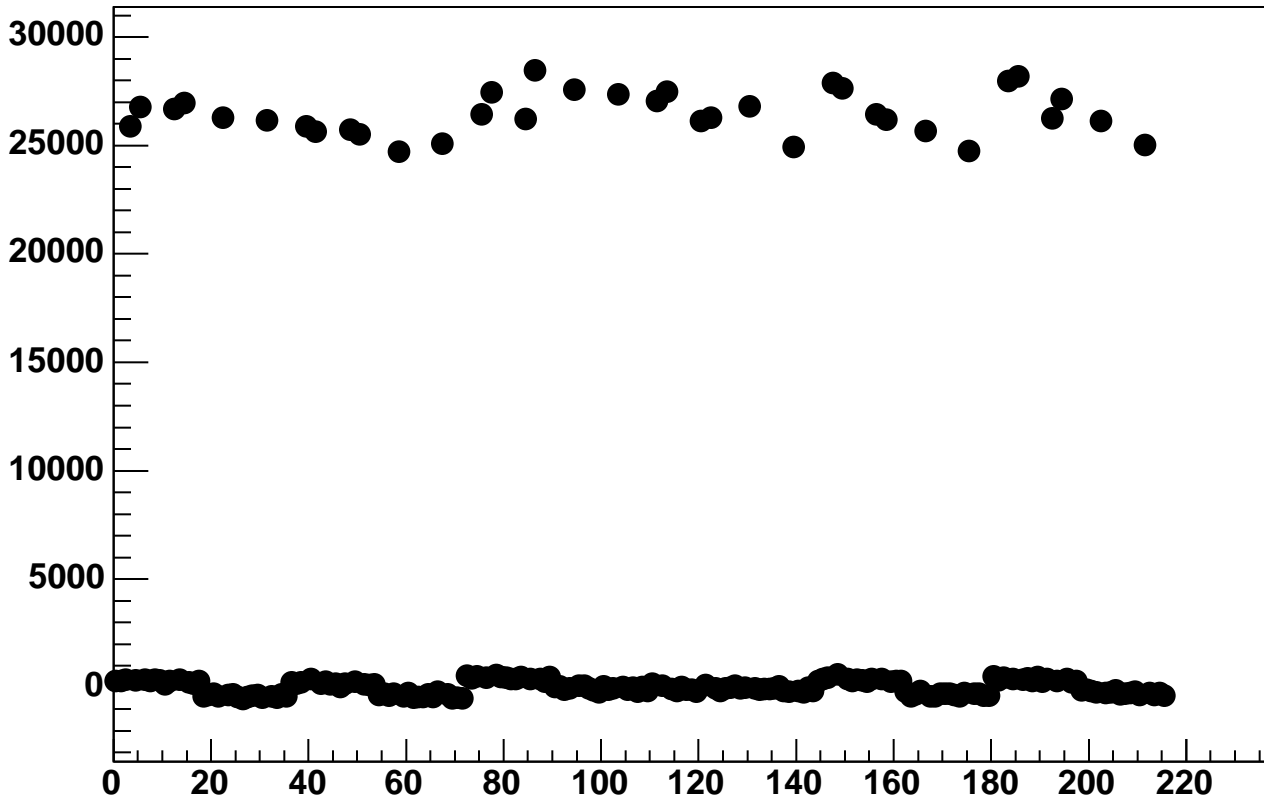
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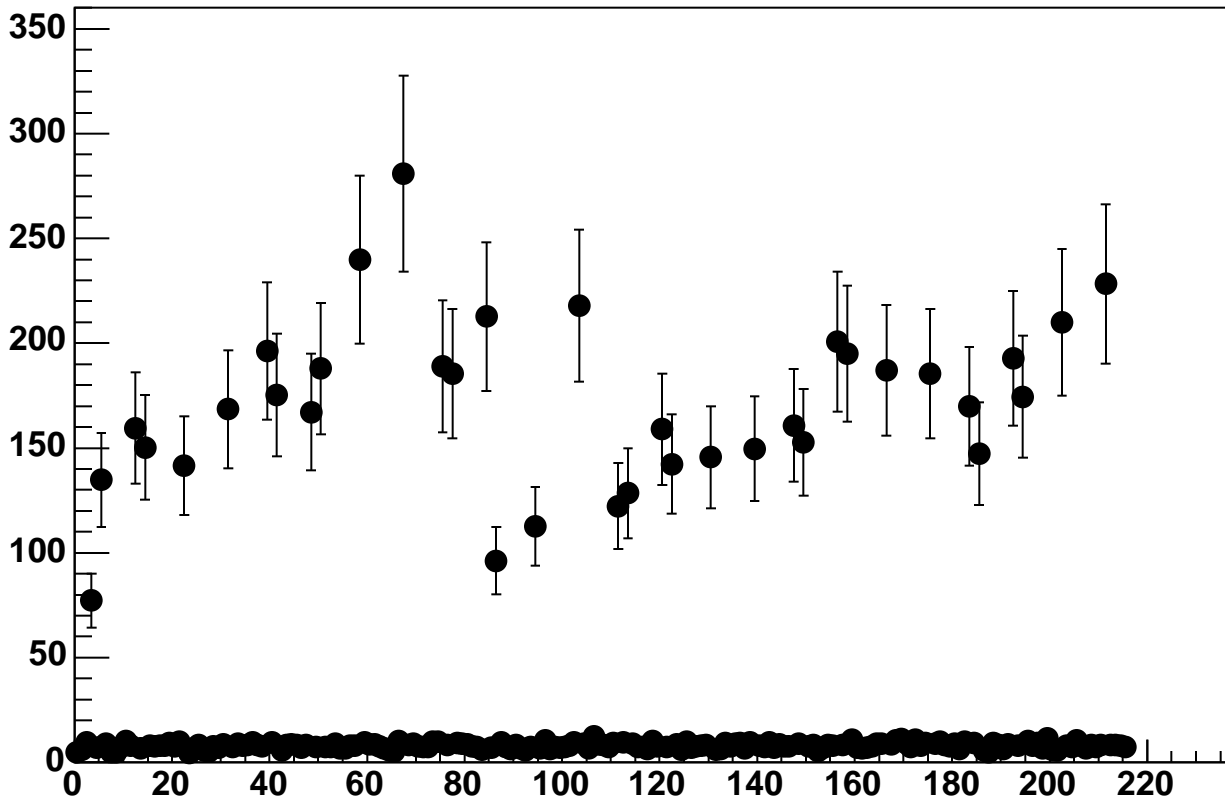
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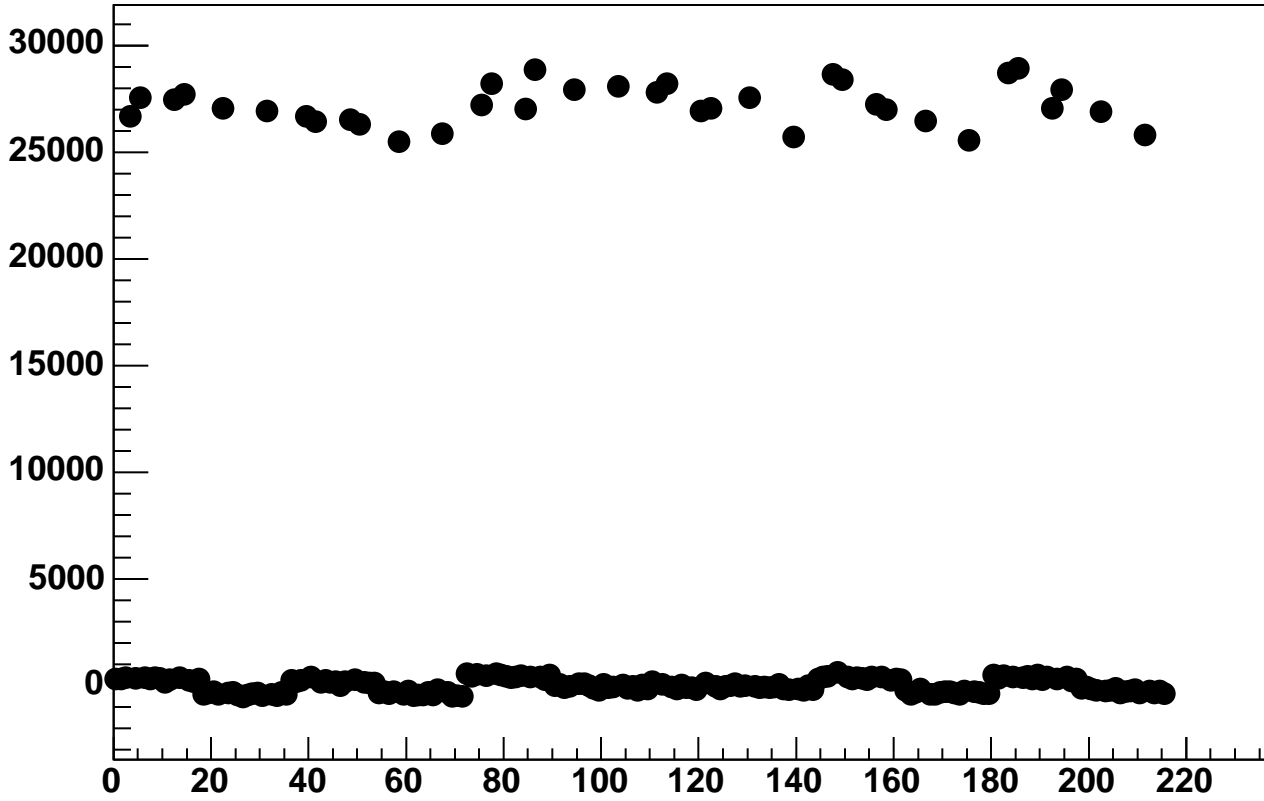
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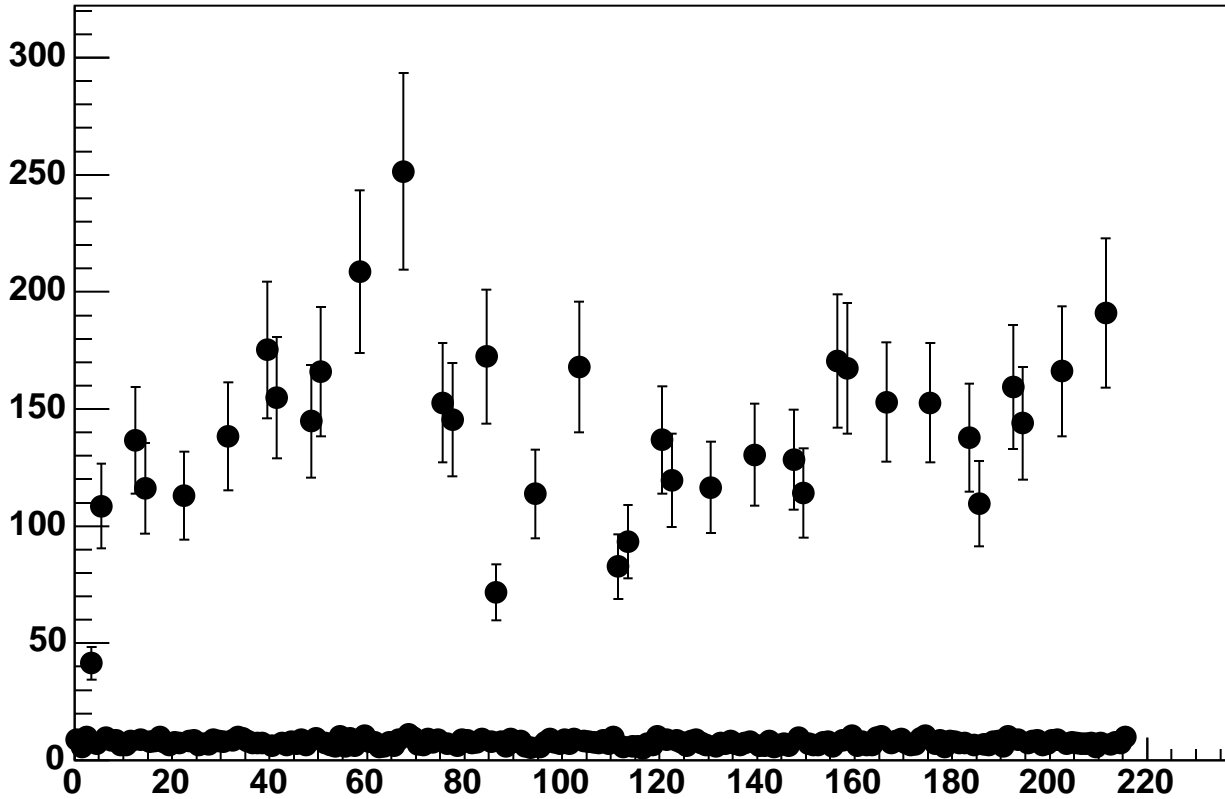
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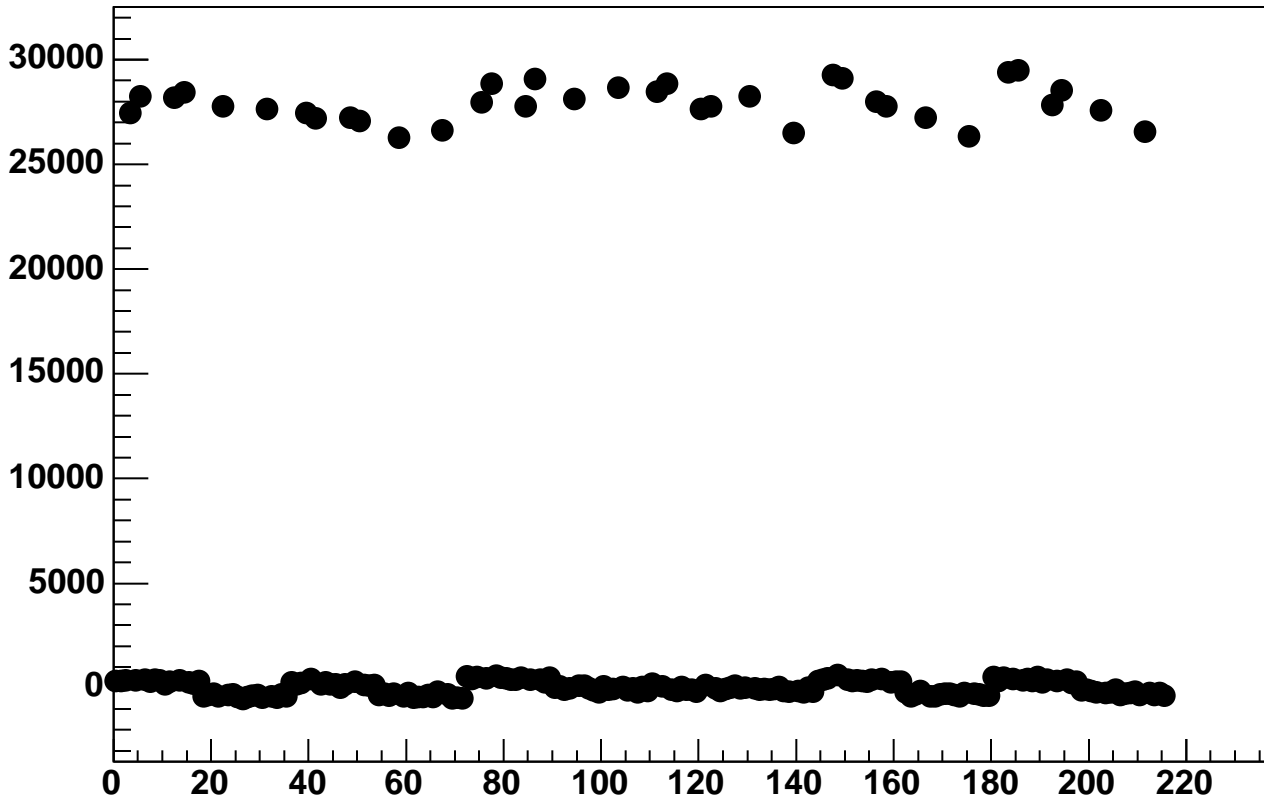
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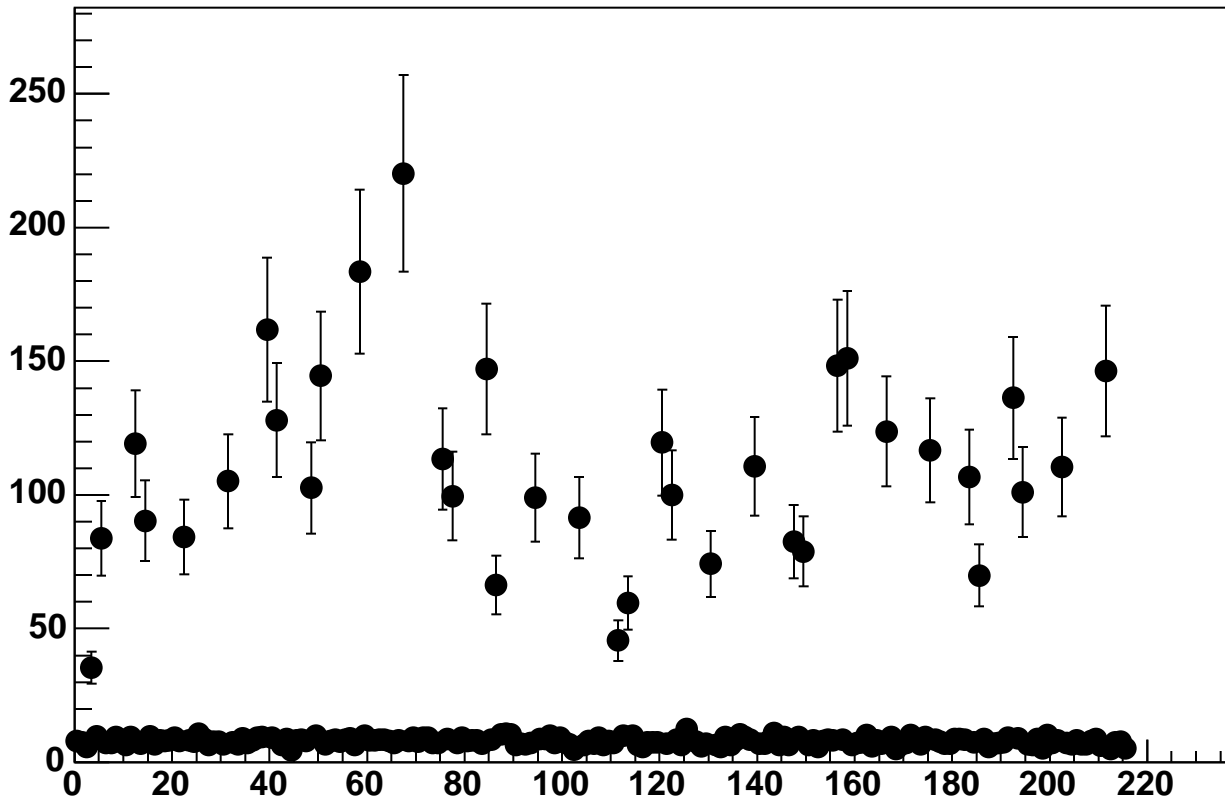
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18\*Chip+Chan



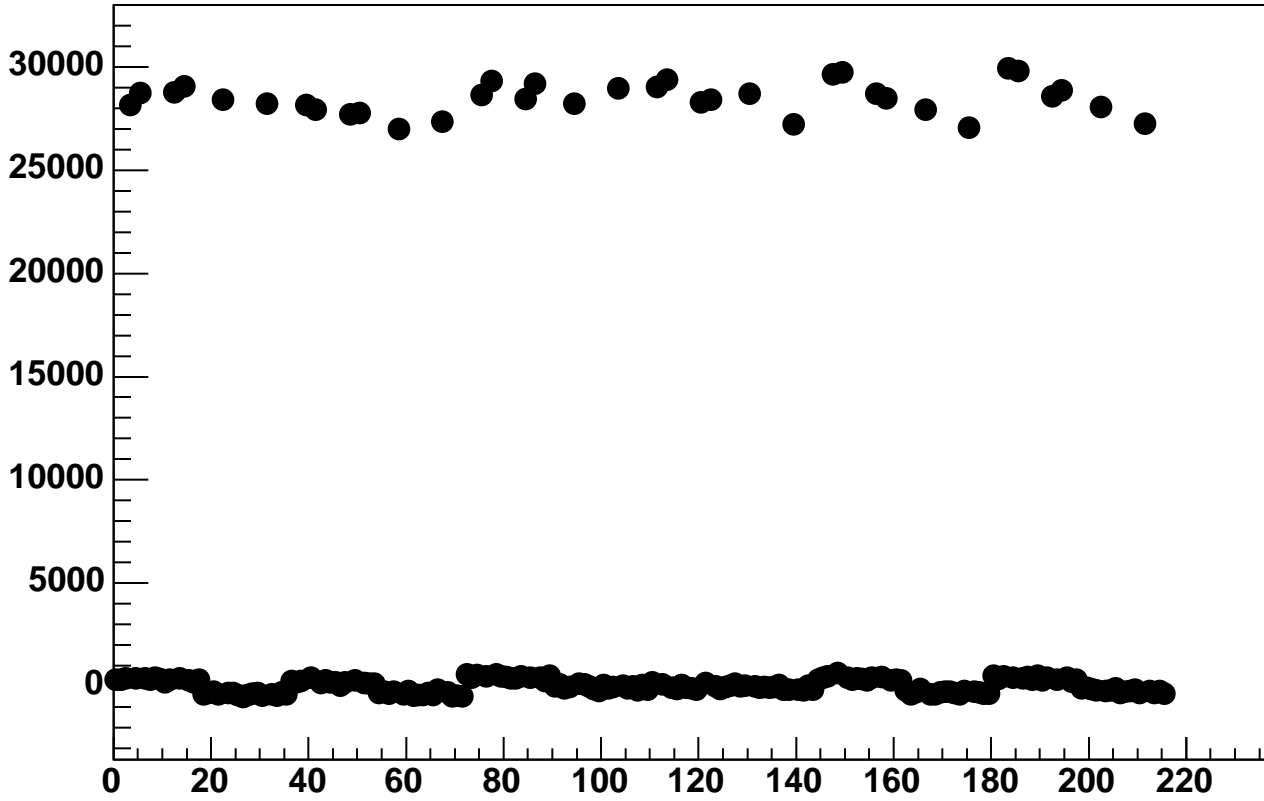
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18\*Chip+Chan



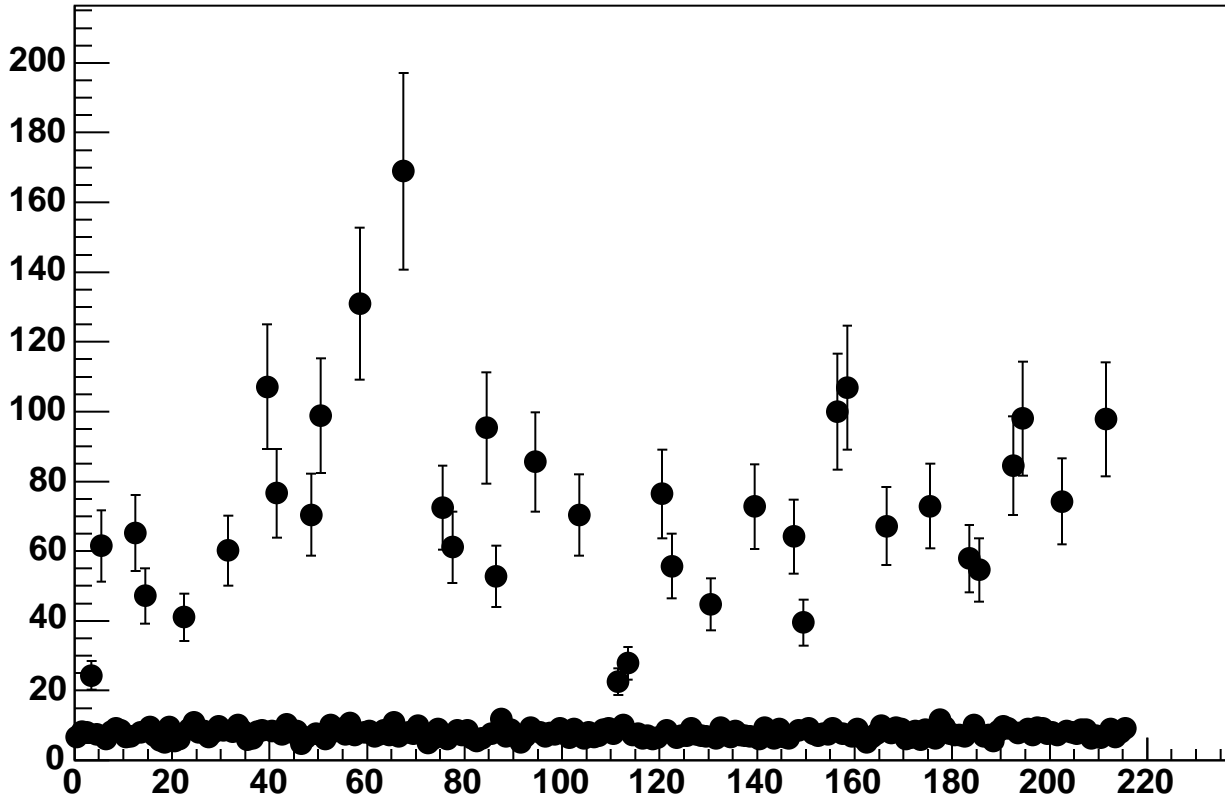
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18\*Chip+Chan



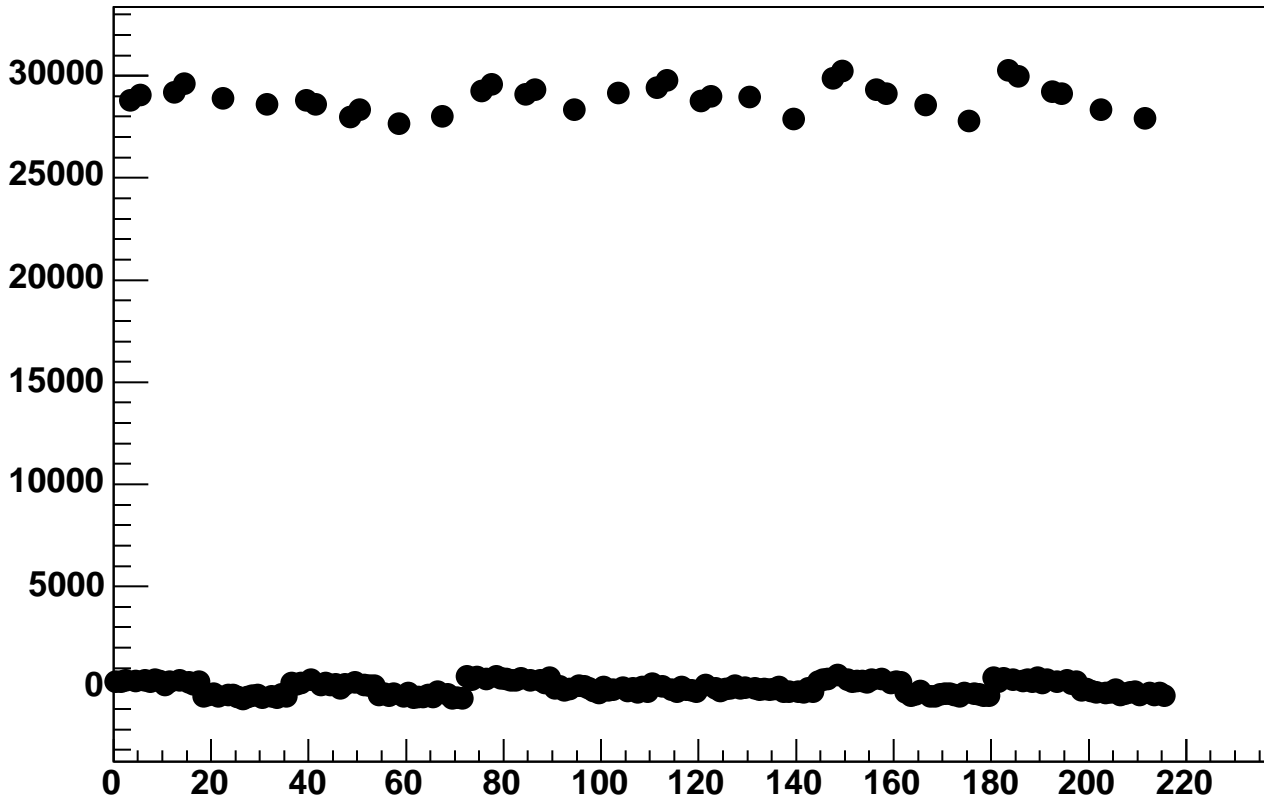
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18\*Chip+Chan



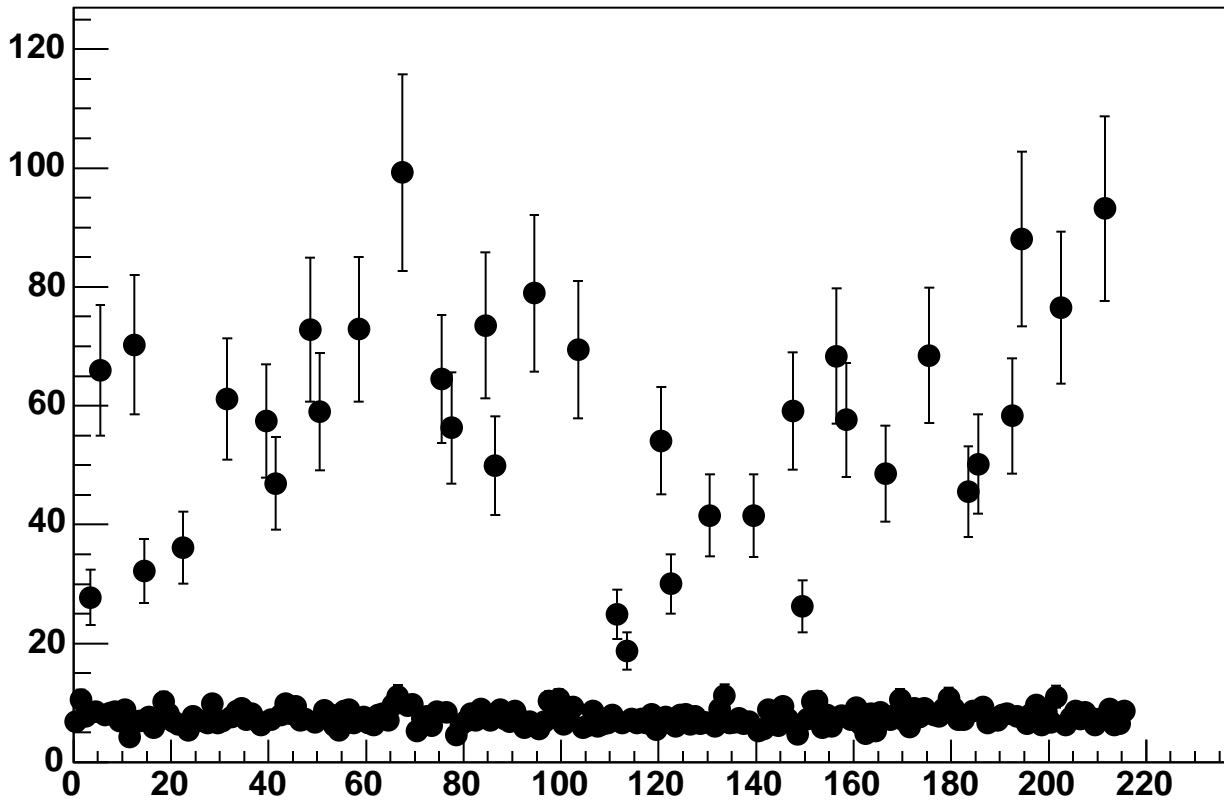
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18\*Chip+Chan



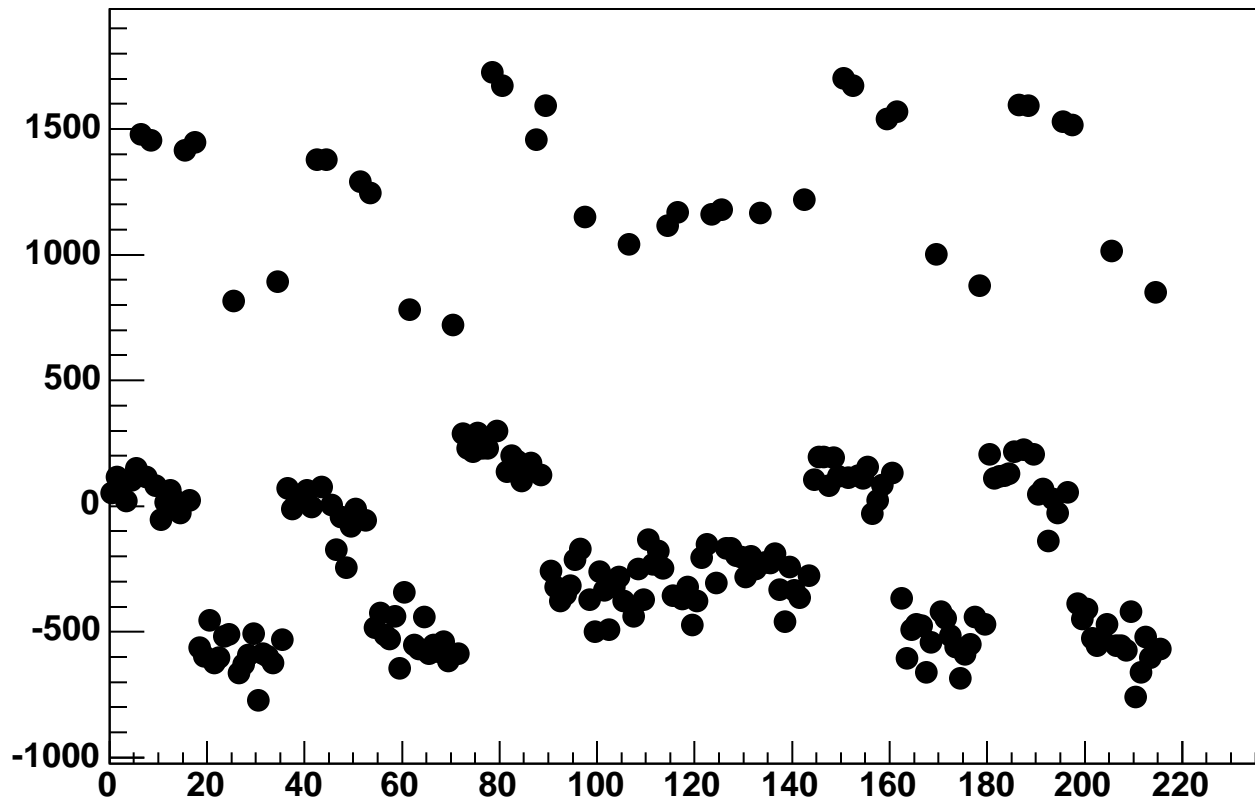
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18\*Chip+Chan



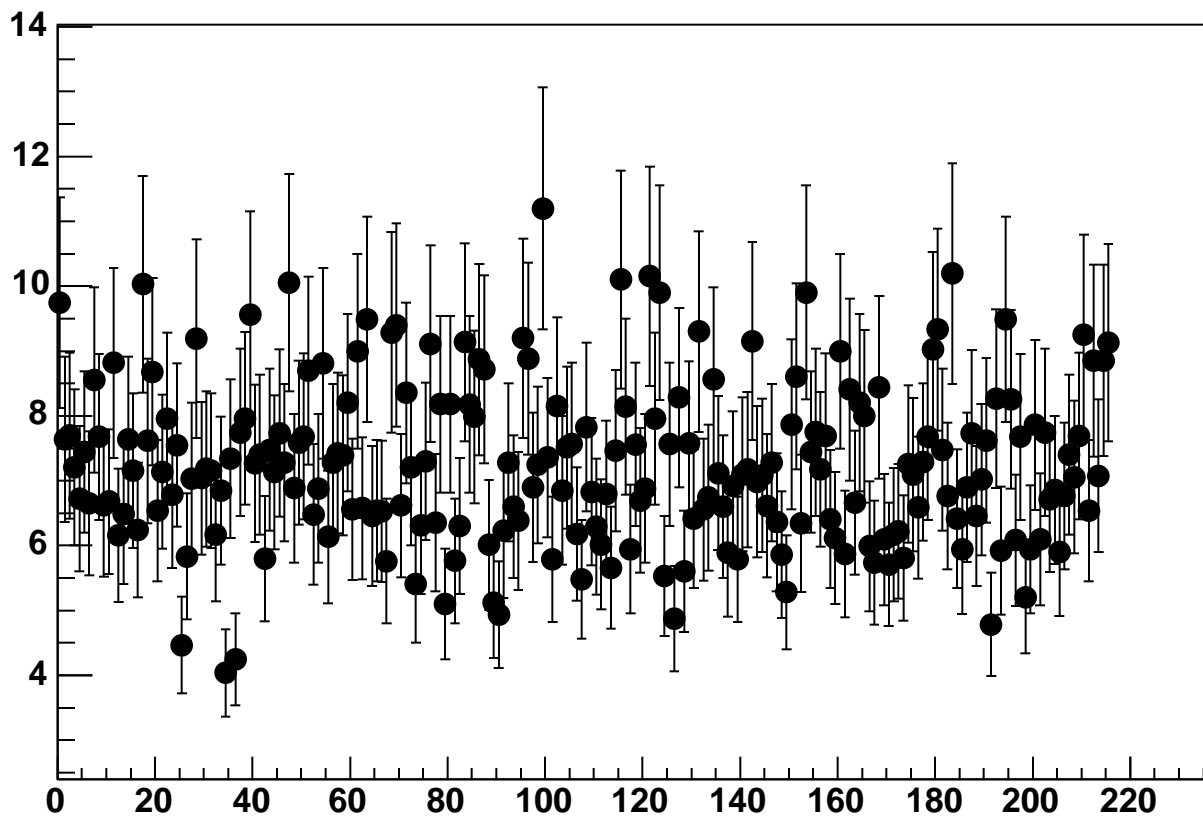
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18\*Chip+Chan



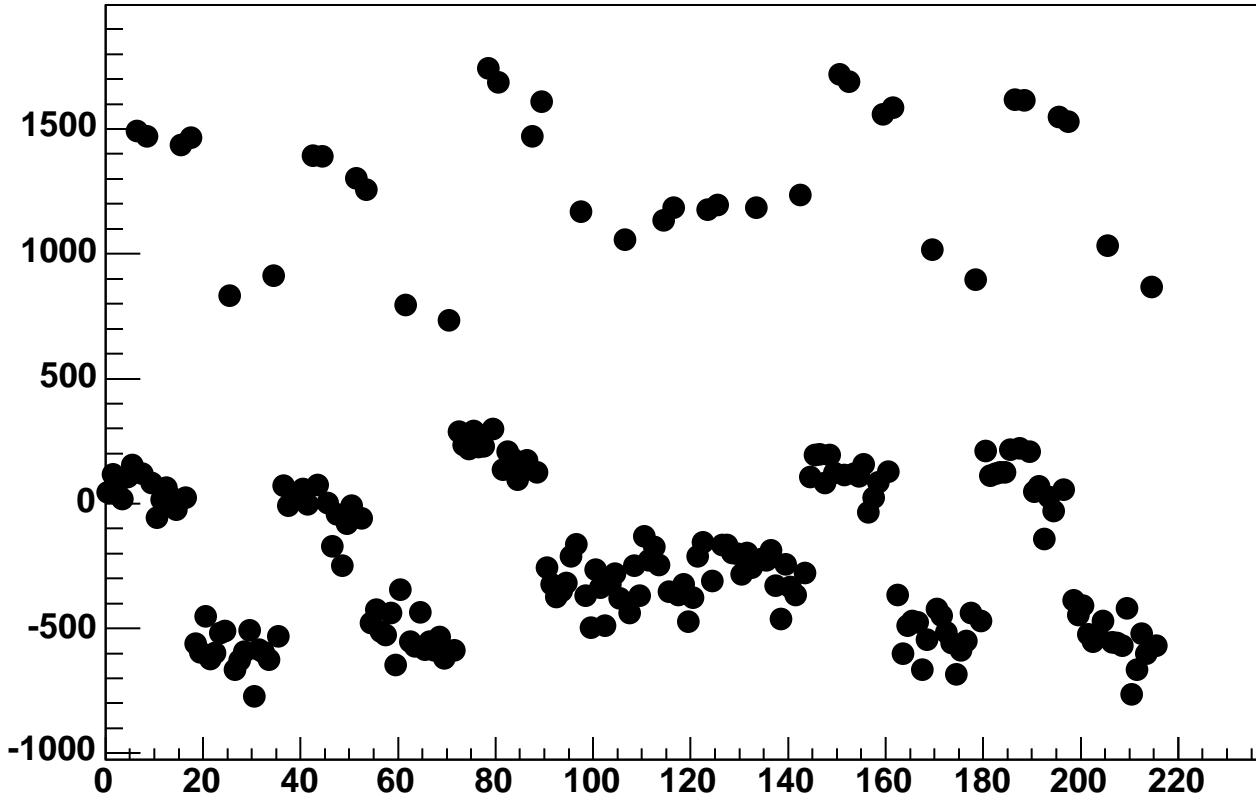
Enable 2, Hold=35, DAC=0, ADC Mean vs 18\*Chip+Chan



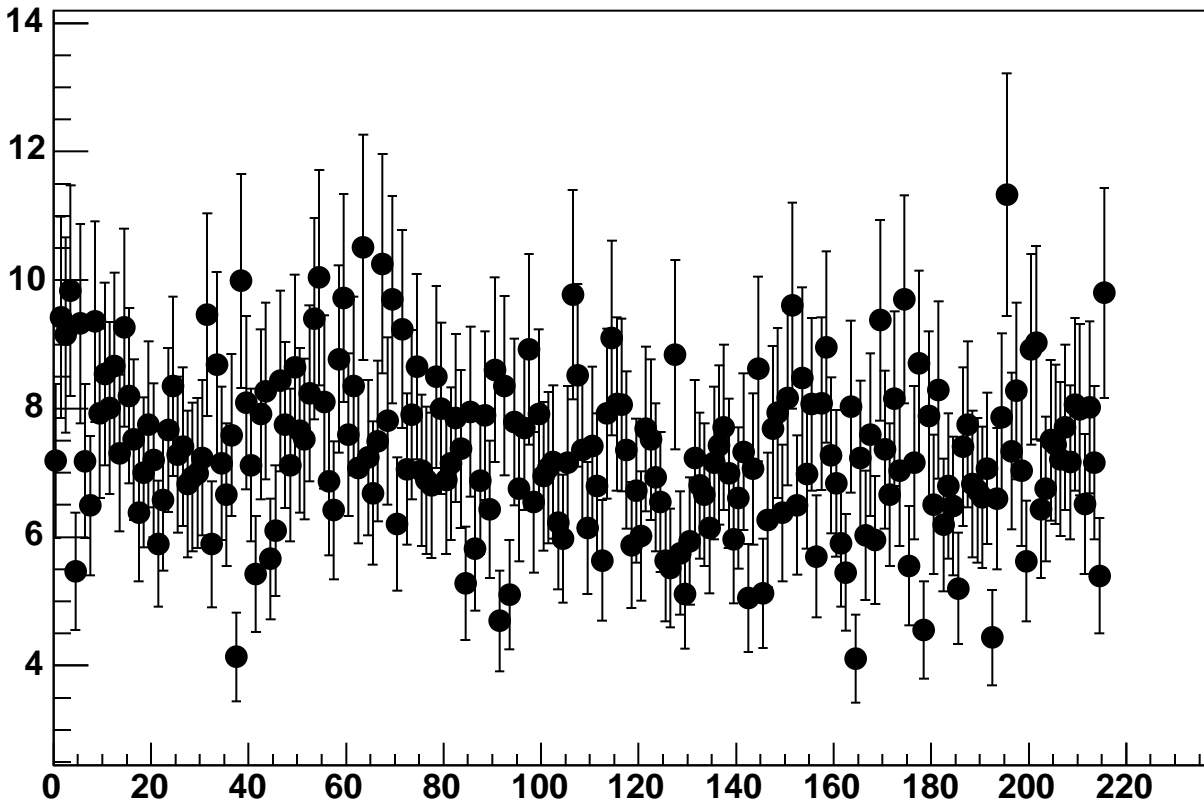
Enable 2, Hold=35, DAC=0, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=35, DAC=400, ADC Mean vs 18\*Chip+Chan

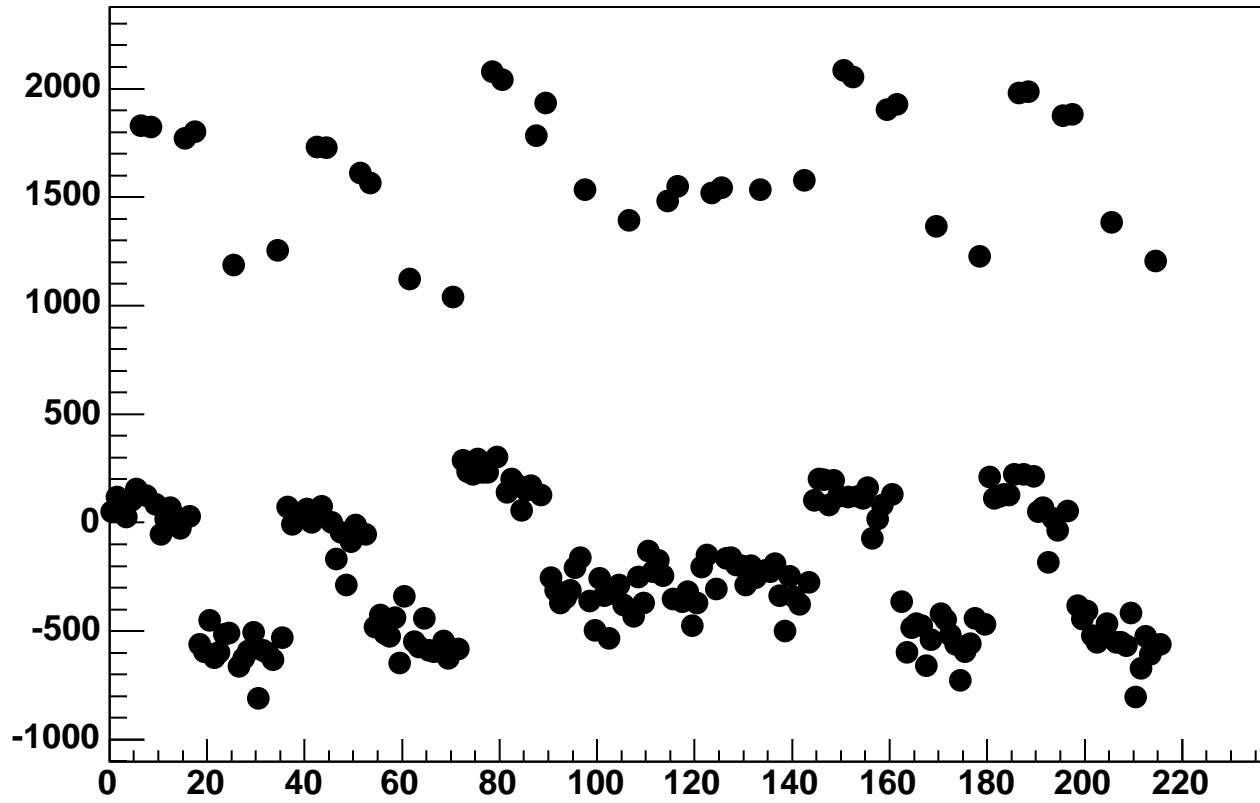


Enable 2, Hold=35, DAC=400, ADC Noise vs 18\*Chip+Chan

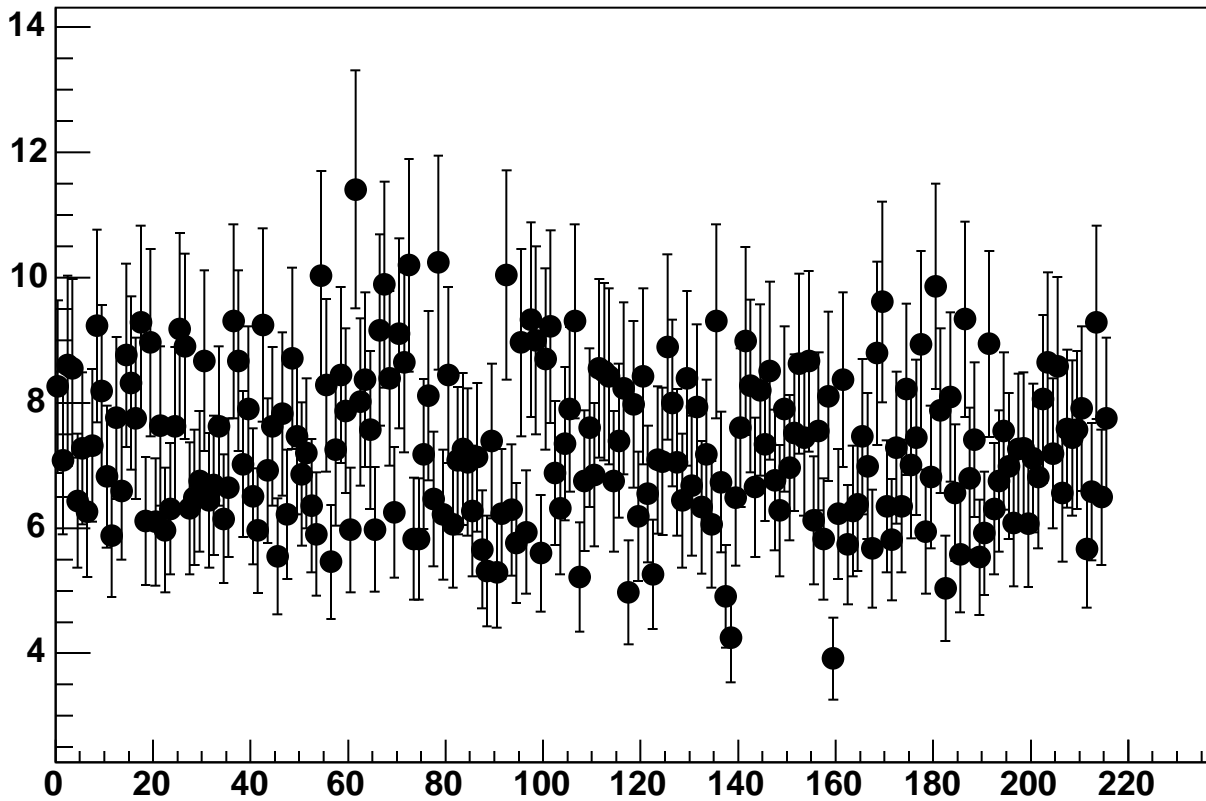




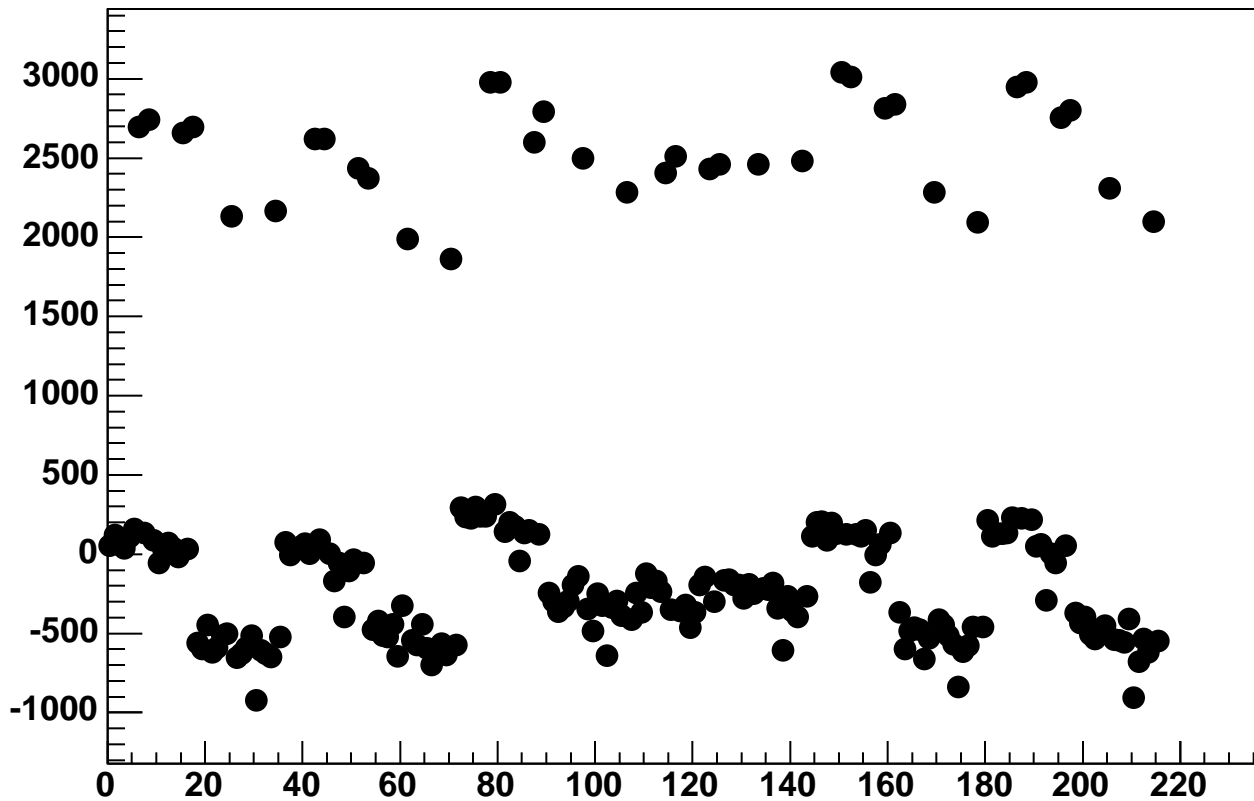
Enable 2, Hold=35, DAC=800, ADC Mean vs 18\*Chip+Chan



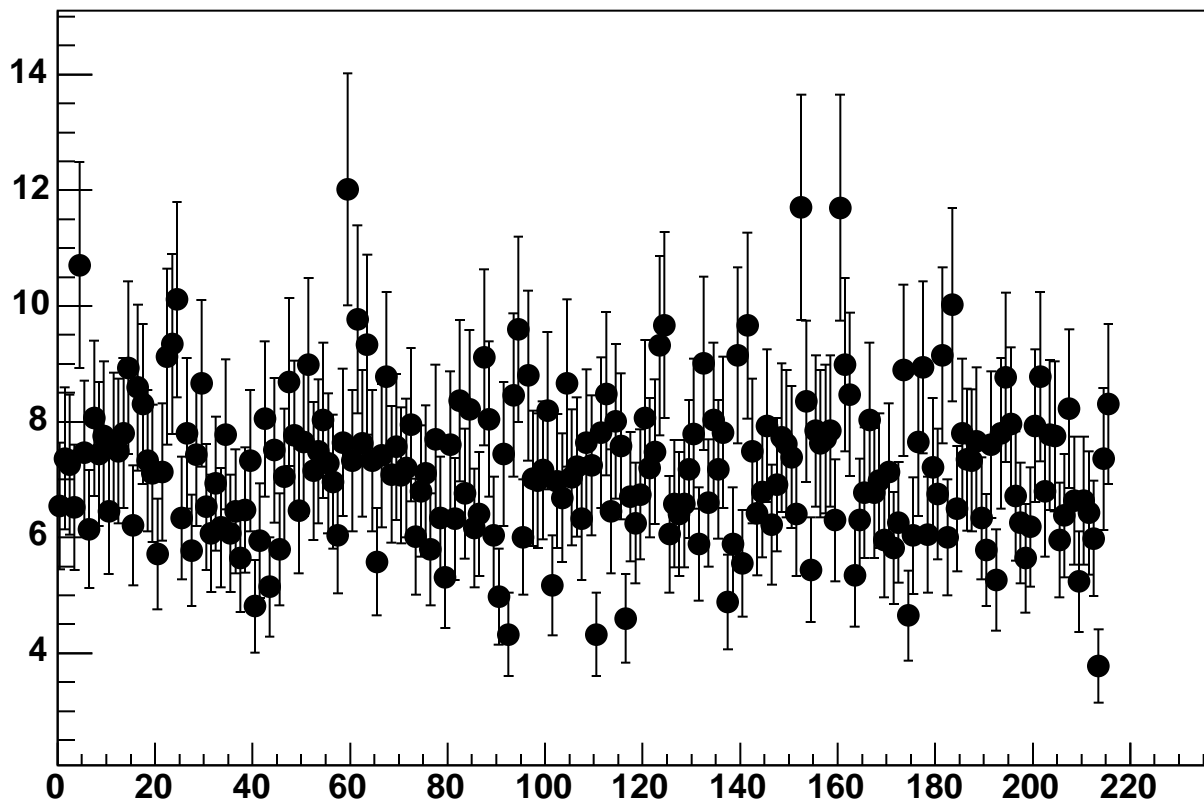
Enable 2, Hold=35, DAC=800, ADC Noise vs 18\*Chip+Chan



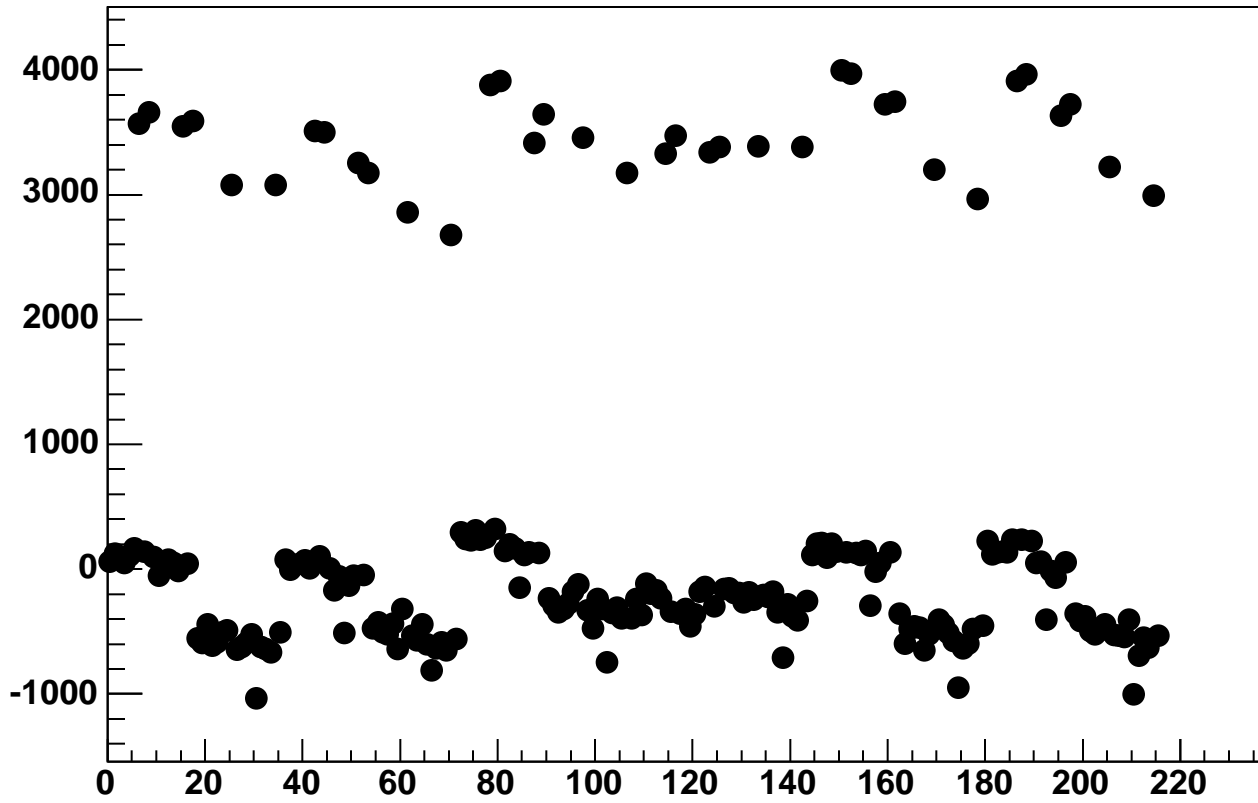
Enable 2, Hold=35, DAC=1200, ADC Mean vs 18\*Chip+Chan



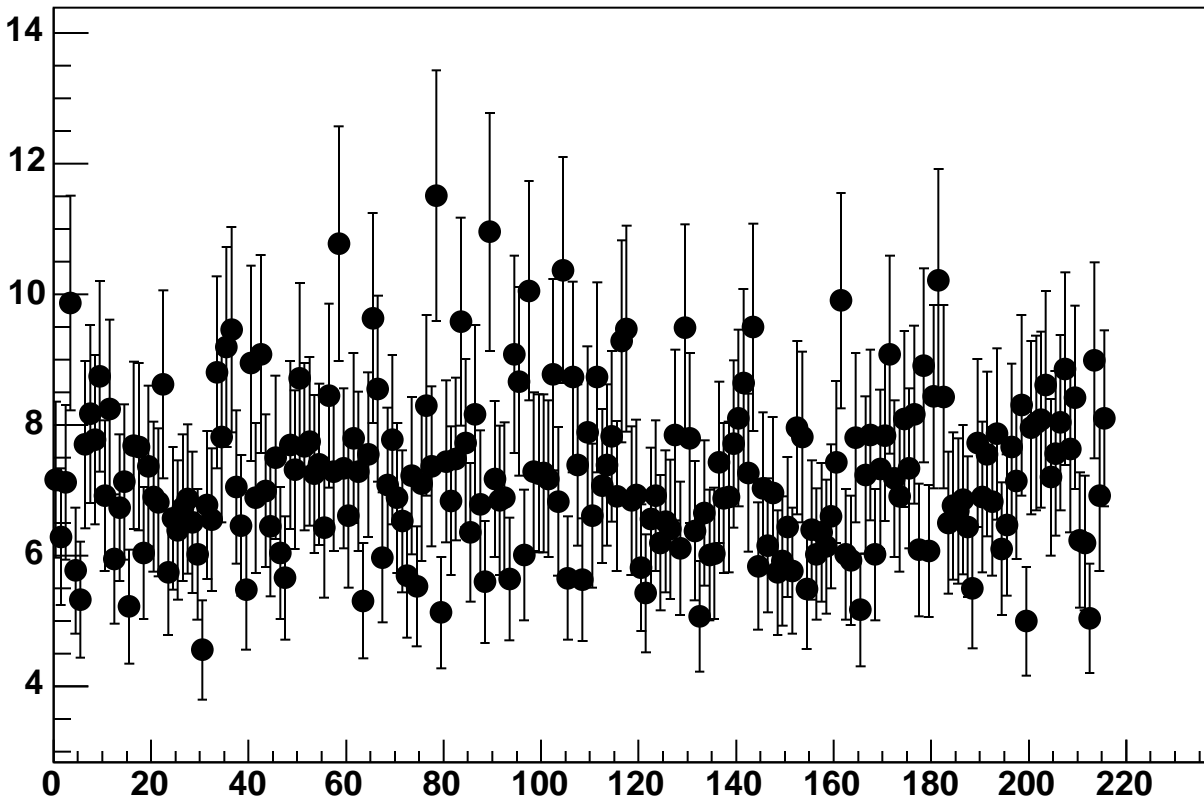
Enable 2, Hold=35, DAC=1200, ADC Noise vs 18\*Chip+Chan



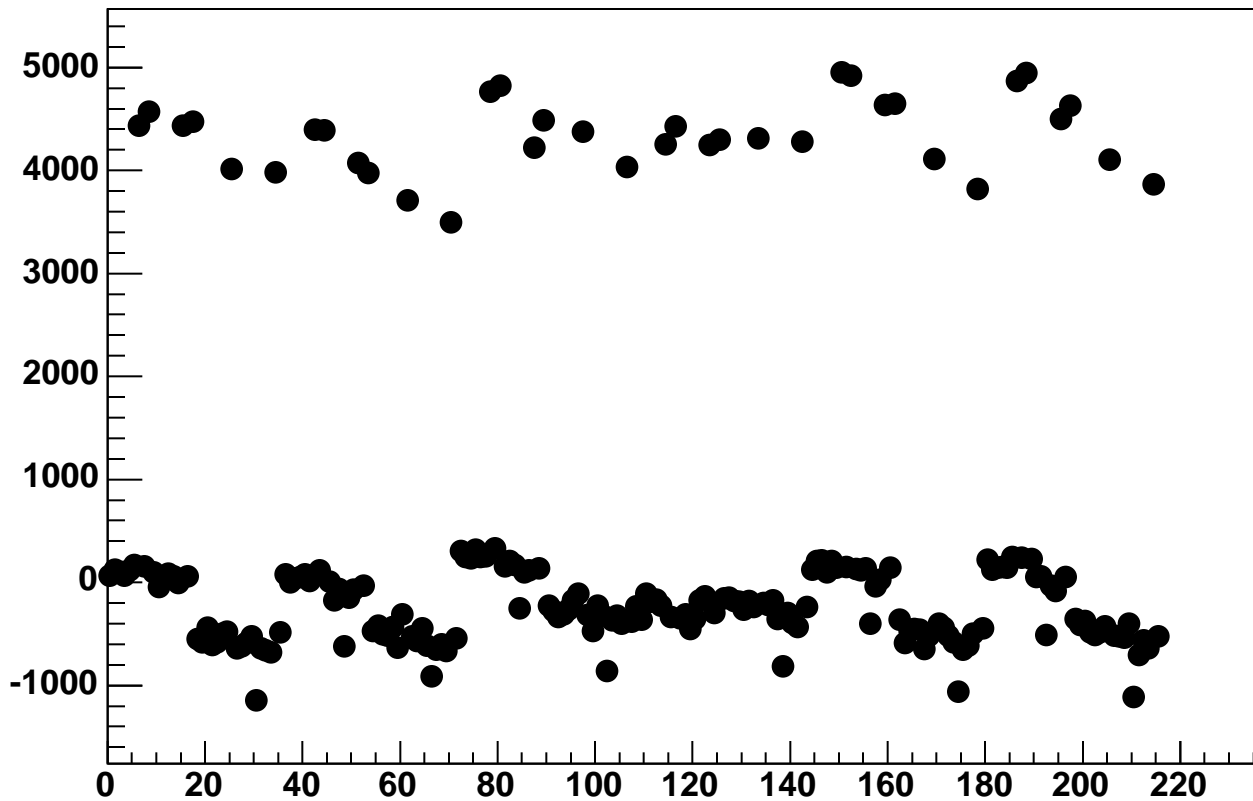
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18\*Chip+Chan



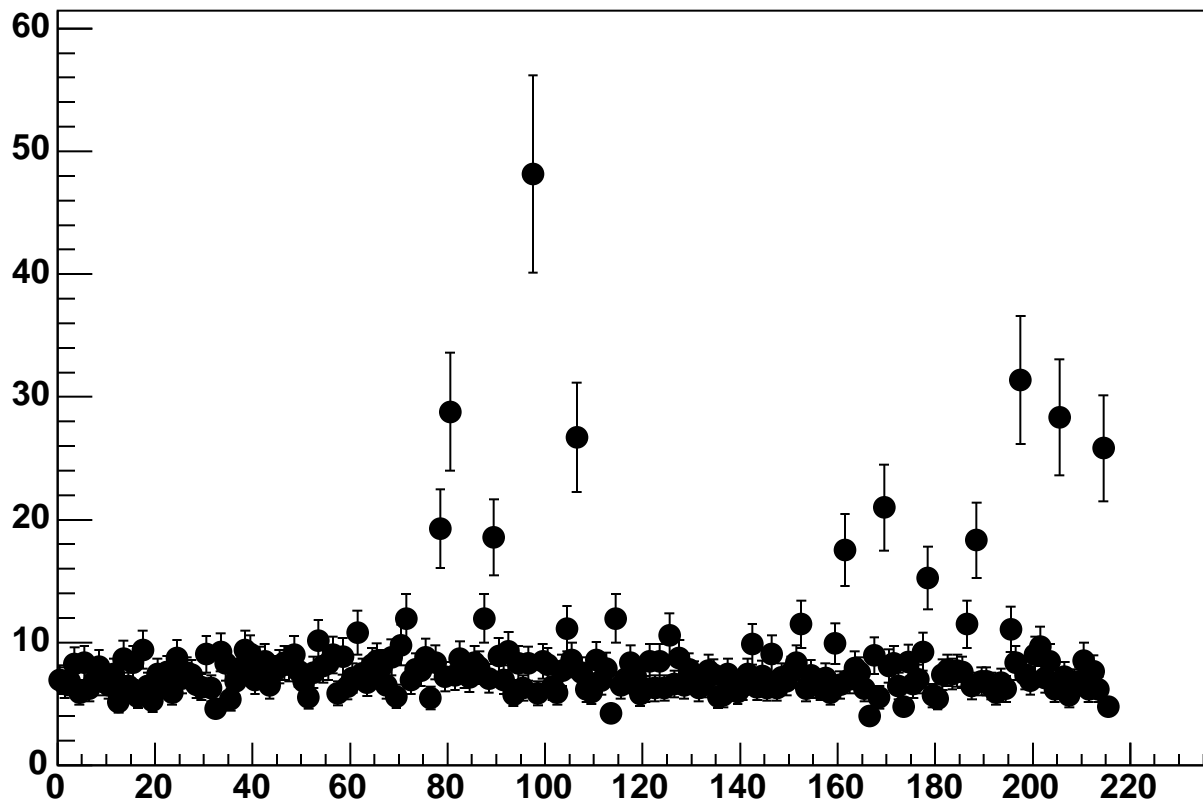
Enable 2, Hold=35, DAC=1600, ADC Noise vs 18\*Chip+Chan



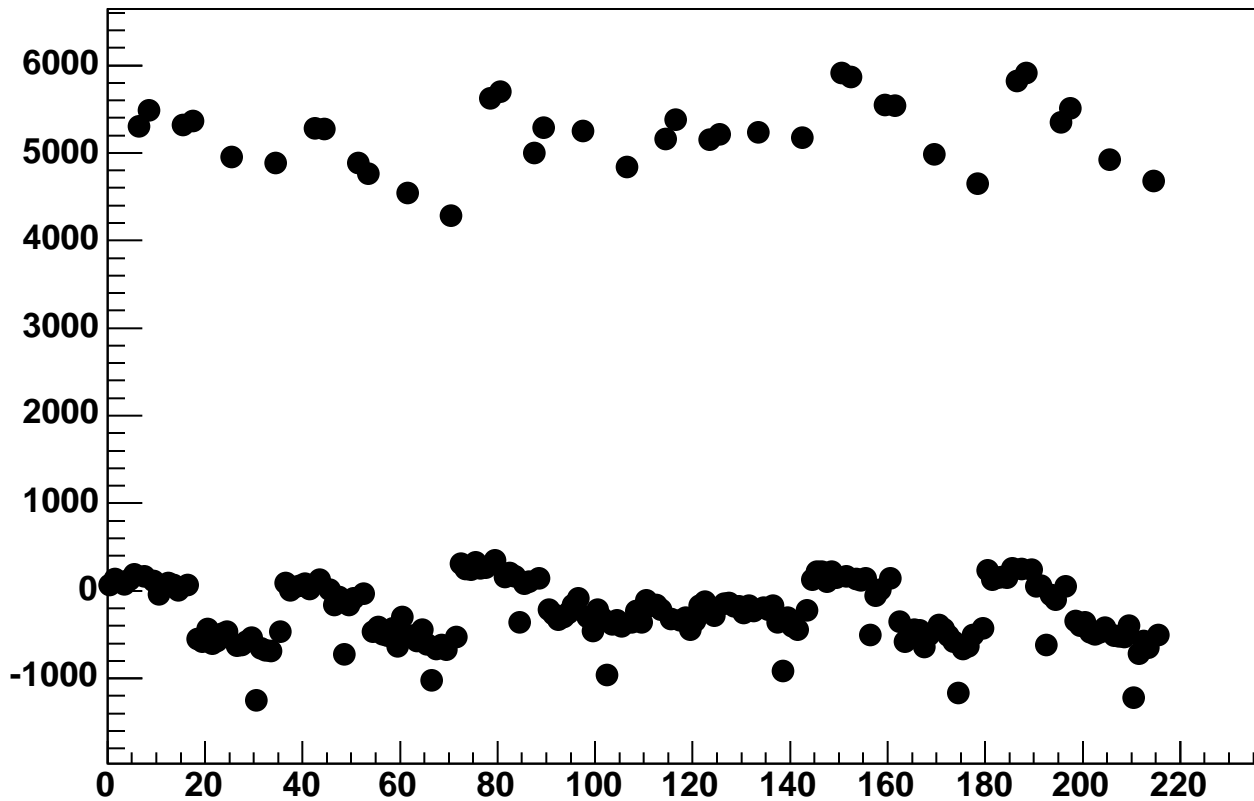
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18\*Chip+Chan



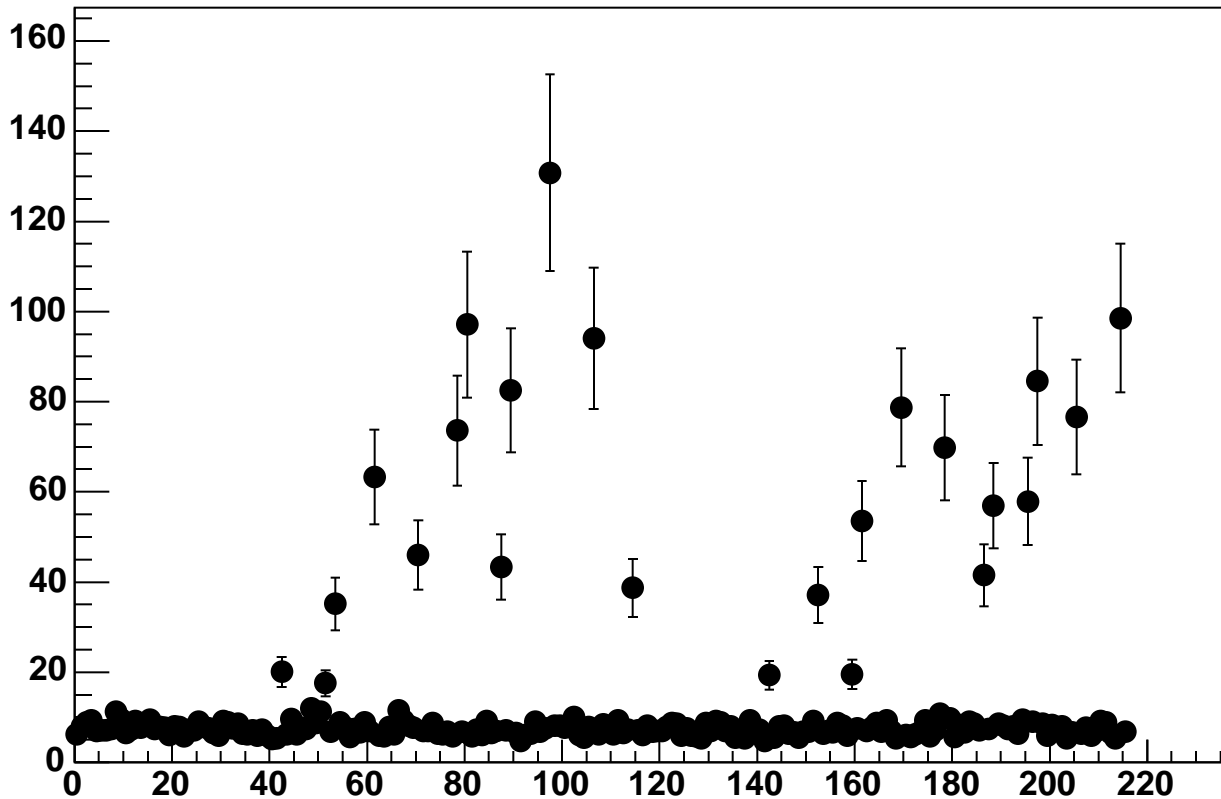
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18\*Chip+Chan



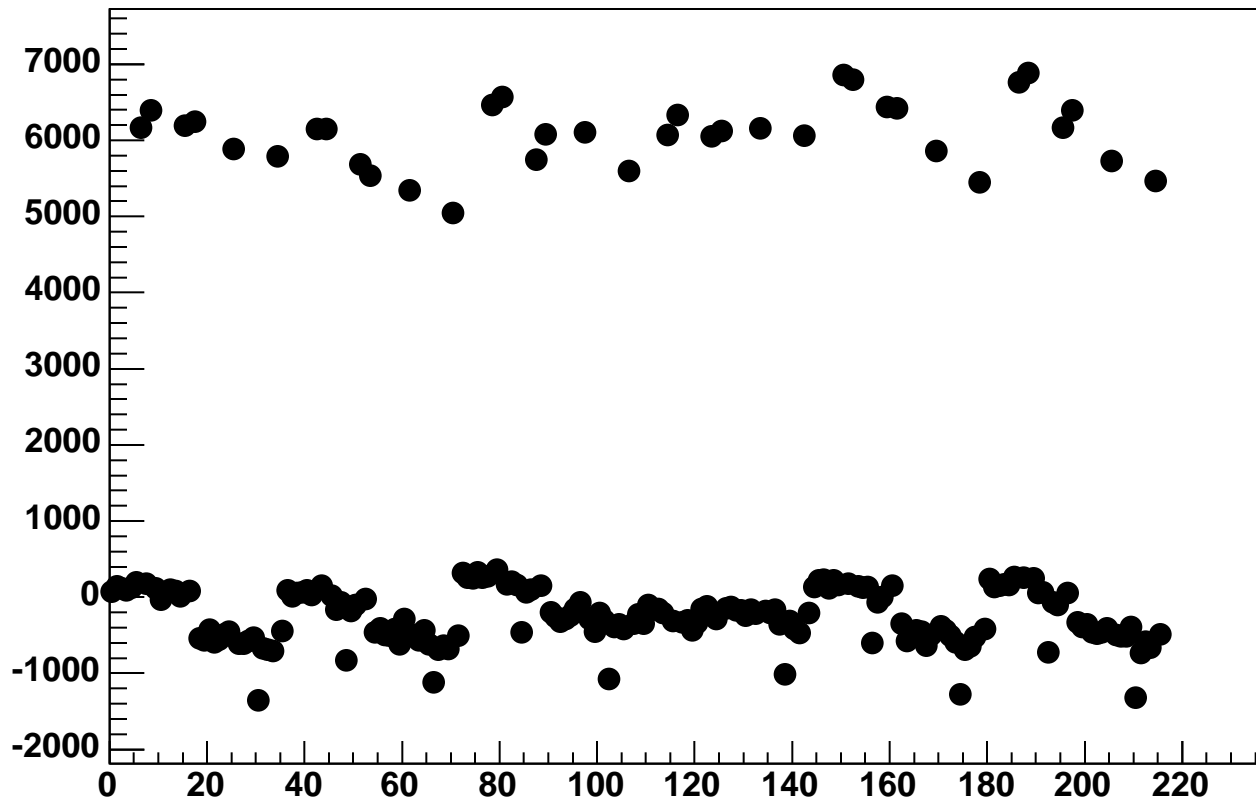
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18\*Chip+Chan



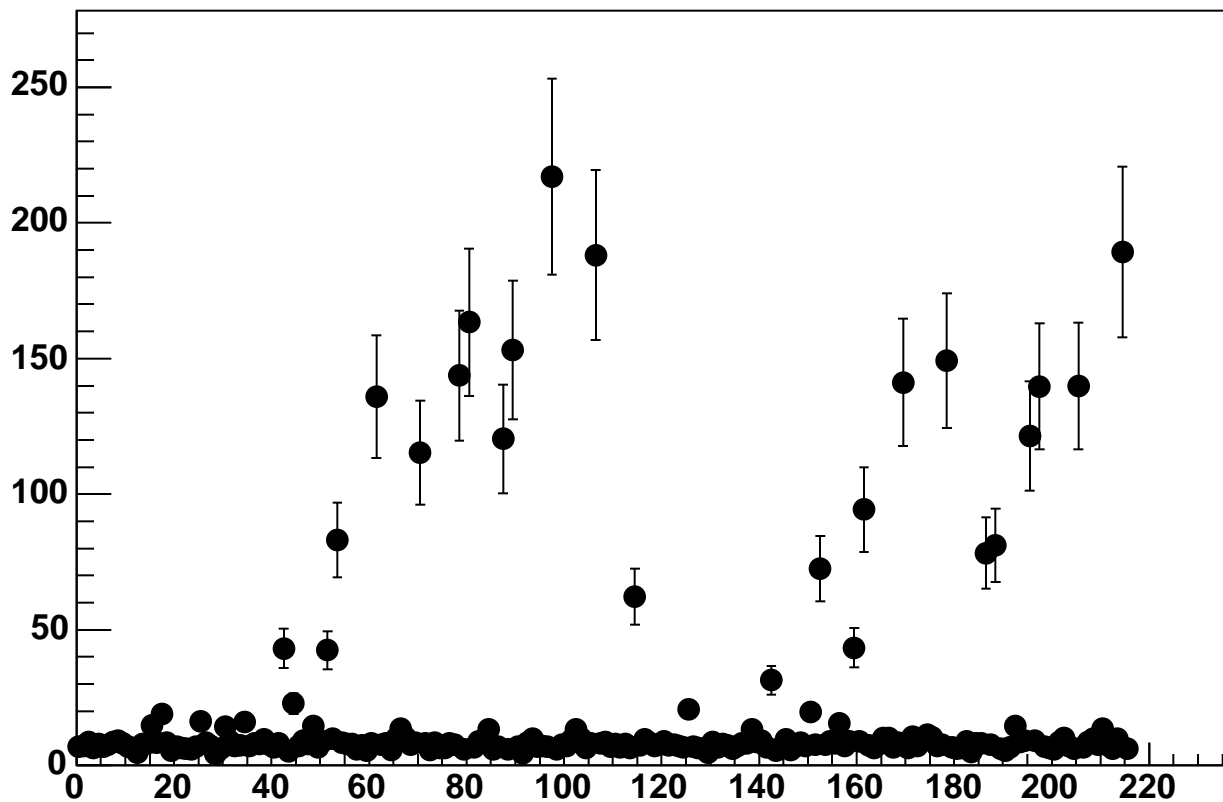
Enable 2, Hold=35, DAC=2400, ADC Noise vs 18\*Chip+Chan



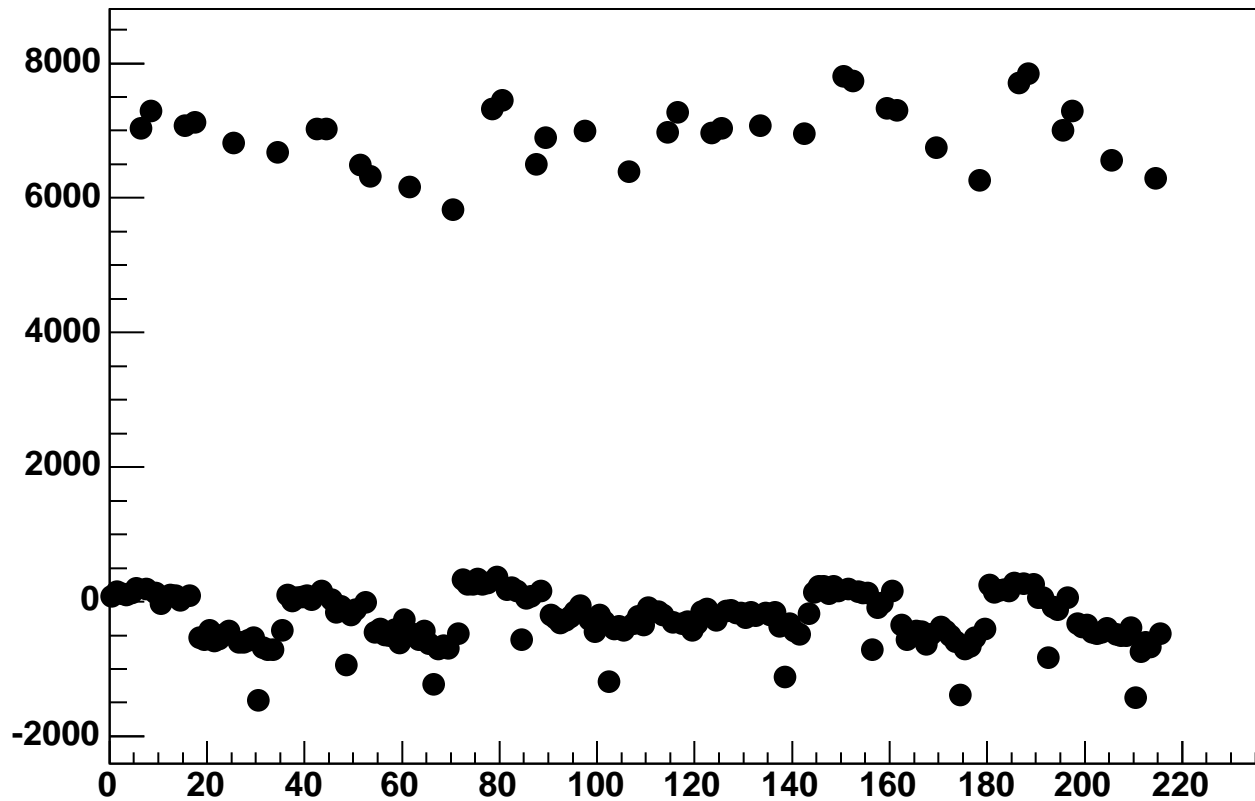
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18\*Chip+Chan



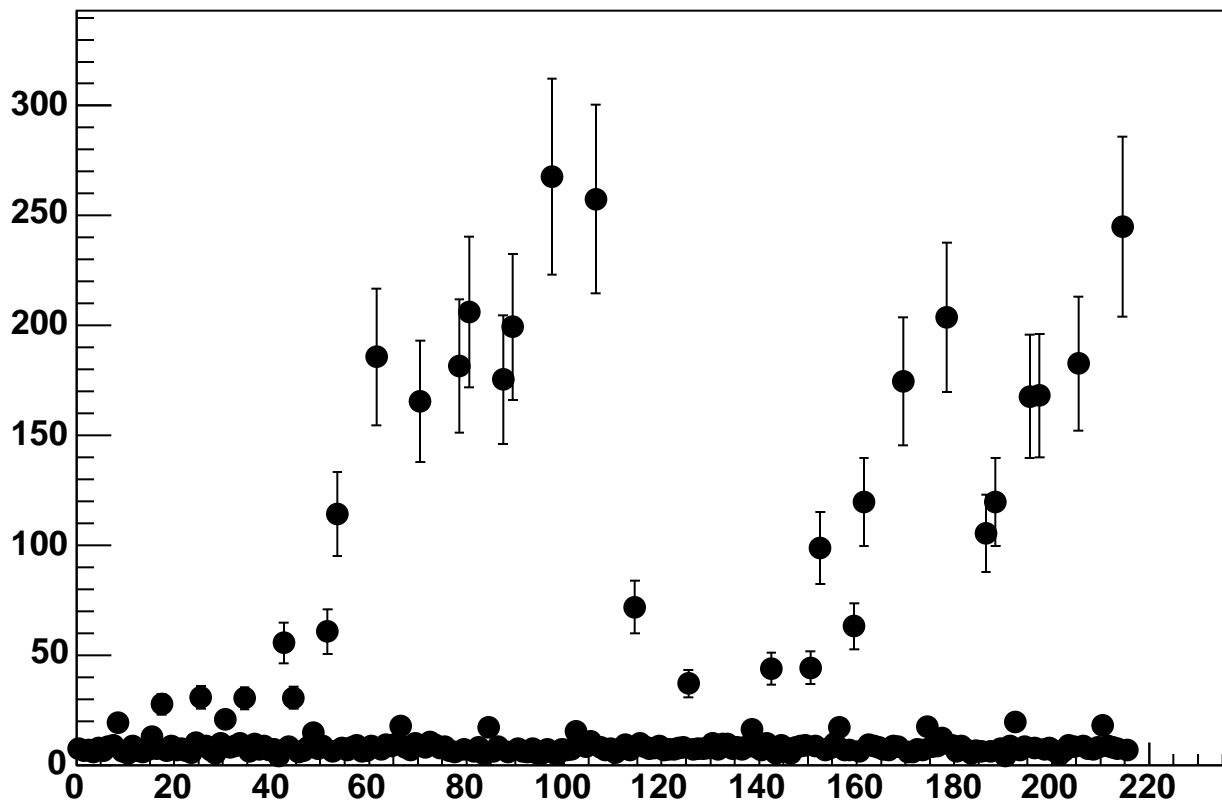
Enable 2, Hold=35, DAC=2800, ADC Noise vs 18\*Chip+Chan



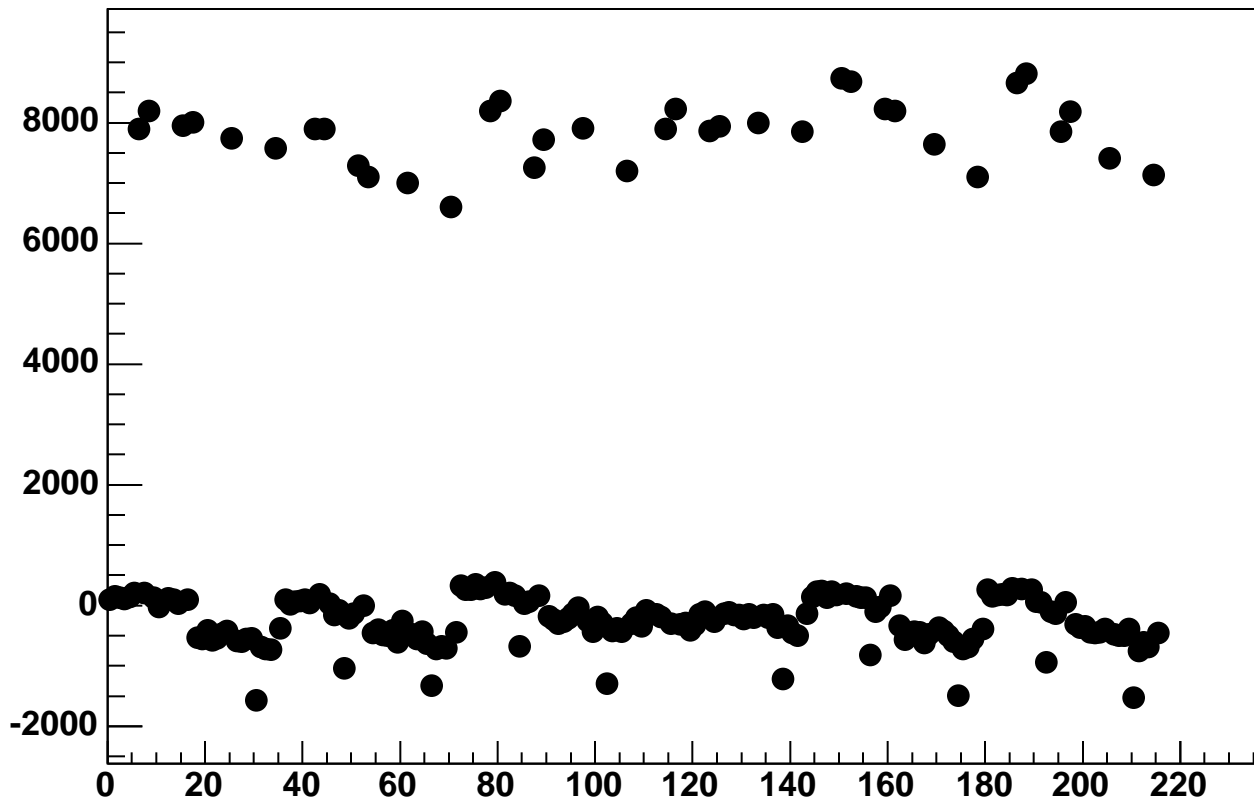
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18\*Chip+Chan



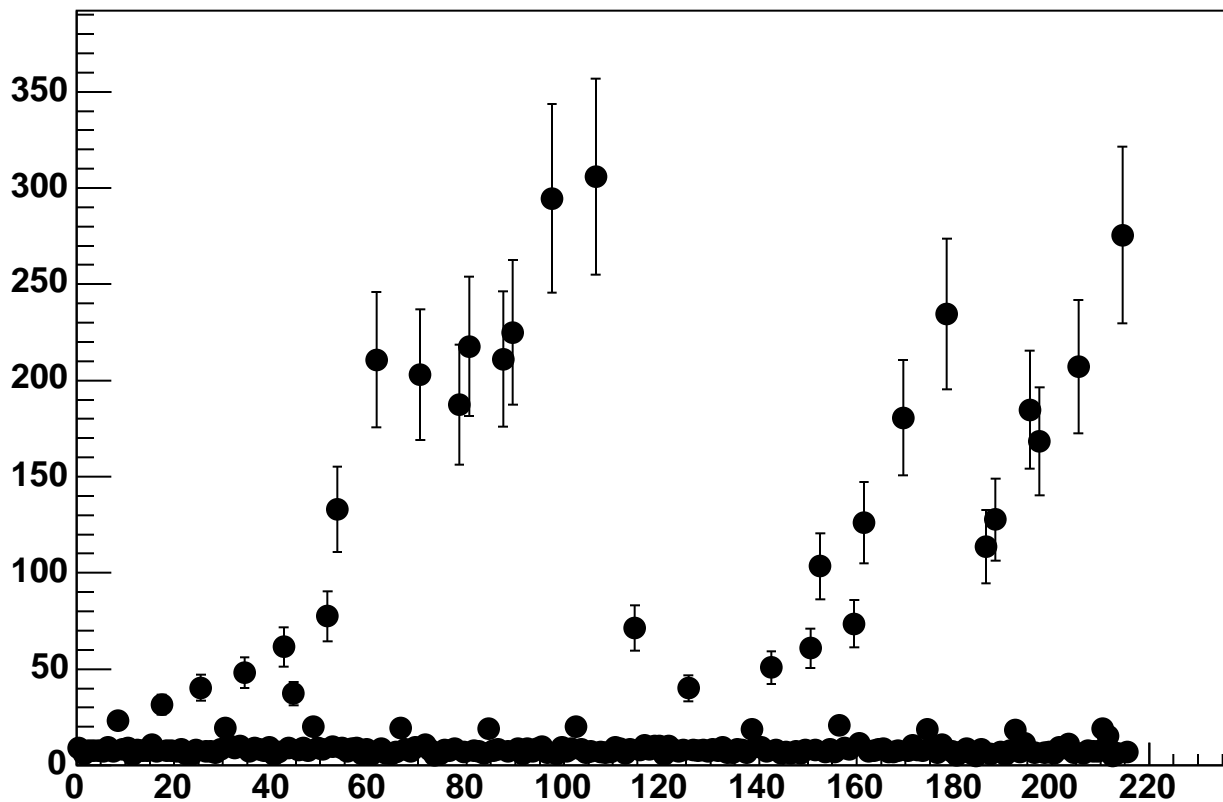
Enable 2, Hold=35, DAC=3200, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=35, DAC=3600, ADC Mean vs 18\*Chip+Chan

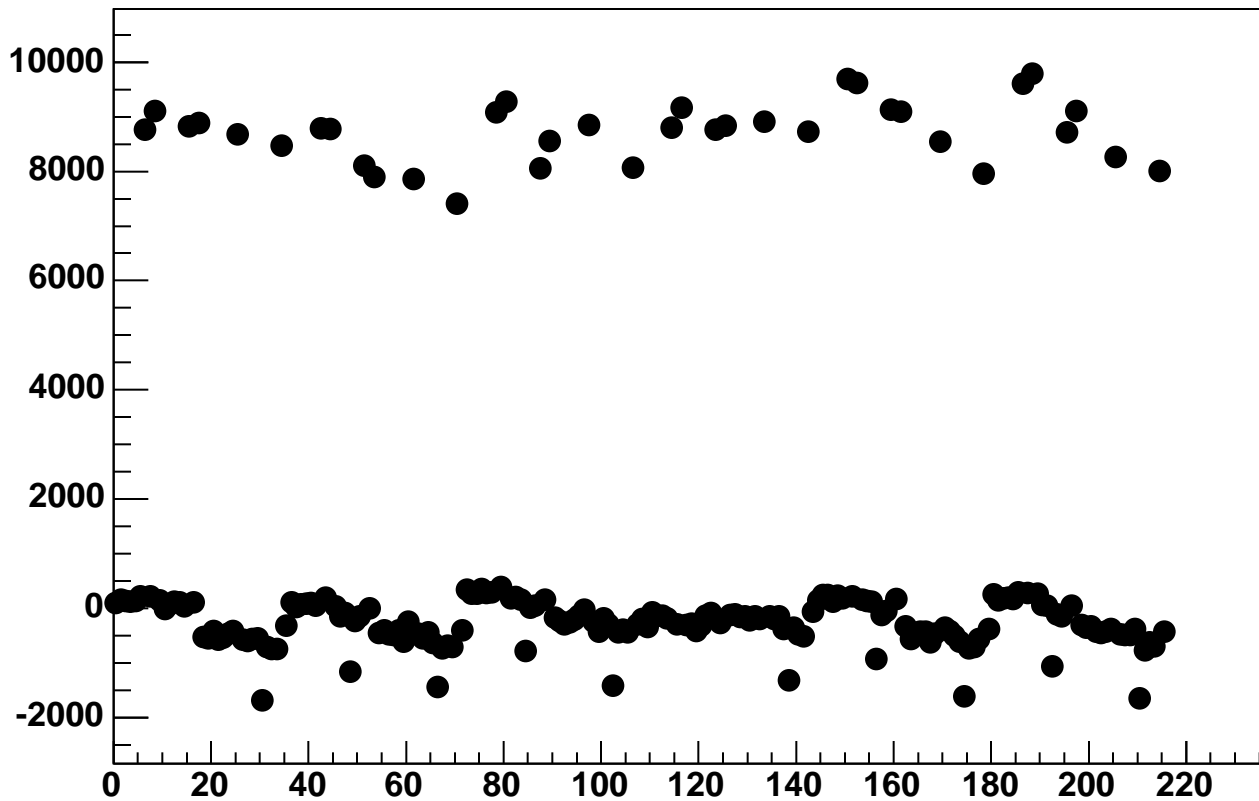


Enable 2, Hold=35, DAC=3600, ADC Noise vs 18\*Chip+Chan

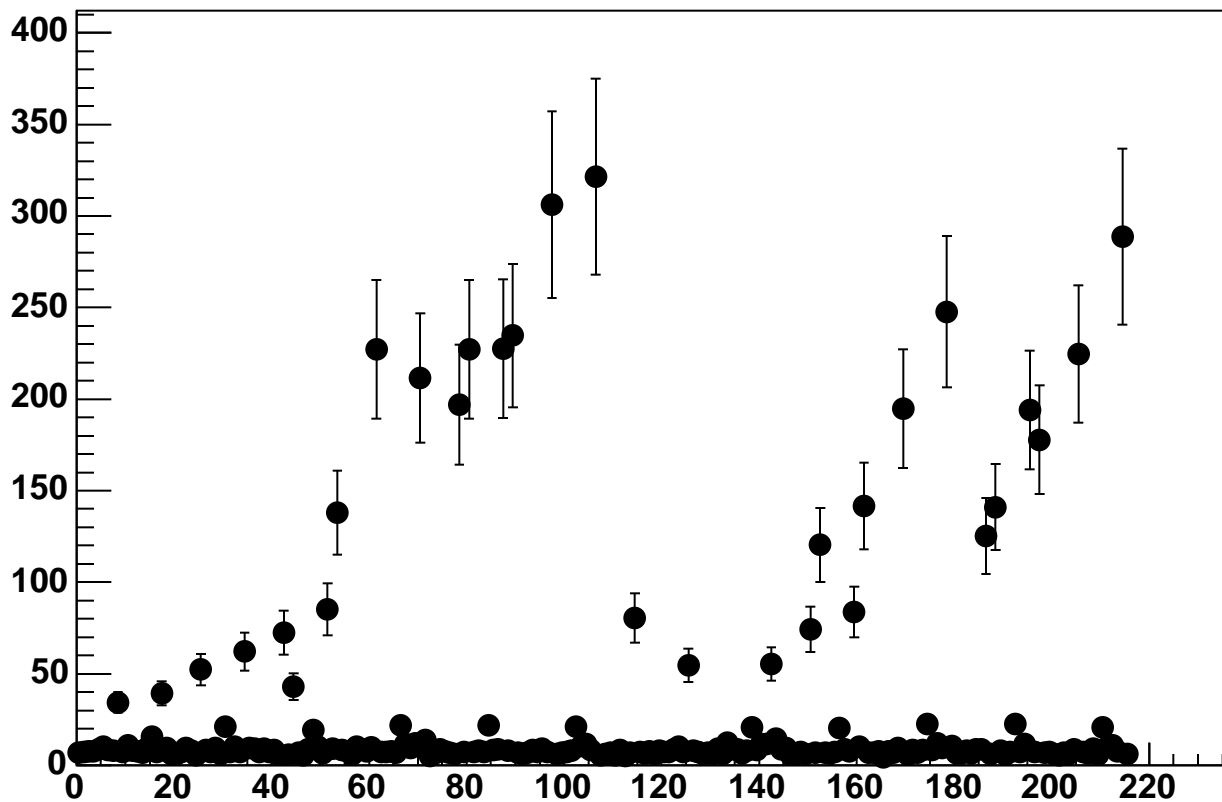




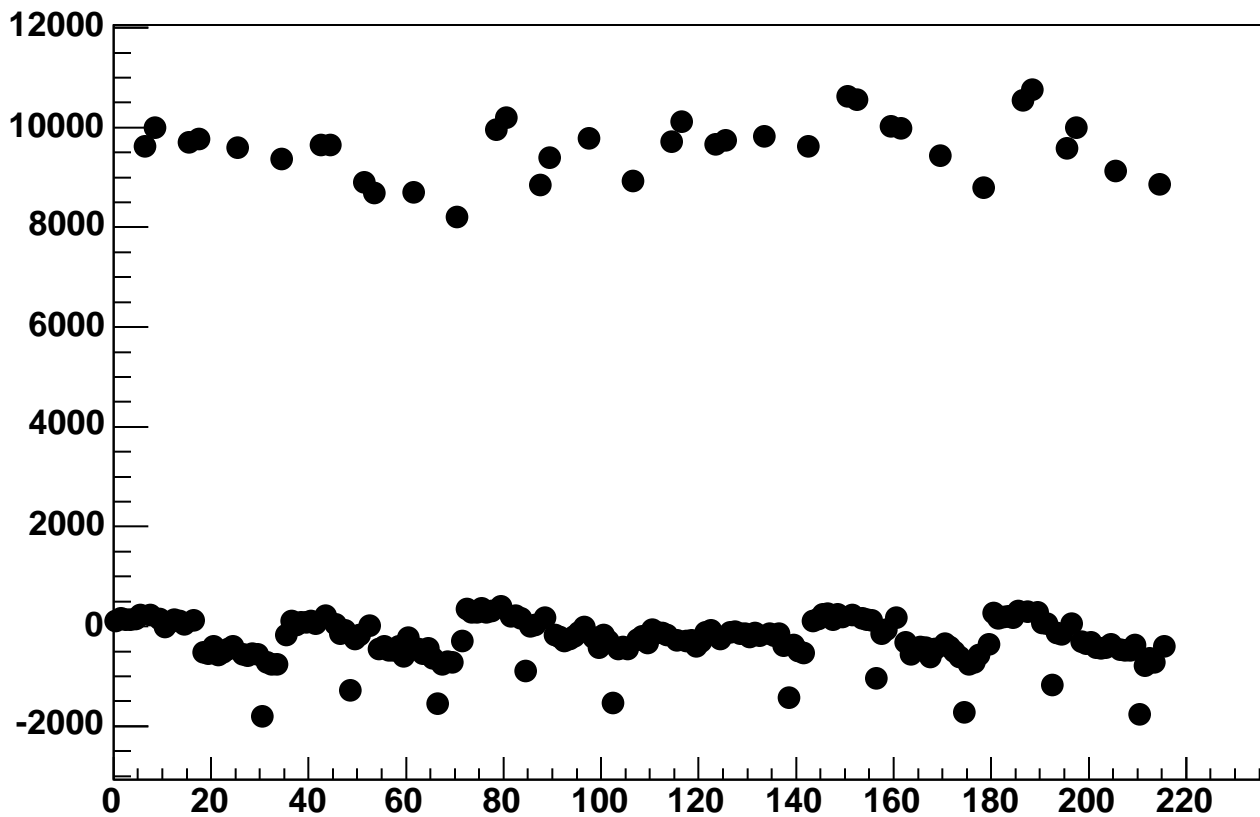
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18\*Chip+Chan



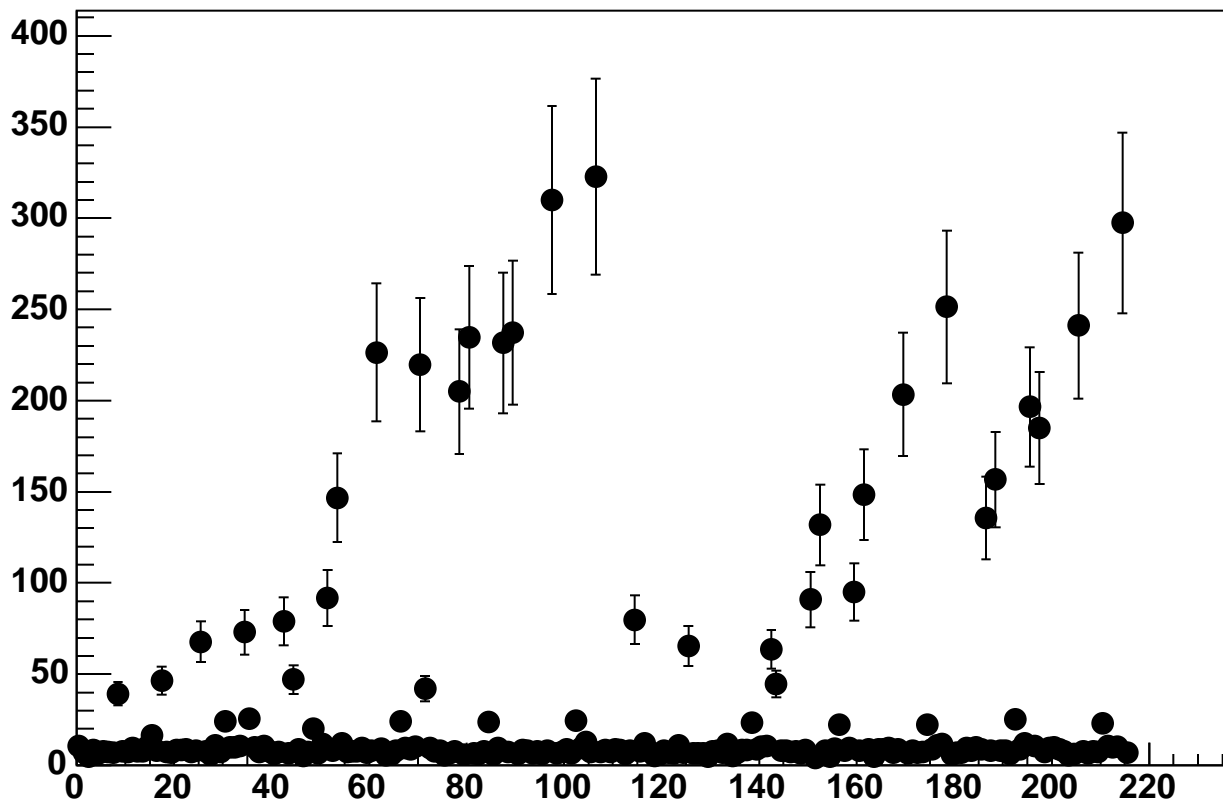
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18\*Chip+Chan



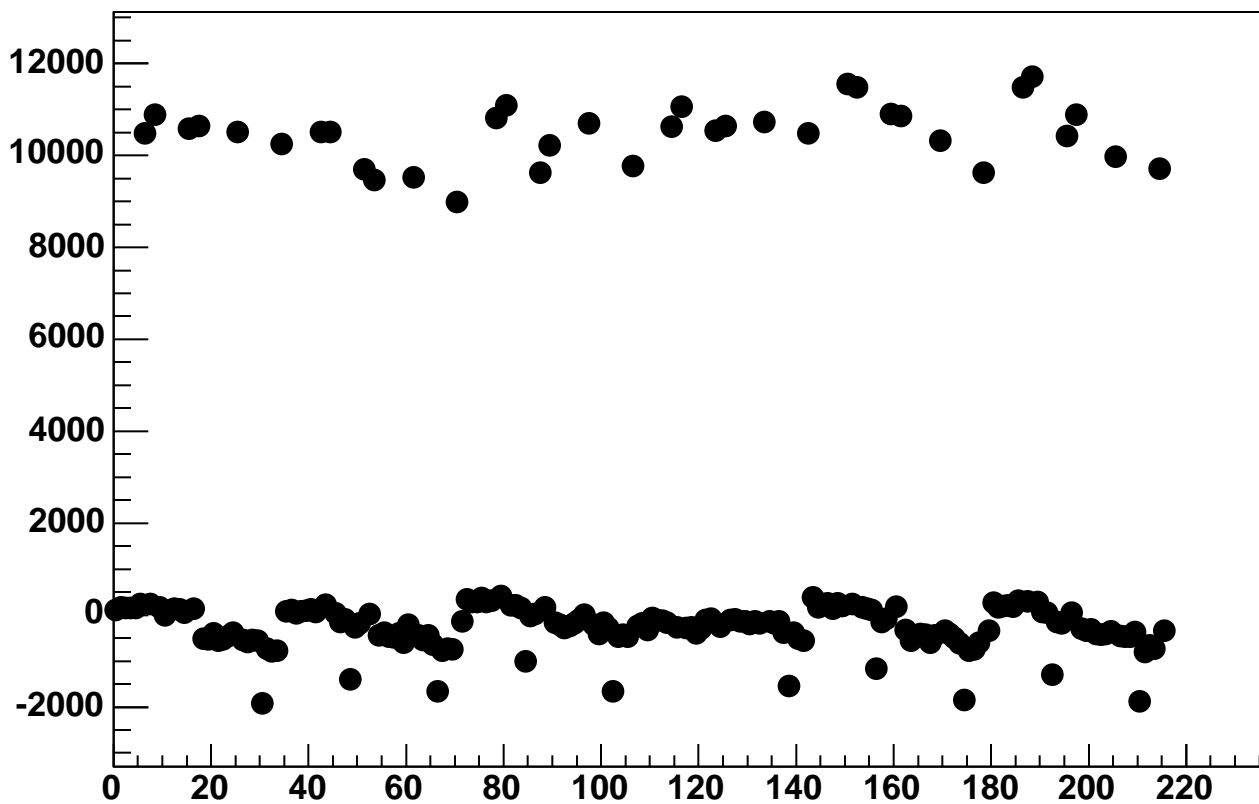
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18\*Chip+Chan



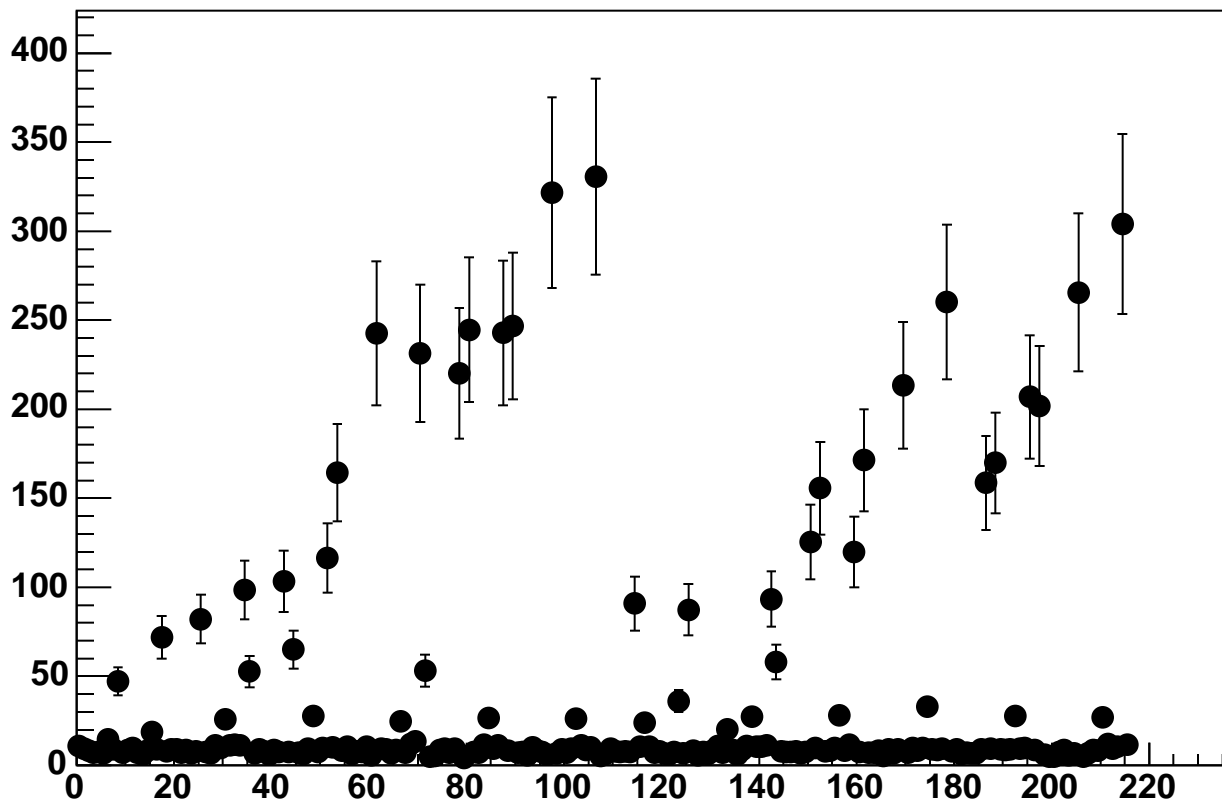
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18\*Chip+Chan



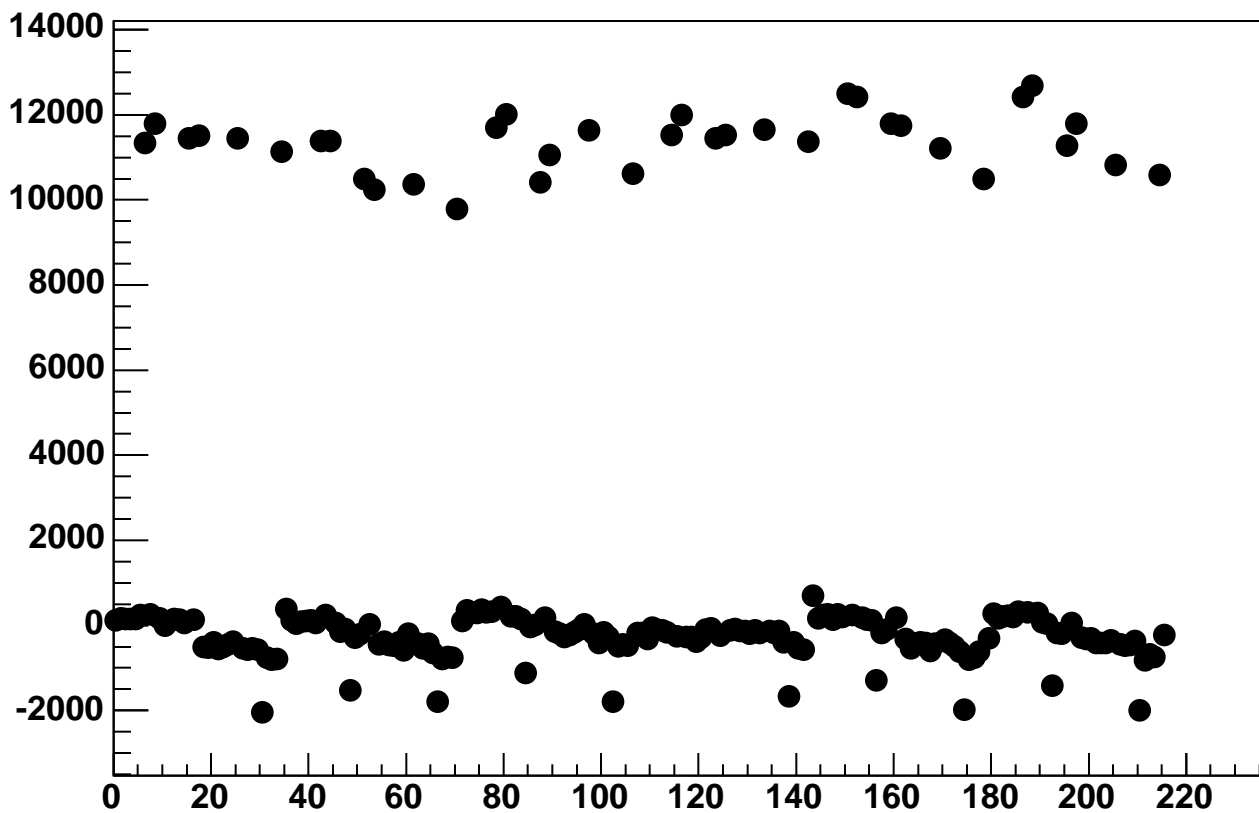
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18\*Chip+Chan



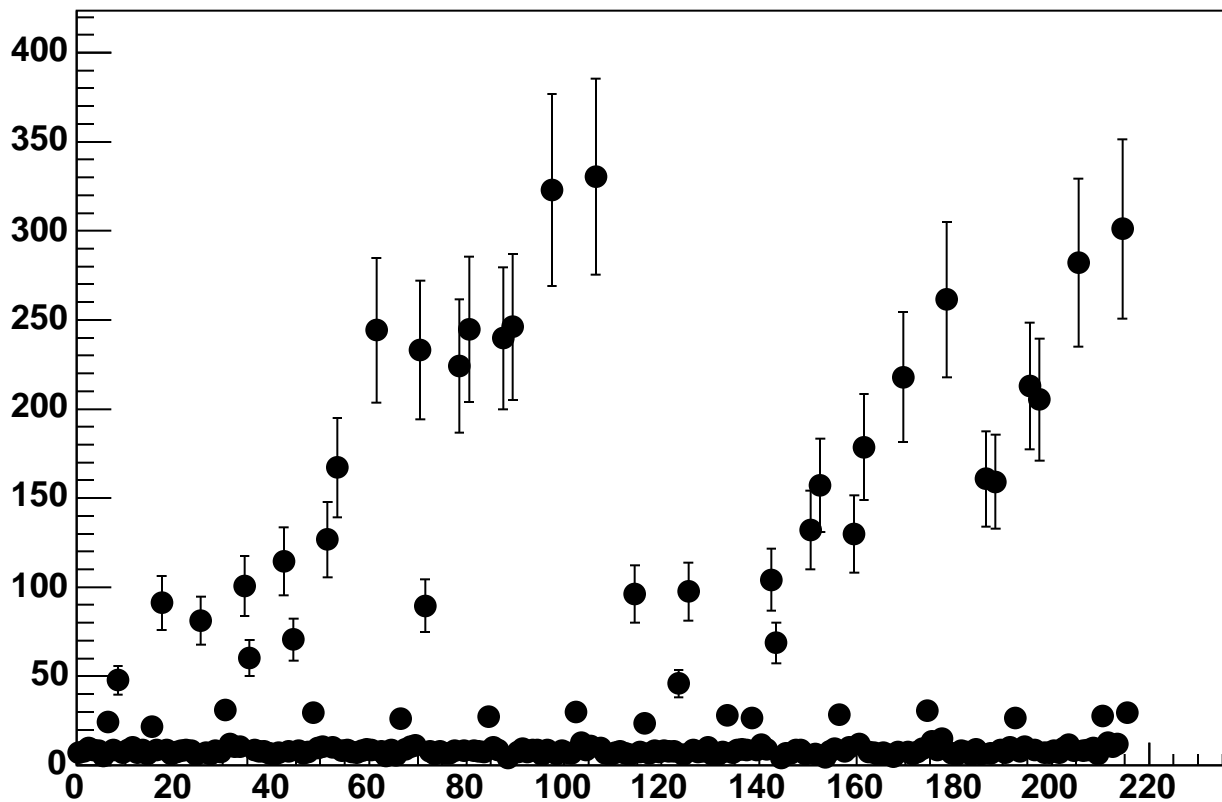
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18\*Chip+Chan



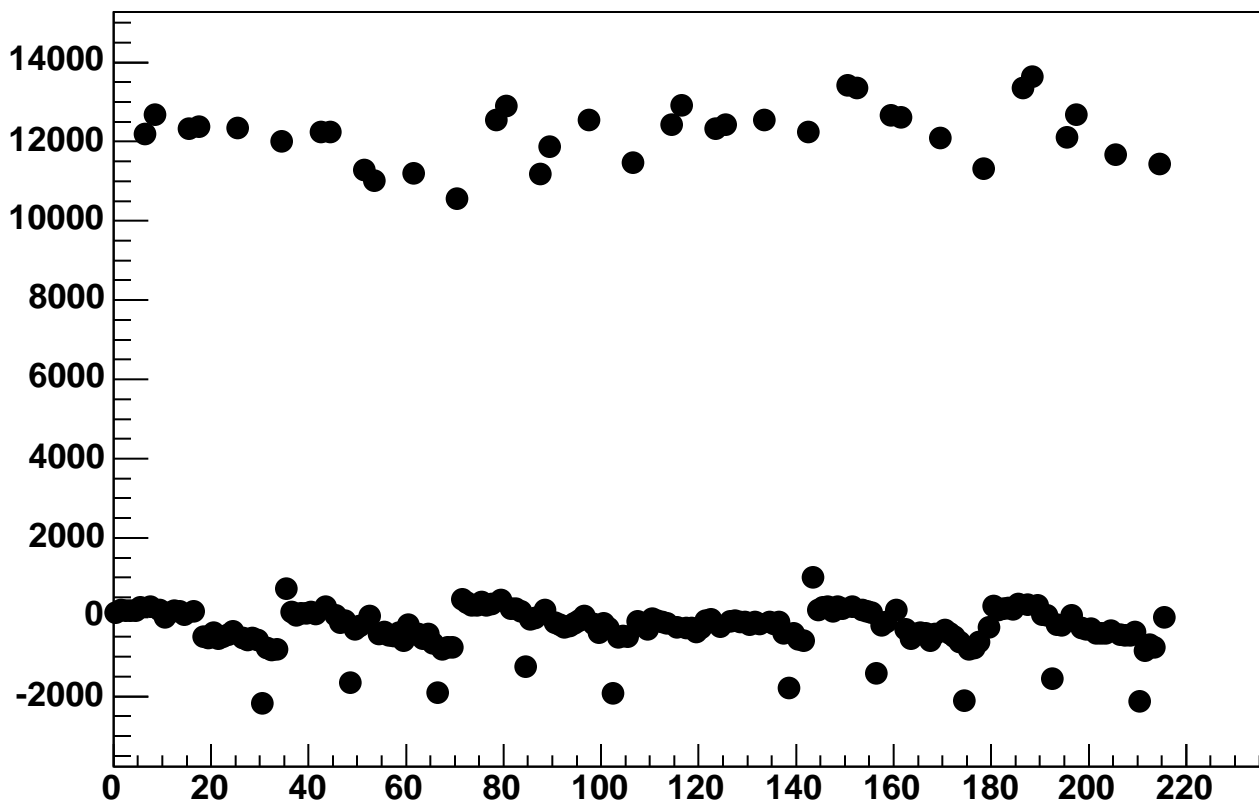
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18\*Chip+Chan



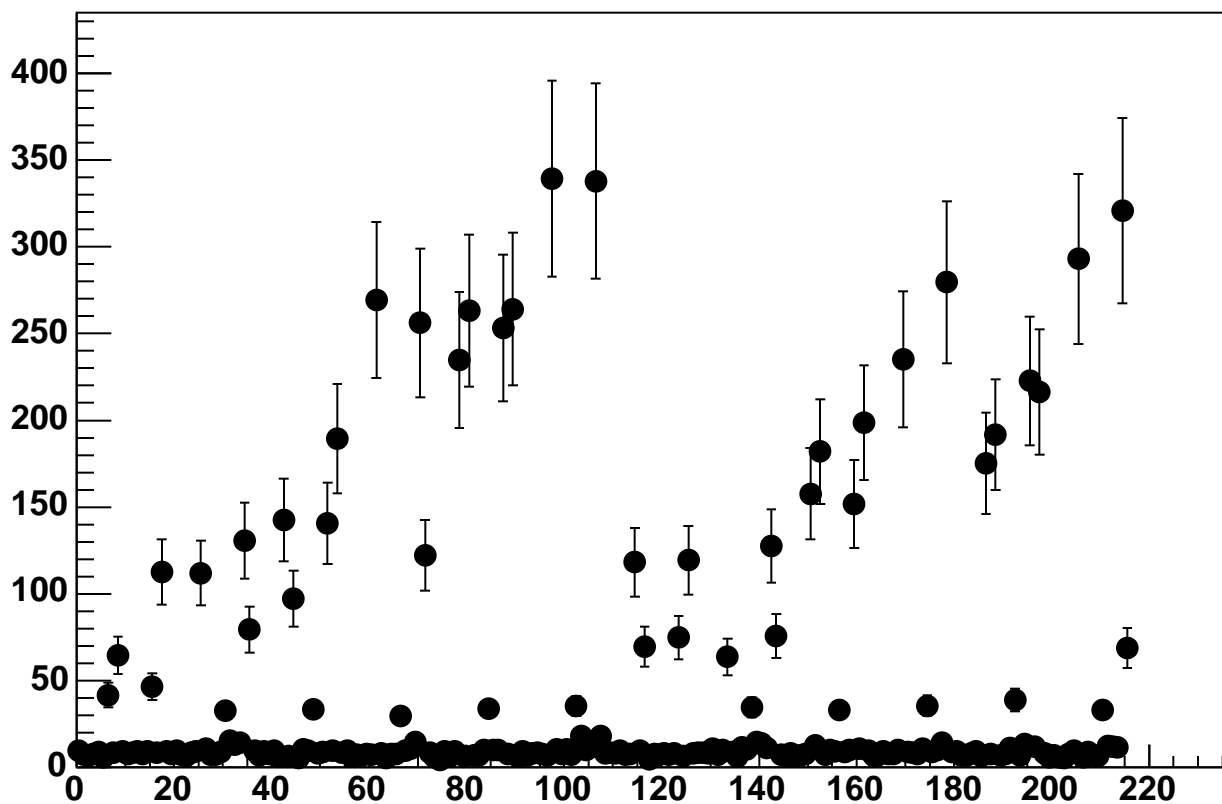
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18\*Chip+Chan



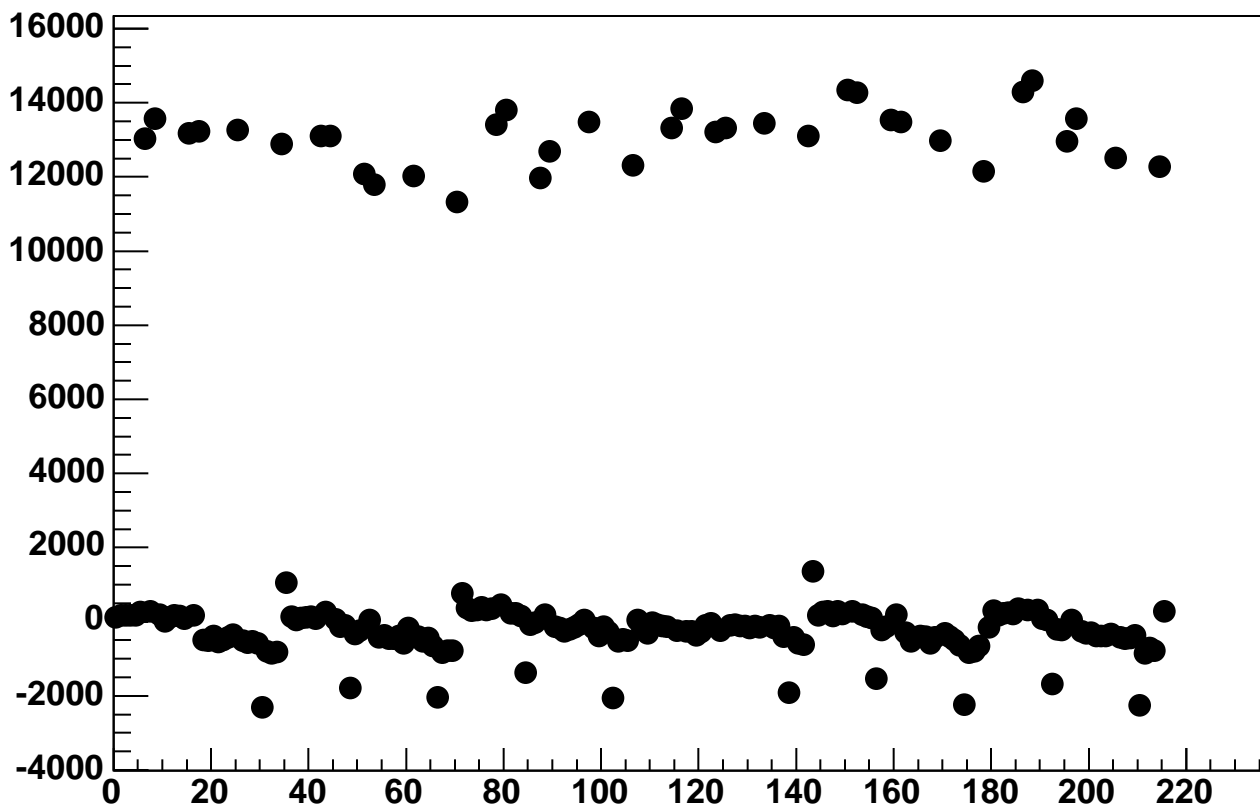
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18\*Chip+Chan



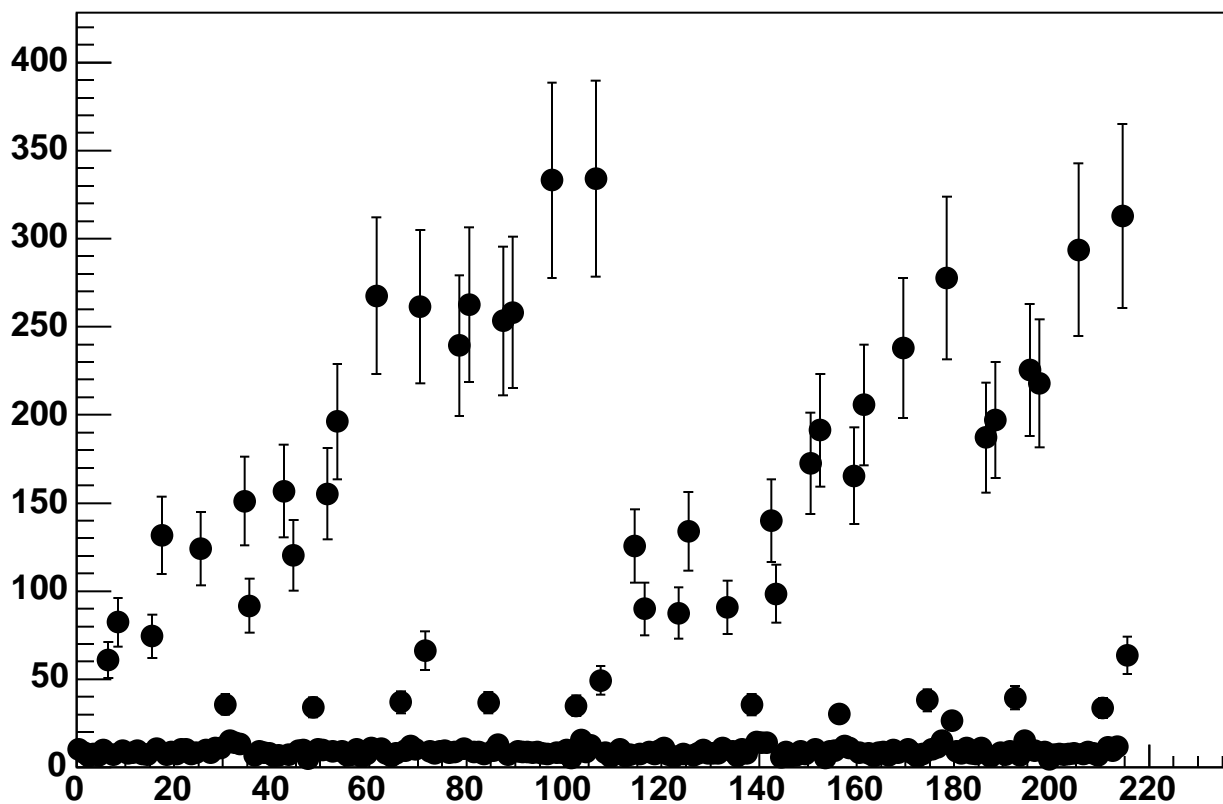
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18\*Chip+Chan



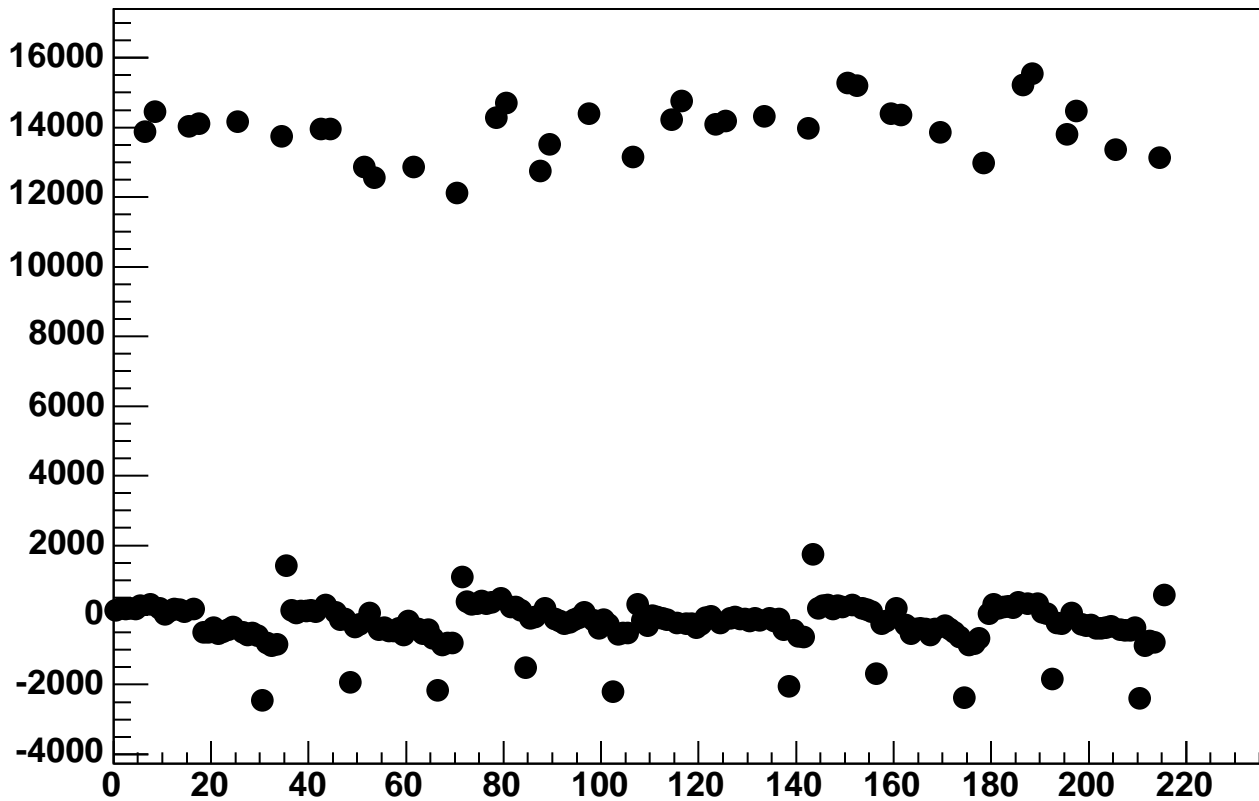
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18\*Chip+Chan



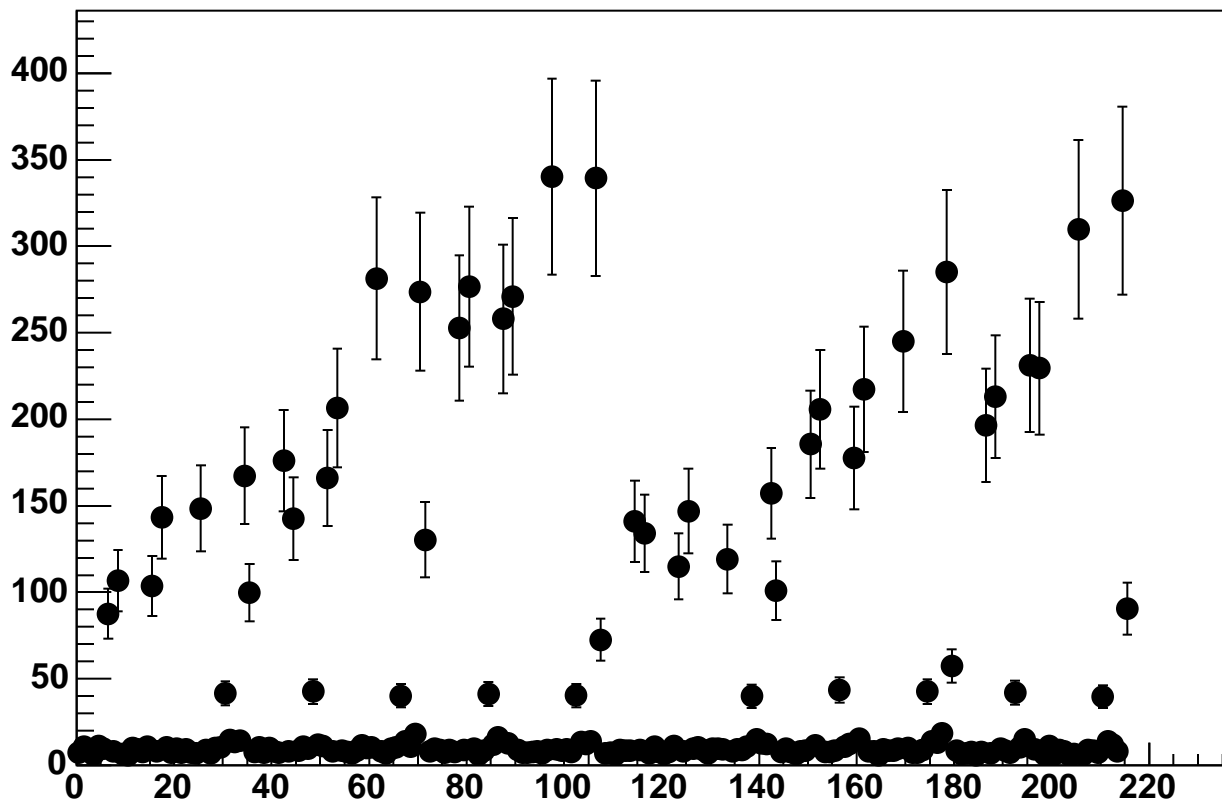
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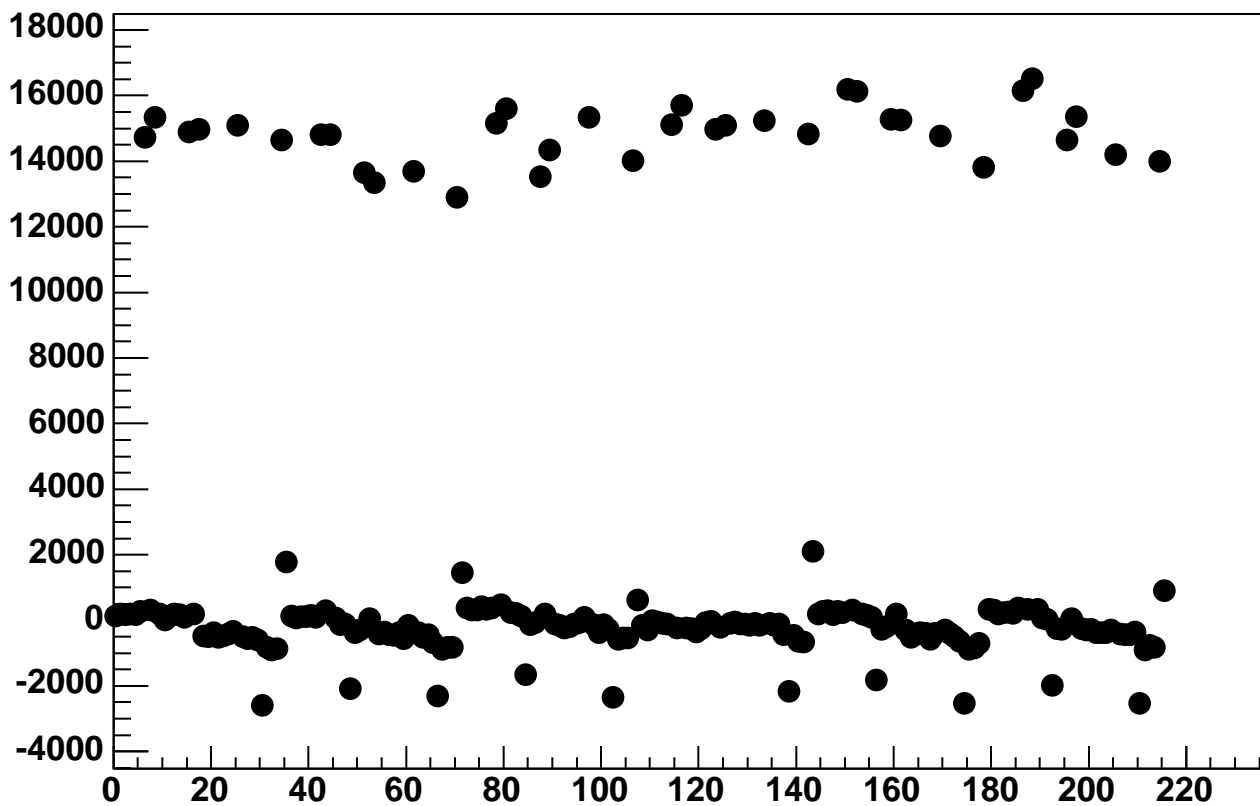
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18\*Chip+Chan



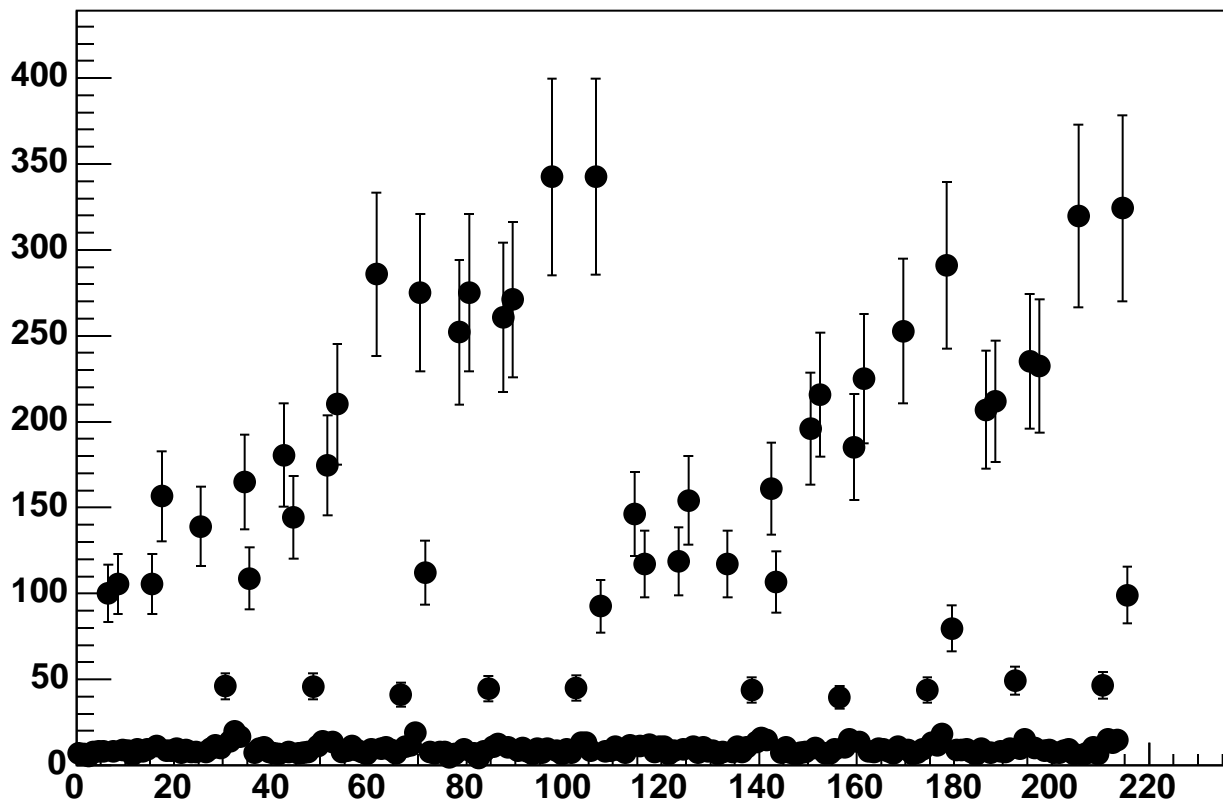
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=35, DAC=6800, ADC Mean vs 18\*Chip+Chan

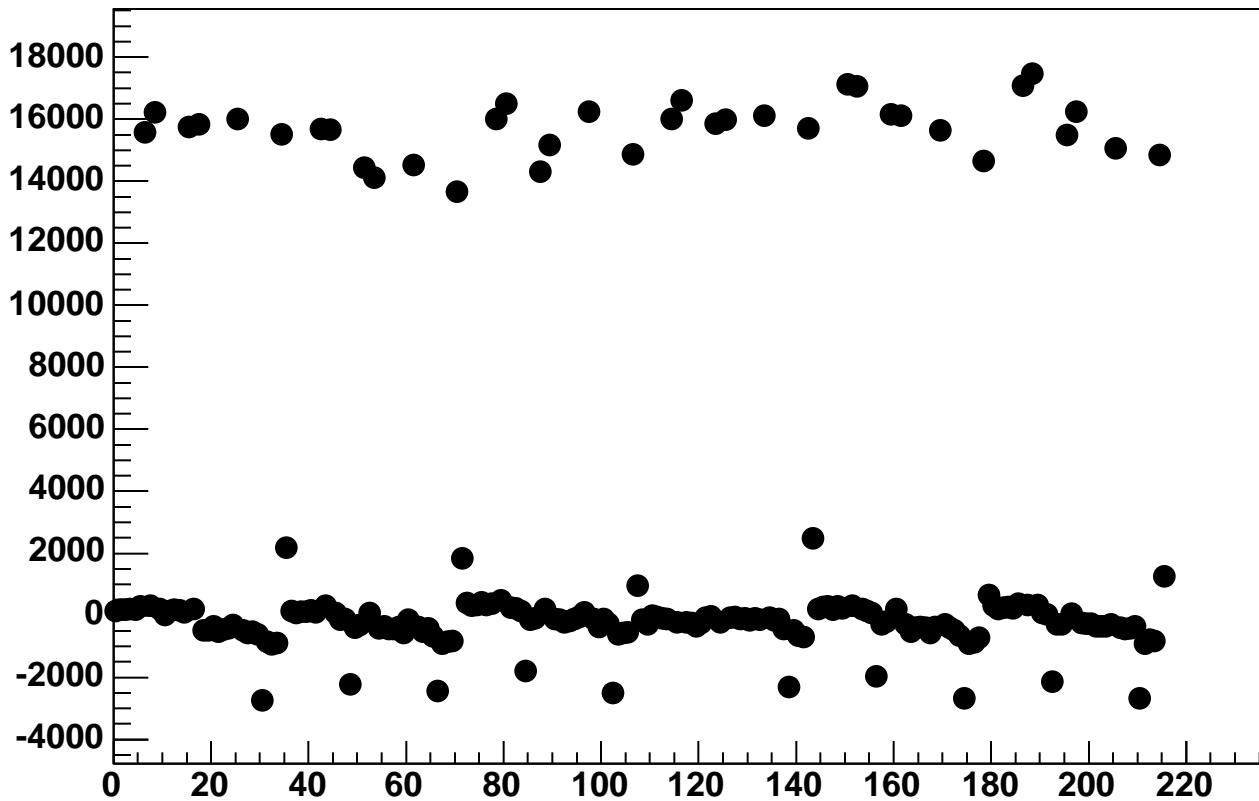


Enable 2, Hold=35, DAC=6800, ADC Noise vs 18\*Chip+Chan

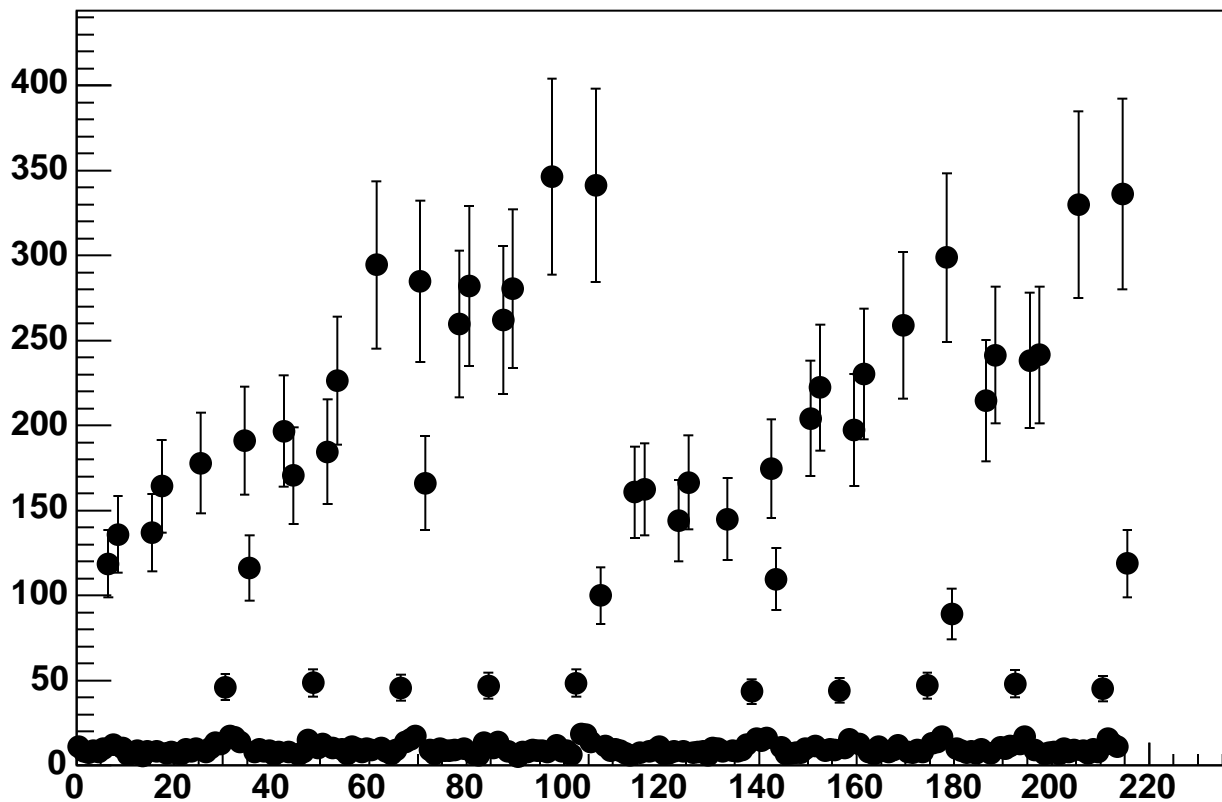




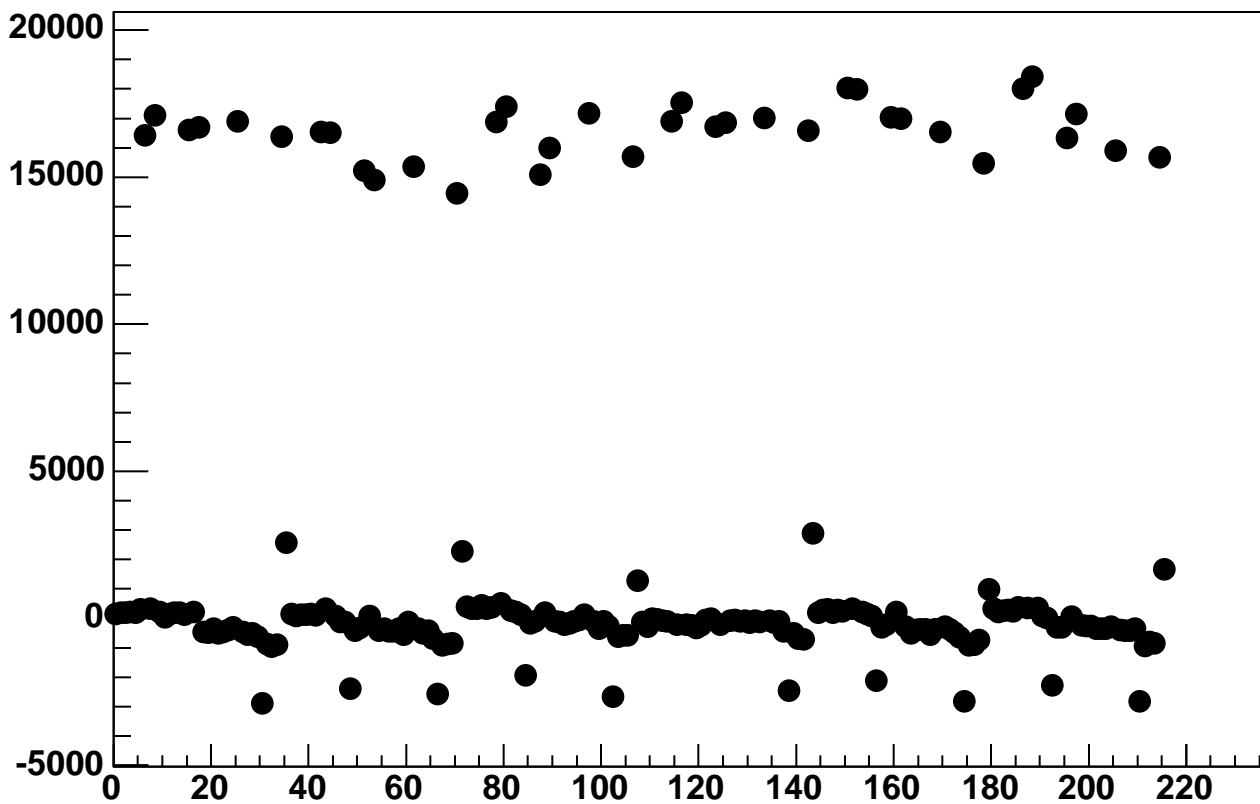
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18\*Chip+Chan



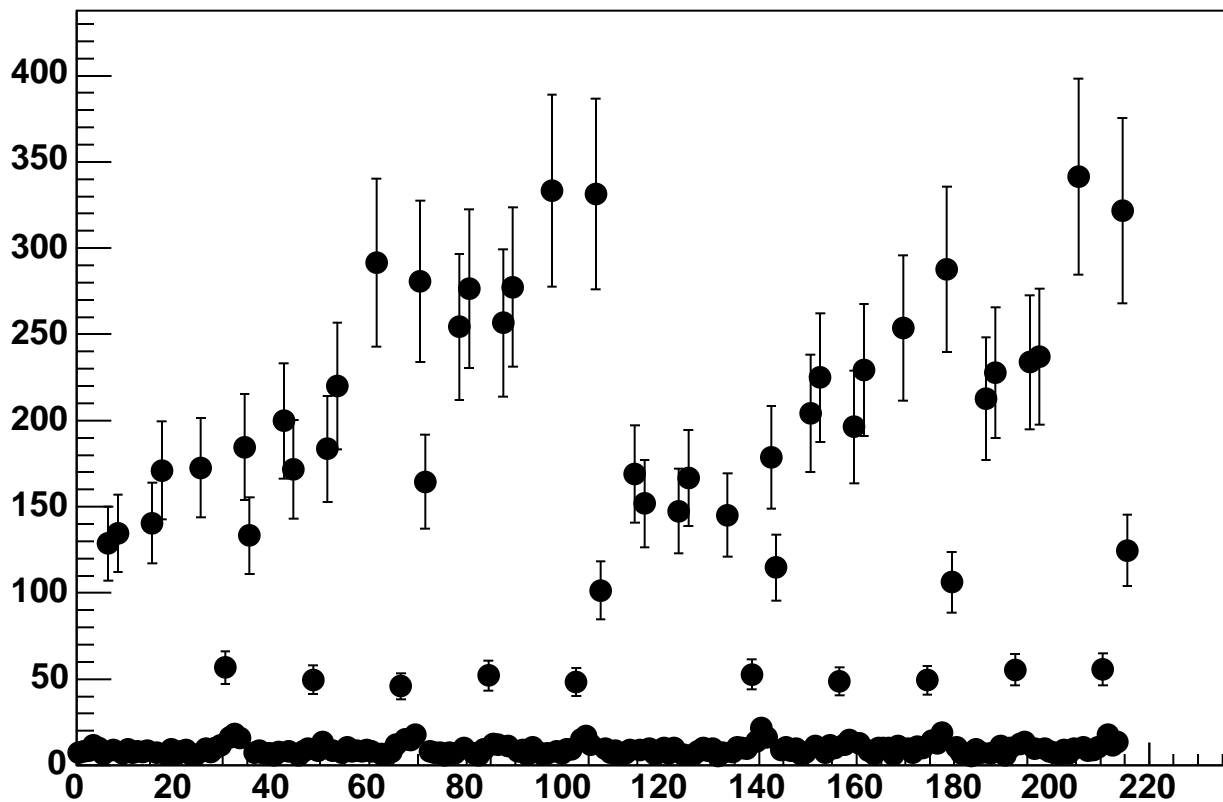
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18\*Chip+Chan



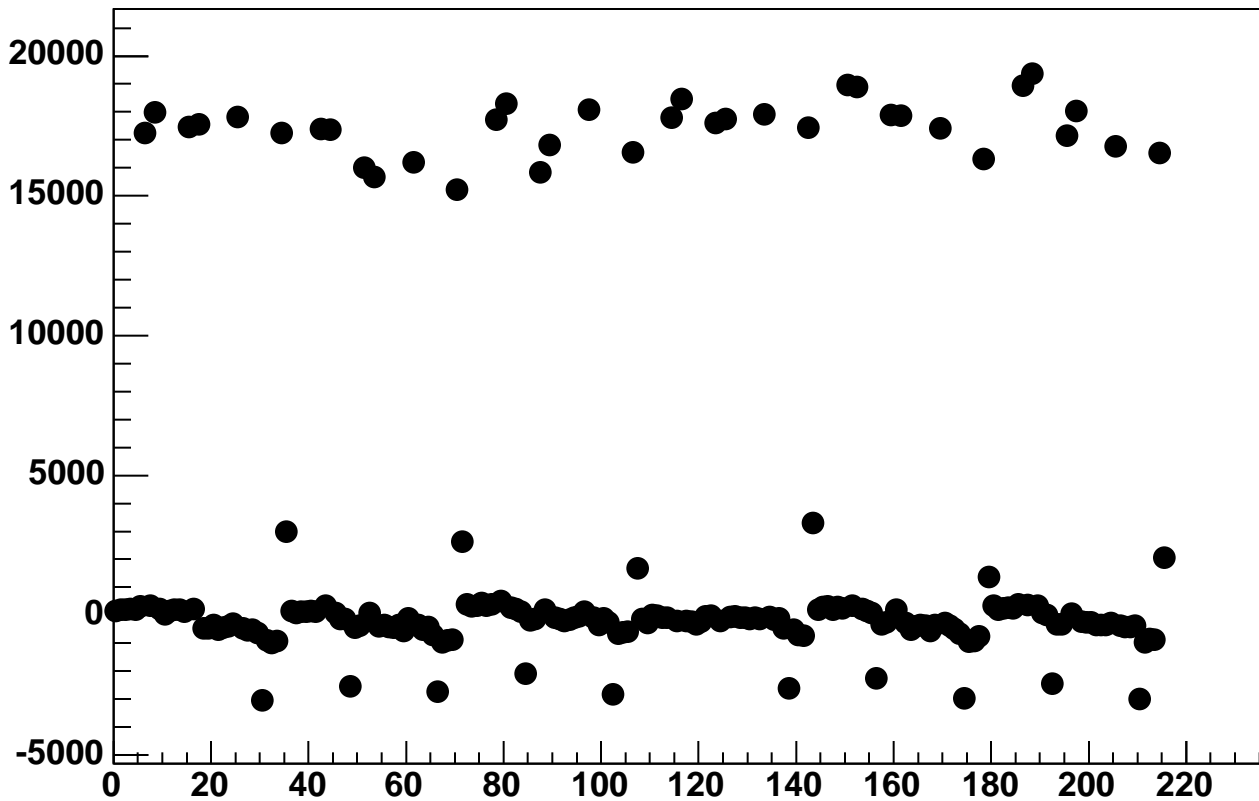
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18\*Chip+Chan



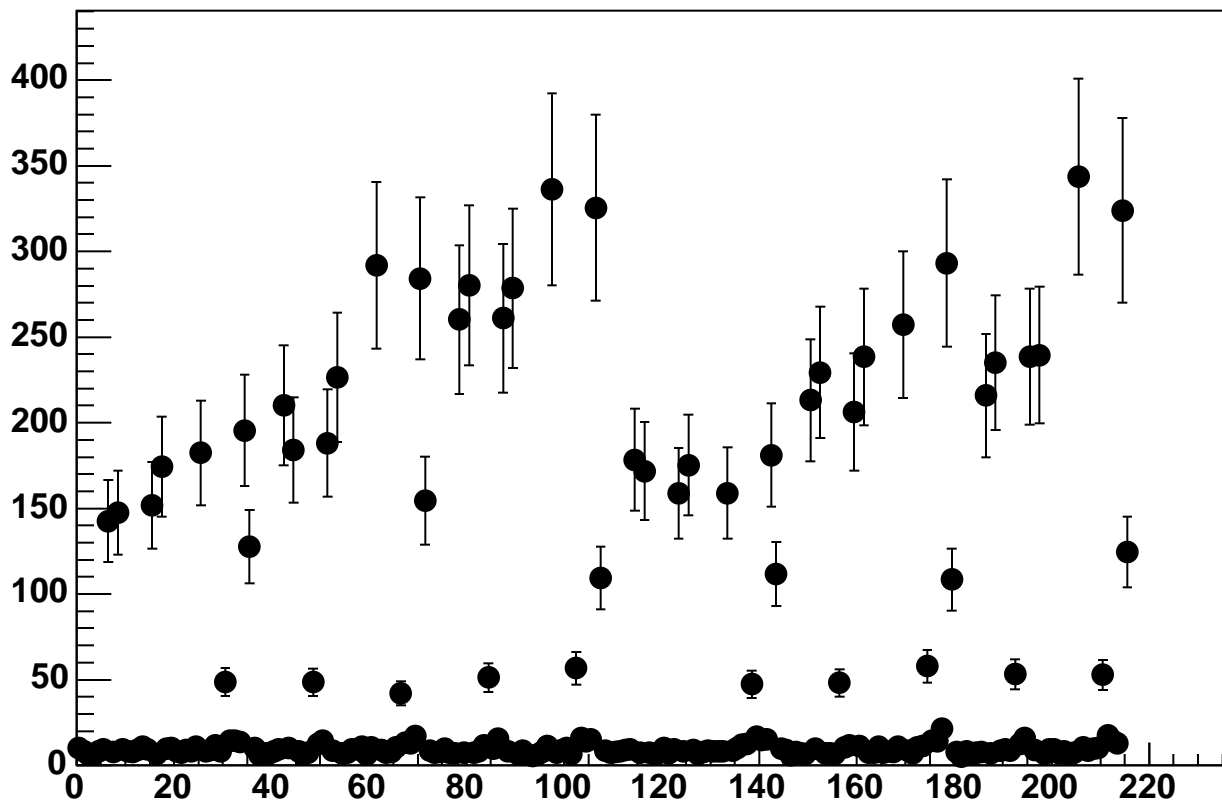
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18\*Chip+Chan



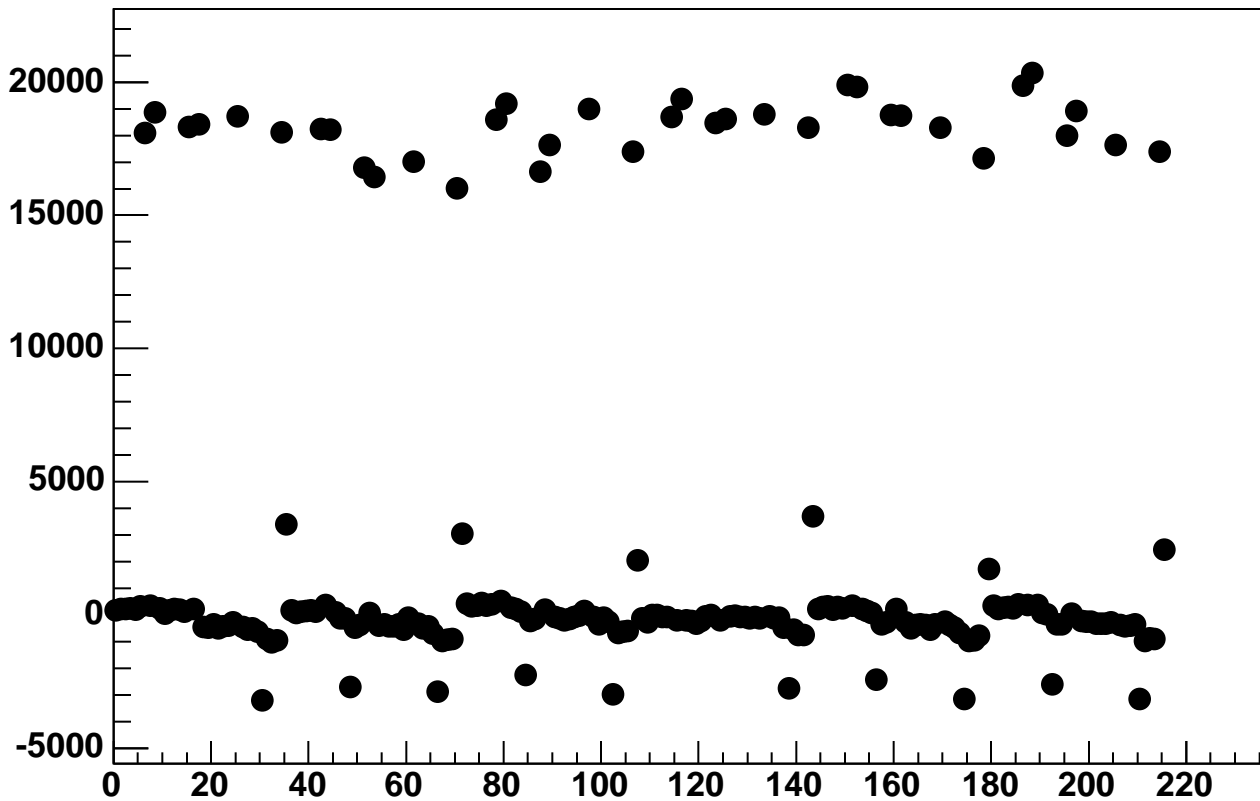
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18\*Chip+Chan



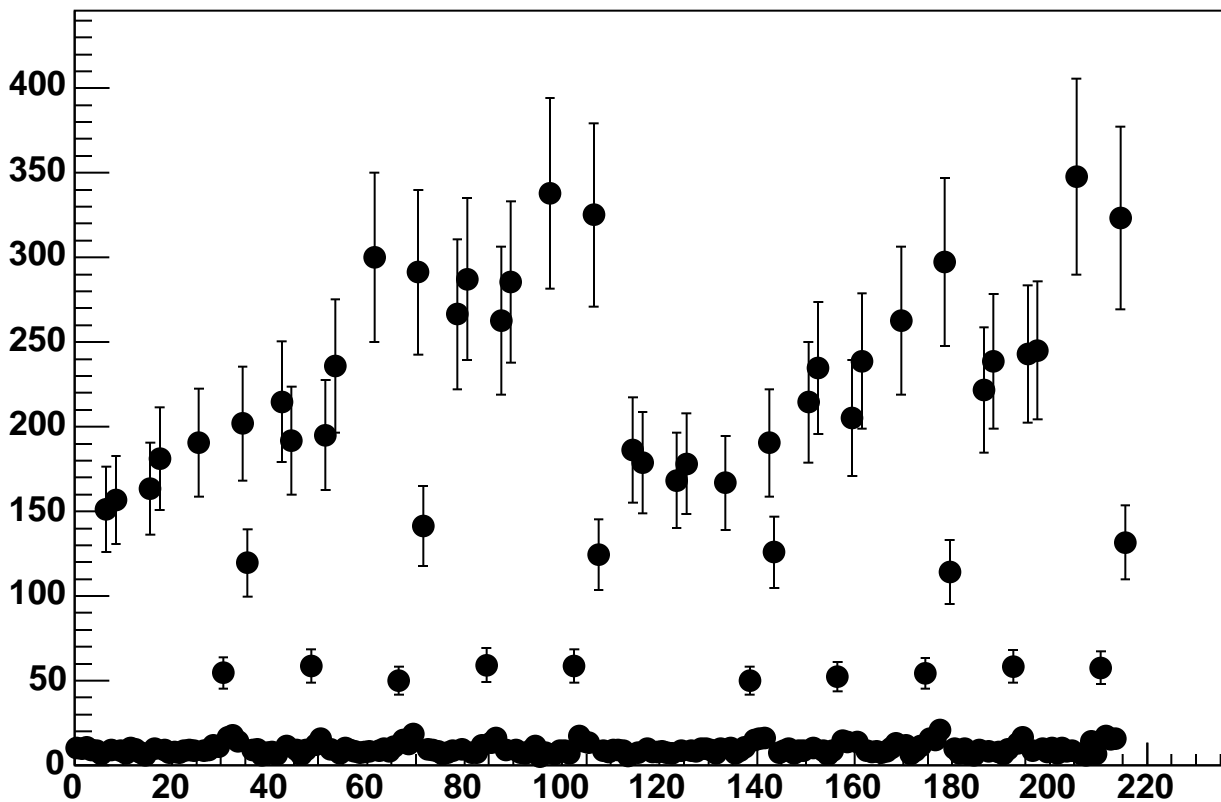
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18\*Chip+Chan



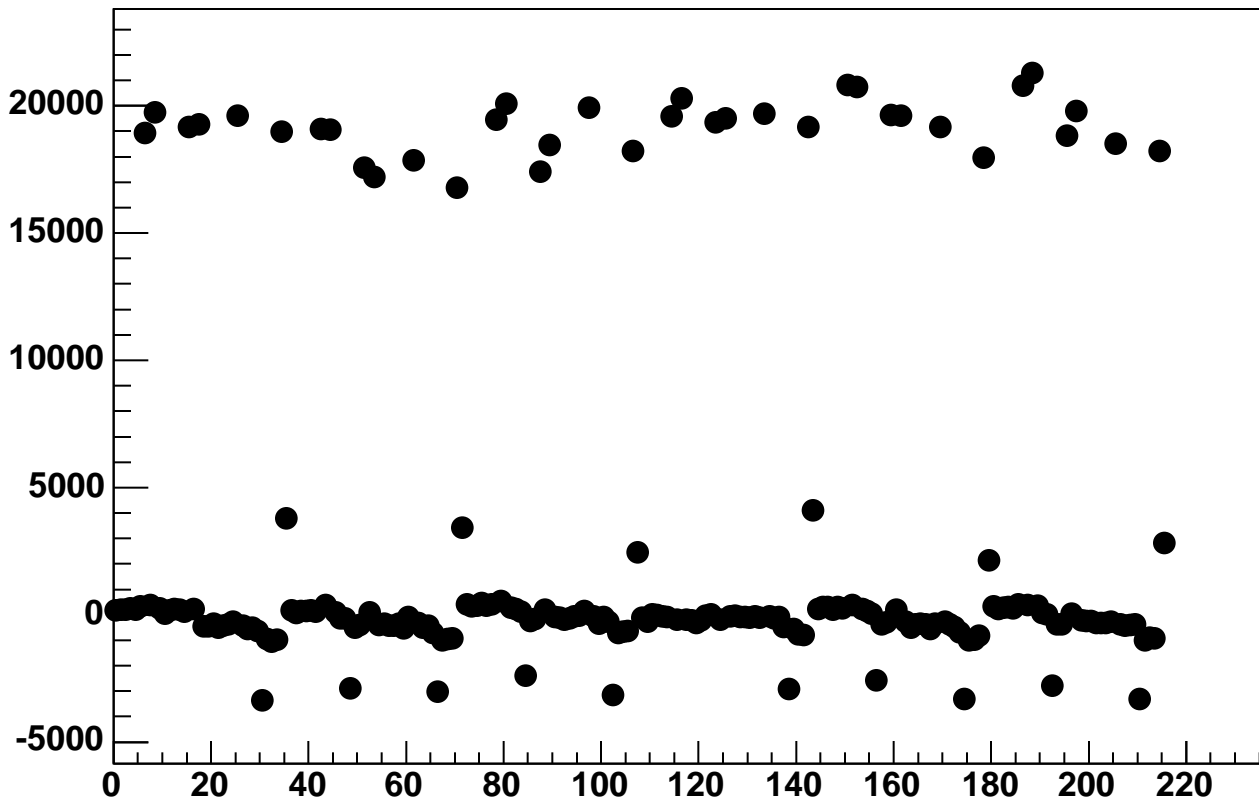
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18\*Chip+Chan



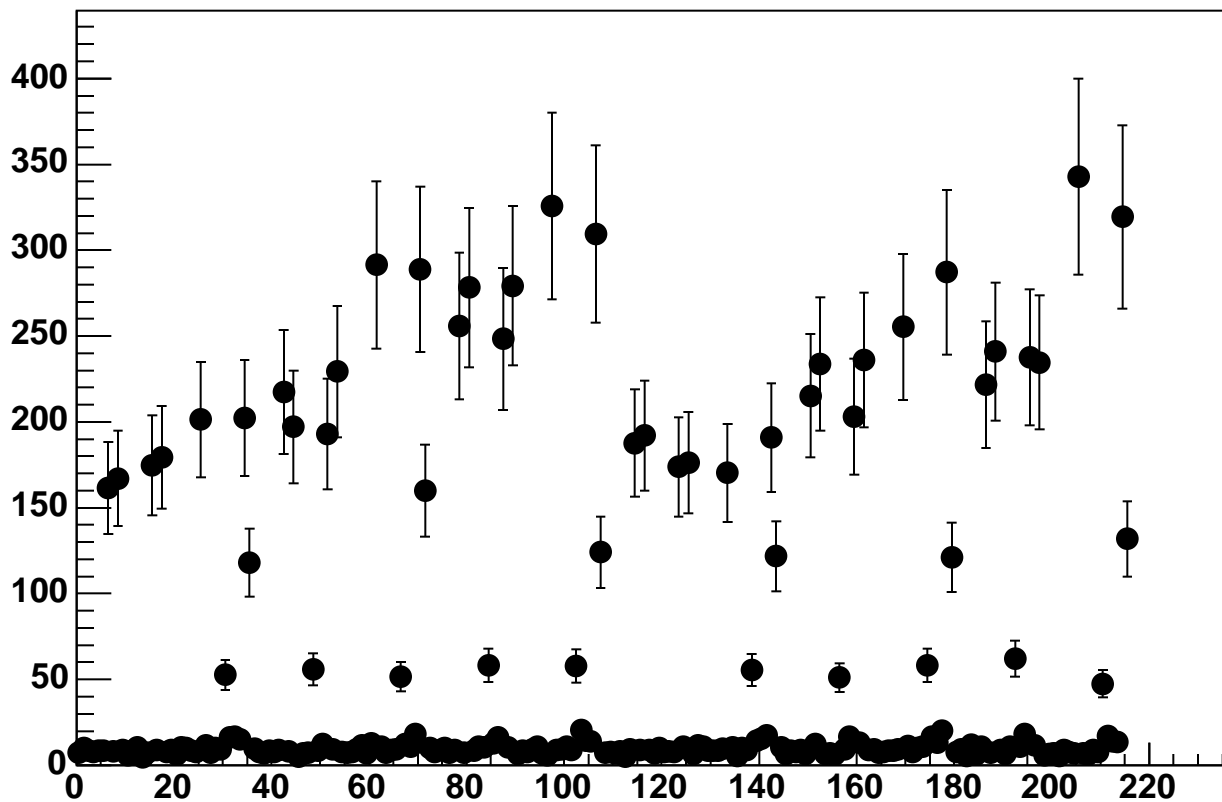
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18\*Chip+Chan



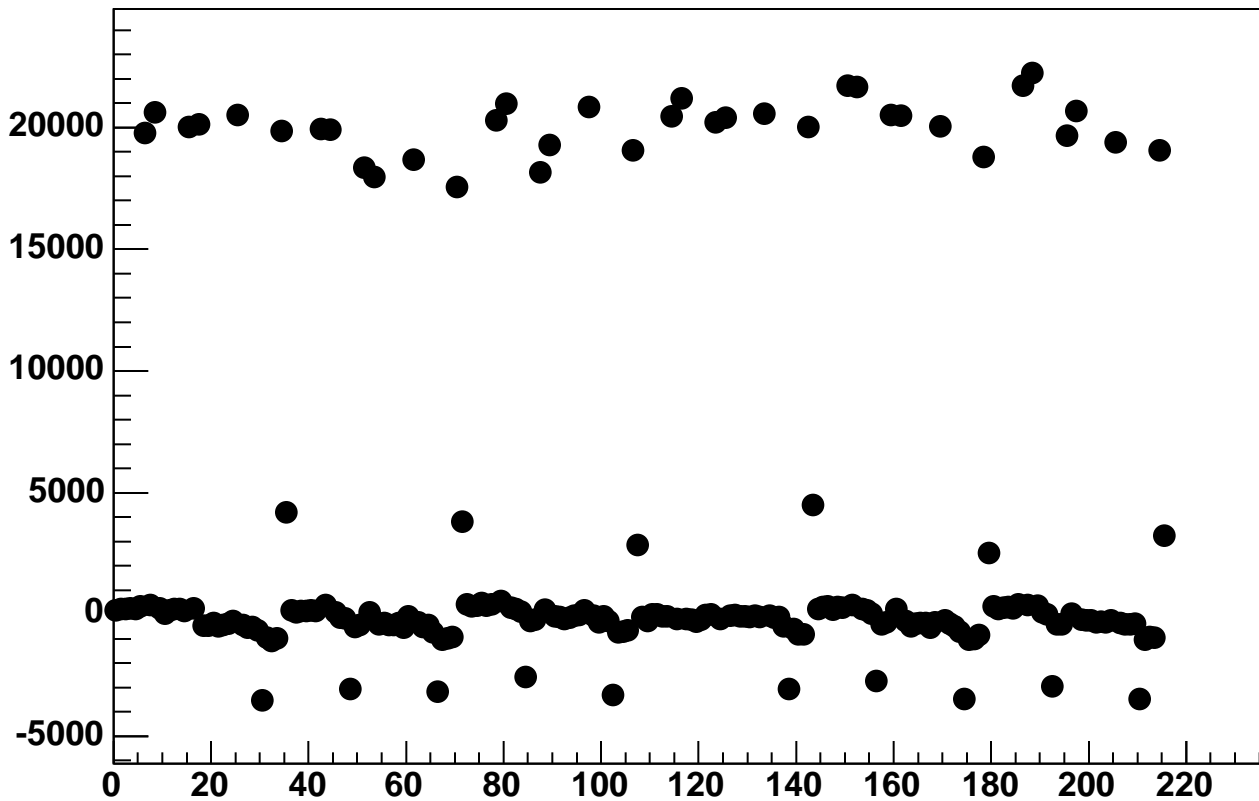
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18\*Chip+Chan



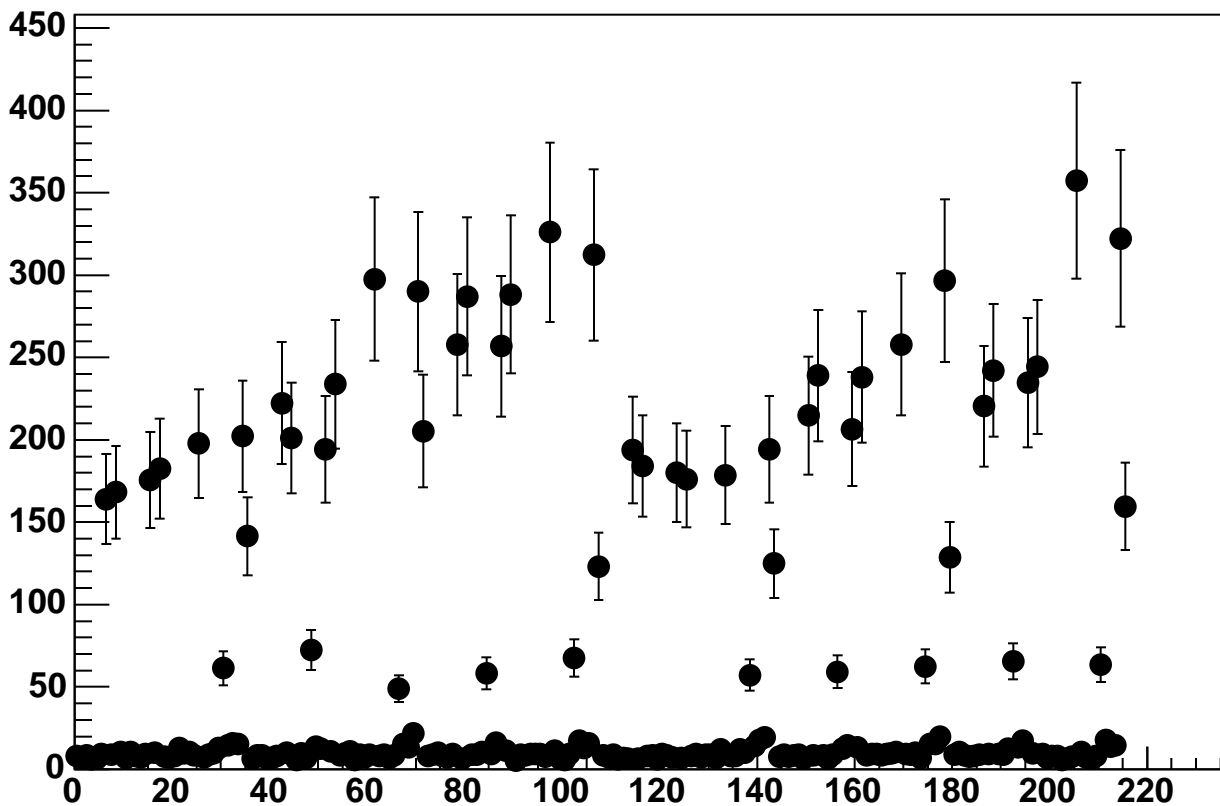
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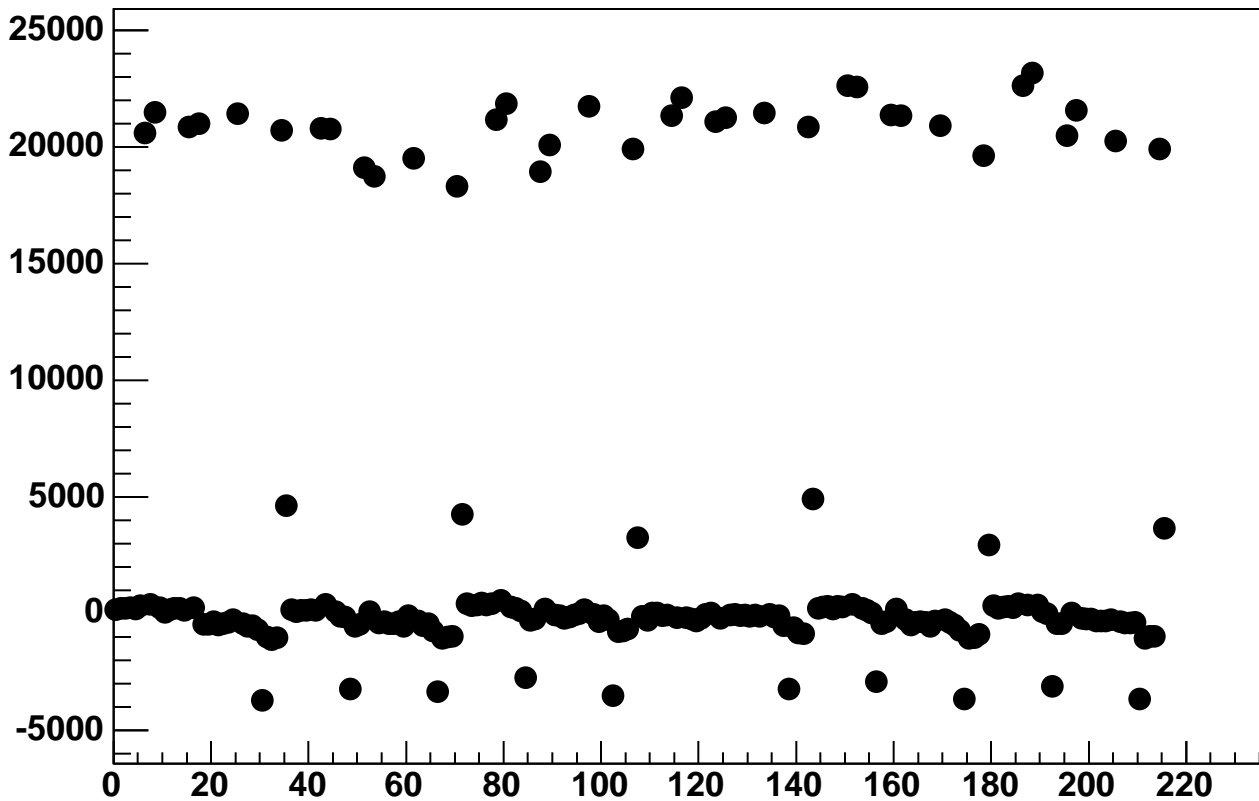
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18\*Chip+Chan



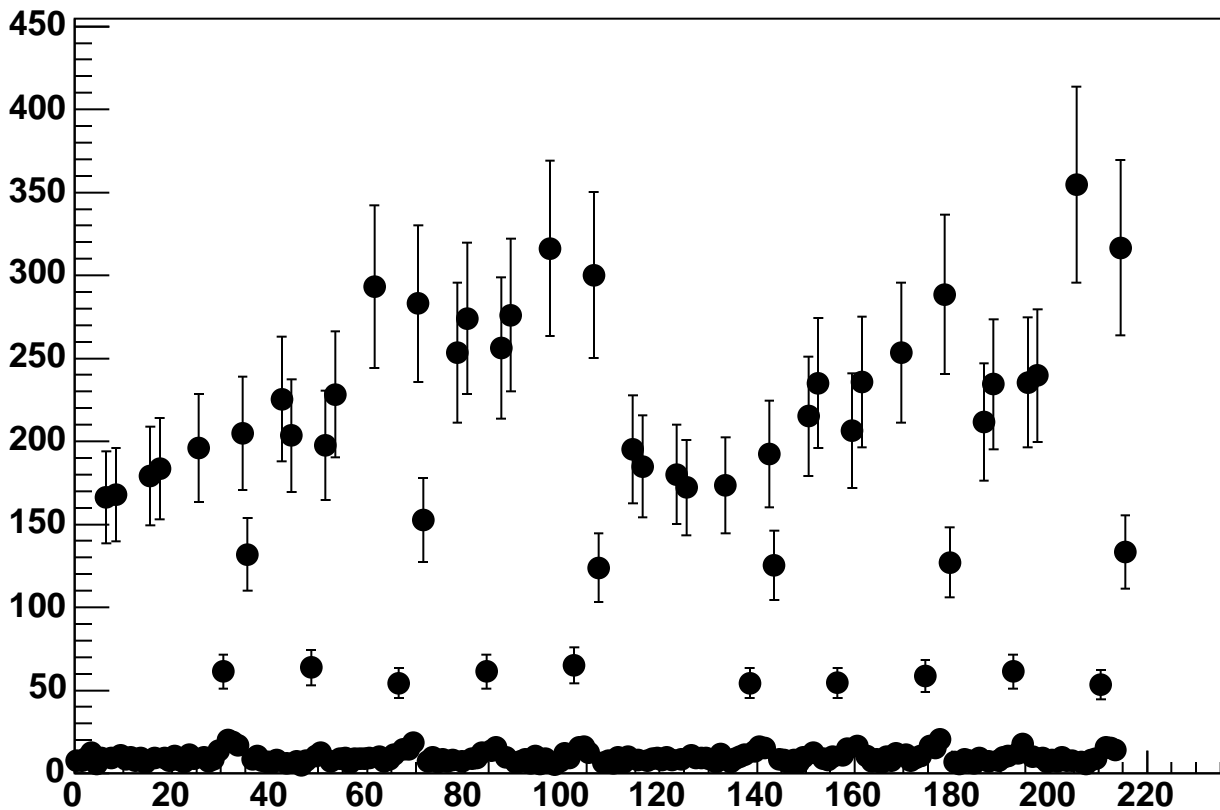
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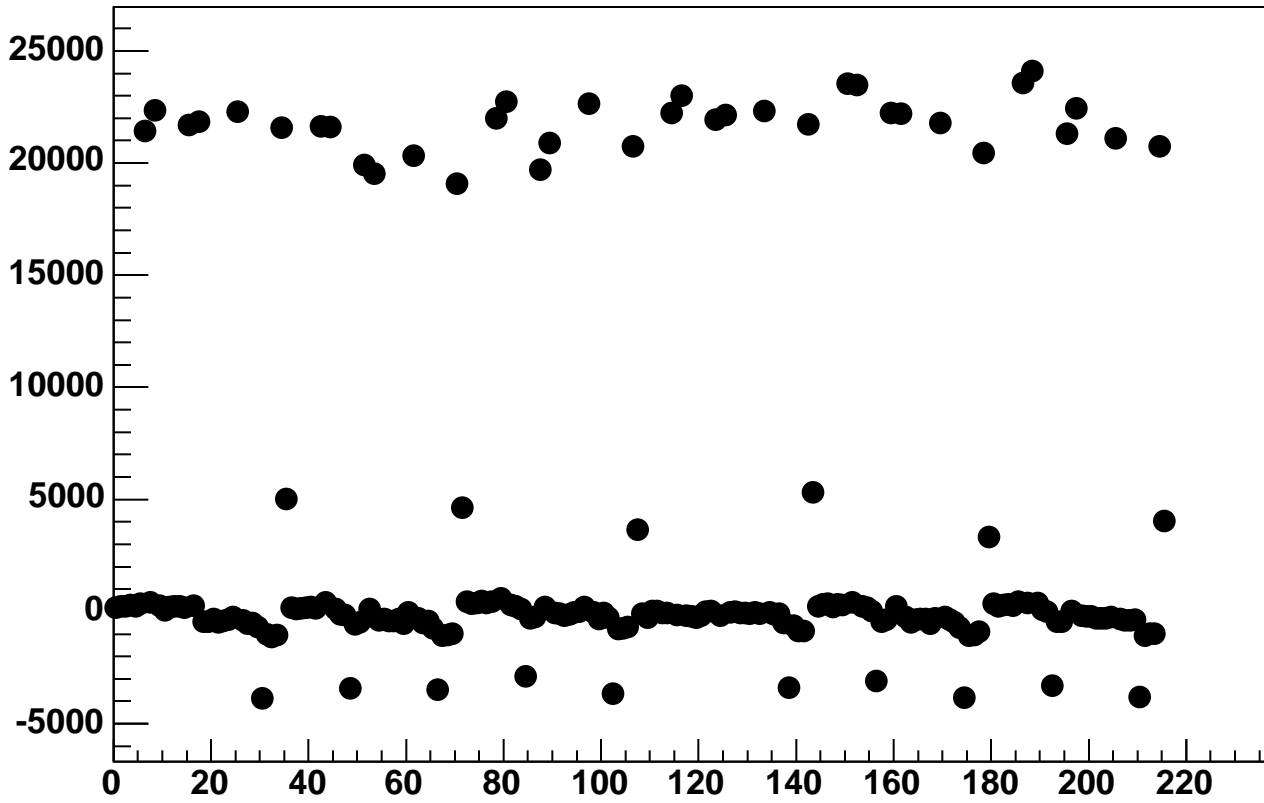
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18\*Chip+Chan



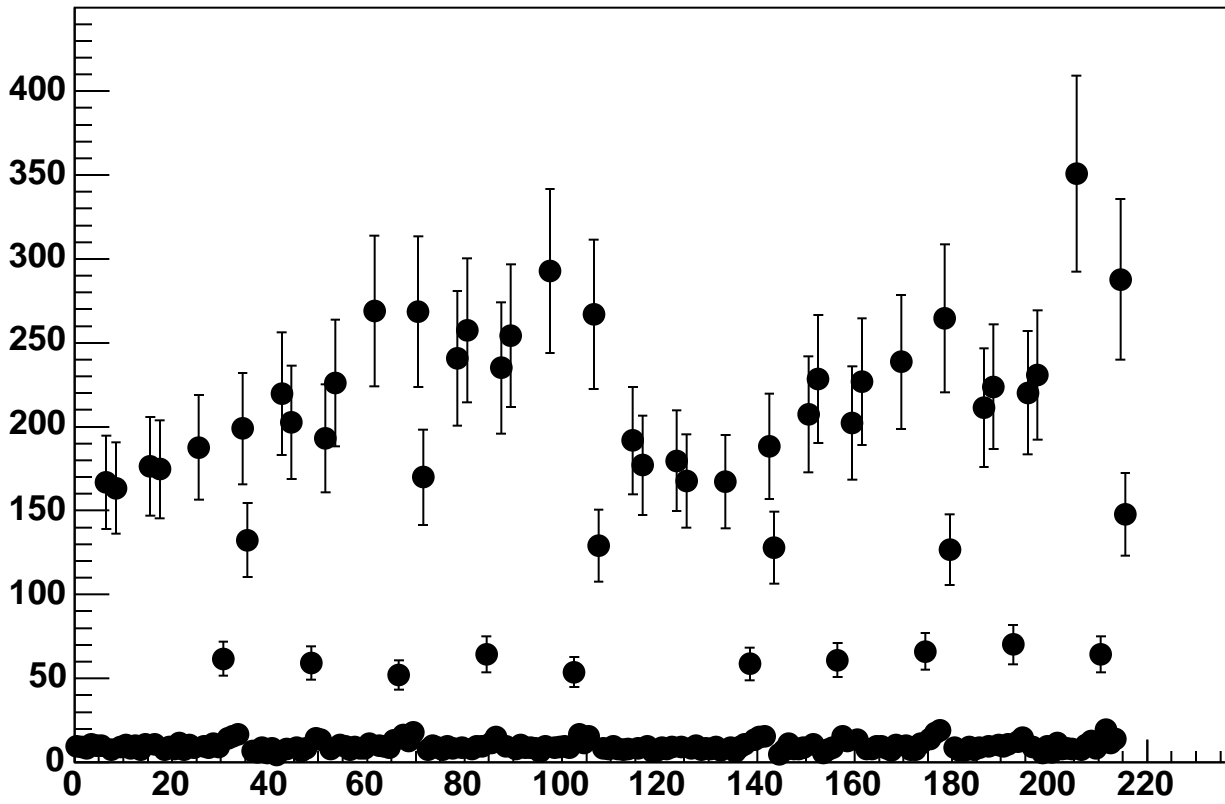
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=35, DAC=10000, ADC Mean vs 18\*Chip+Chan

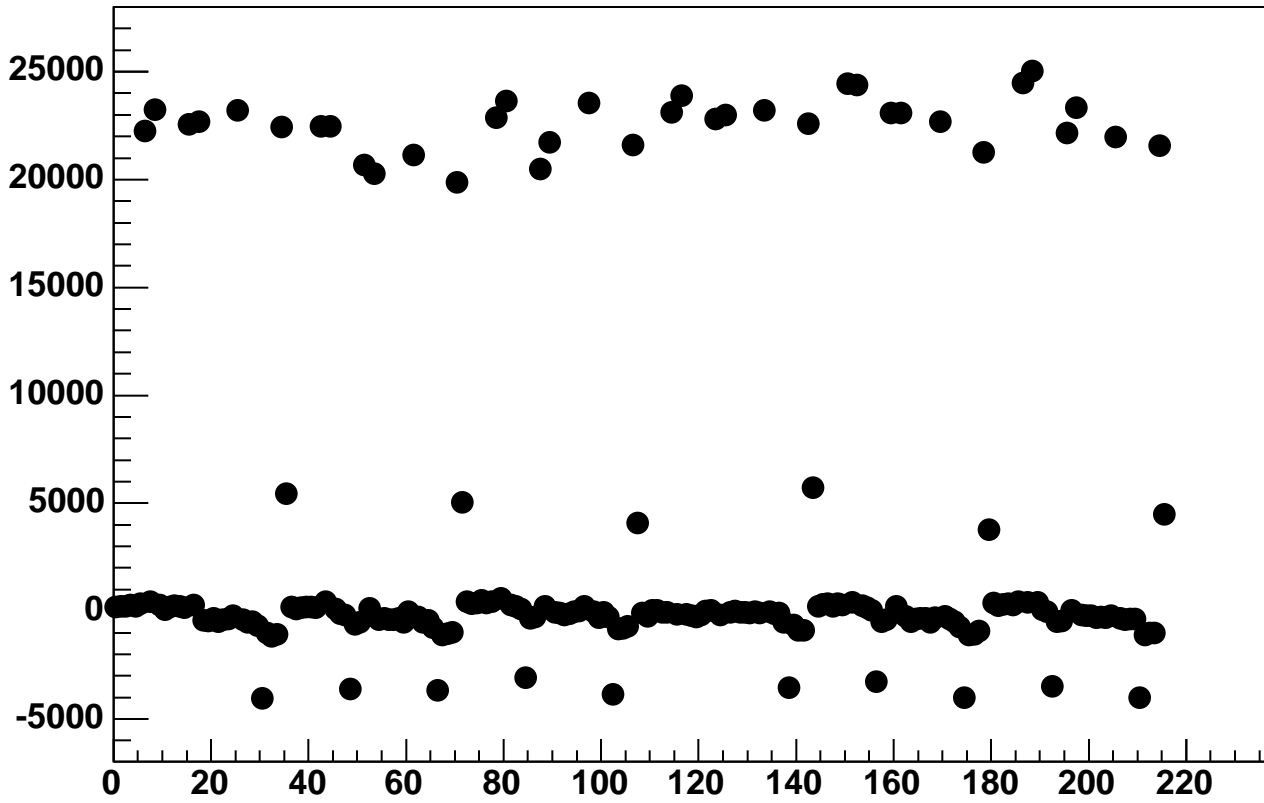


Enable 2, Hold=35, DAC=10000, ADC Noise vs 18\*Chip+Chan

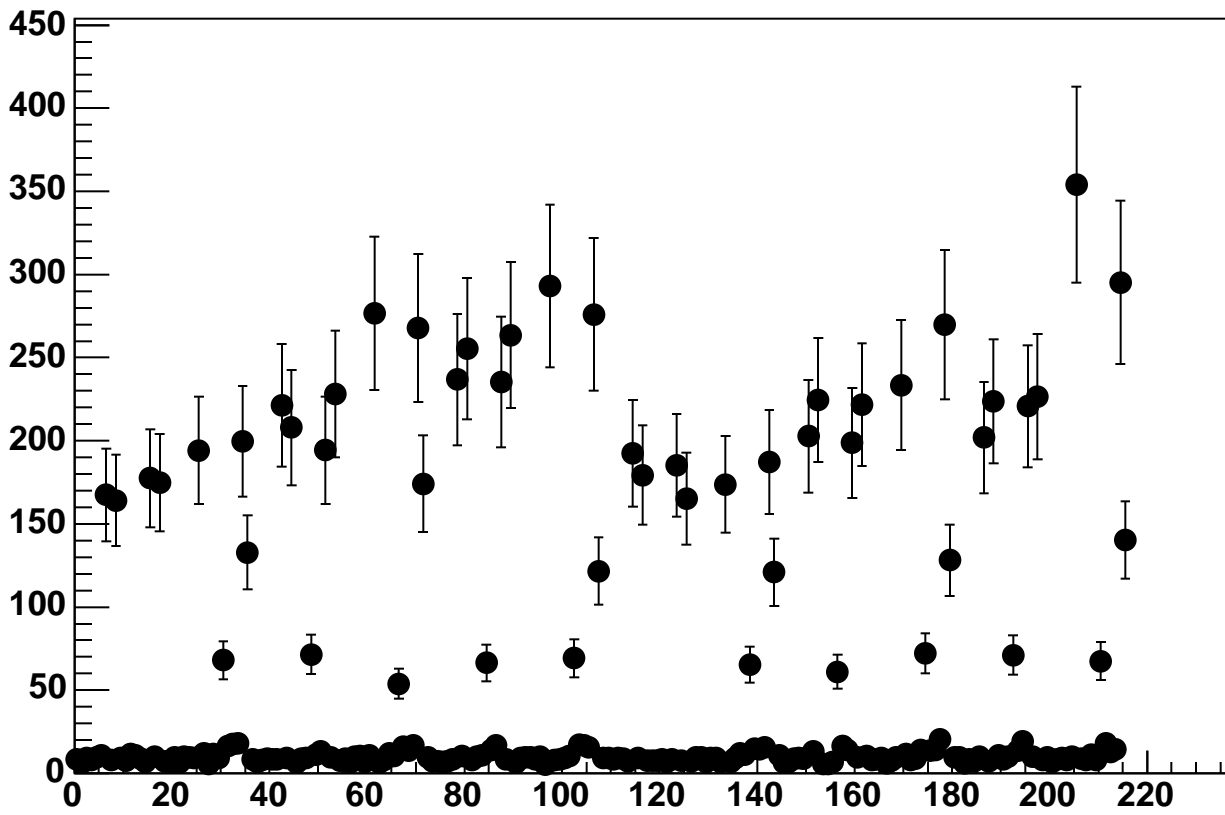




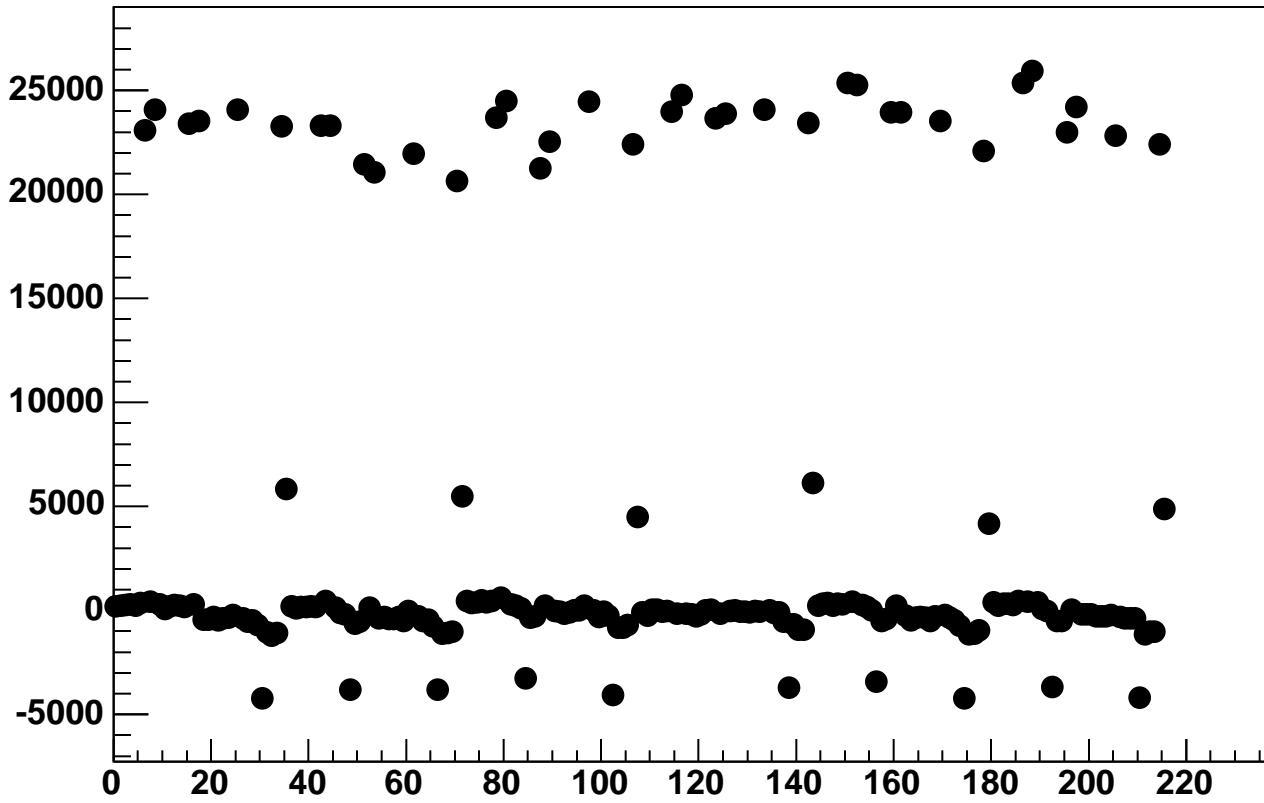
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18\*Chip+Chan



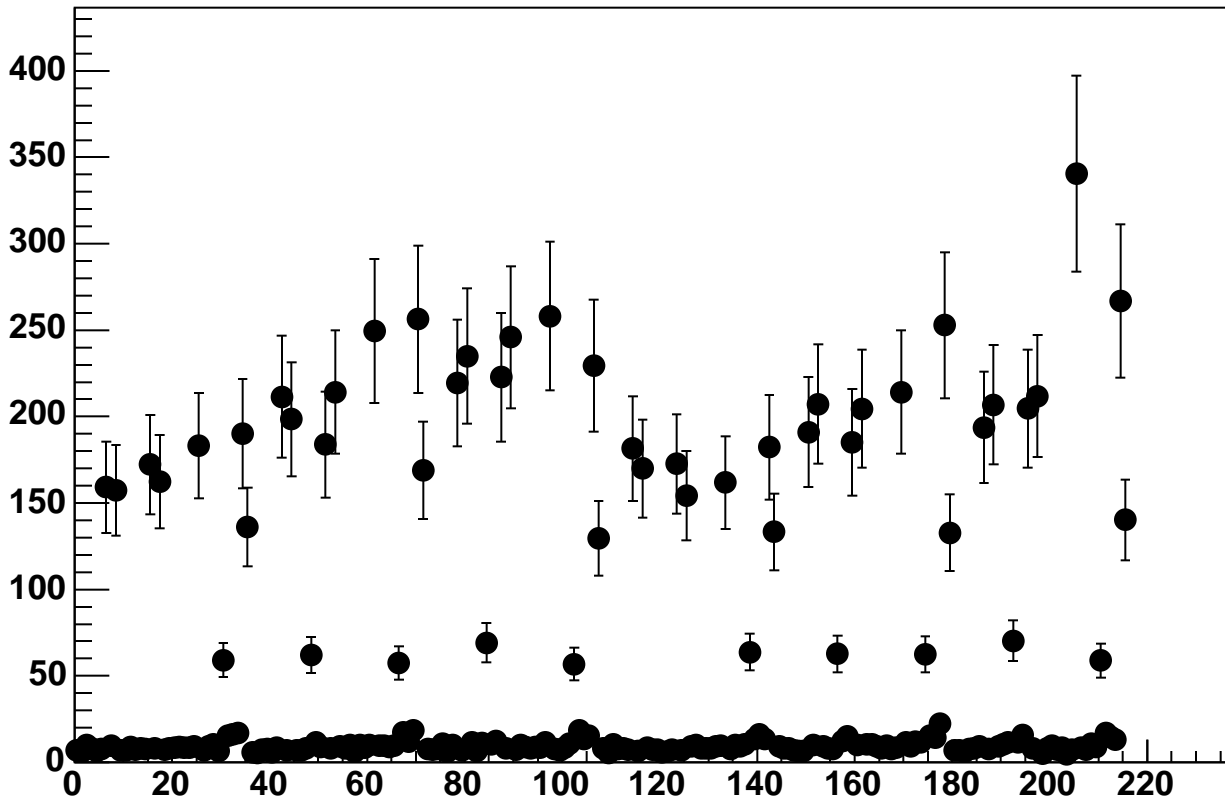
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18\*Chip+Chan



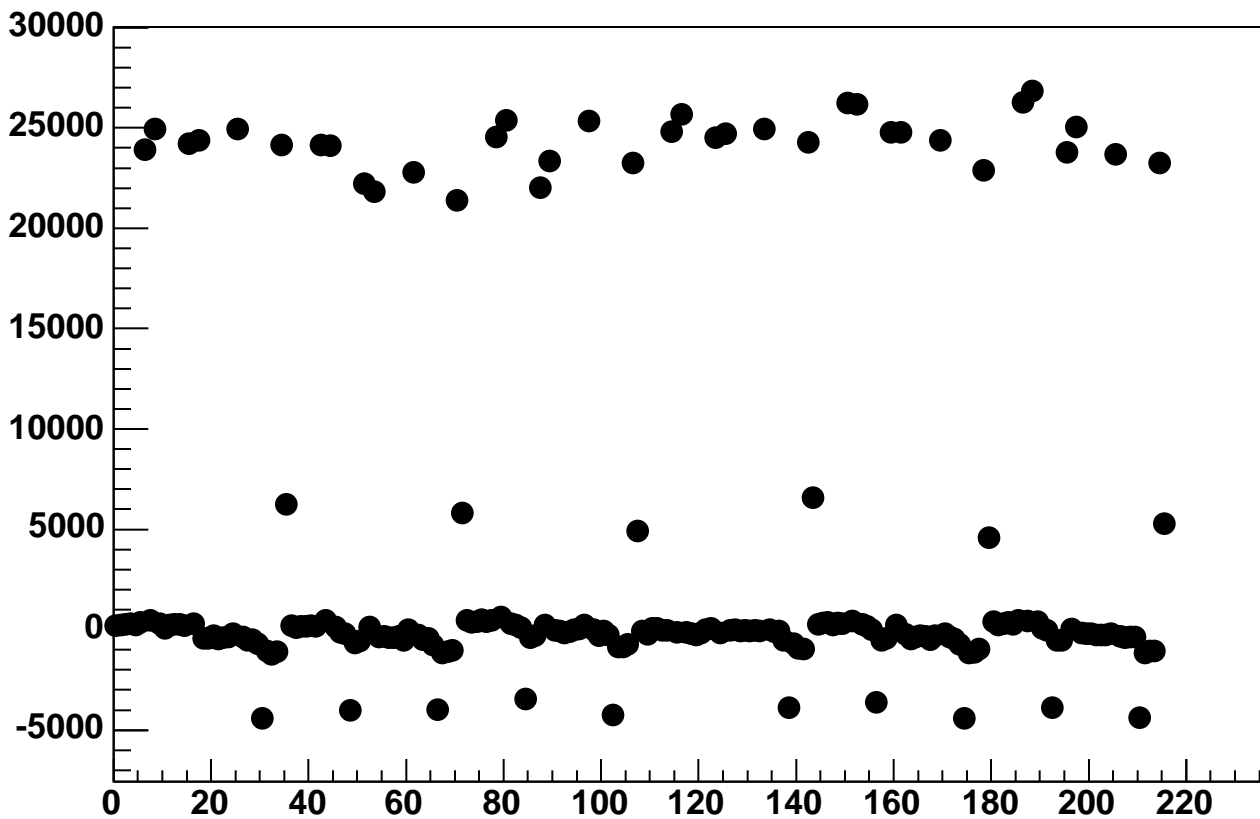
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18\*Chip+Chan



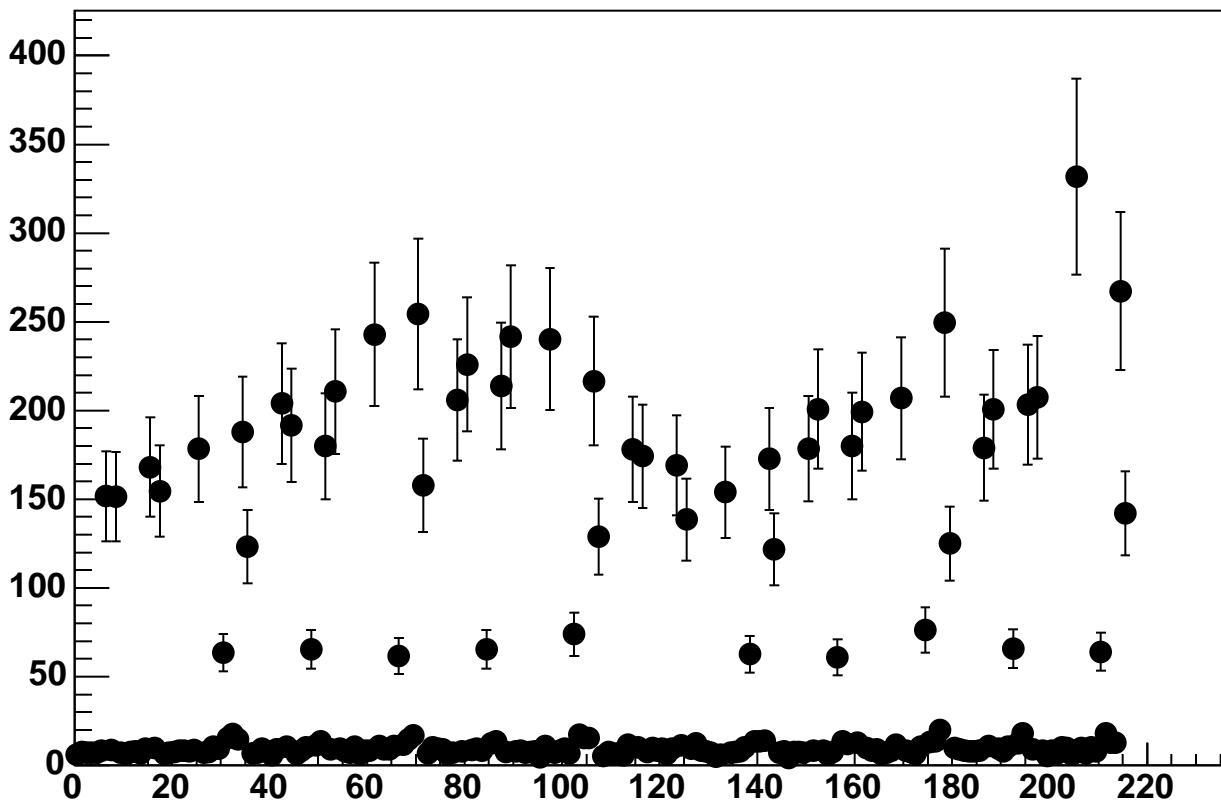
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18\*Chip+Chan



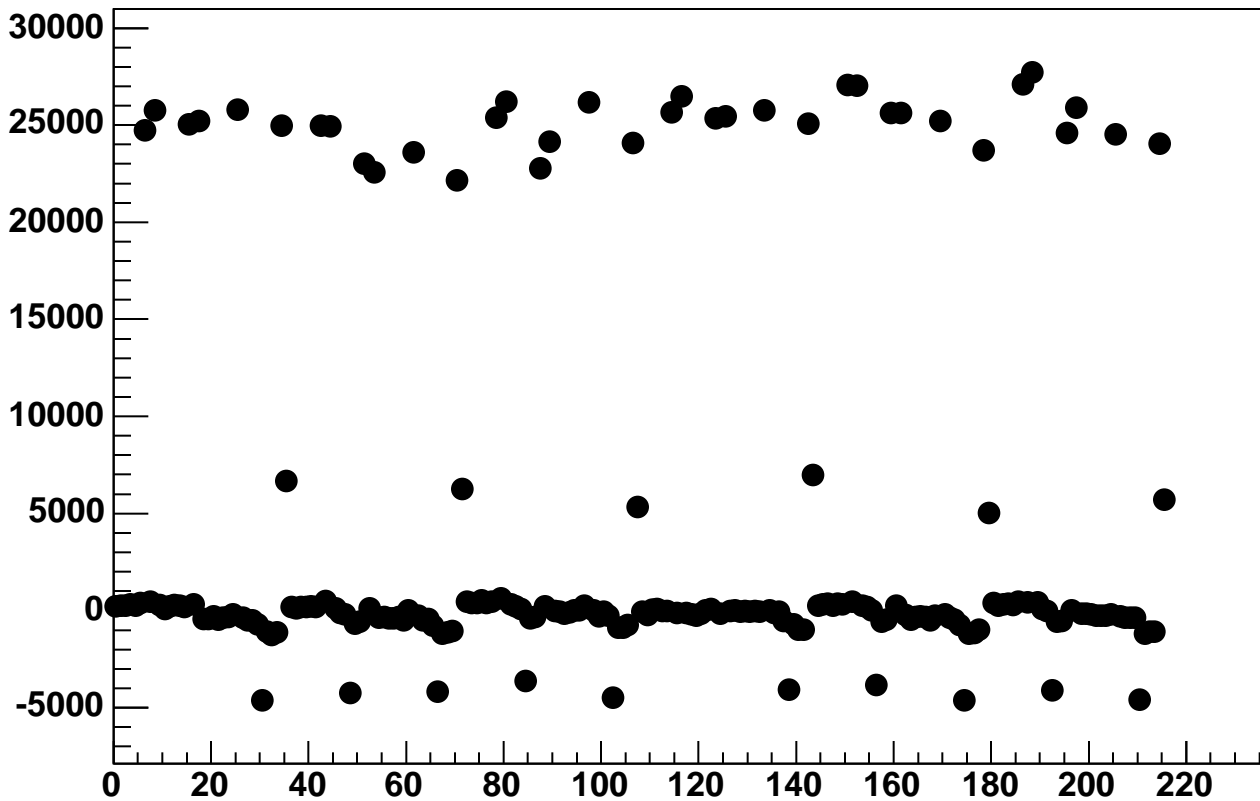
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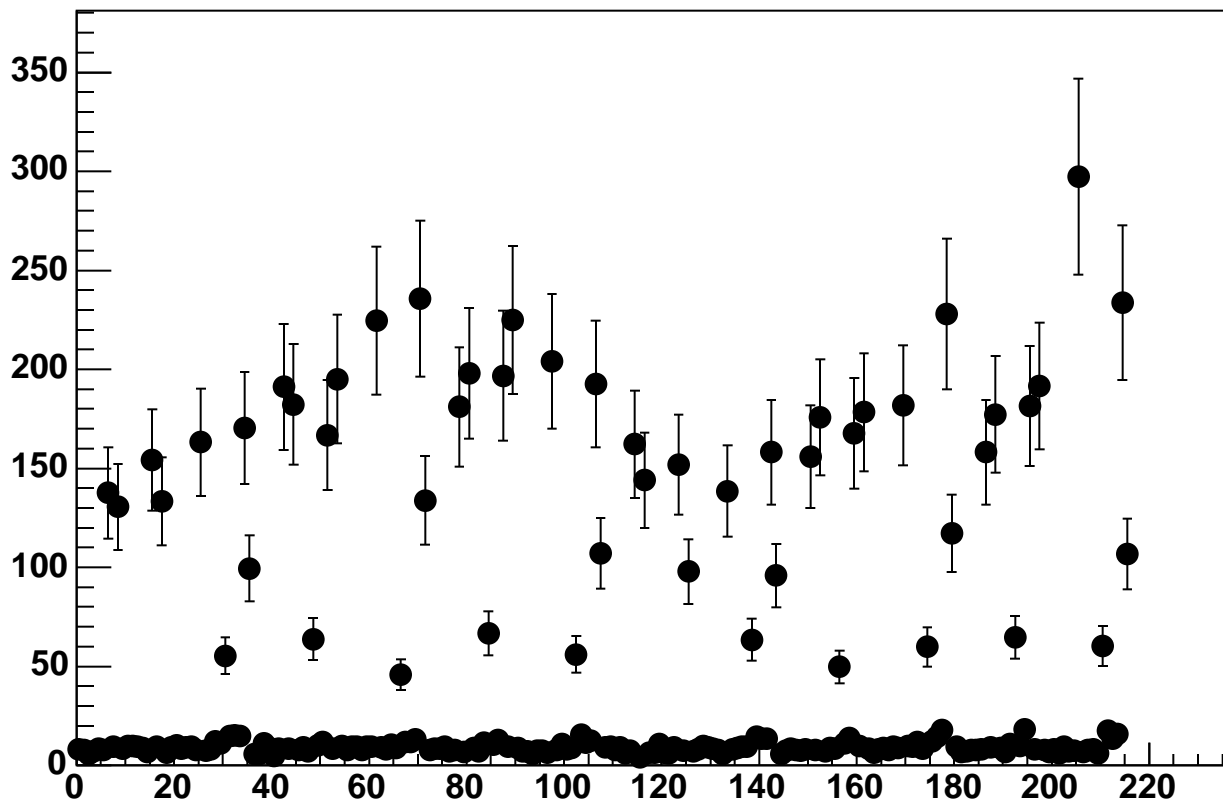
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18\*Chip+Chan



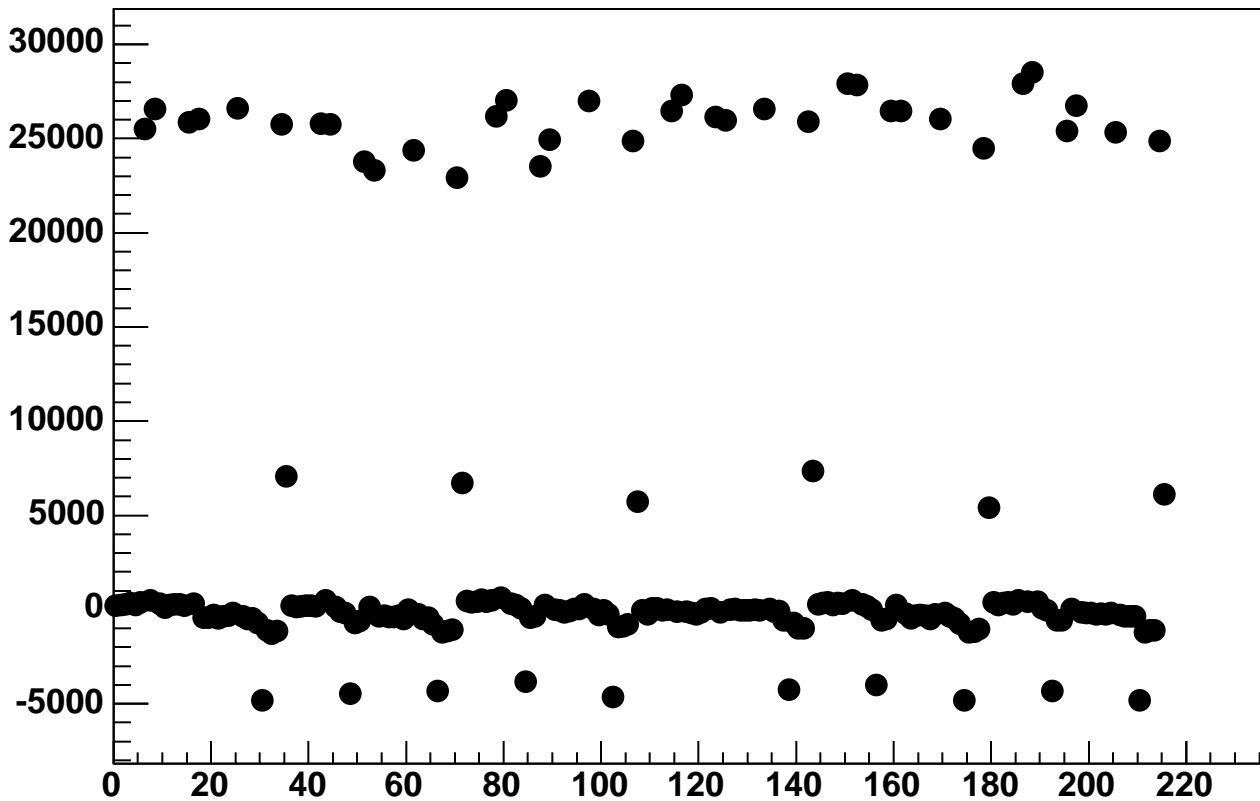
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18\*Chip+Chan



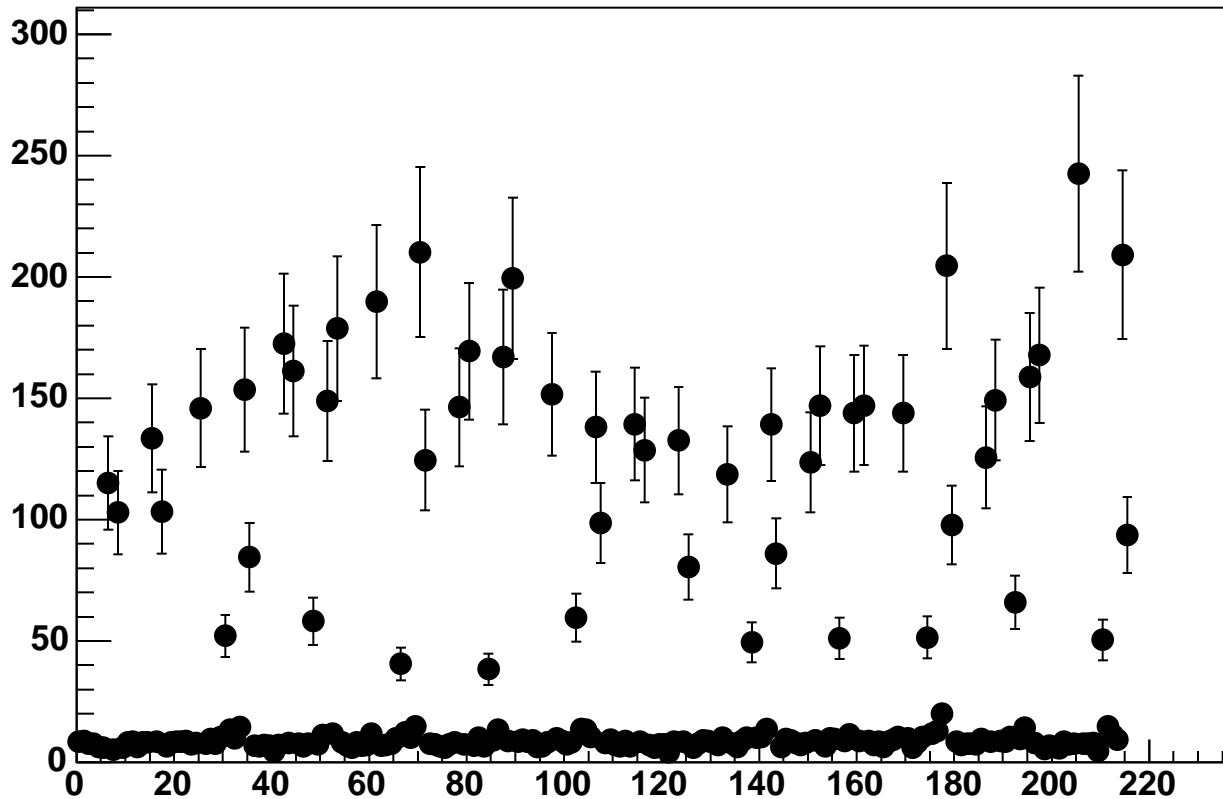
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18\*Chip+Chan



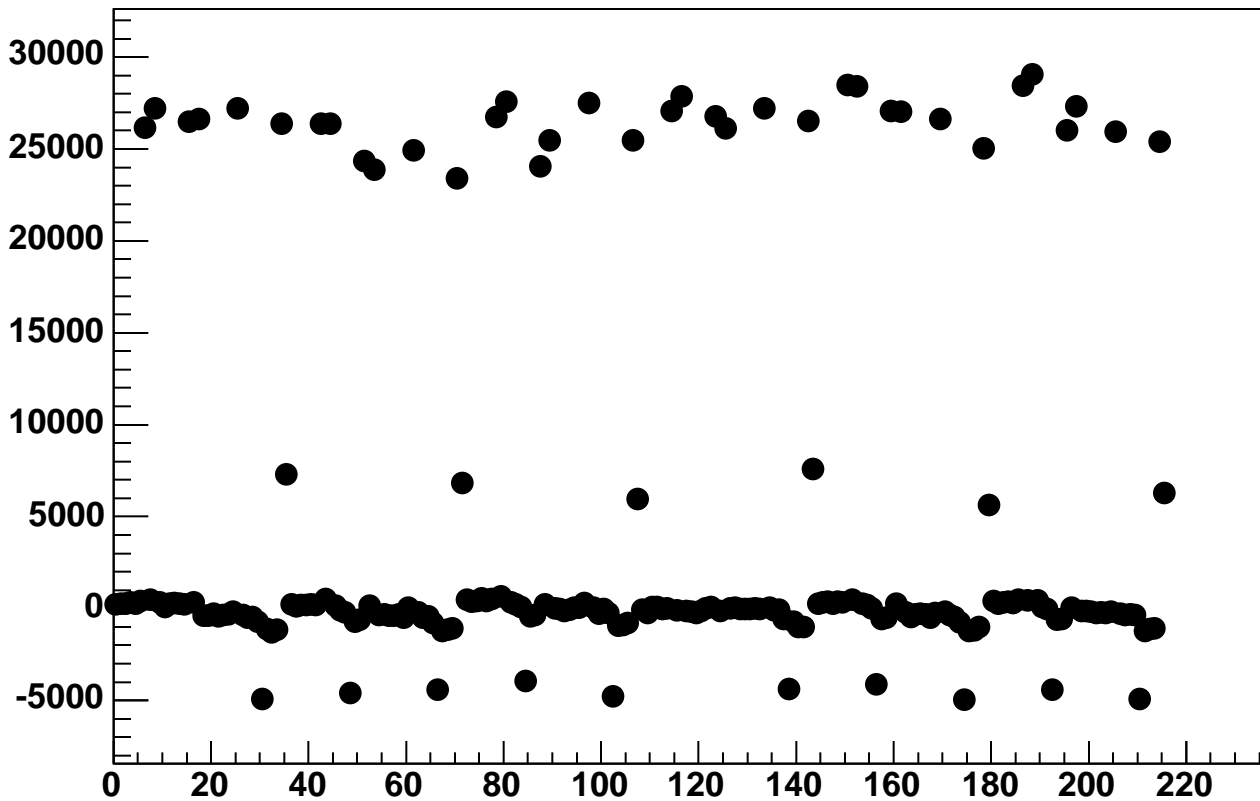
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18\*Chip+Chan



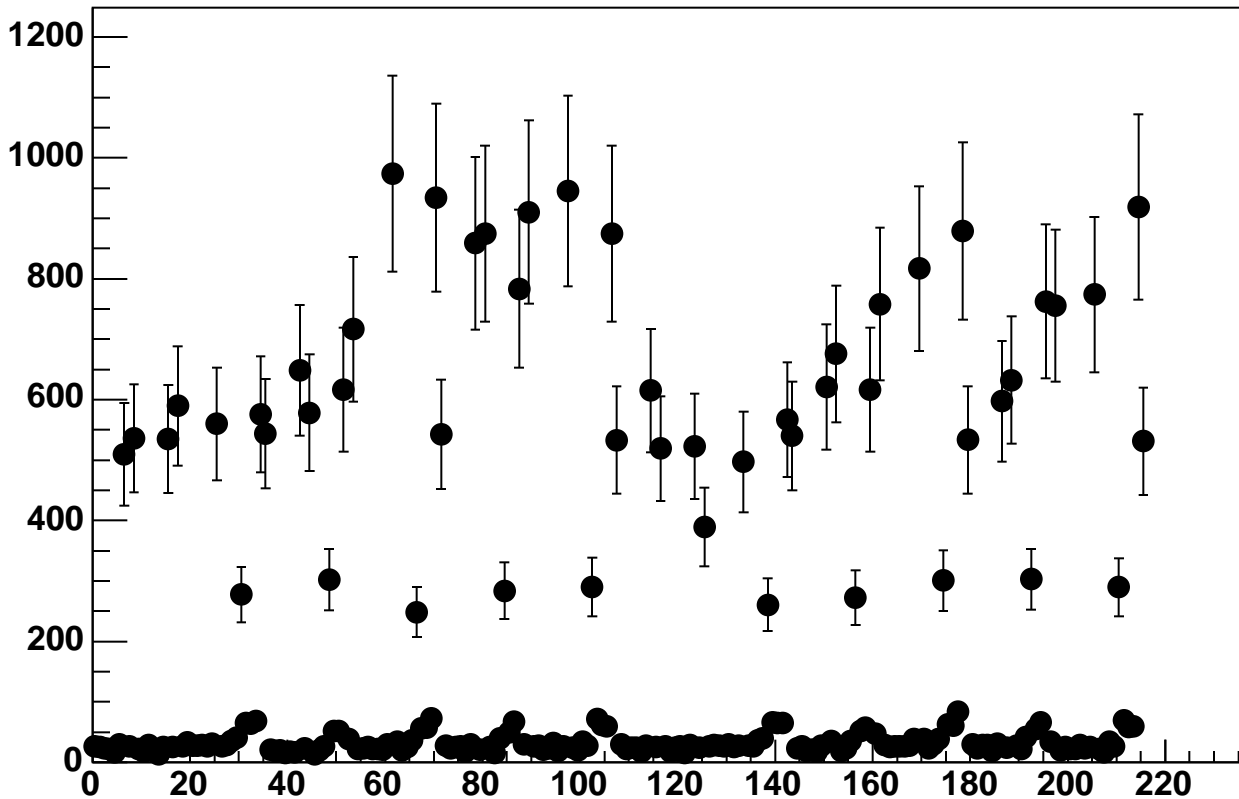
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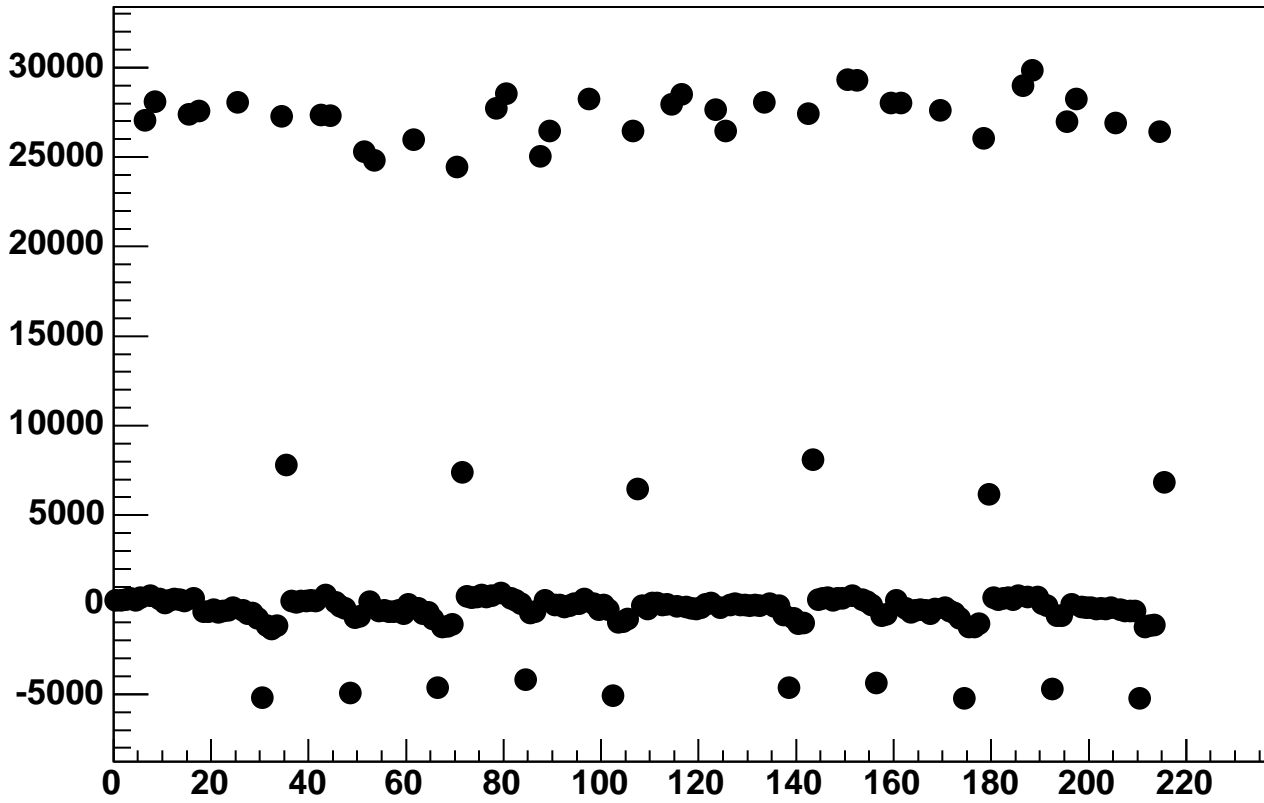
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18\*Chip+Chan



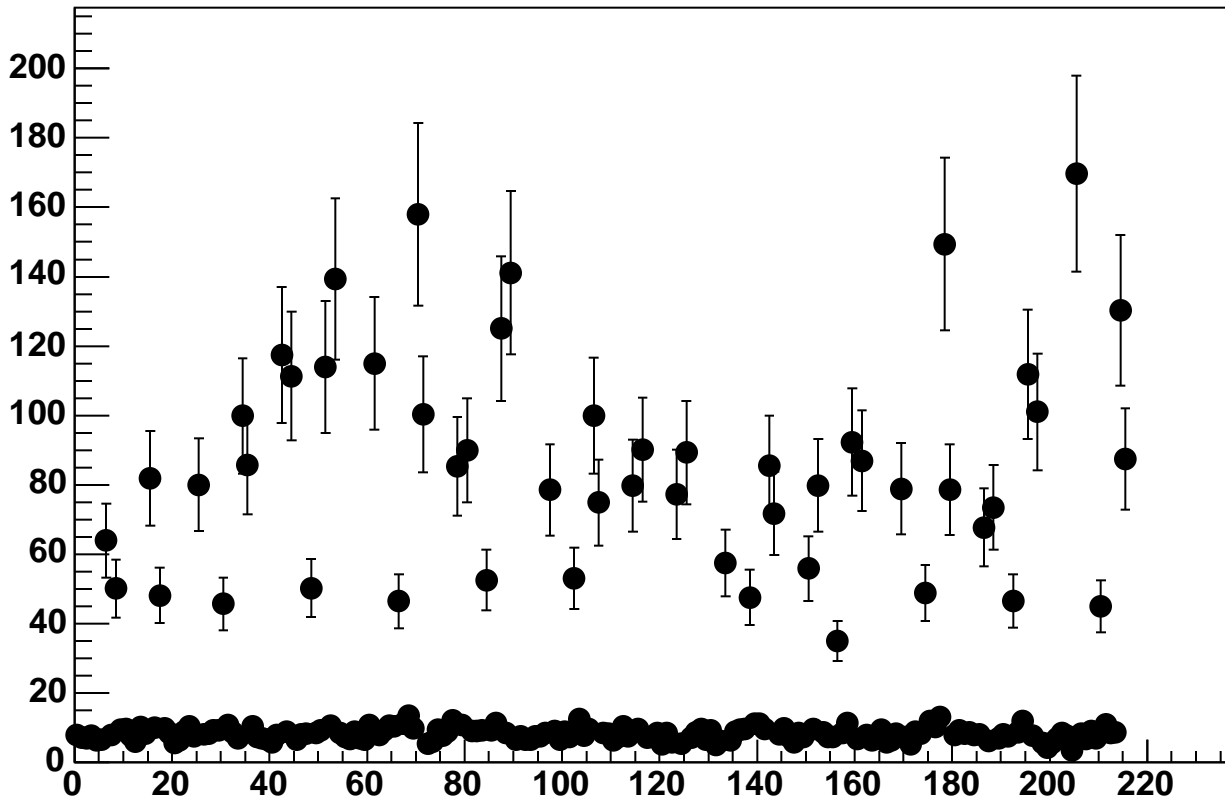
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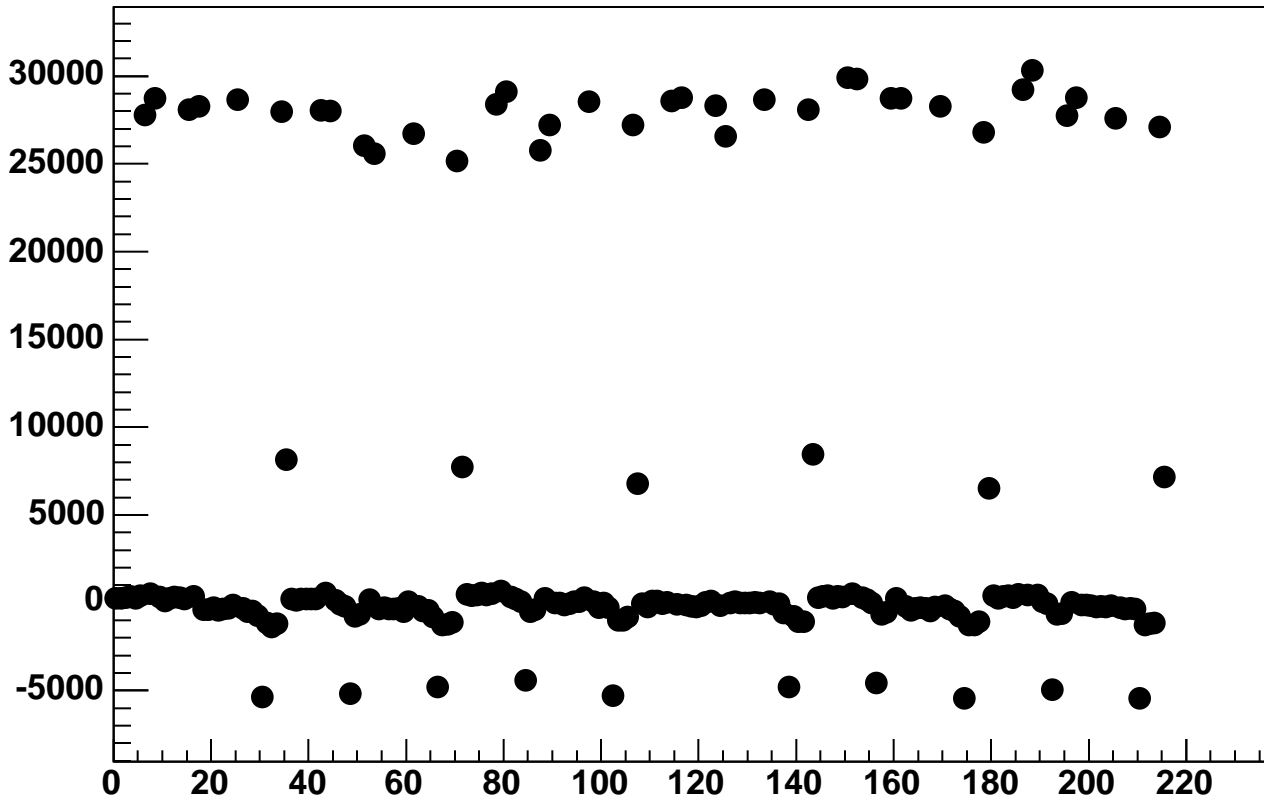
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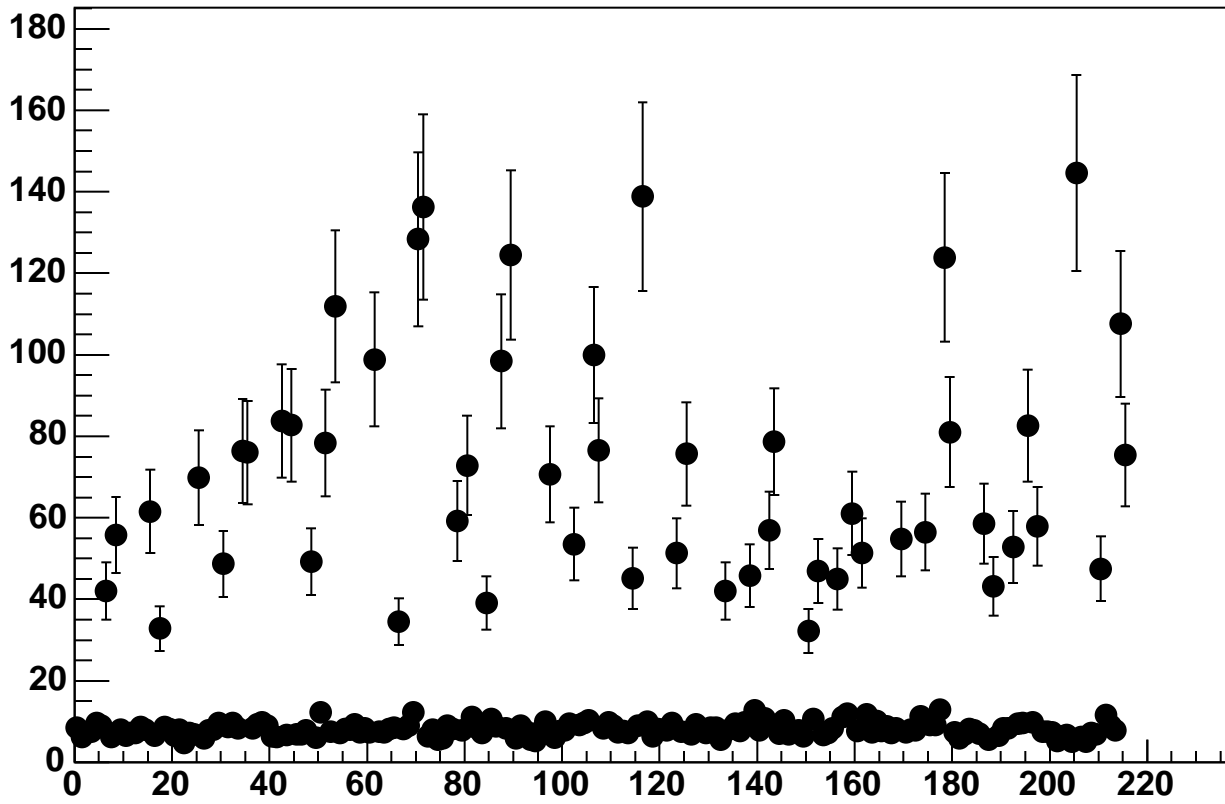
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18\*Chip+Chan



Enable 2, Hold=35, DAC=13200, ADC Mean vs 18\*Chip+Chan

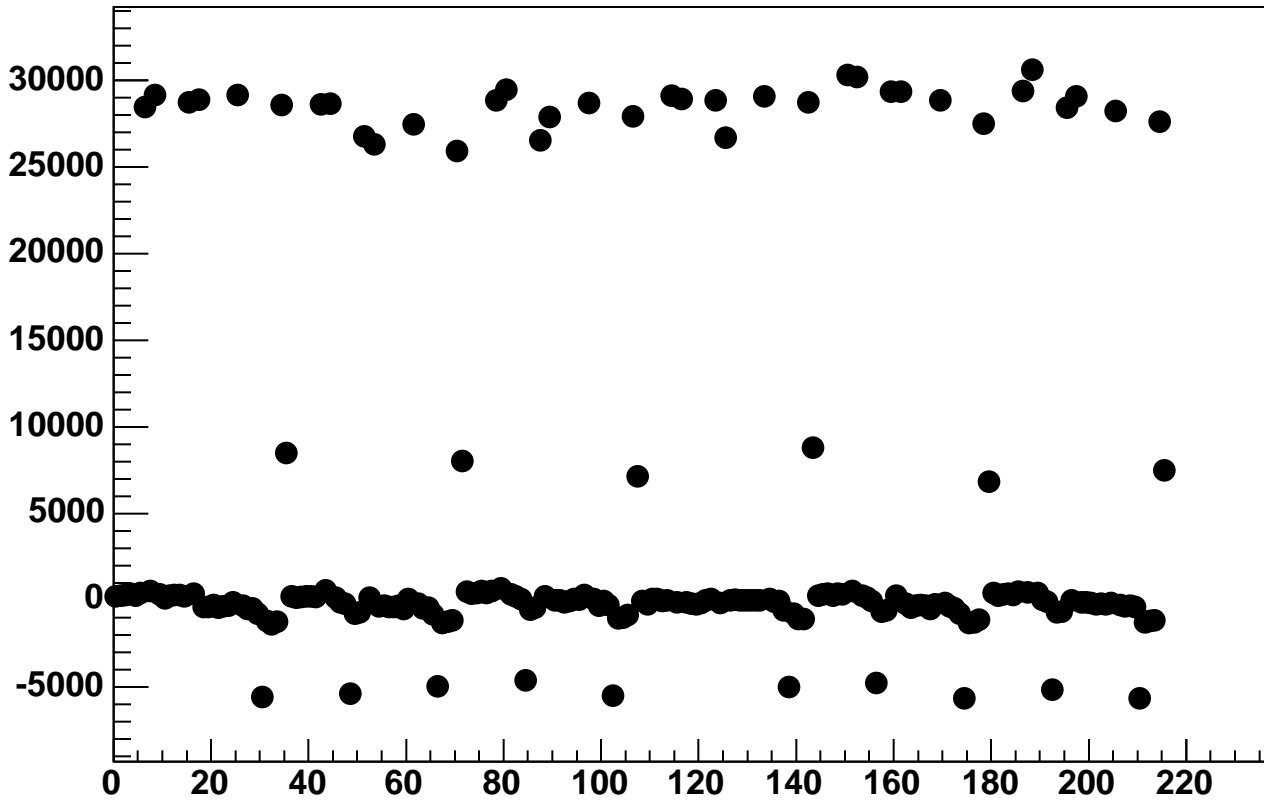


Enable 2, Hold=35, DAC=13200, ADC Noise vs 18\*Chip+Chan

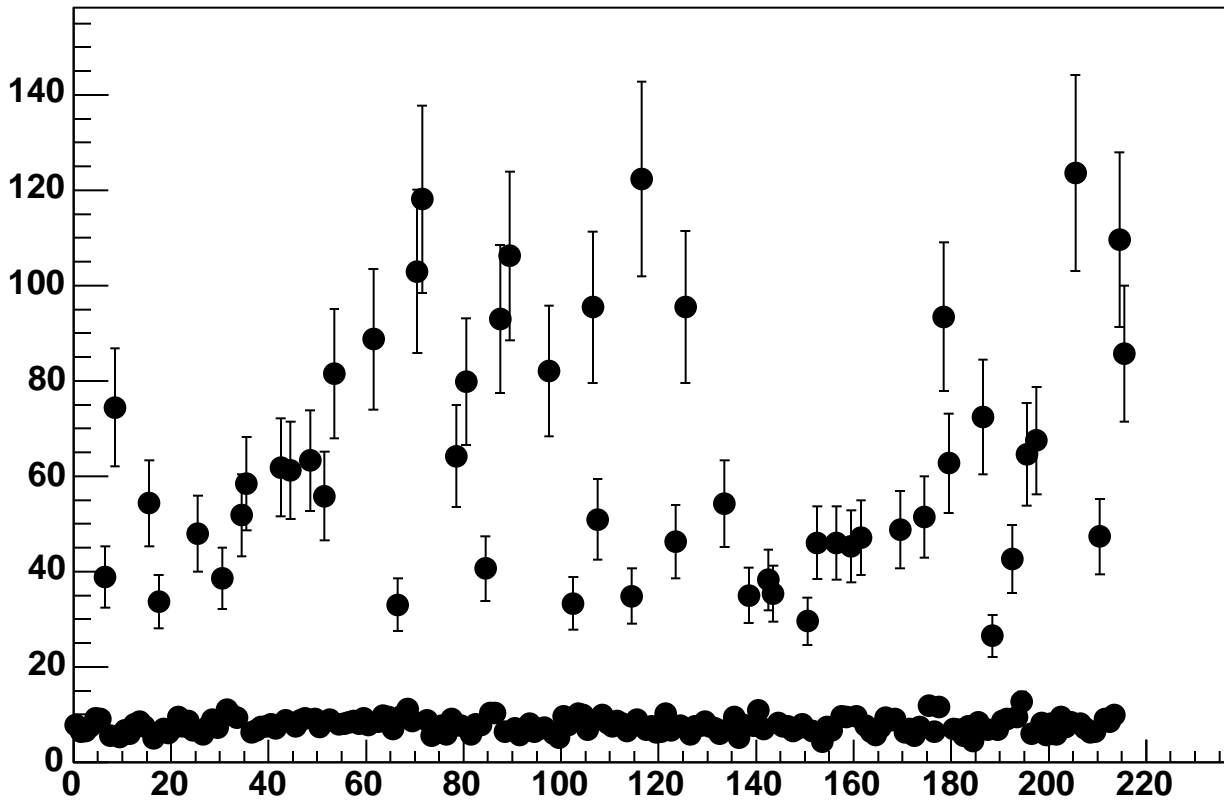




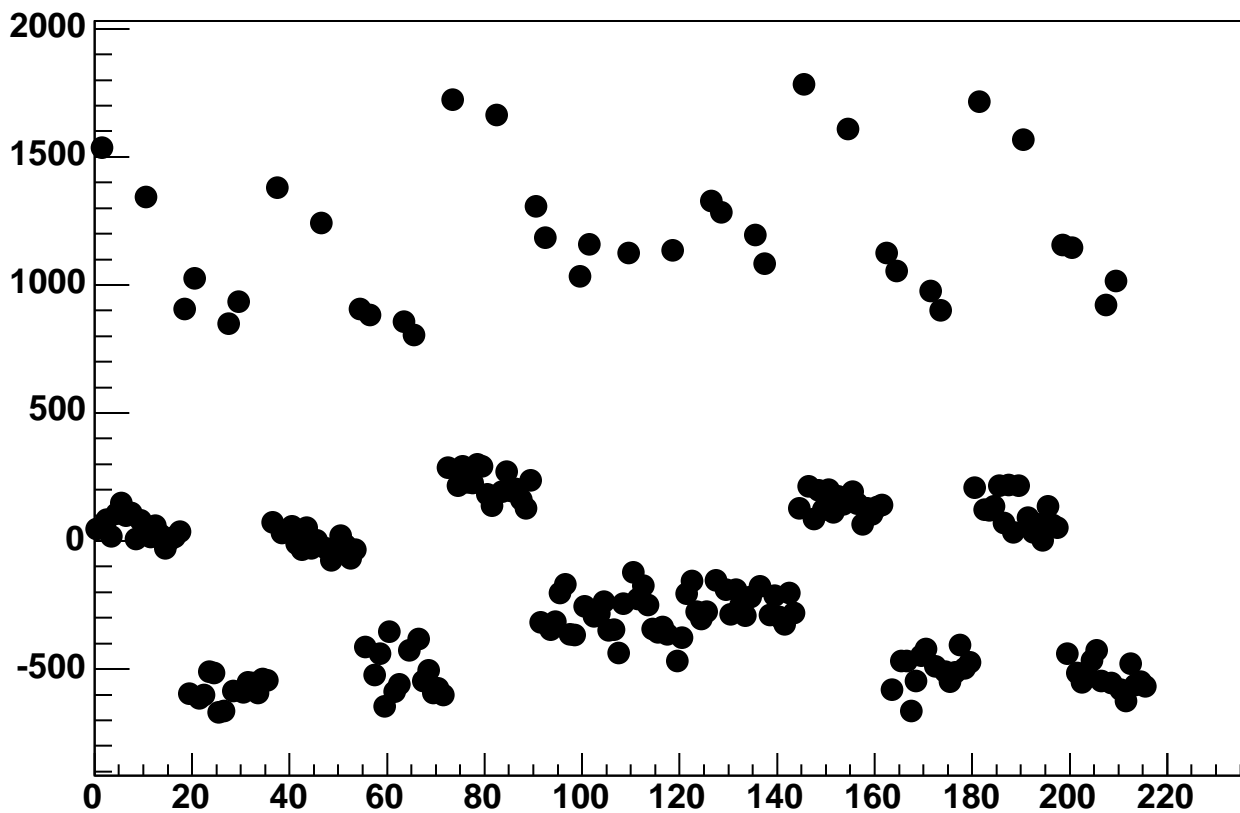
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18\*Chip+Chan



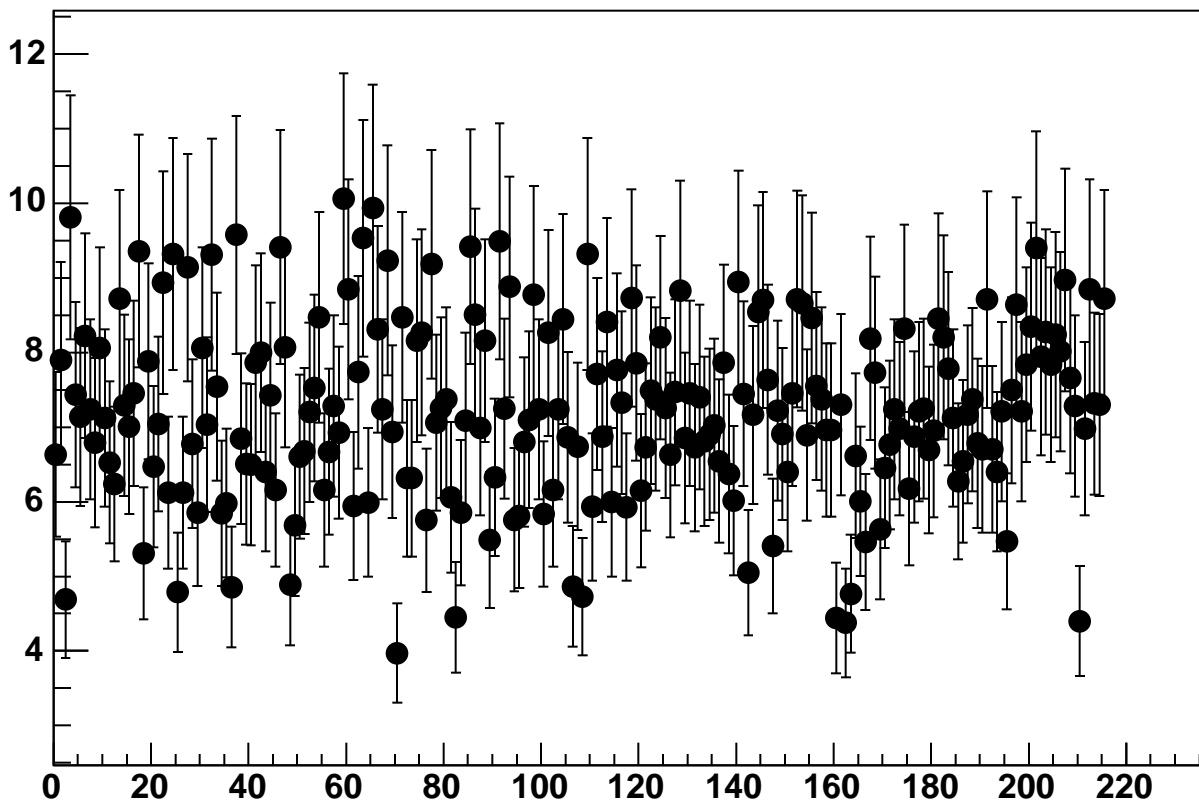
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18\*Chip+Chan



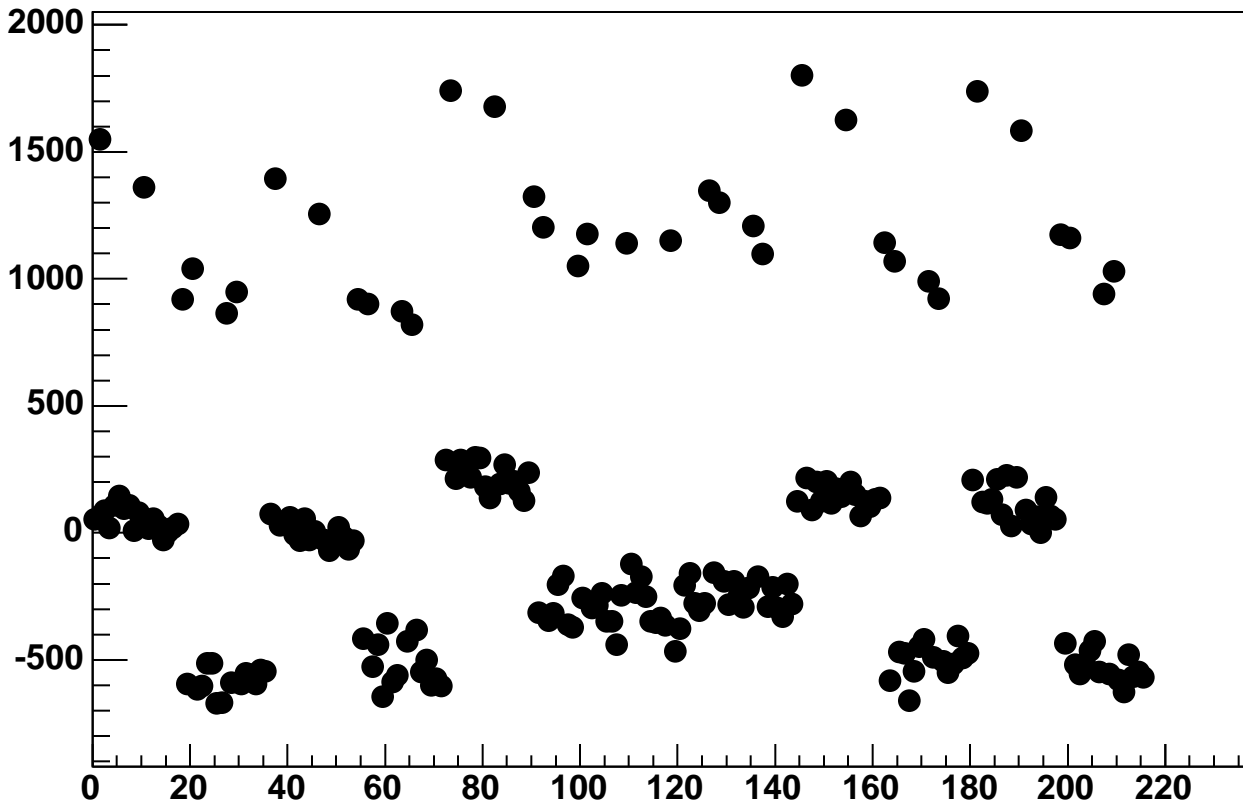
Enable 3, Hold=35, DAC=0, ADC Mean vs 18\*Chip+Chan



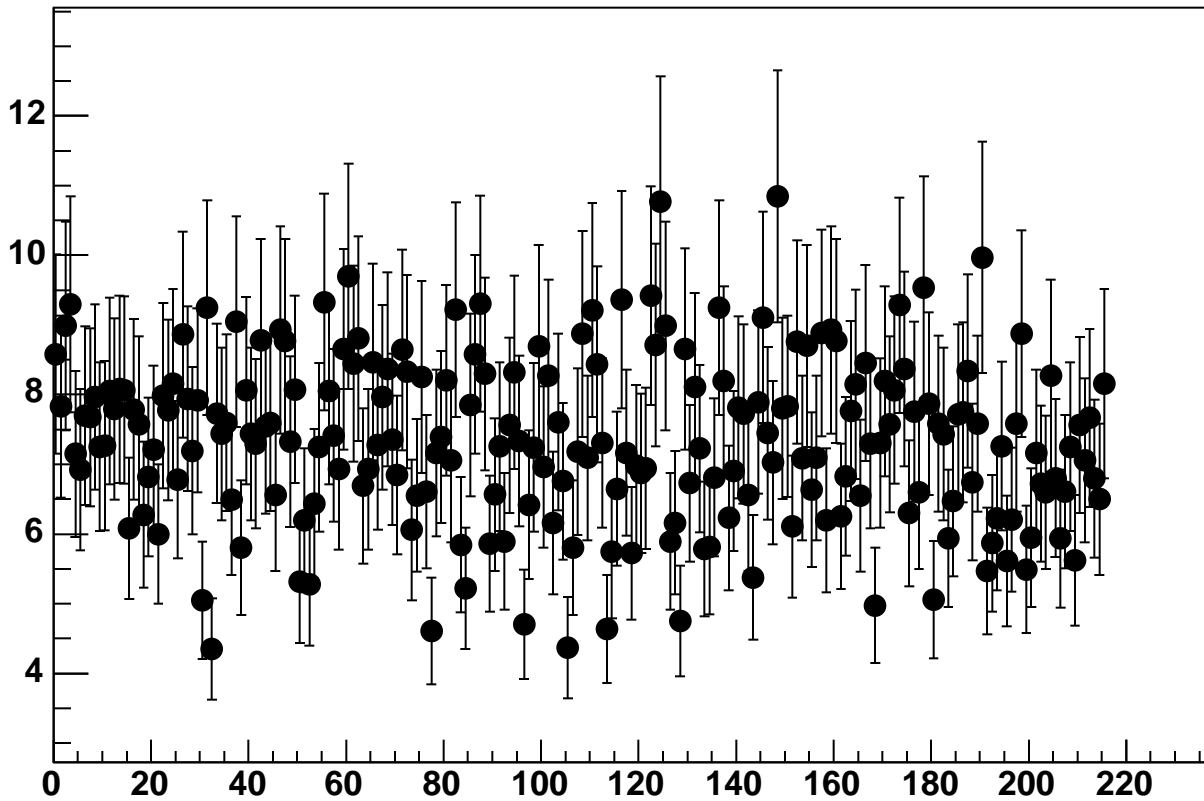
Enable 3, Hold=35, DAC=0, ADC Noise vs 18\*Chip+Chan



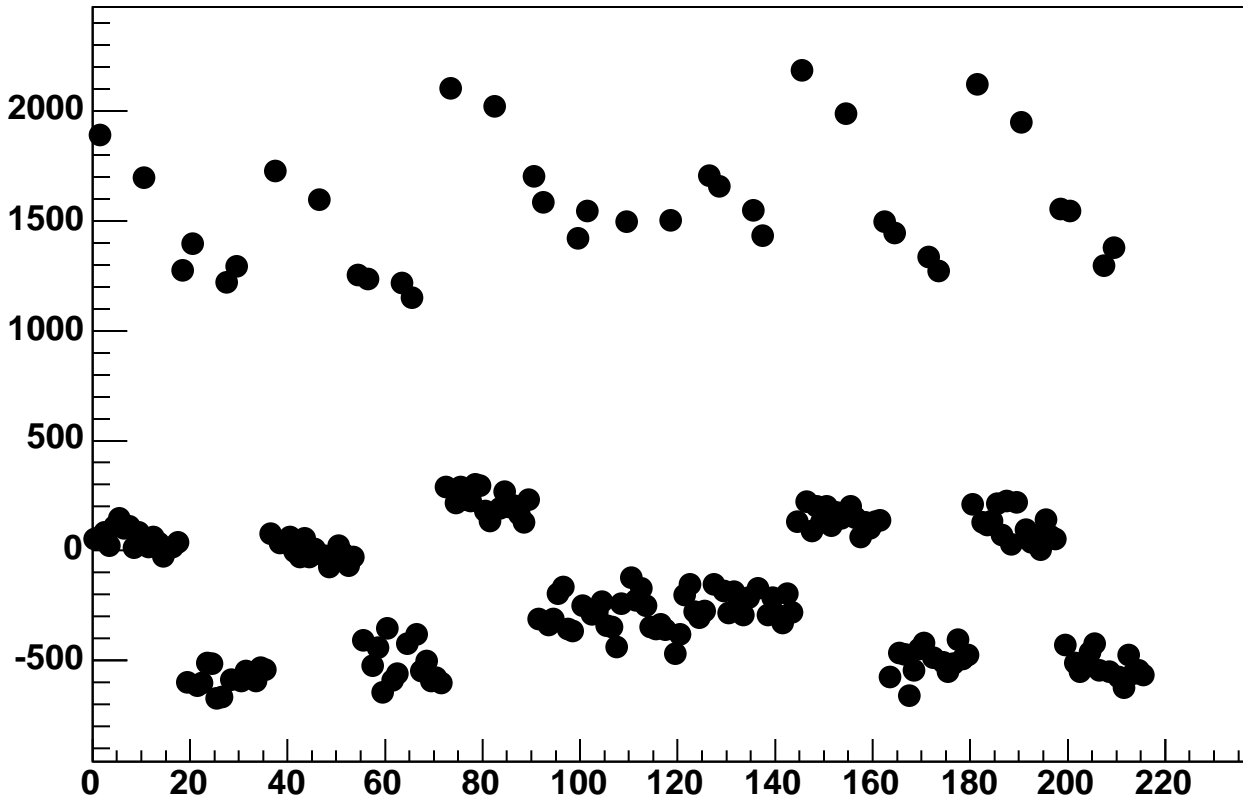
Enable 3, Hold=35, DAC=400, ADC Mean vs 18\*Chip+Chan



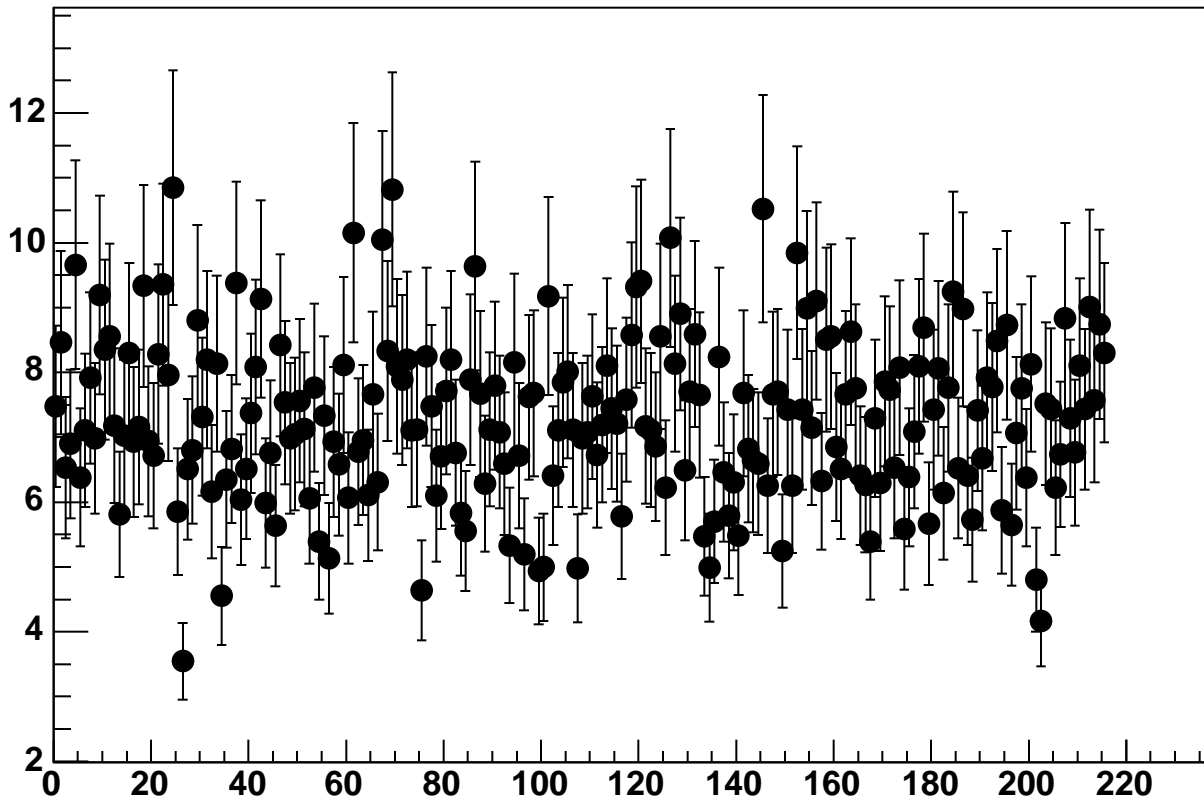
Enable 3, Hold=35, DAC=400, ADC Noise vs 18\*Chip+Chan



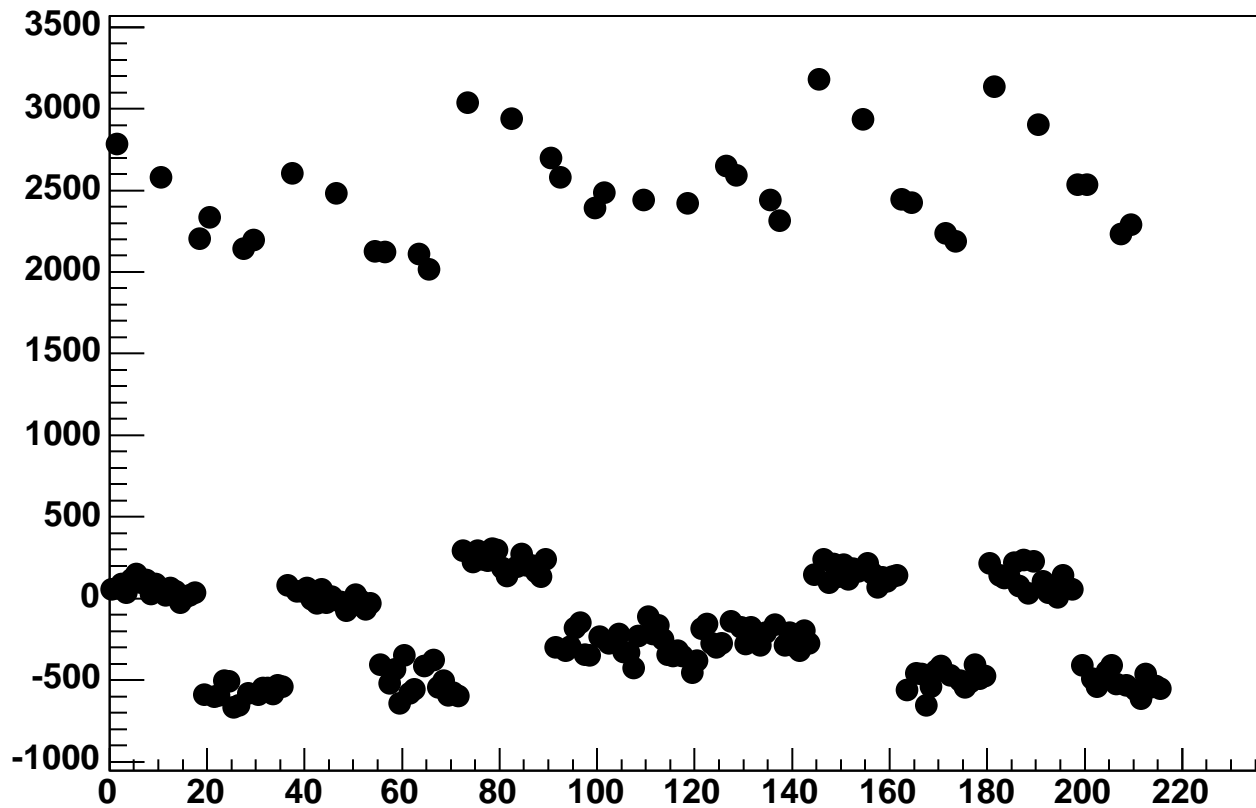
Enable 3, Hold=35, DAC=800, ADC Mean vs 18\*Chip+Chan



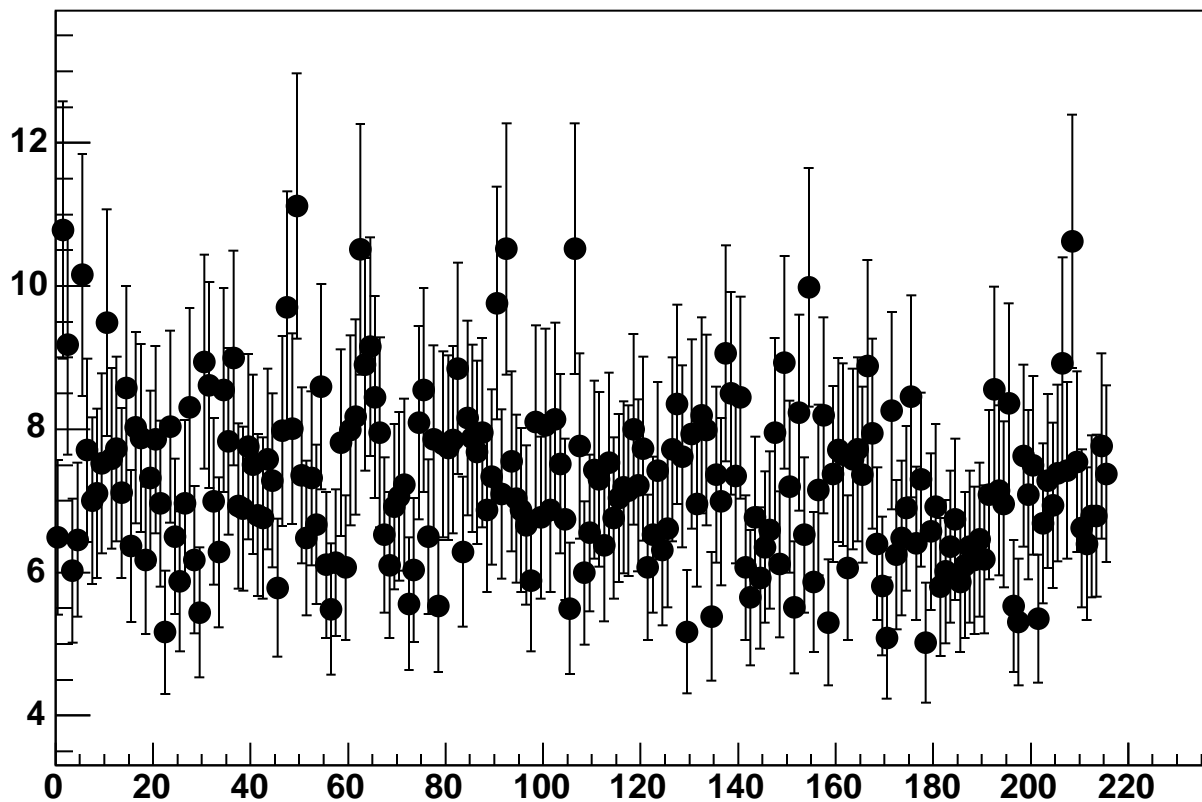
Enable 3, Hold=35, DAC=800, ADC Noise vs 18\*Chip+Chan



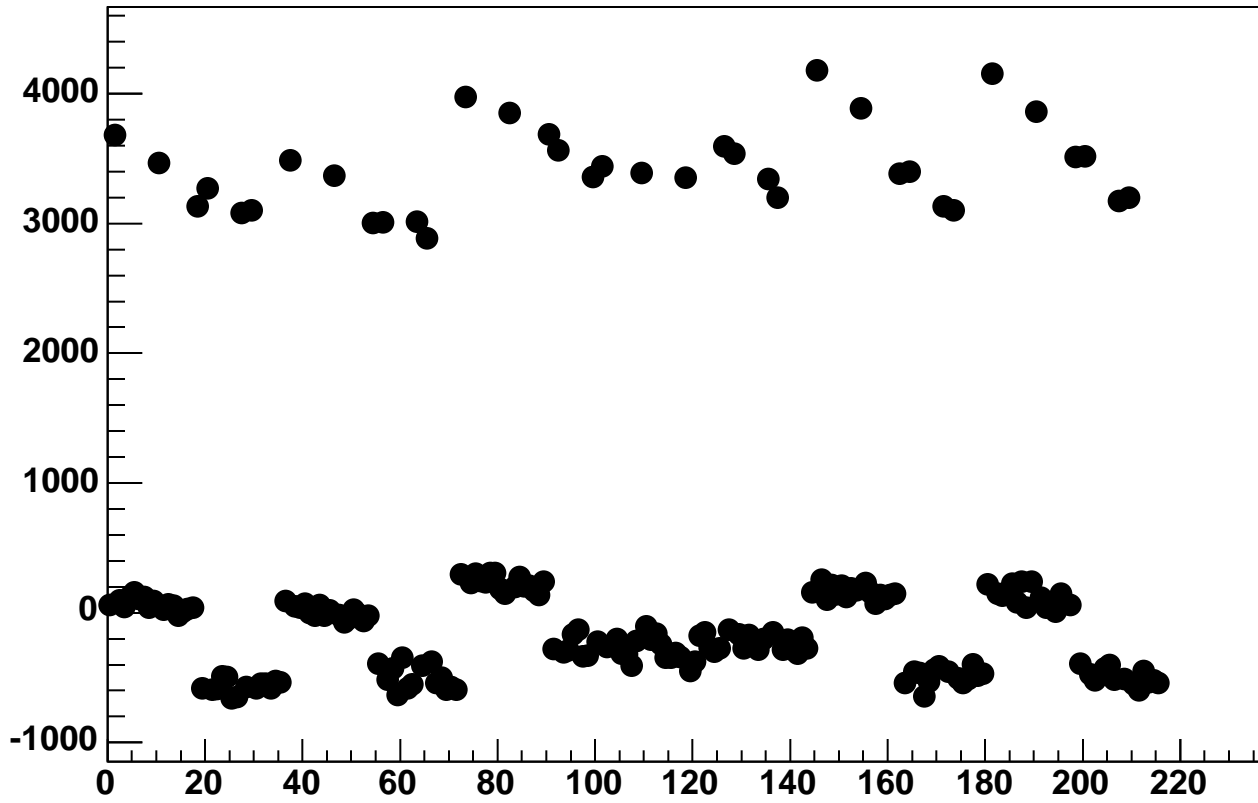
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18\*Chip+Chan



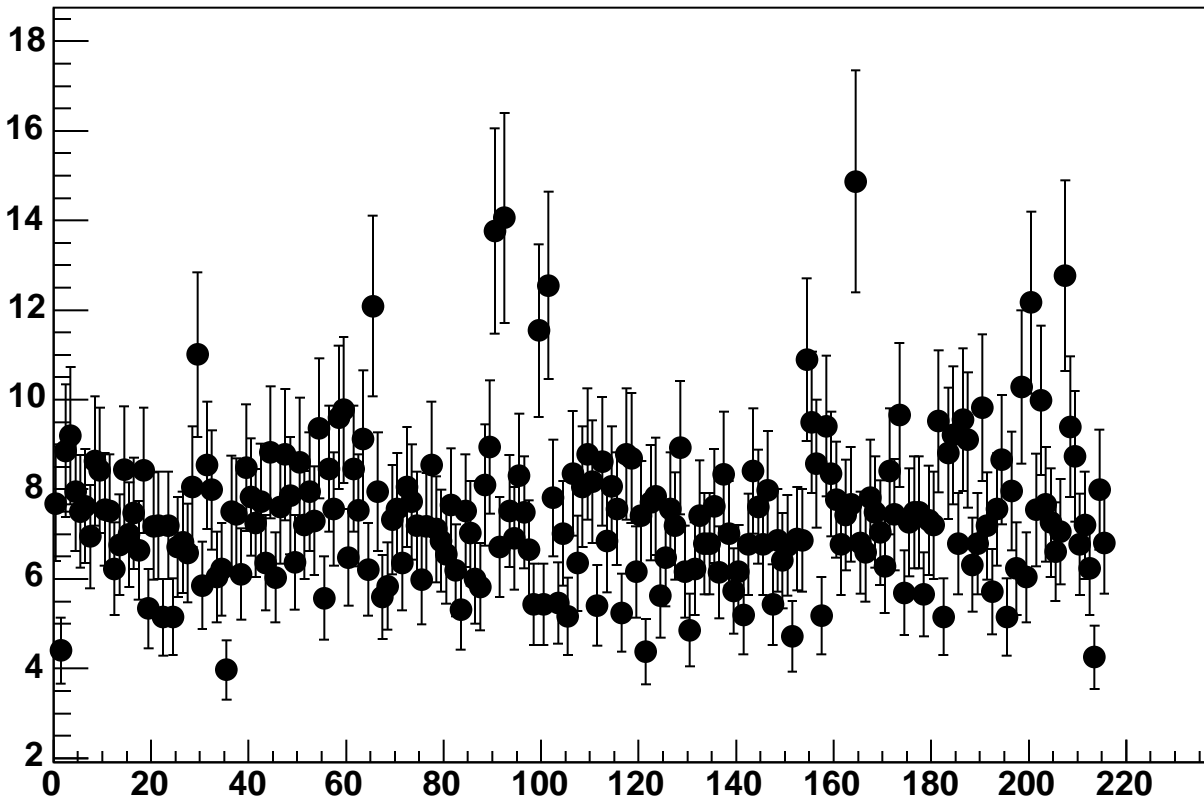
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18\*Chip+Chan



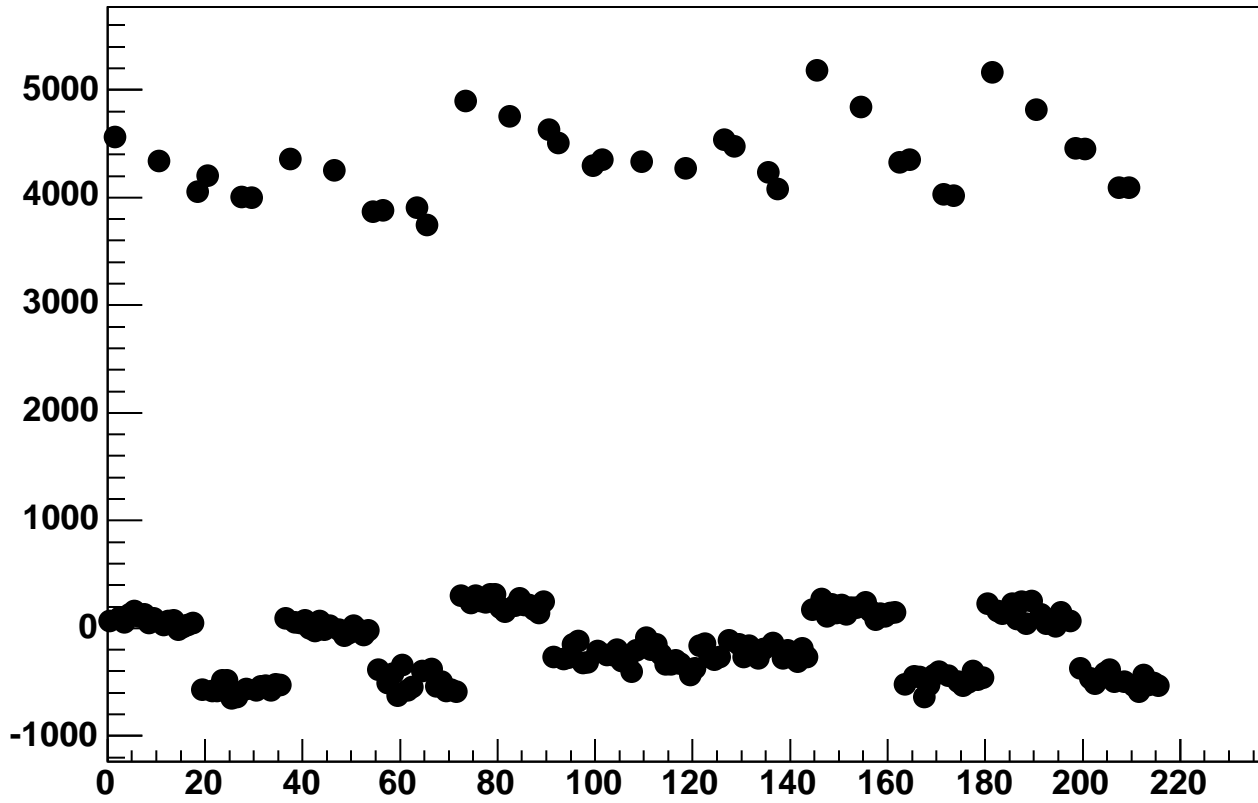
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18\*Chip+Chan



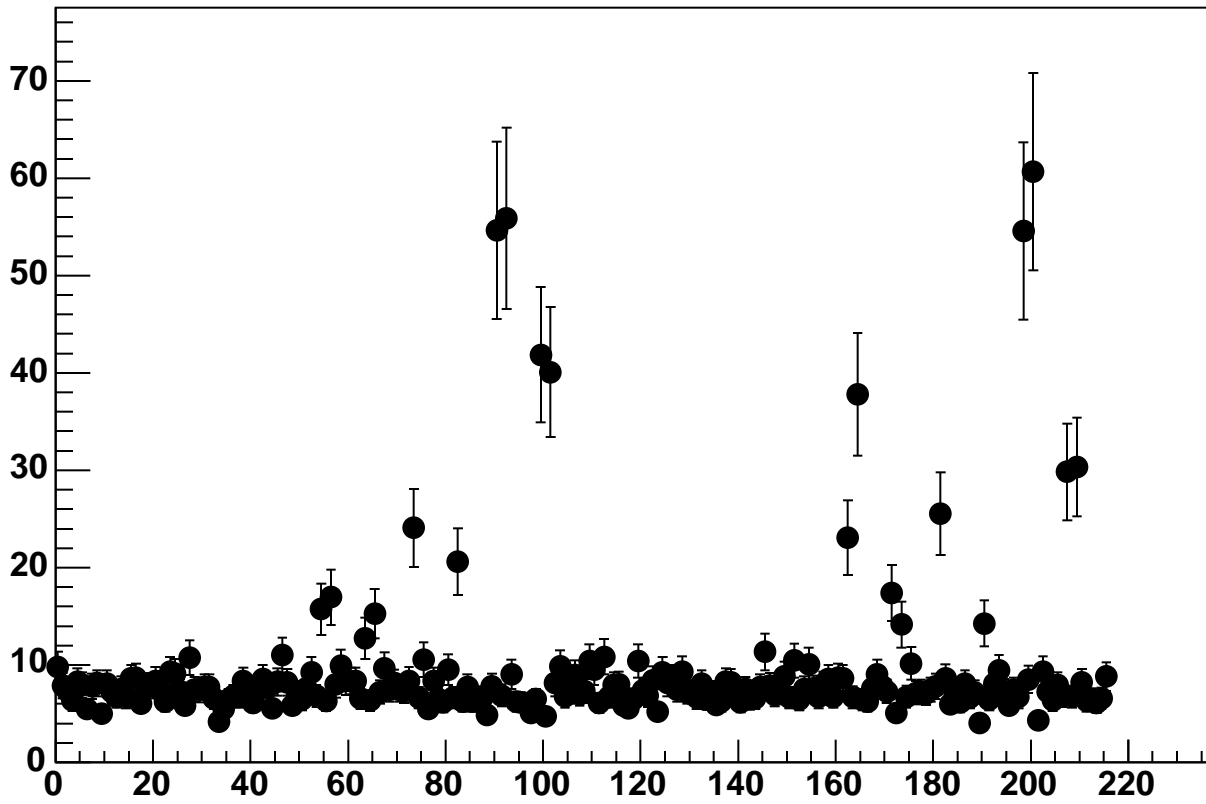
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18\*Chip+Chan



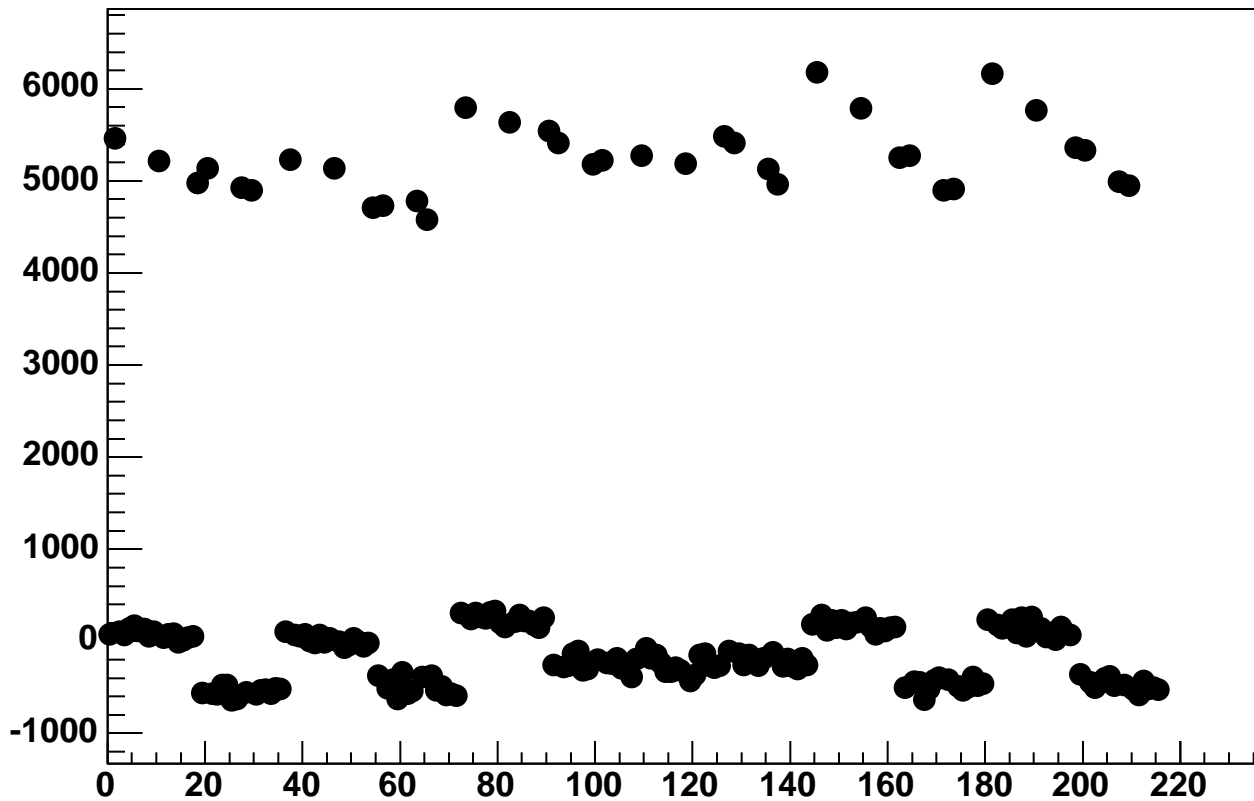
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18\*Chip+Chan



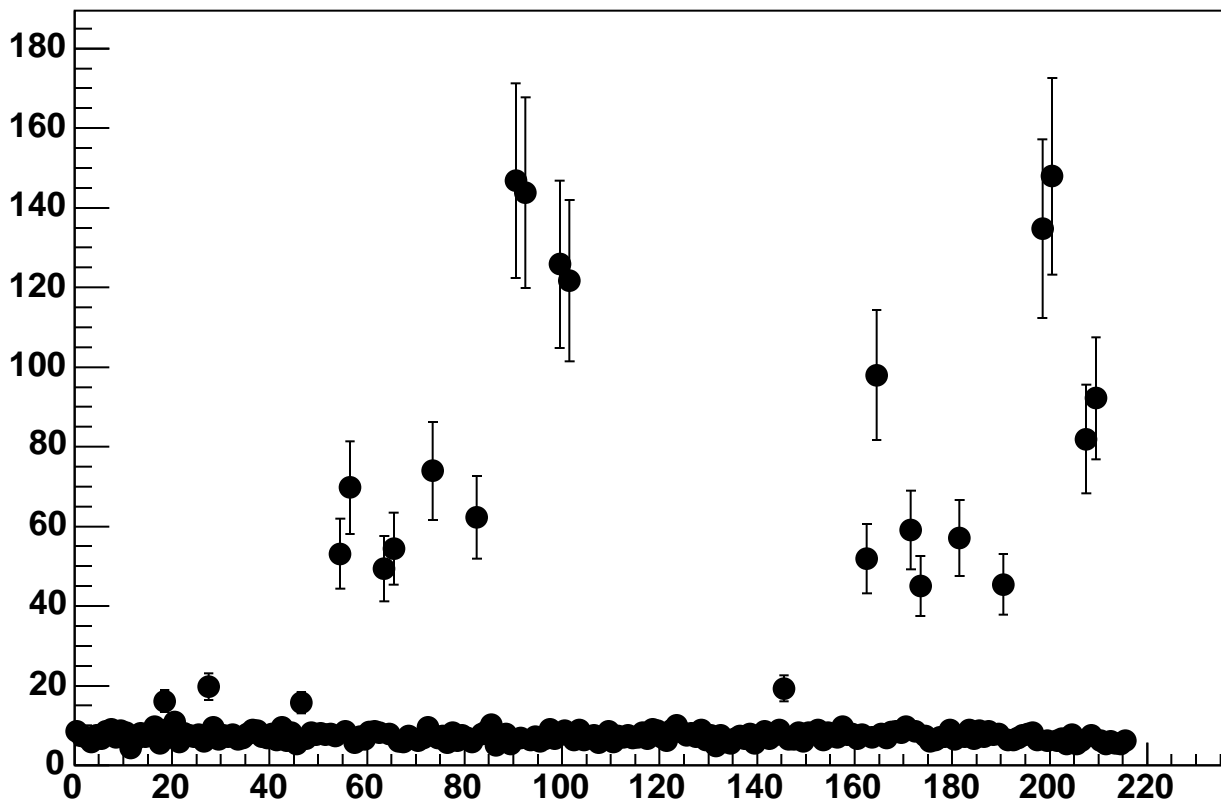
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=35, DAC=2400, ADC Mean vs 18\*Chip+Chan

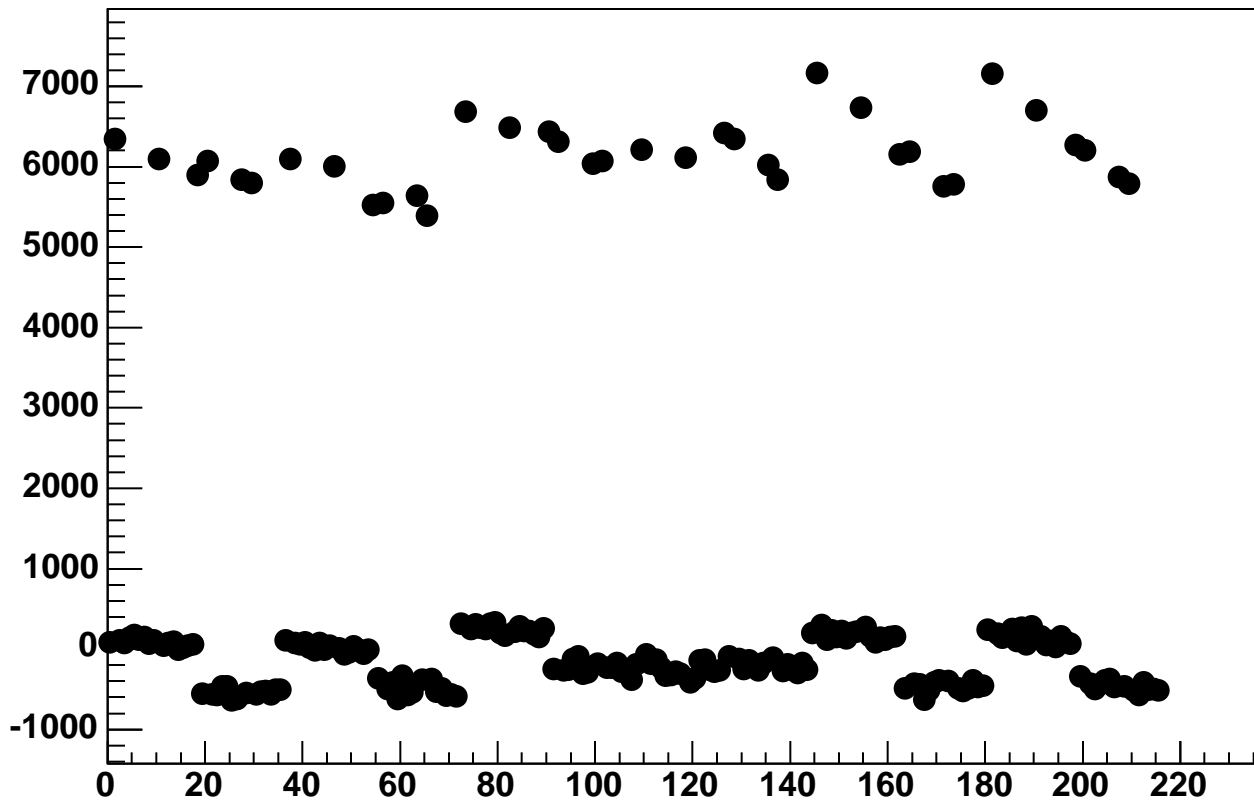


Enable 3, Hold=35, DAC=2400, ADC Noise vs 18\*Chip+Chan

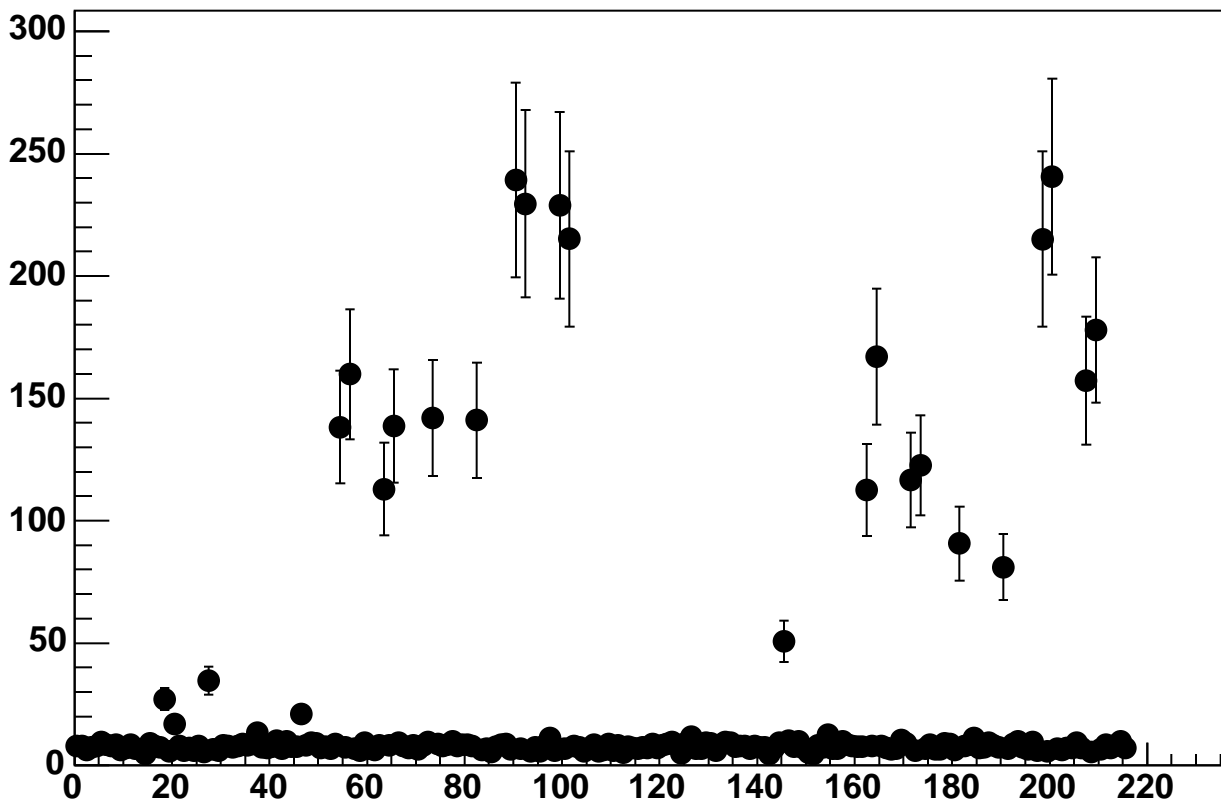




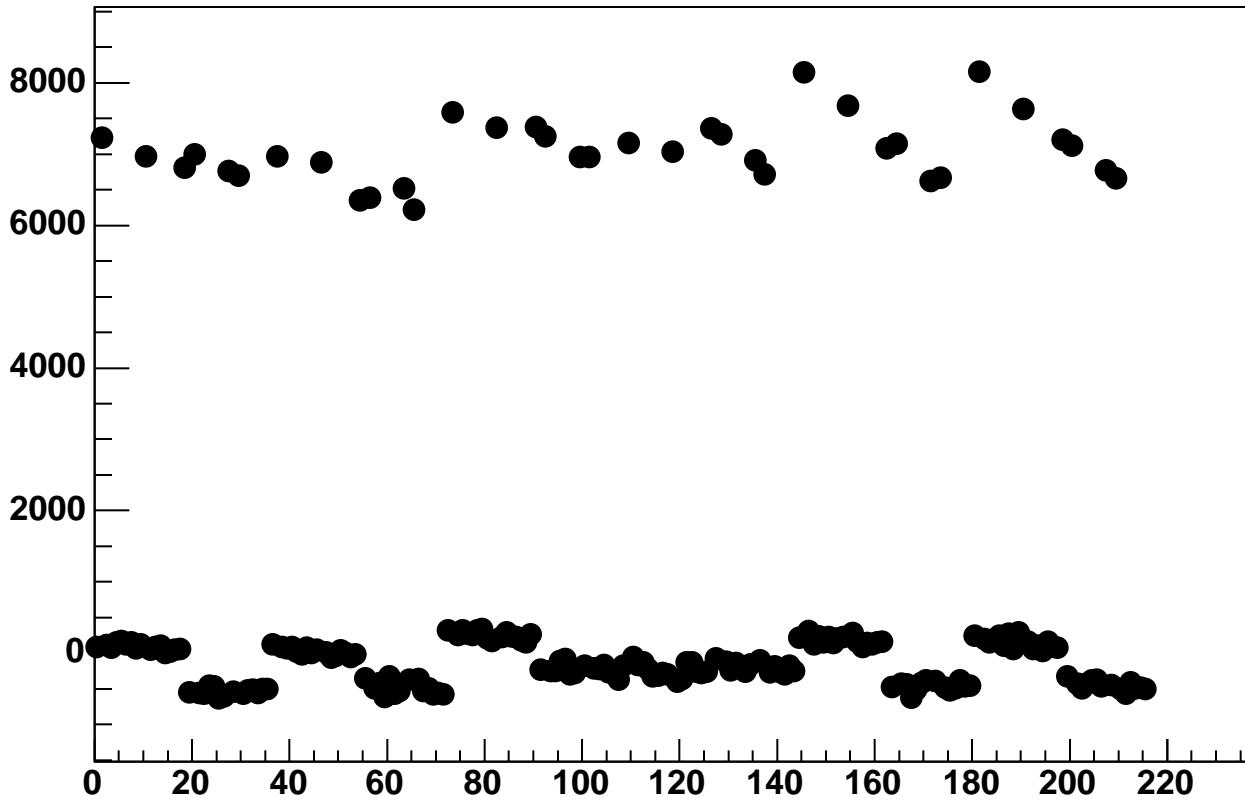
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18\*Chip+Chan



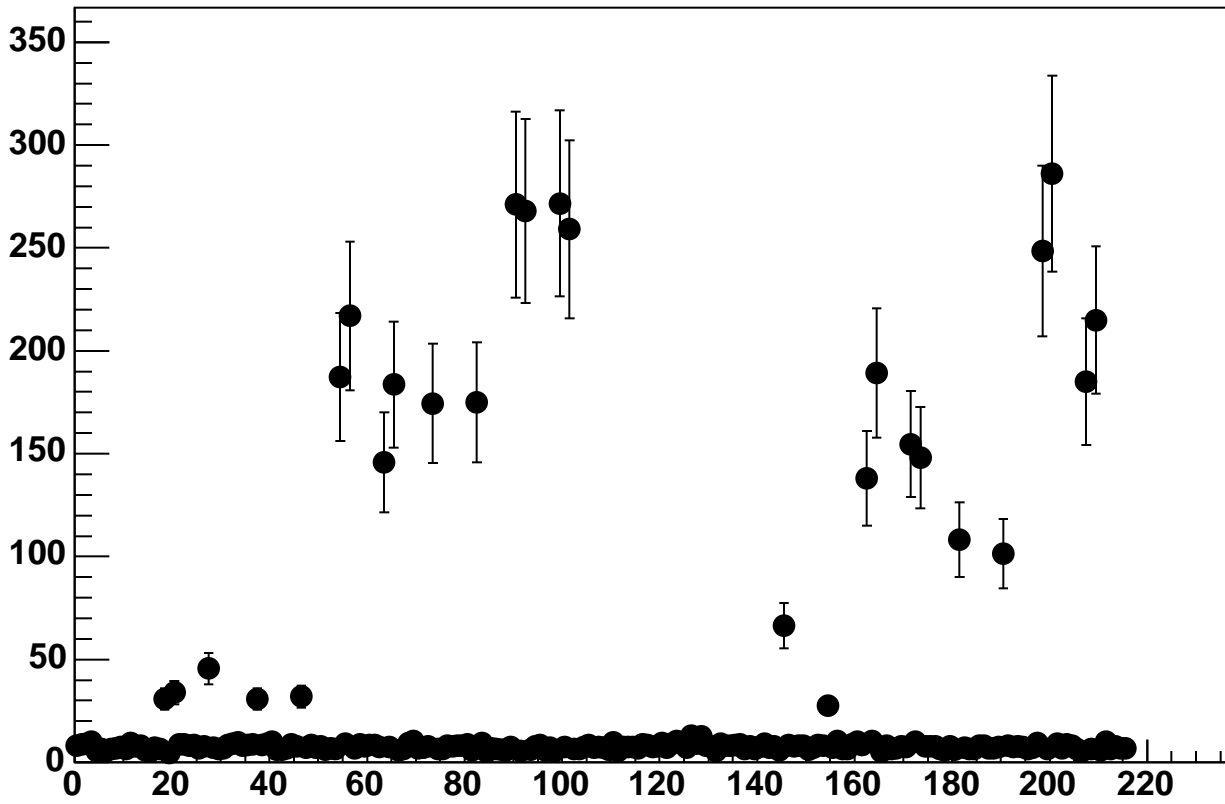
Enable 3, Hold=35, DAC=2800, ADC Noise vs 18\*Chip+Chan



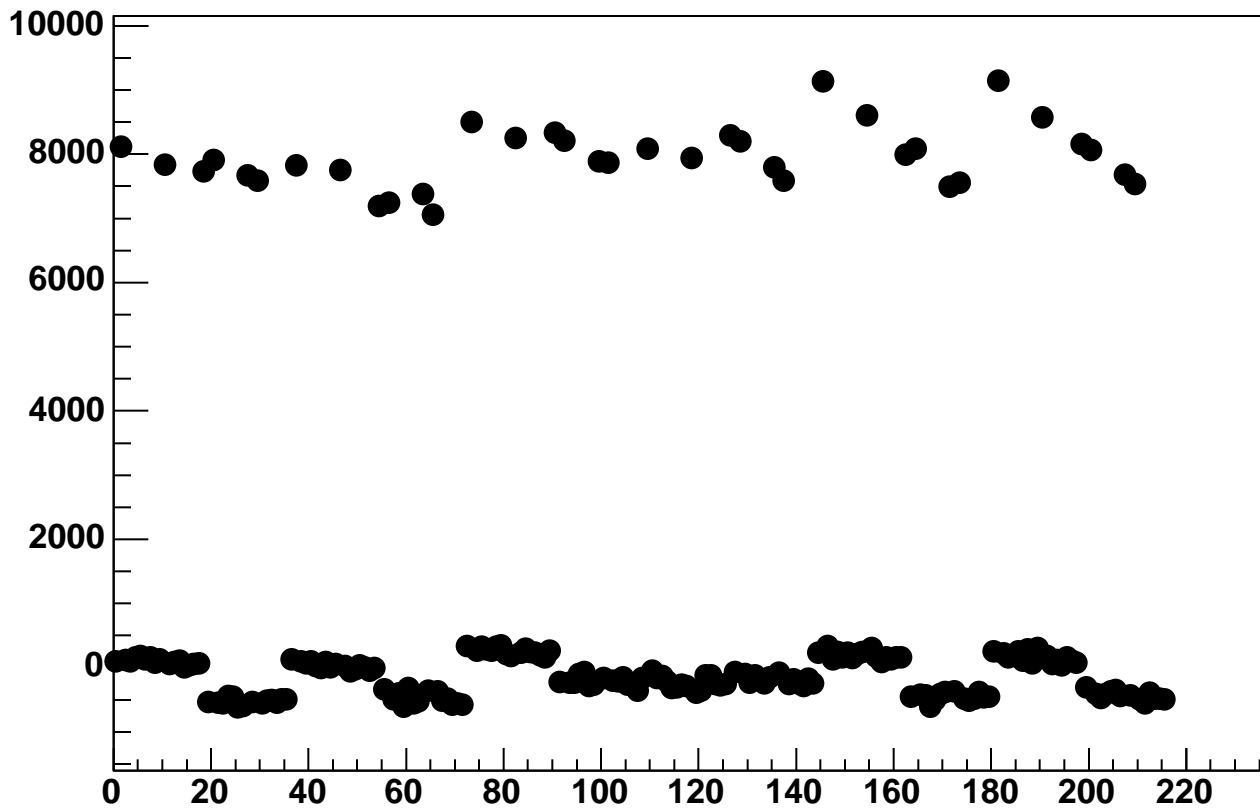
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18\*Chip+Chan



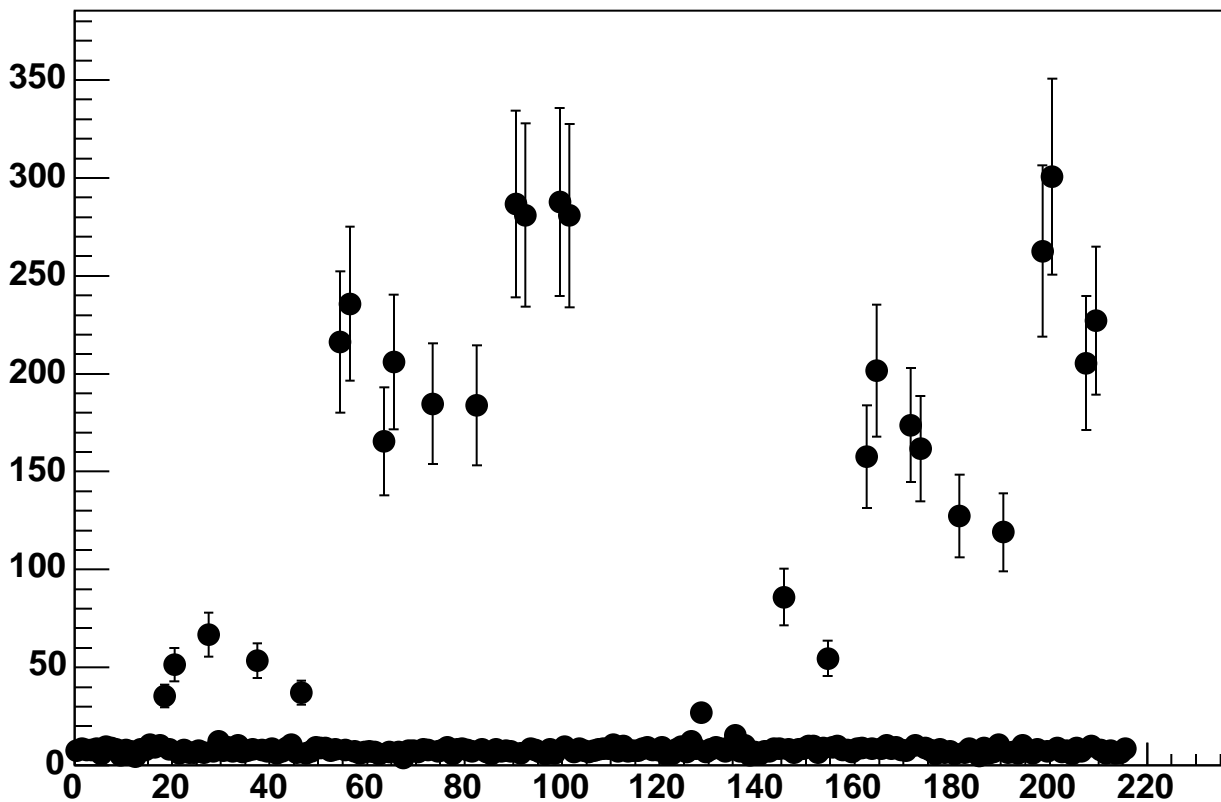
Enable 3, Hold=35, DAC=3200, ADC Noise vs 18\*Chip+Chan



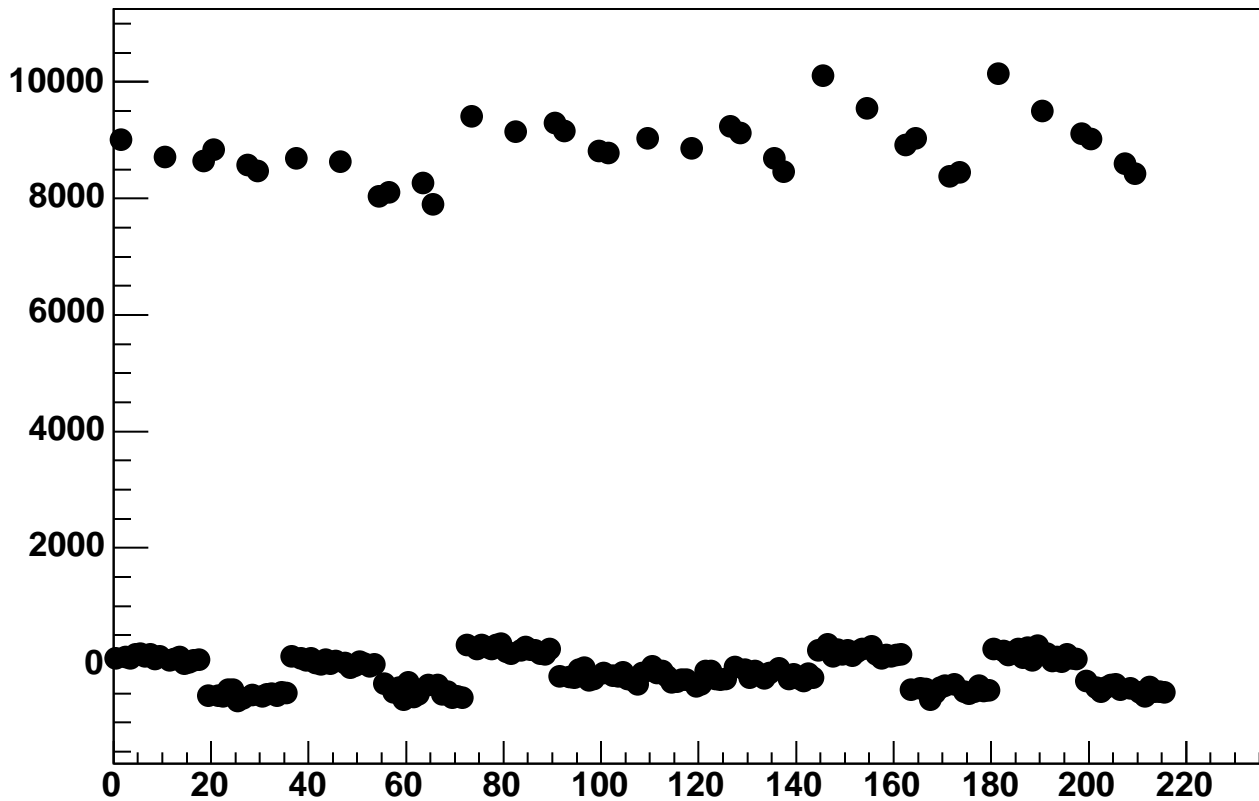
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18\*Chip+Chan



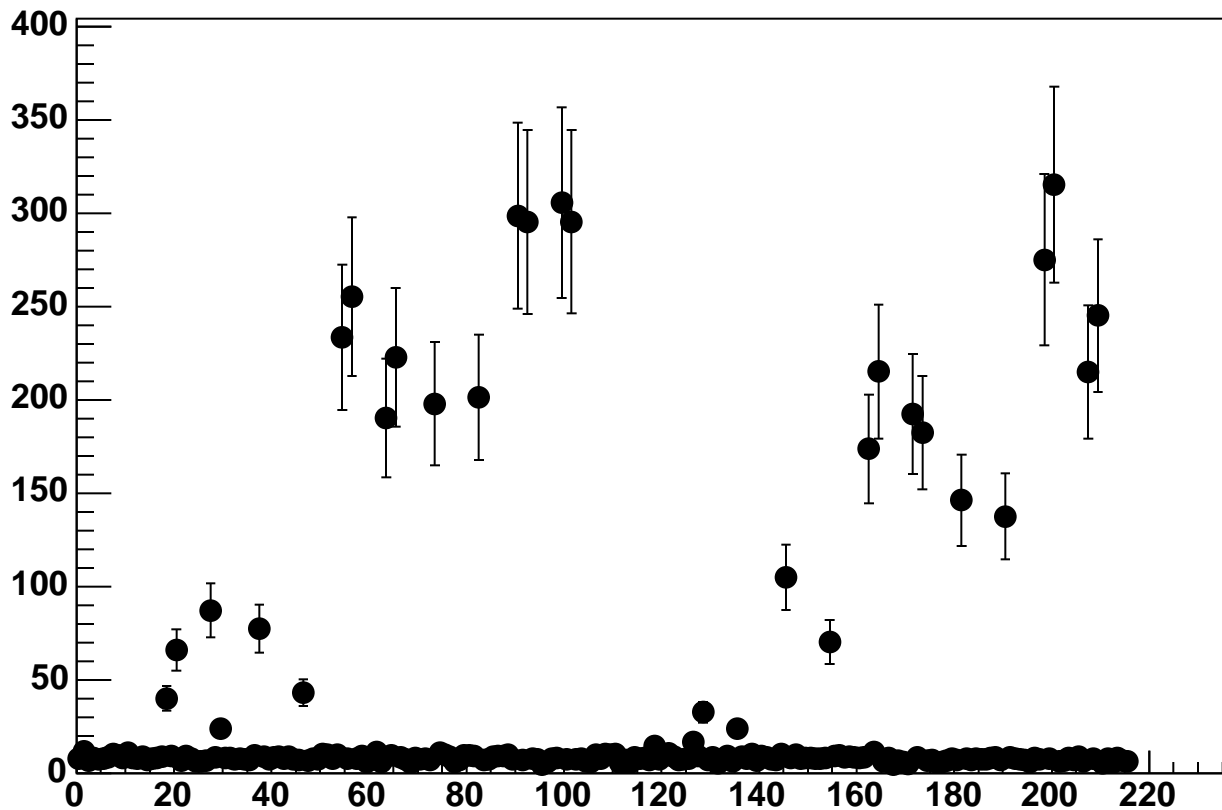
Enable 3, Hold=35, DAC=3600, ADC Noise vs 18\*Chip+Chan



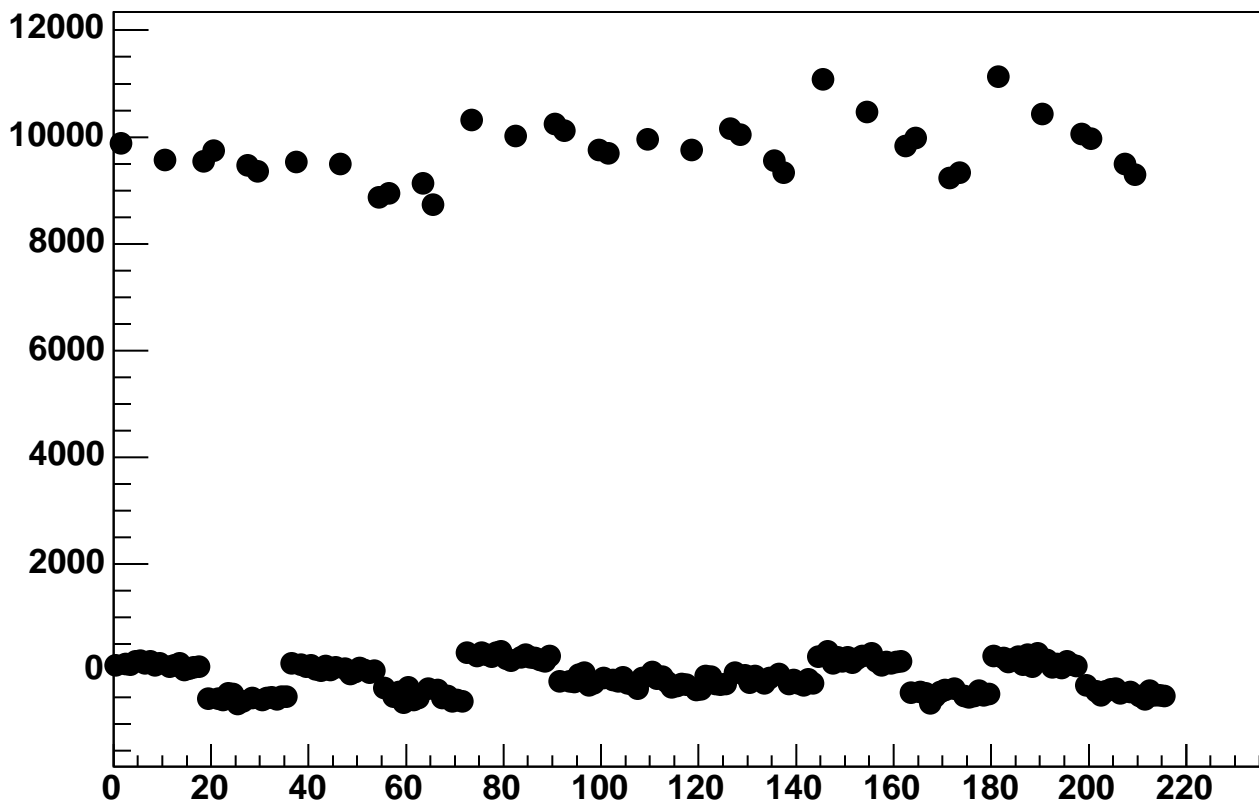
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18\*Chip+Chan



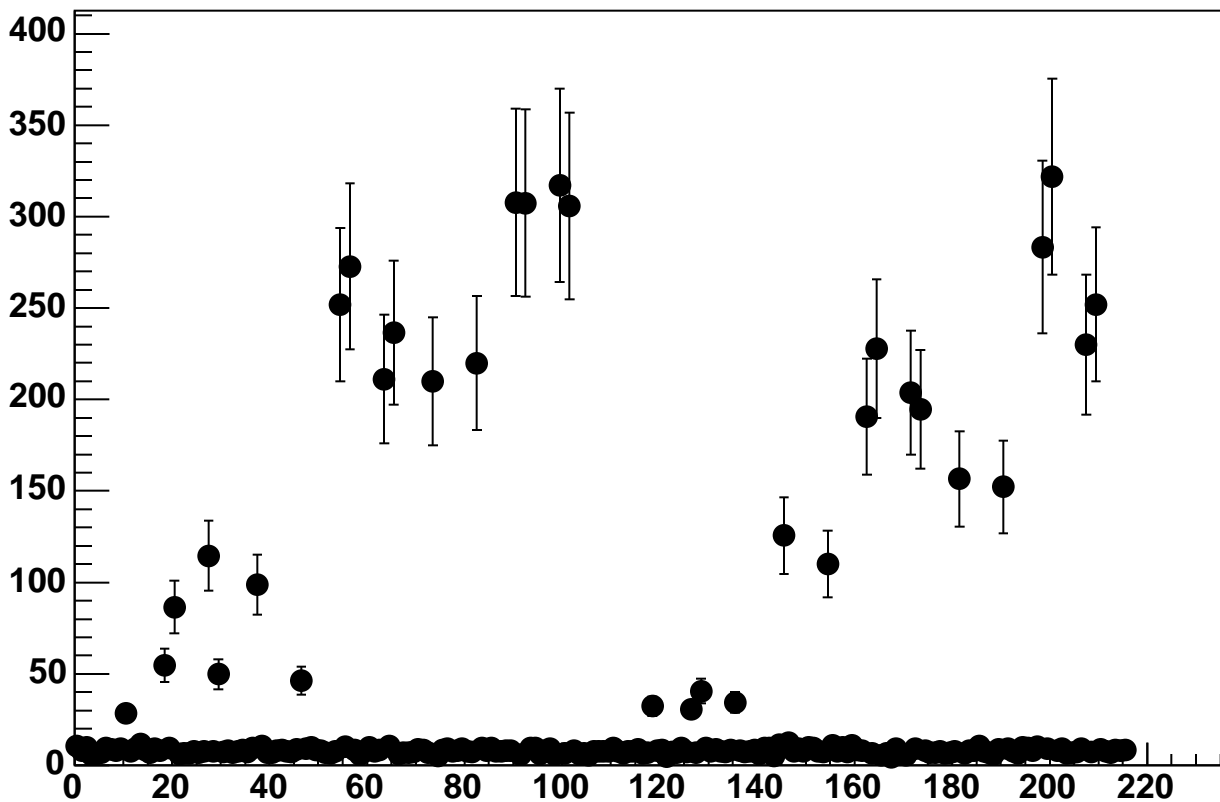
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18\*Chip+Chan



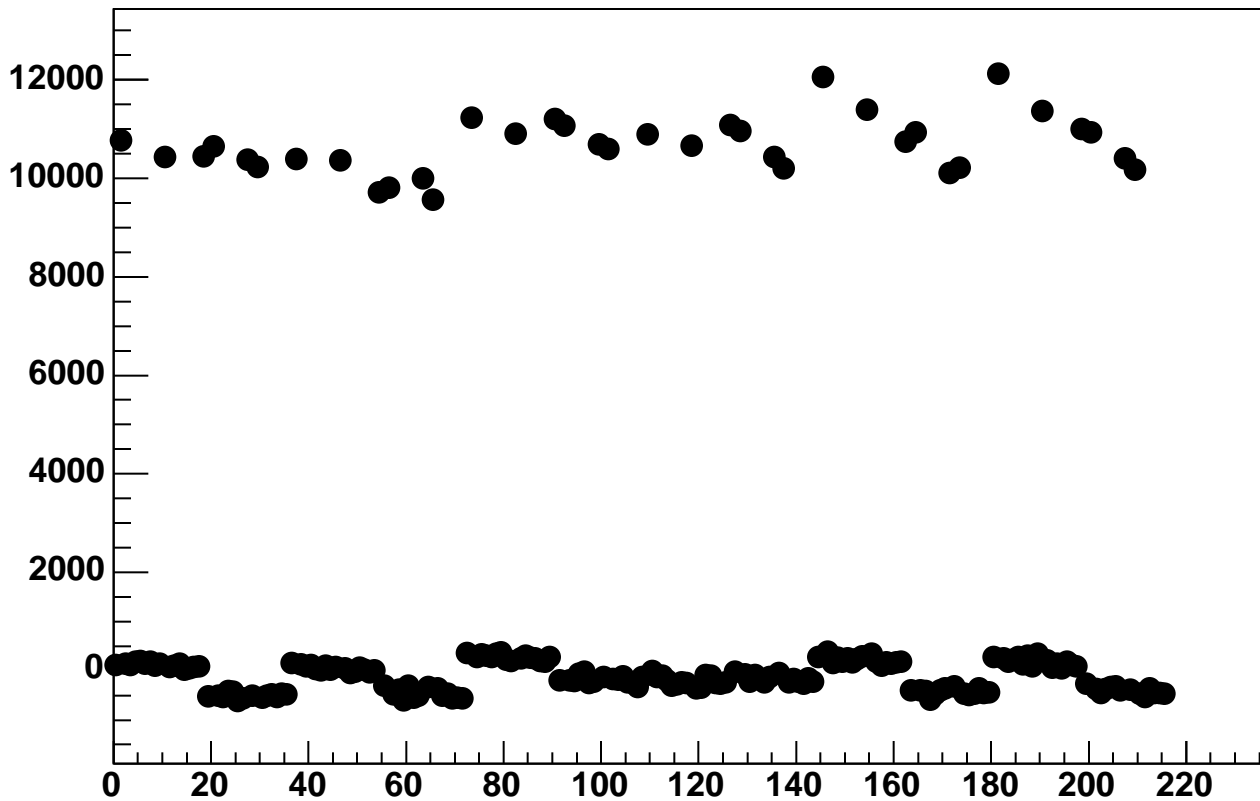
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18\*Chip+Chan



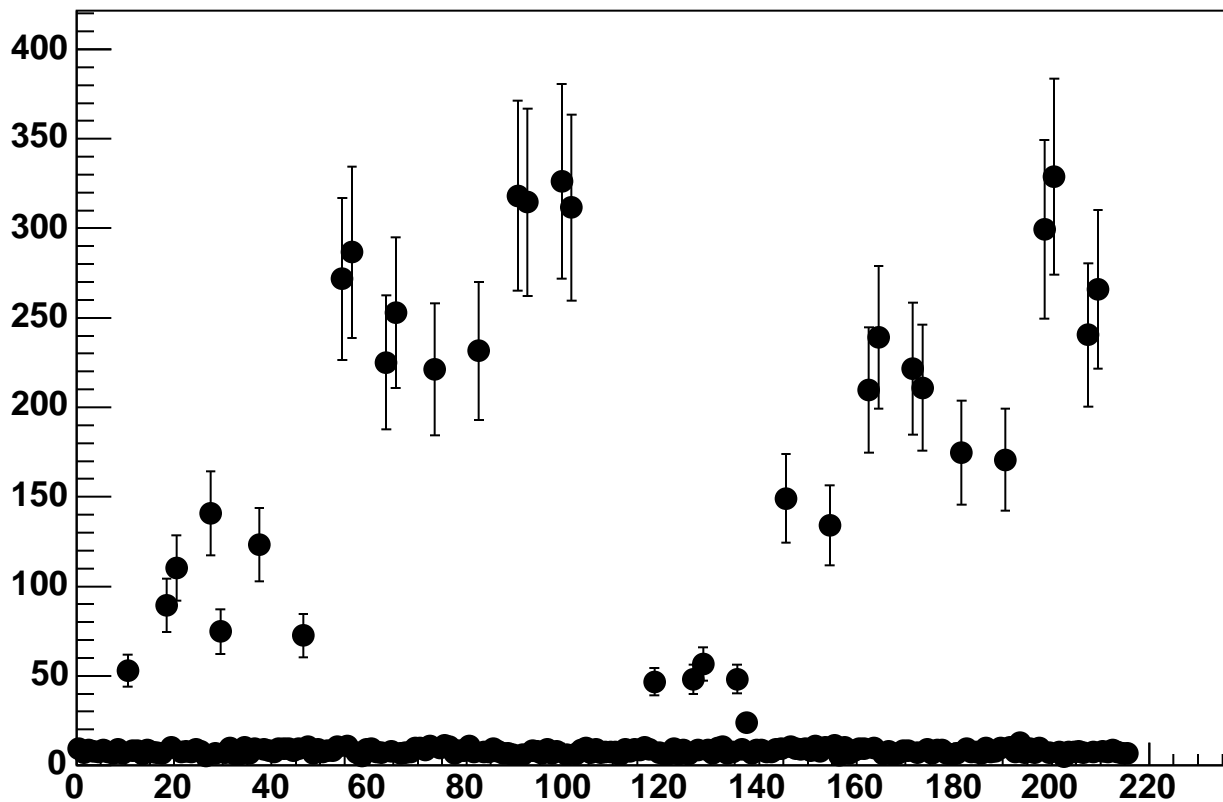
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18\*Chip+Chan



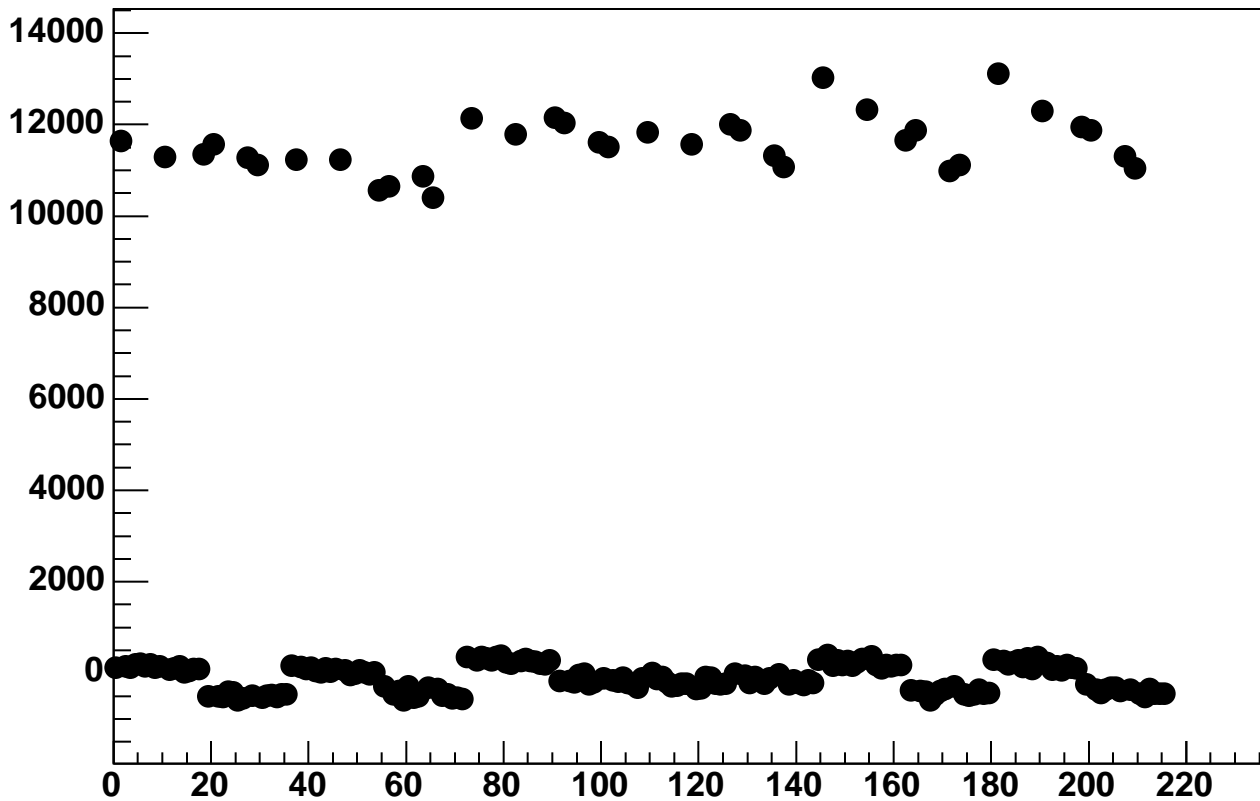
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18\*Chip+Chan



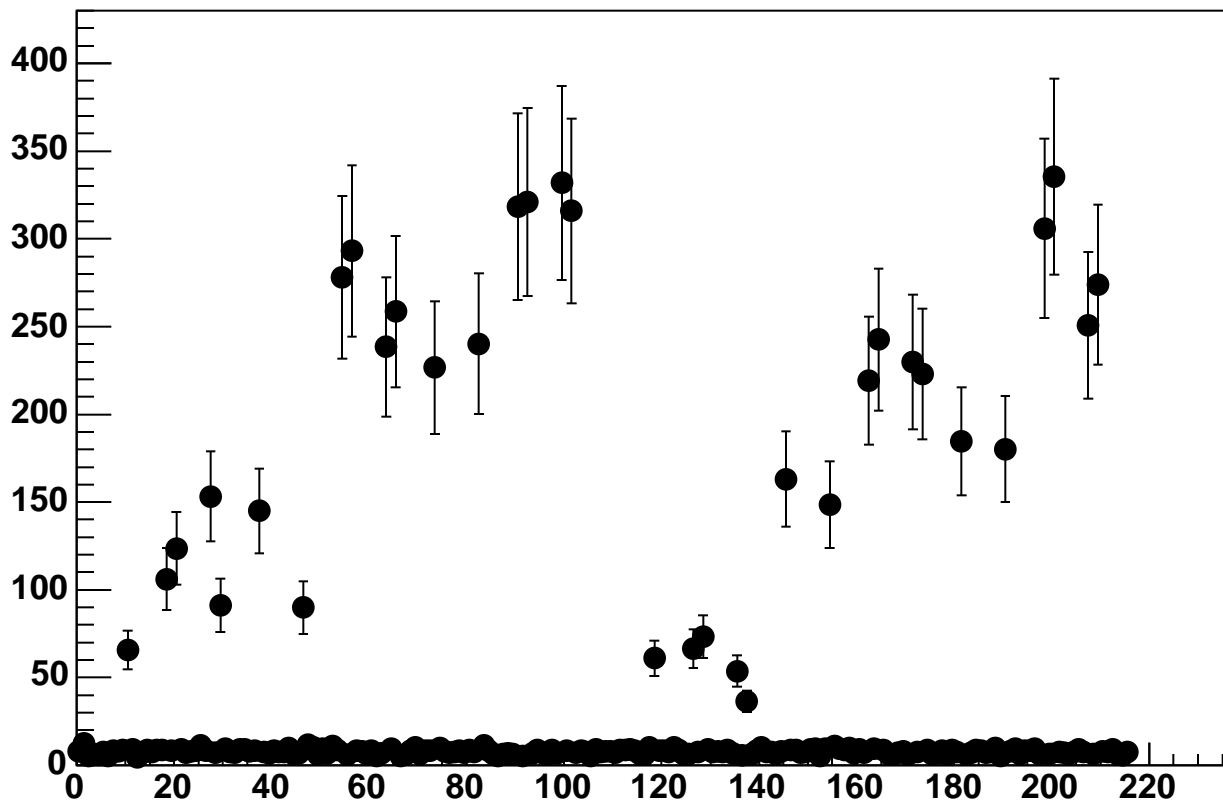
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18\*Chip+Chan



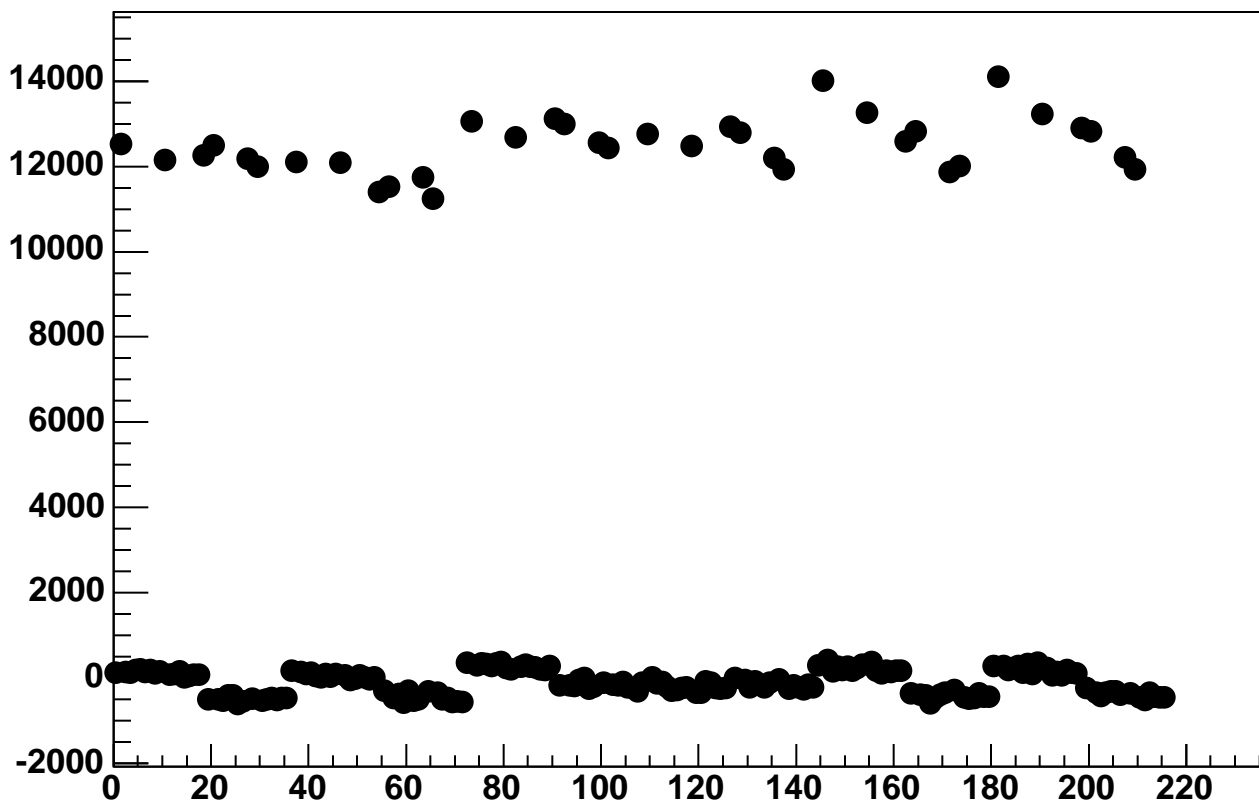
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18\*Chip+Chan



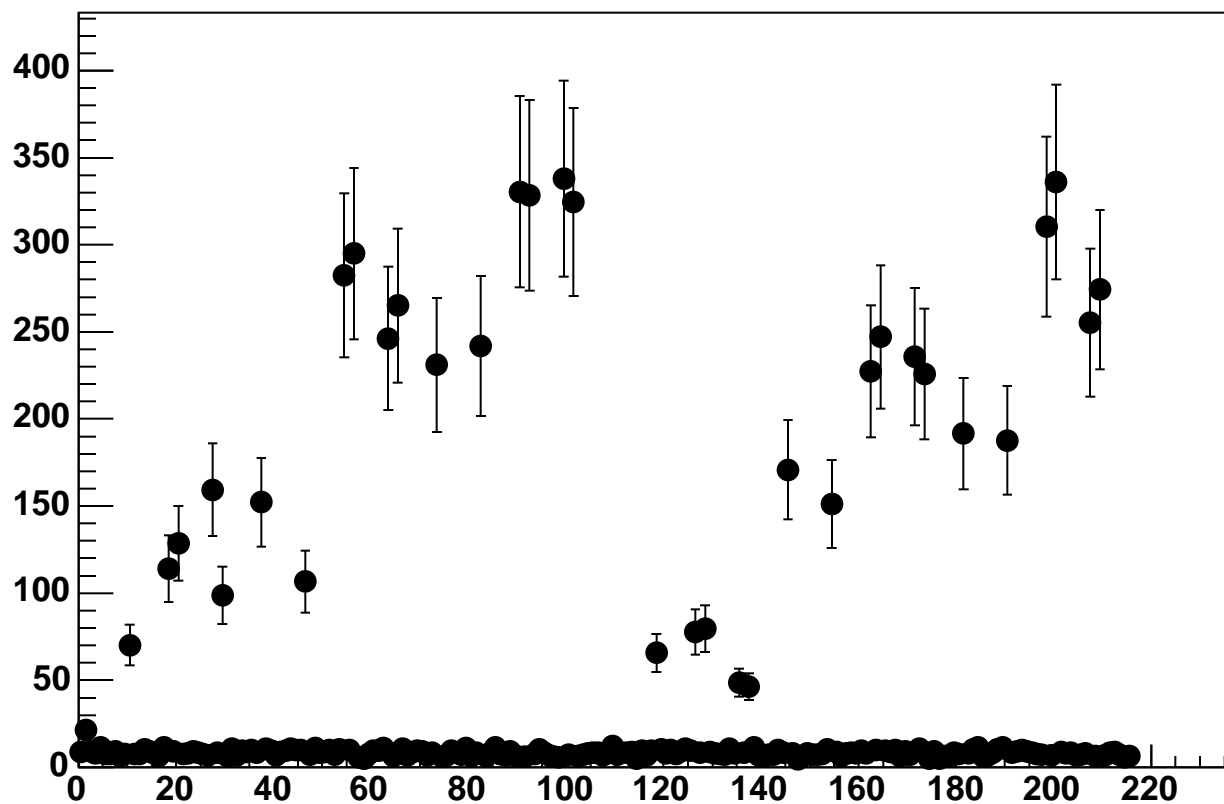
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=35, DAC=5600, ADC Mean vs 18\*Chip+Chan

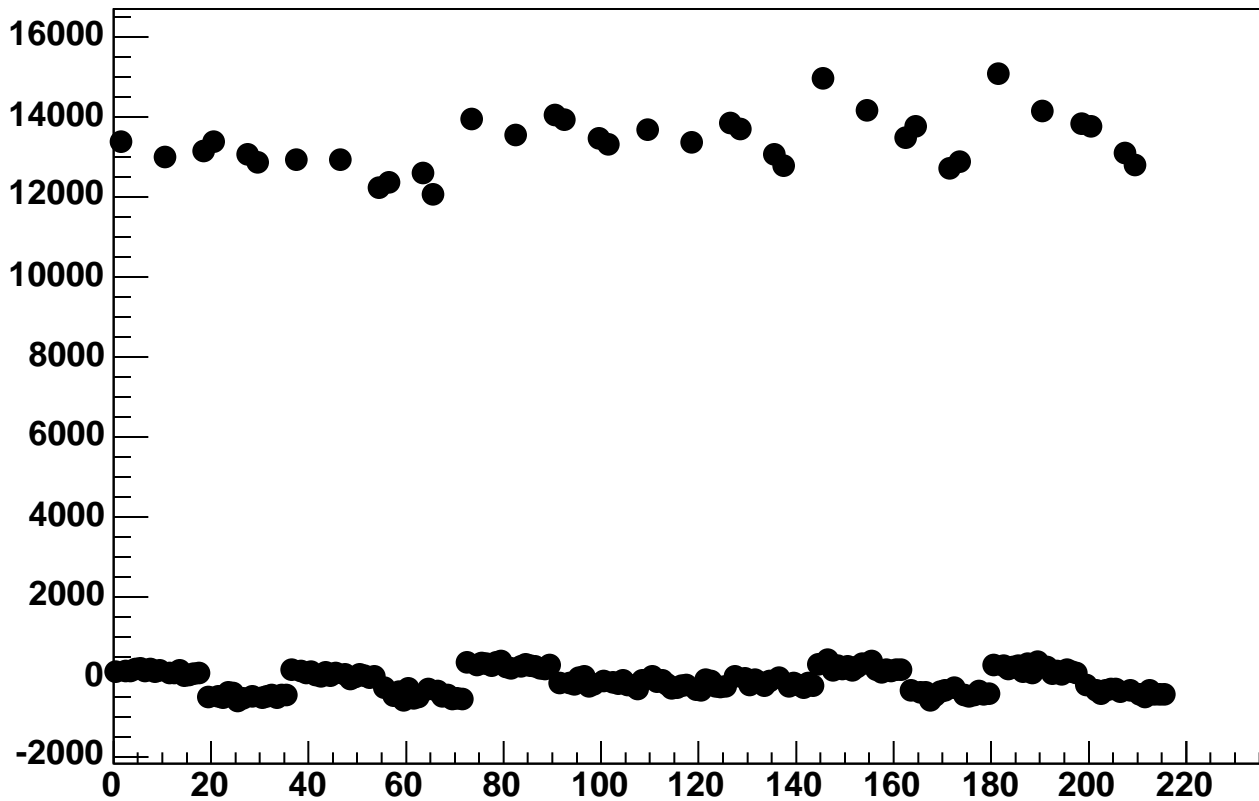


Enable 3, Hold=35, DAC=5600, ADC Noise vs 18\*Chip+Chan

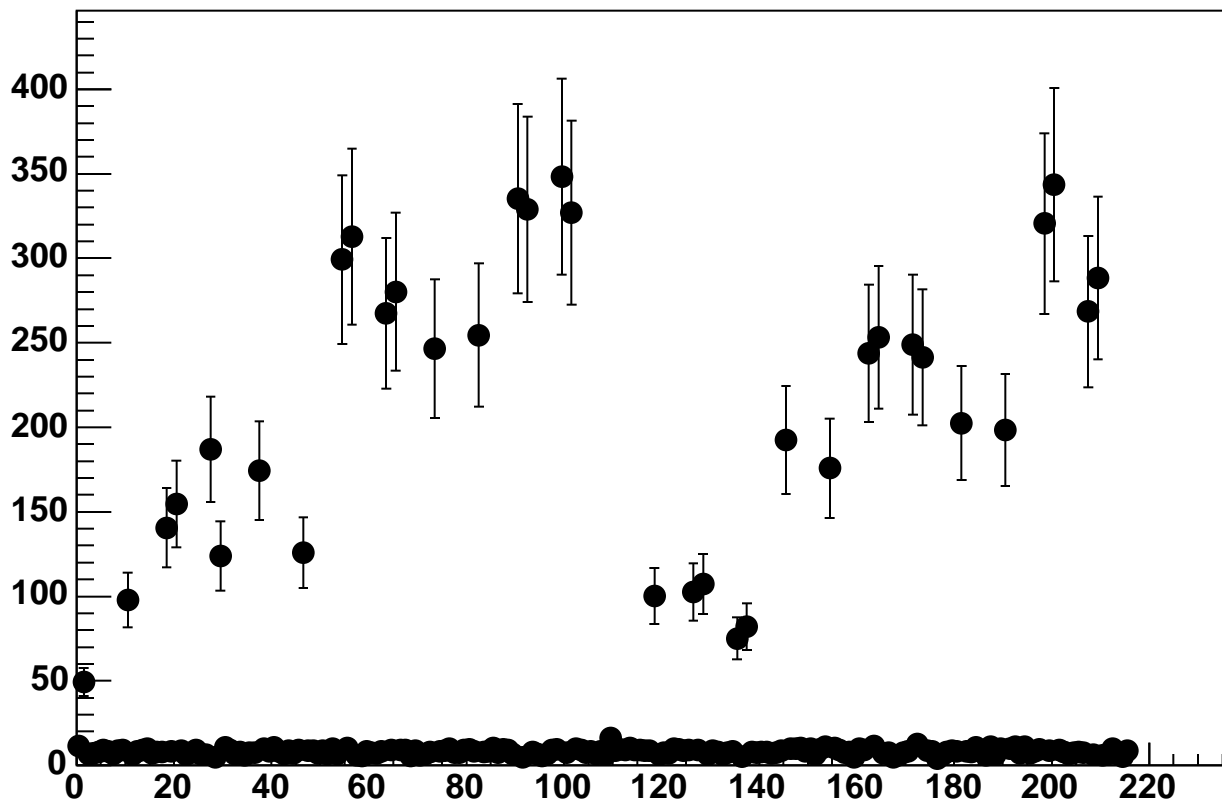




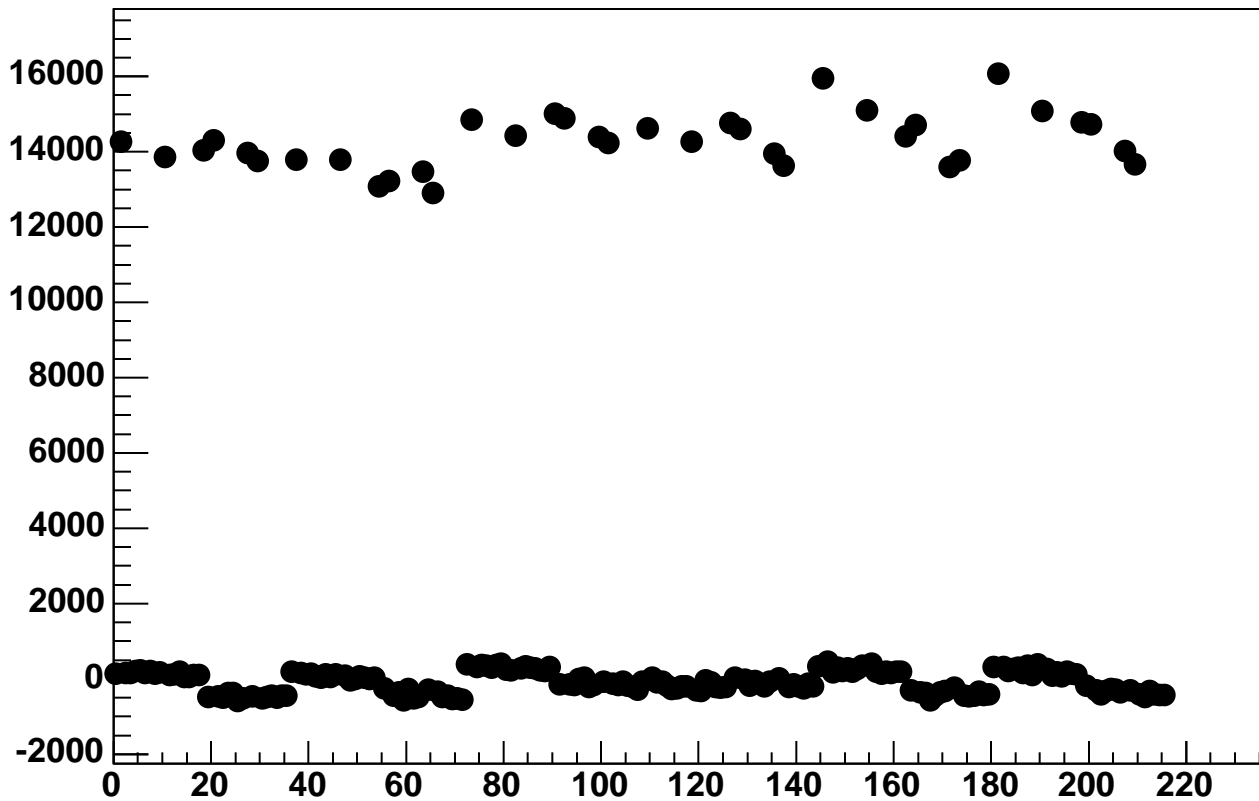
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18\*Chip+Chan



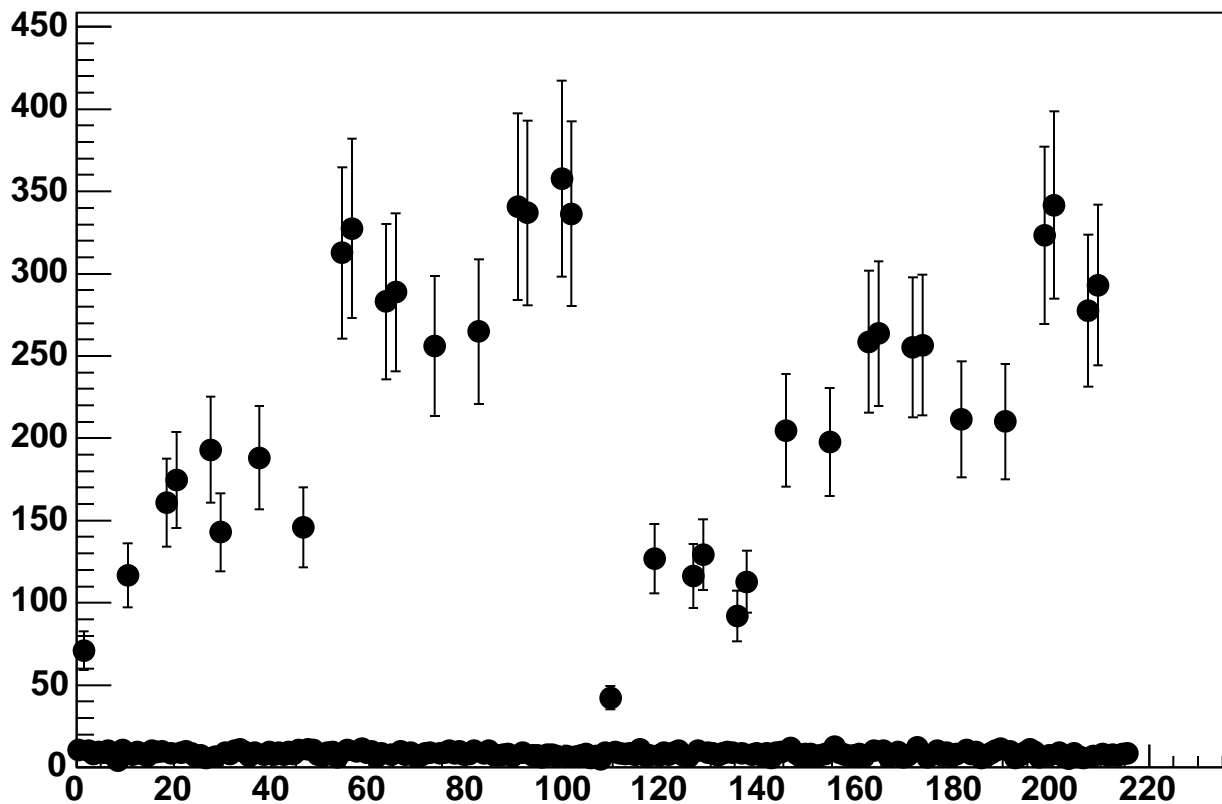
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18\*Chip+Chan



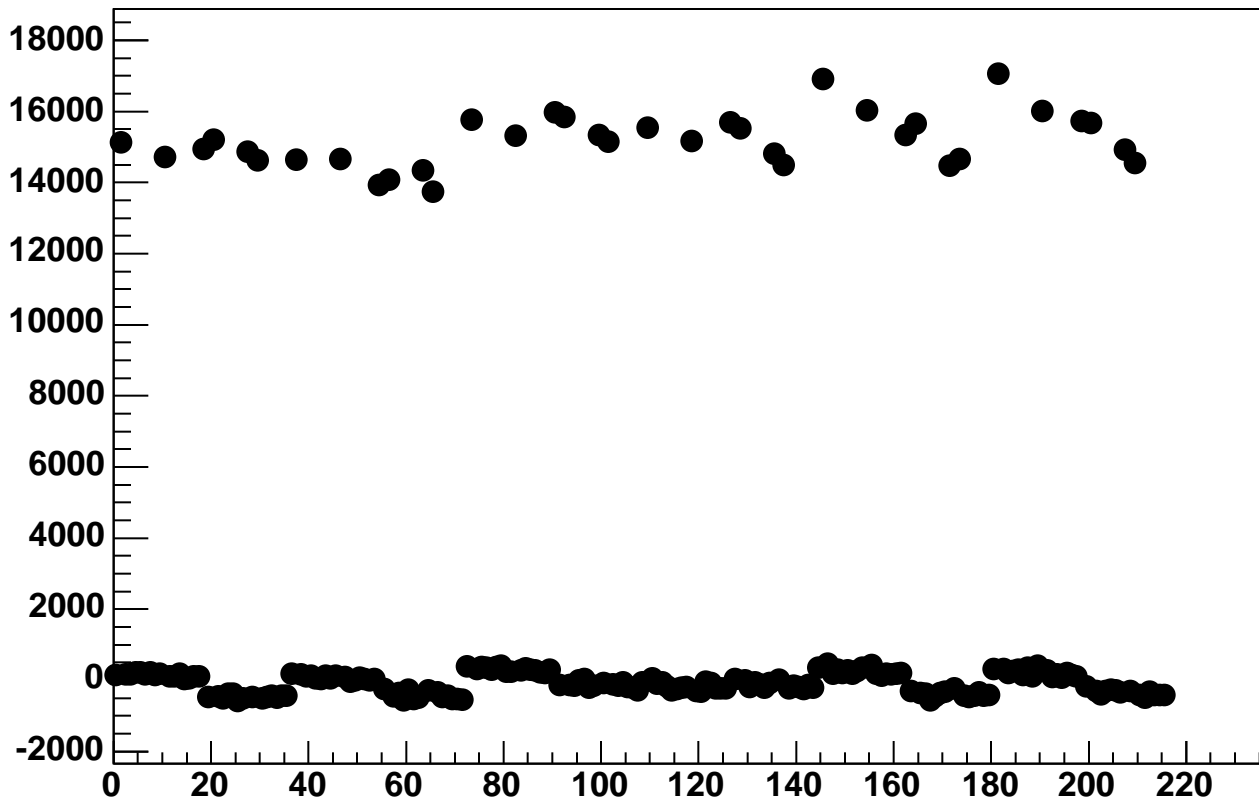
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18\*Chip+Chan



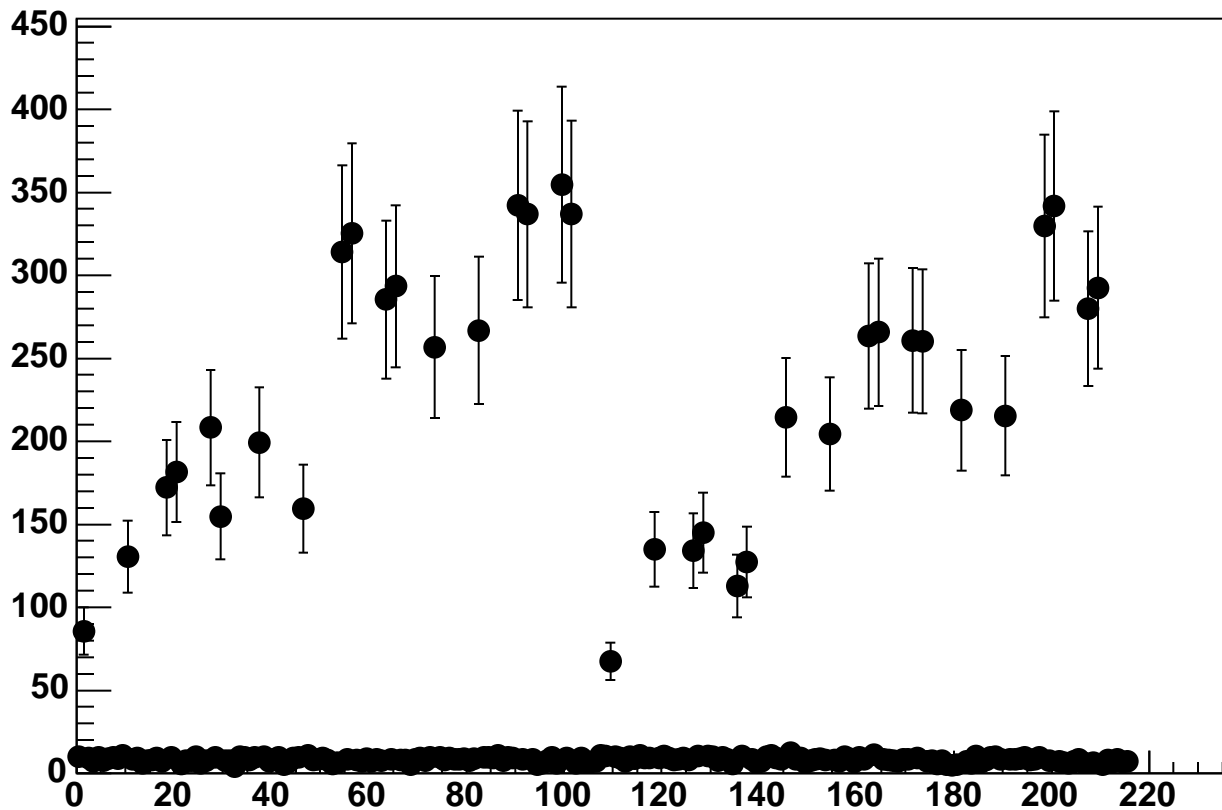
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18\*Chip+Chan



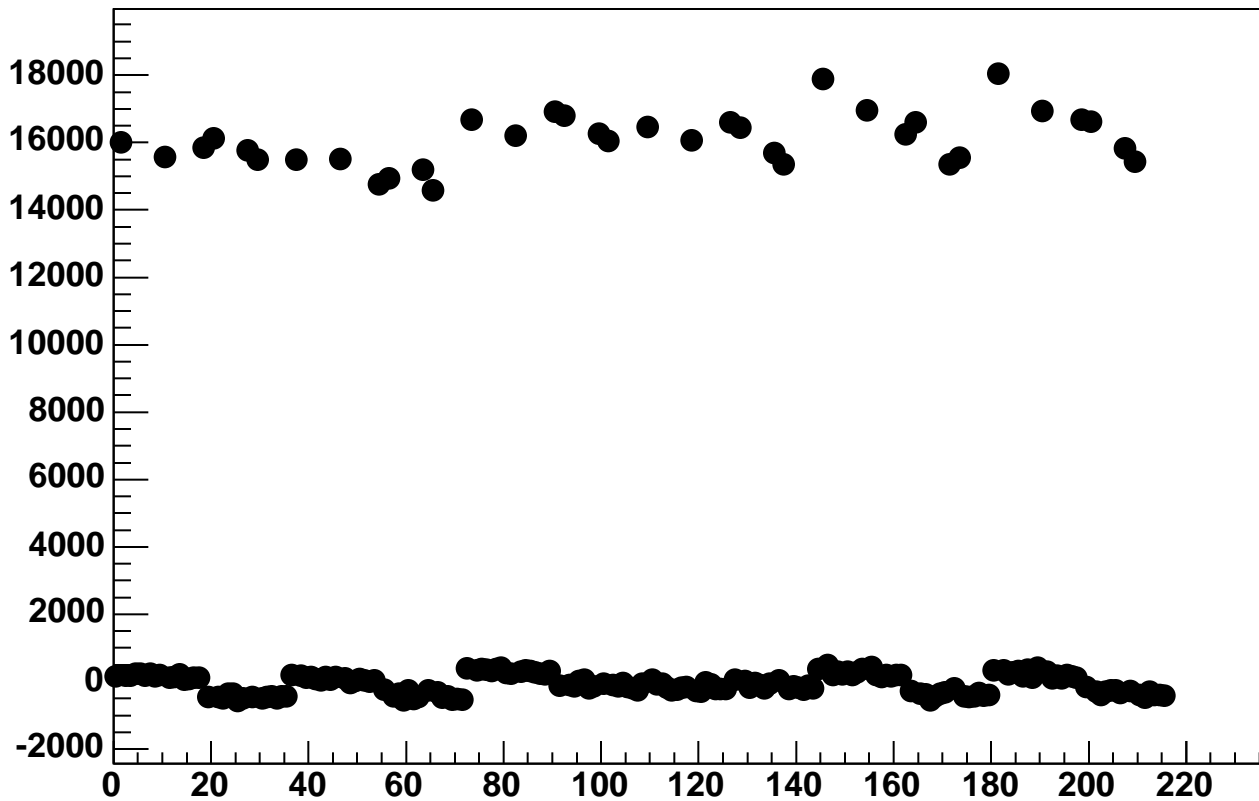
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18\*Chip+Chan



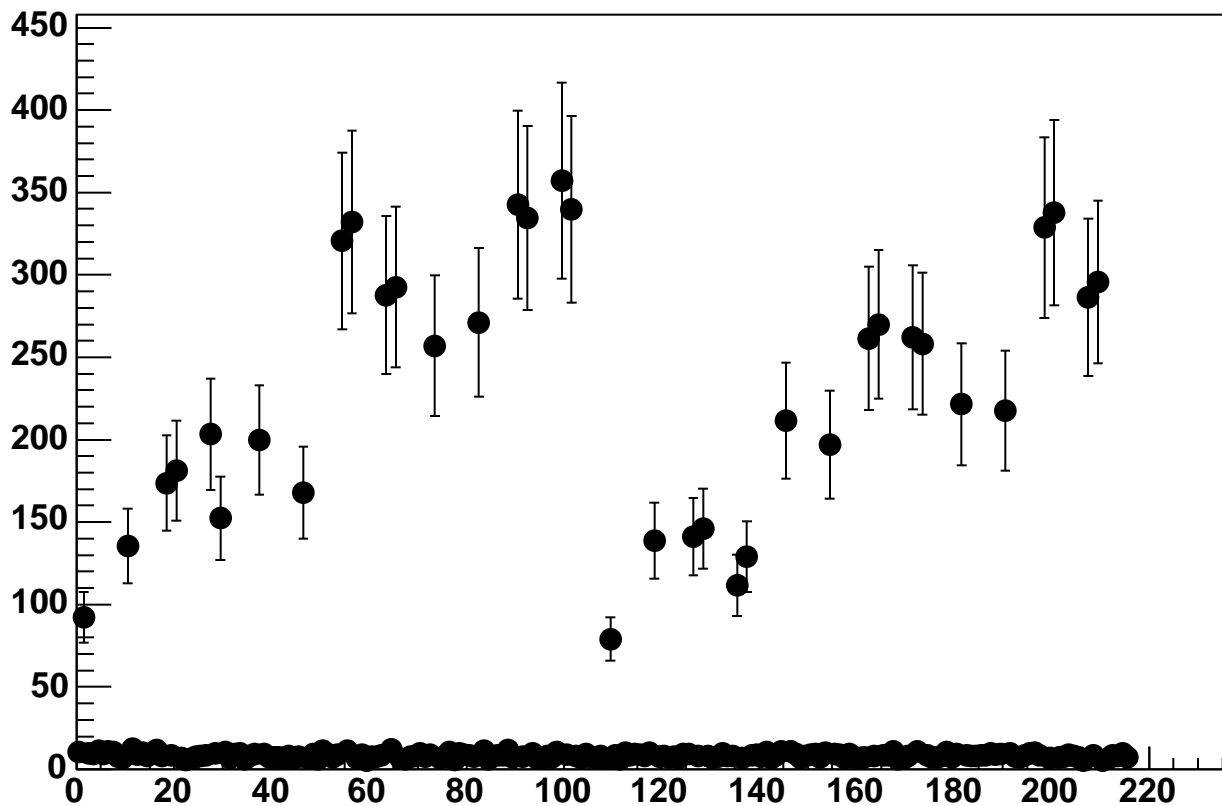
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18\*Chip+Chan



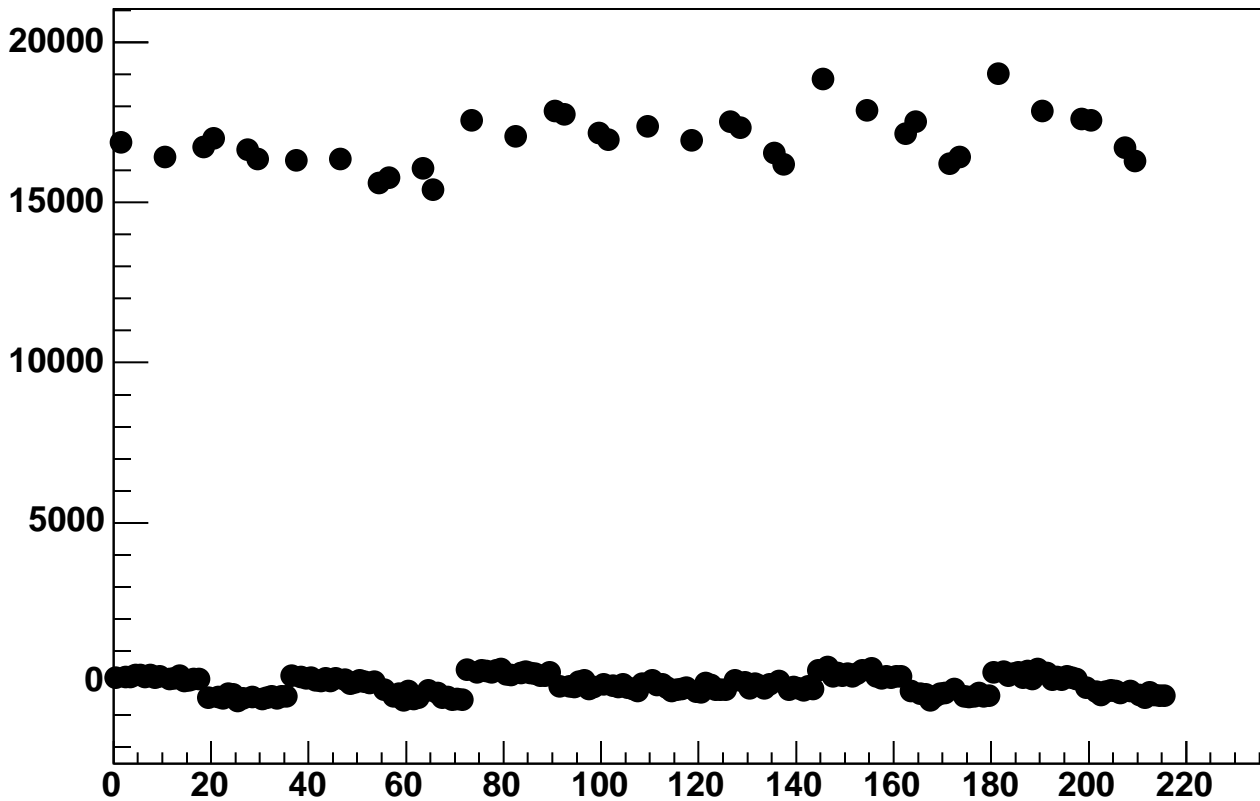
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18\*Chip+Chan



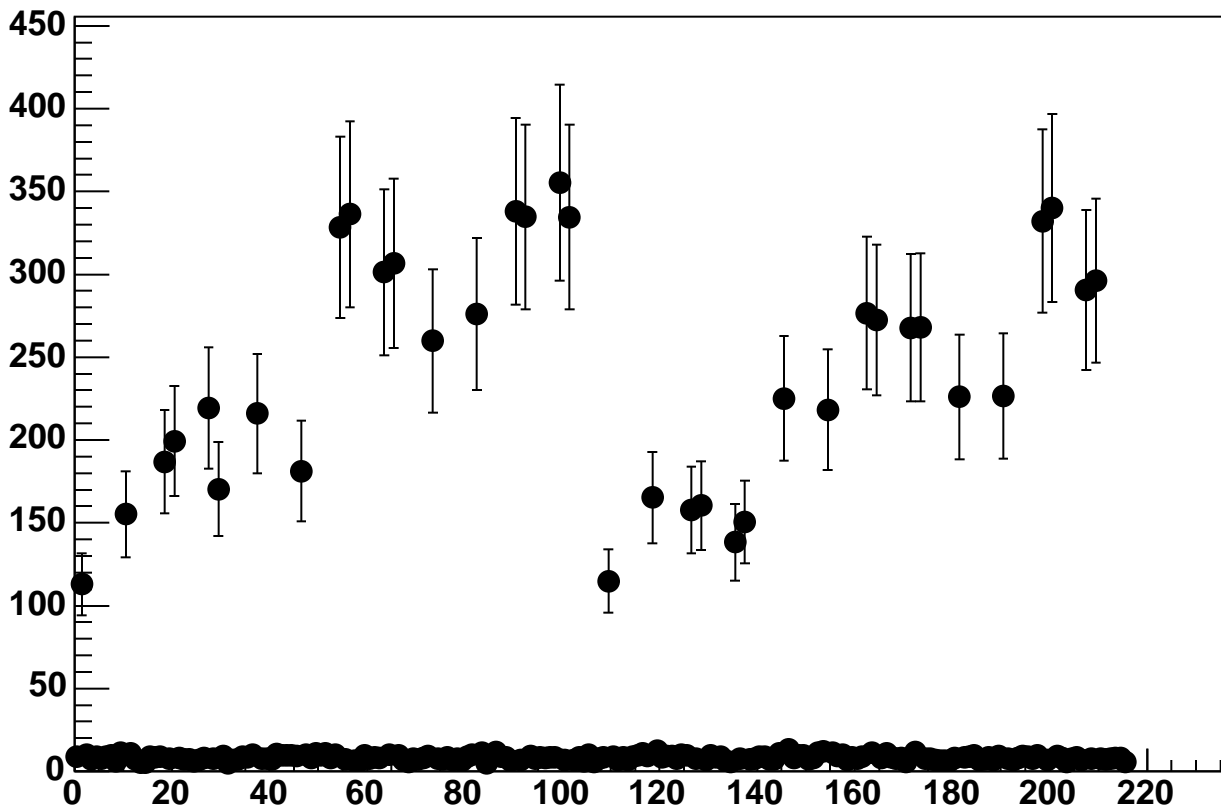
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18\*Chip+Chan



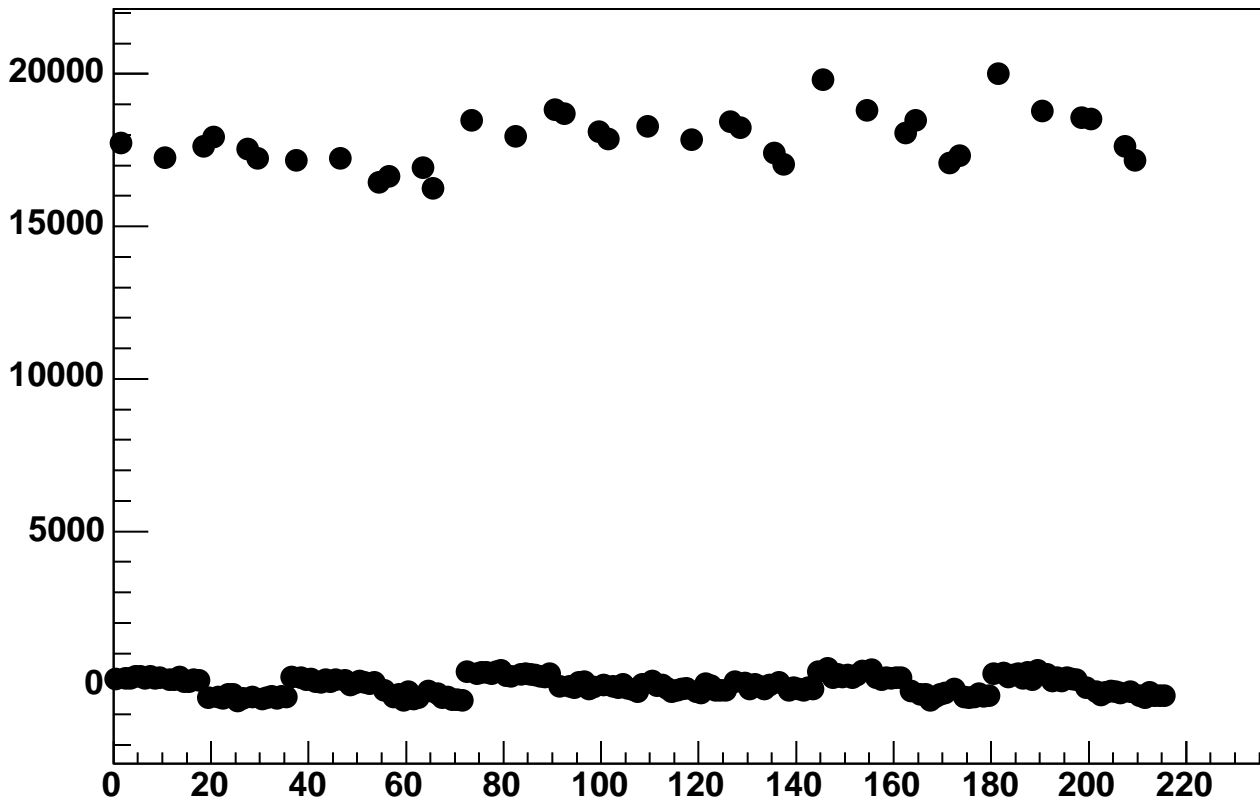
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18\*Chip+Chan



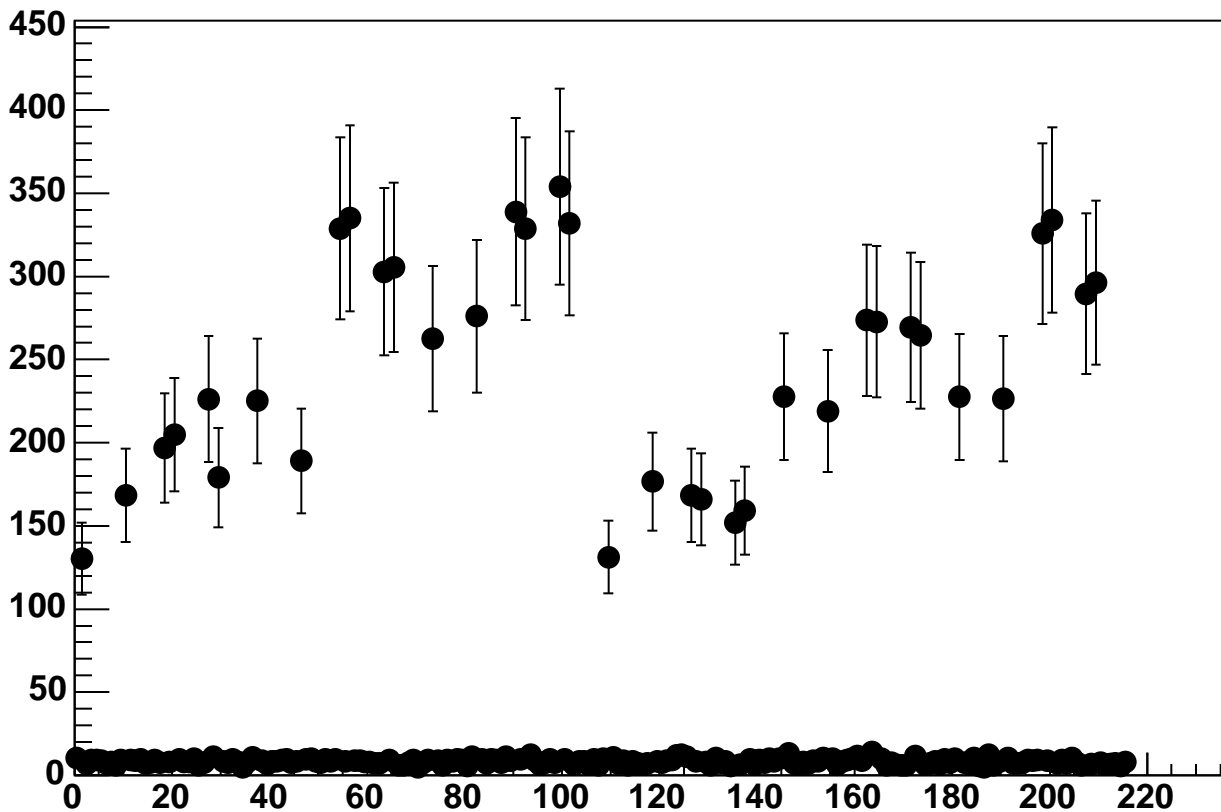
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18\*Chip+Chan



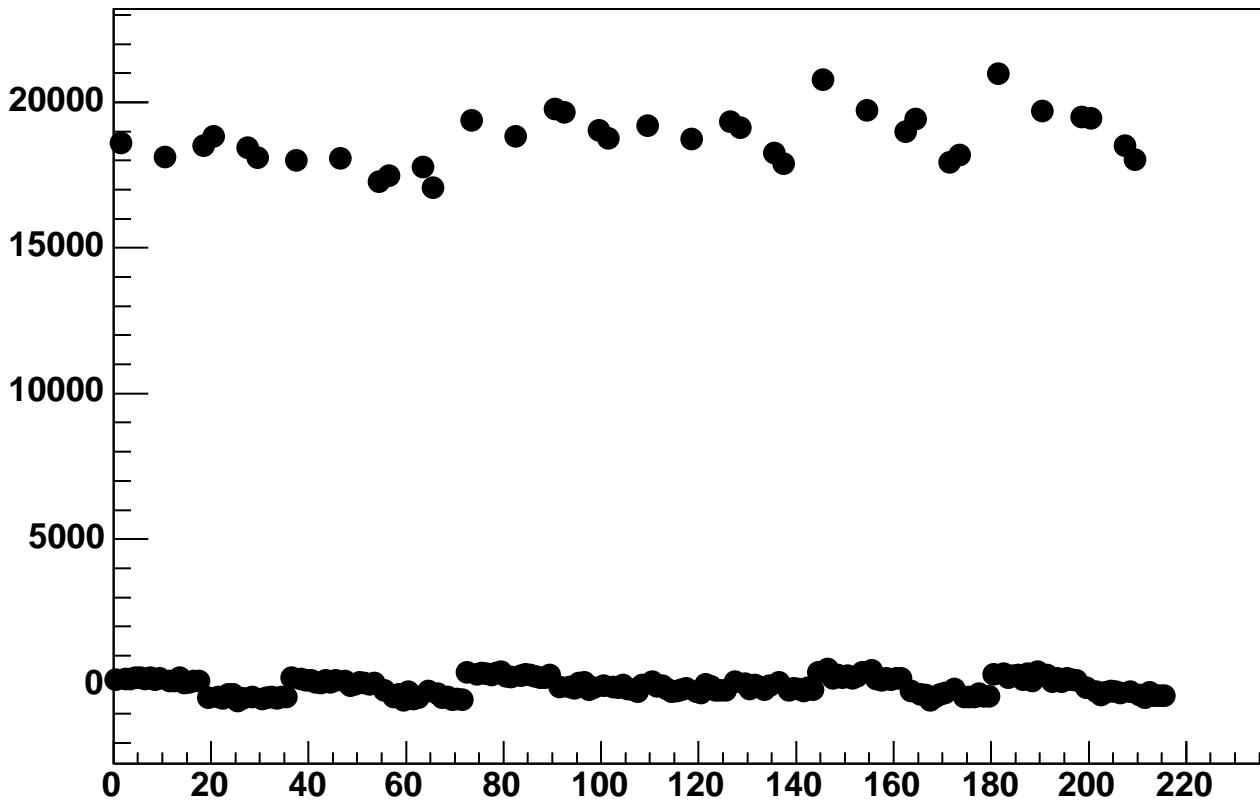
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18\*Chip+Chan



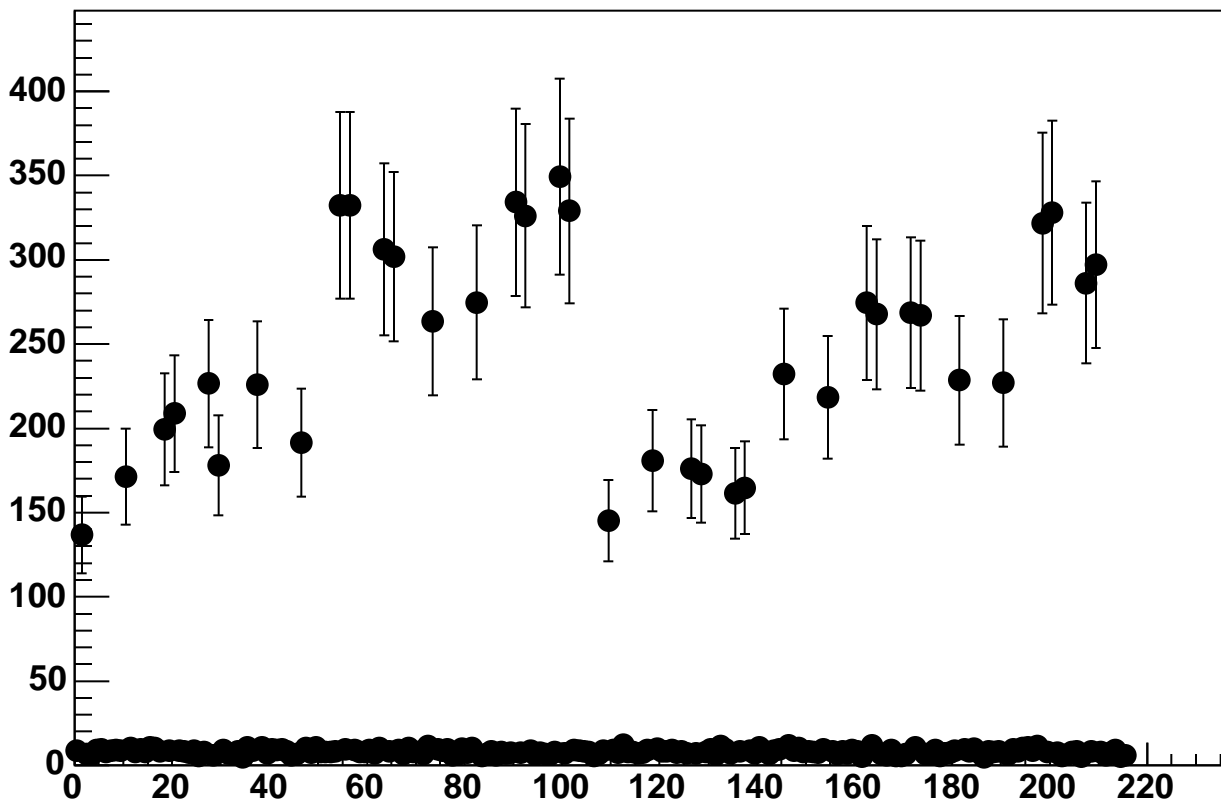
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18\*Chip+Chan



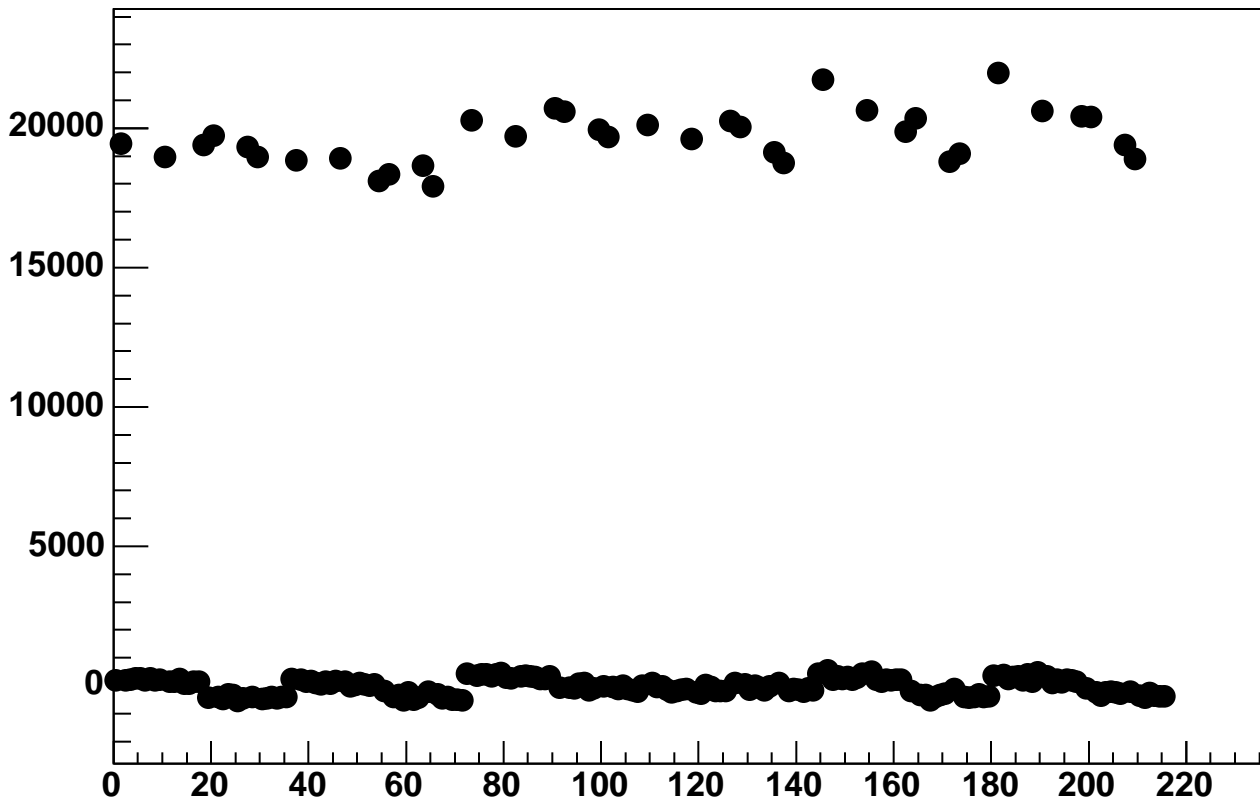
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18\*Chip+Chan



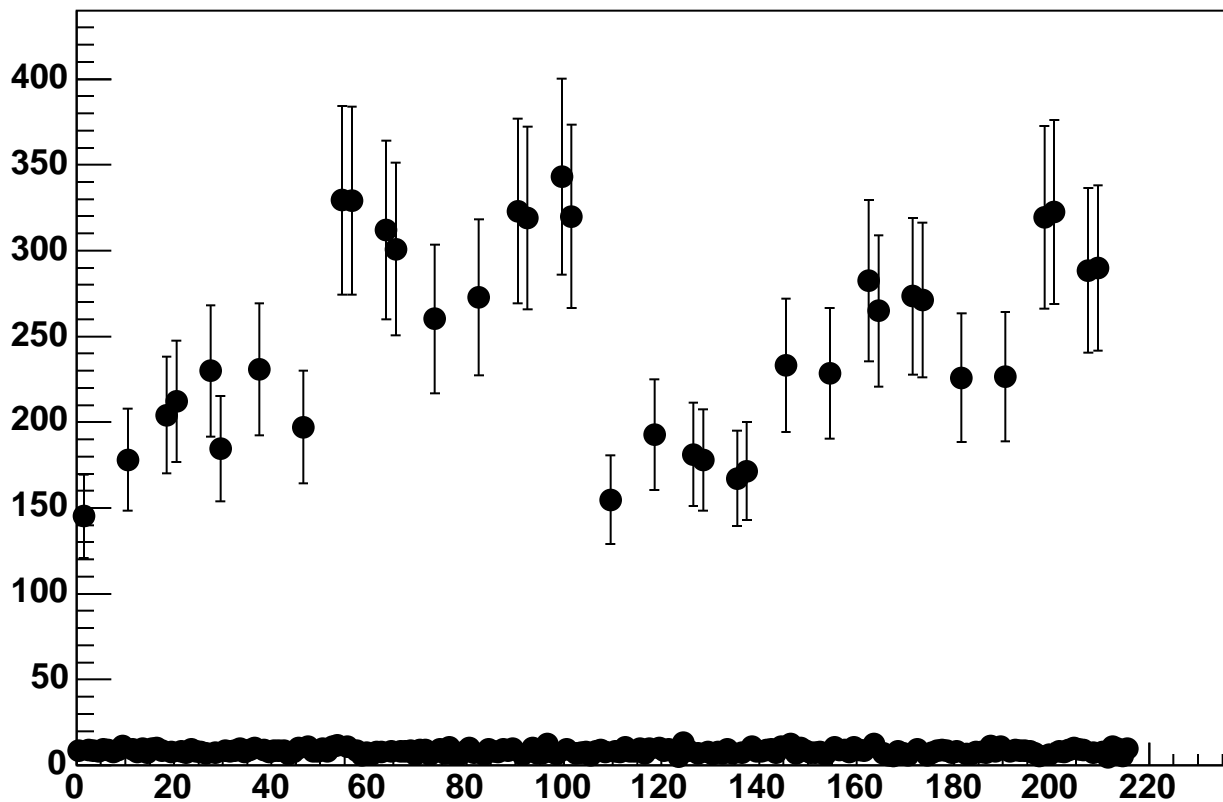
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=35, DAC=8800, ADC Mean vs 18\*Chip+Chan

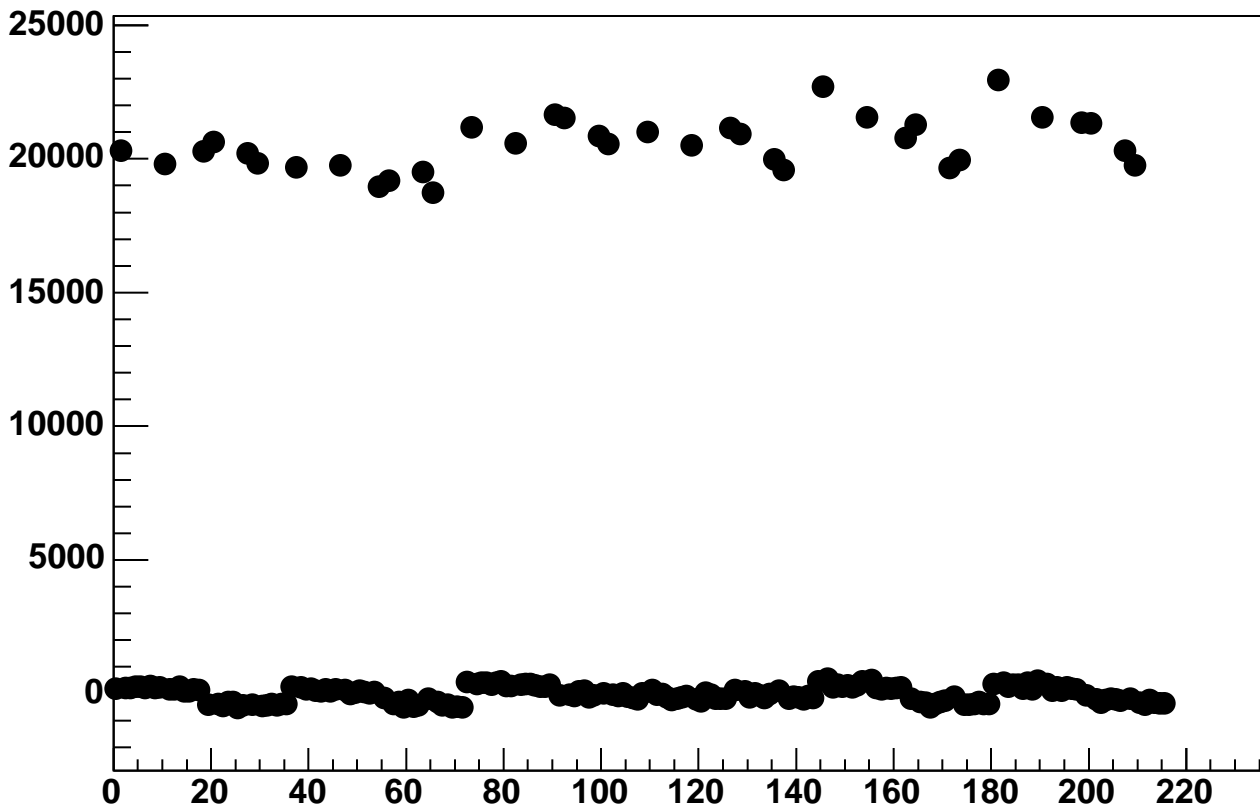


Enable 3, Hold=35, DAC=8800, ADC Noise vs 18\*Chip+Chan

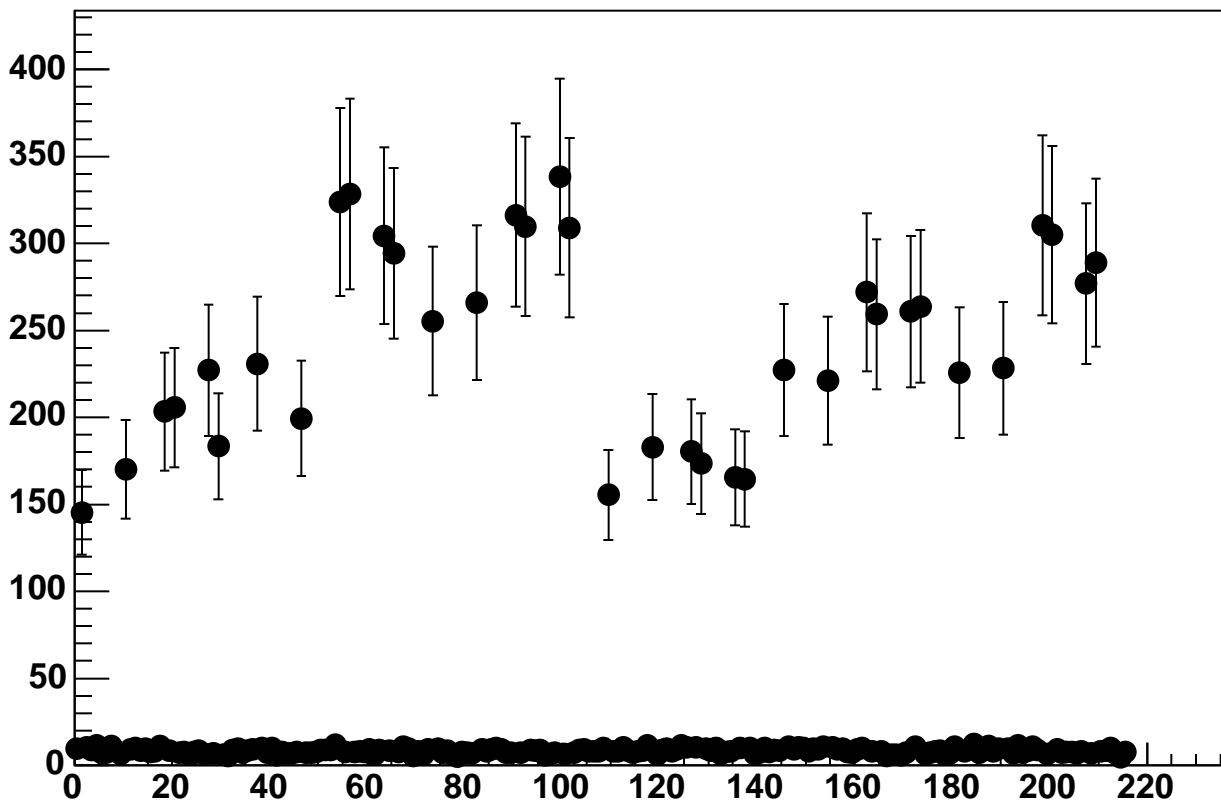




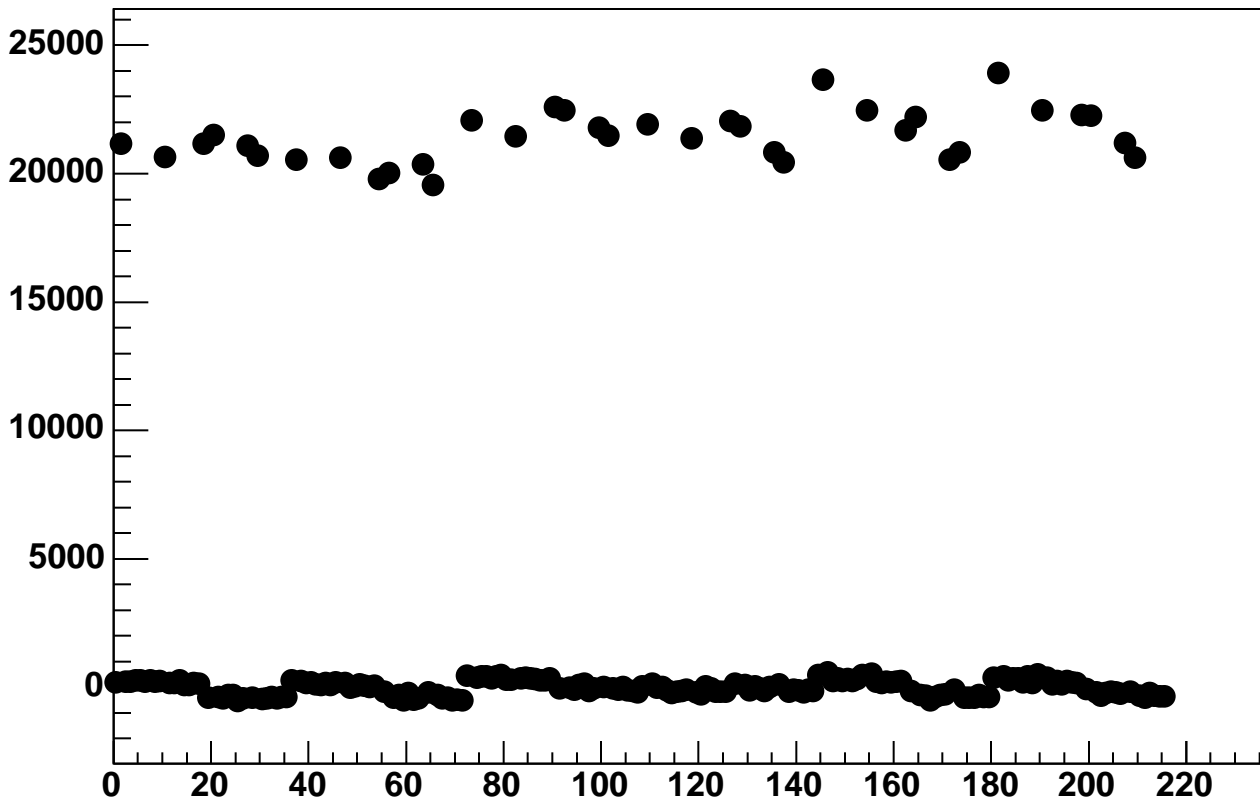
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18\*Chip+Chan



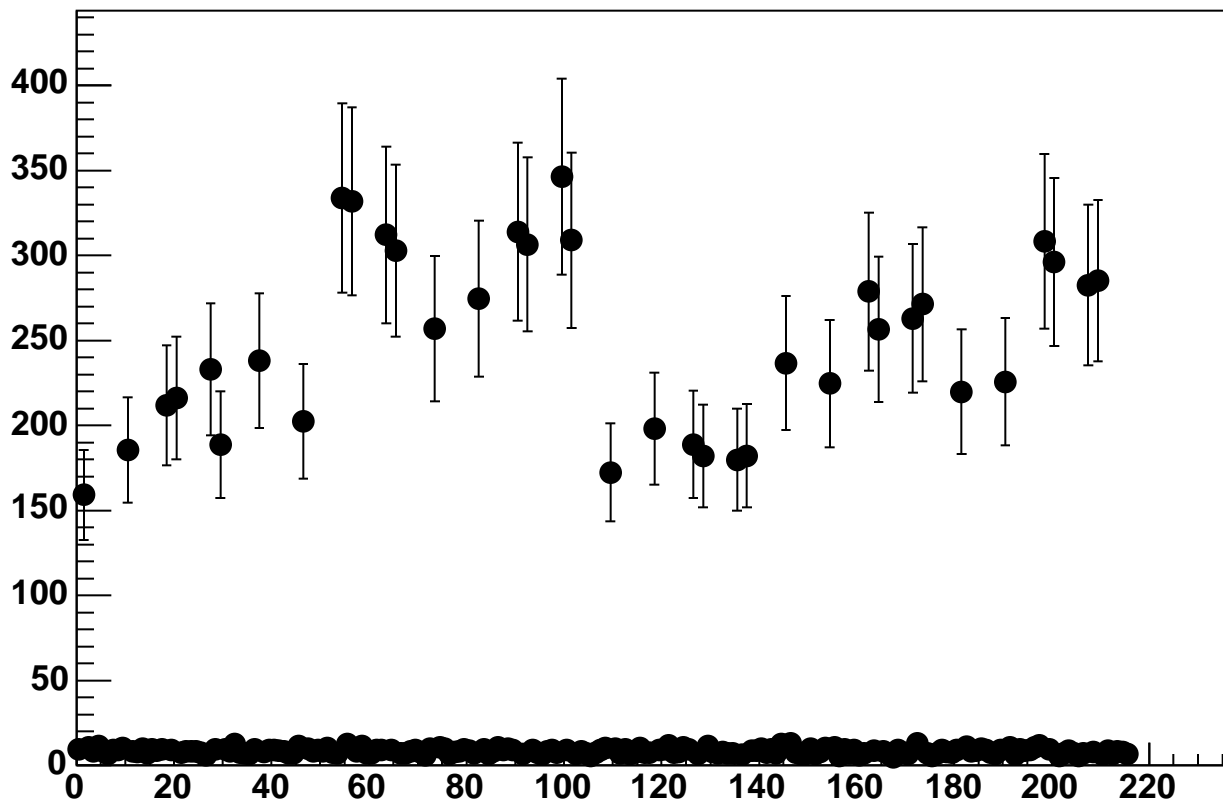
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18\*Chip+Chan



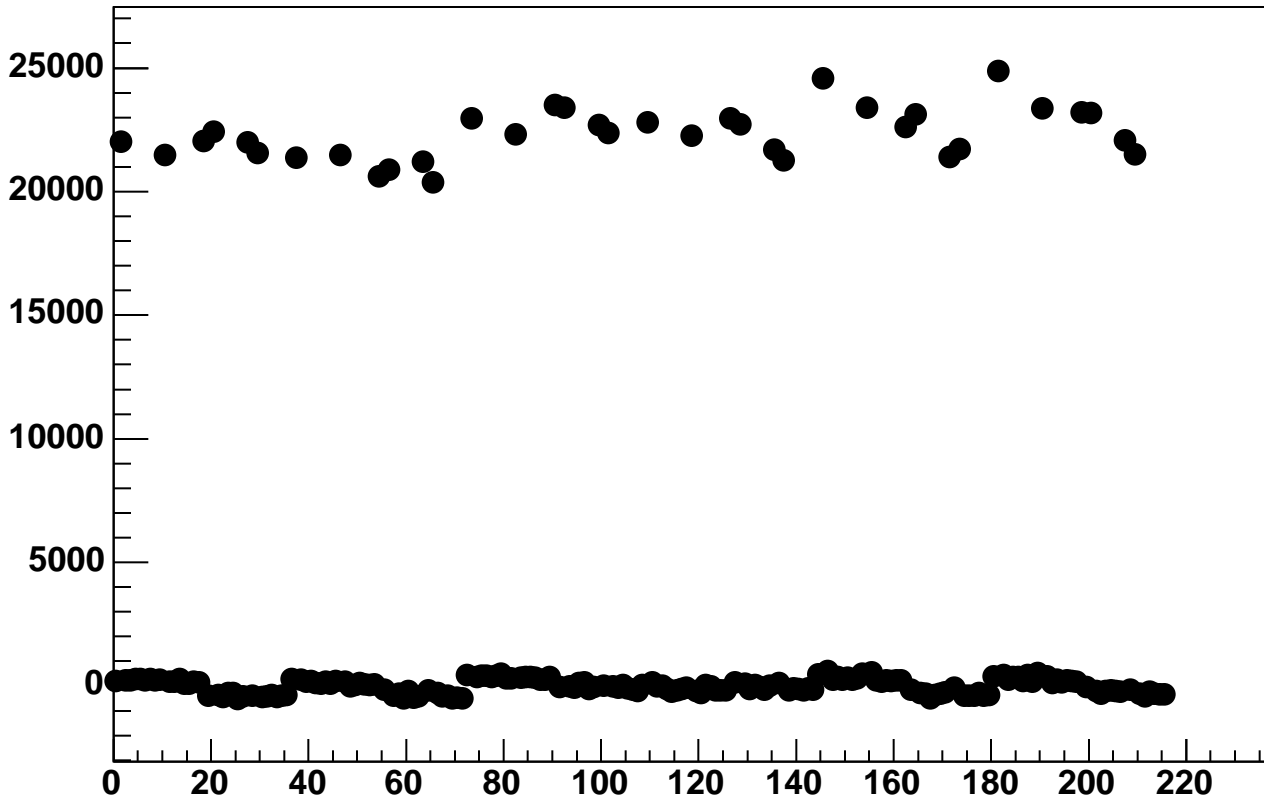
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18\*Chip+Chan



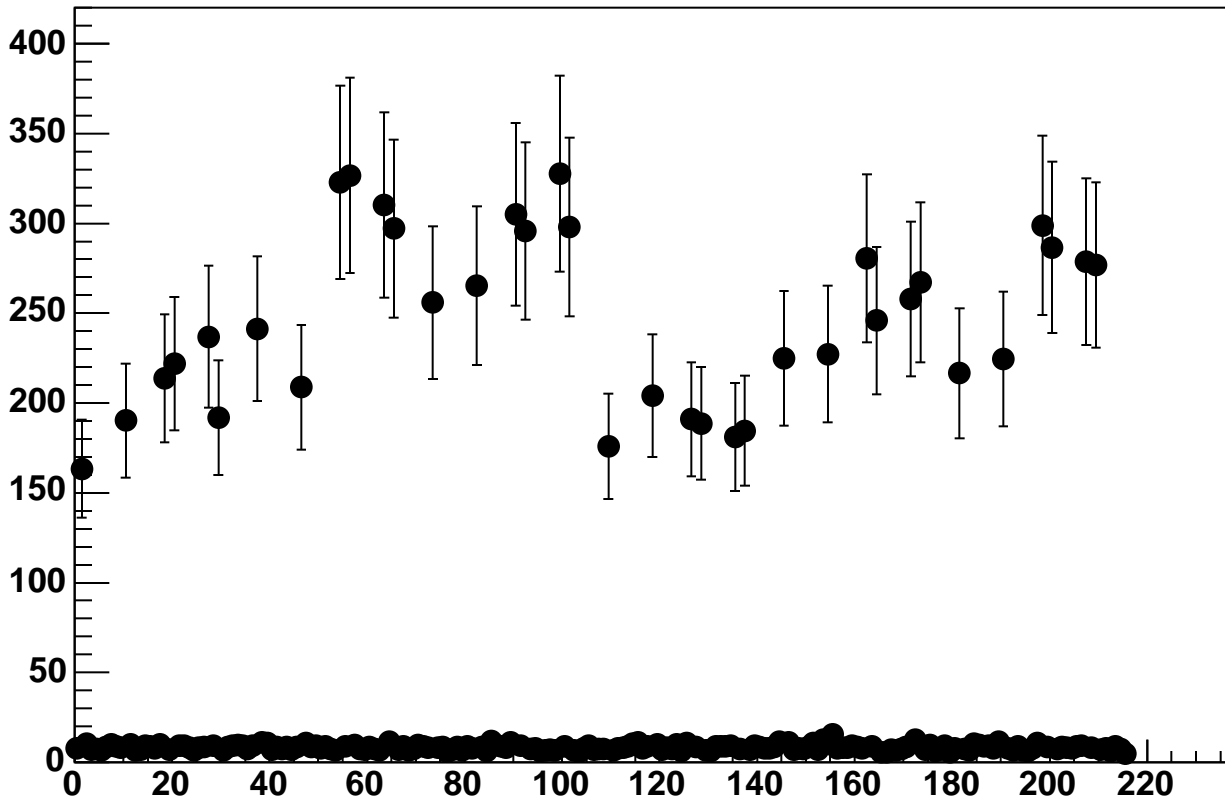
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18\*Chip+Chan



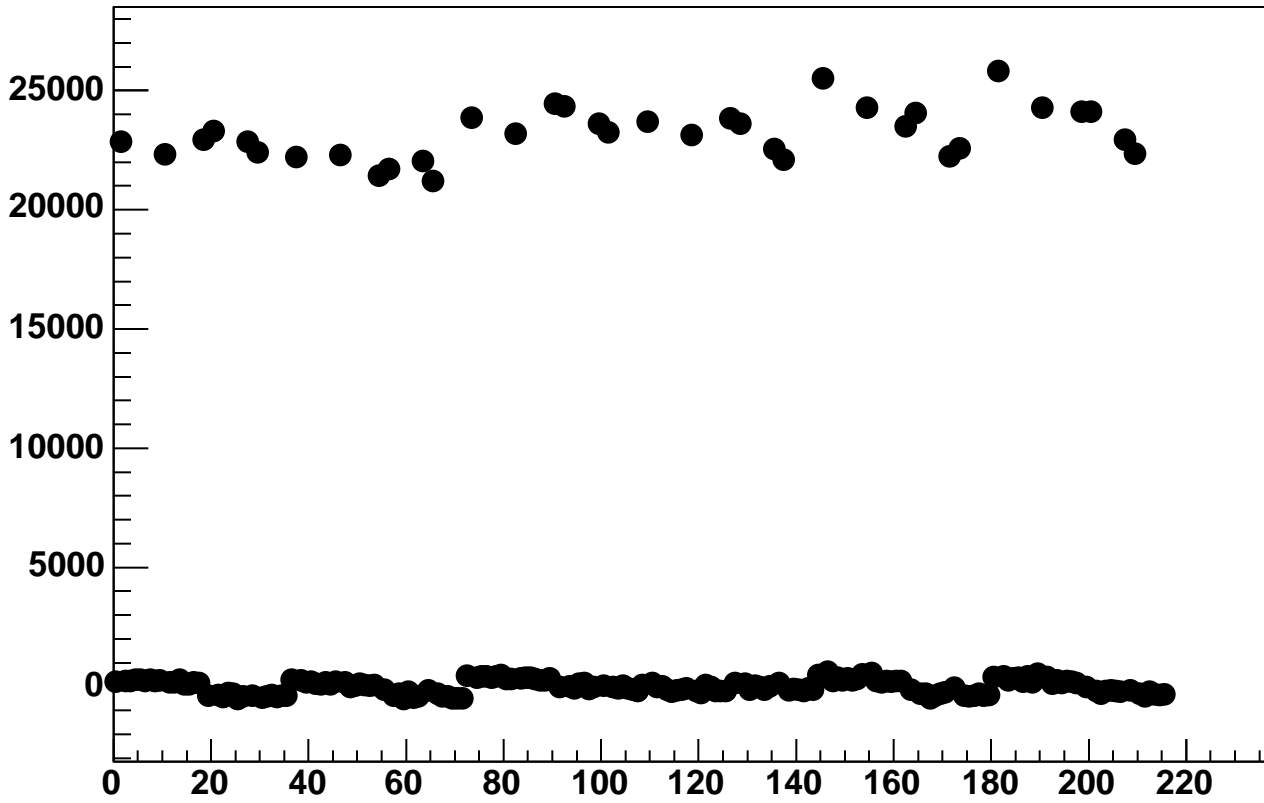
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18\*Chip+Chan



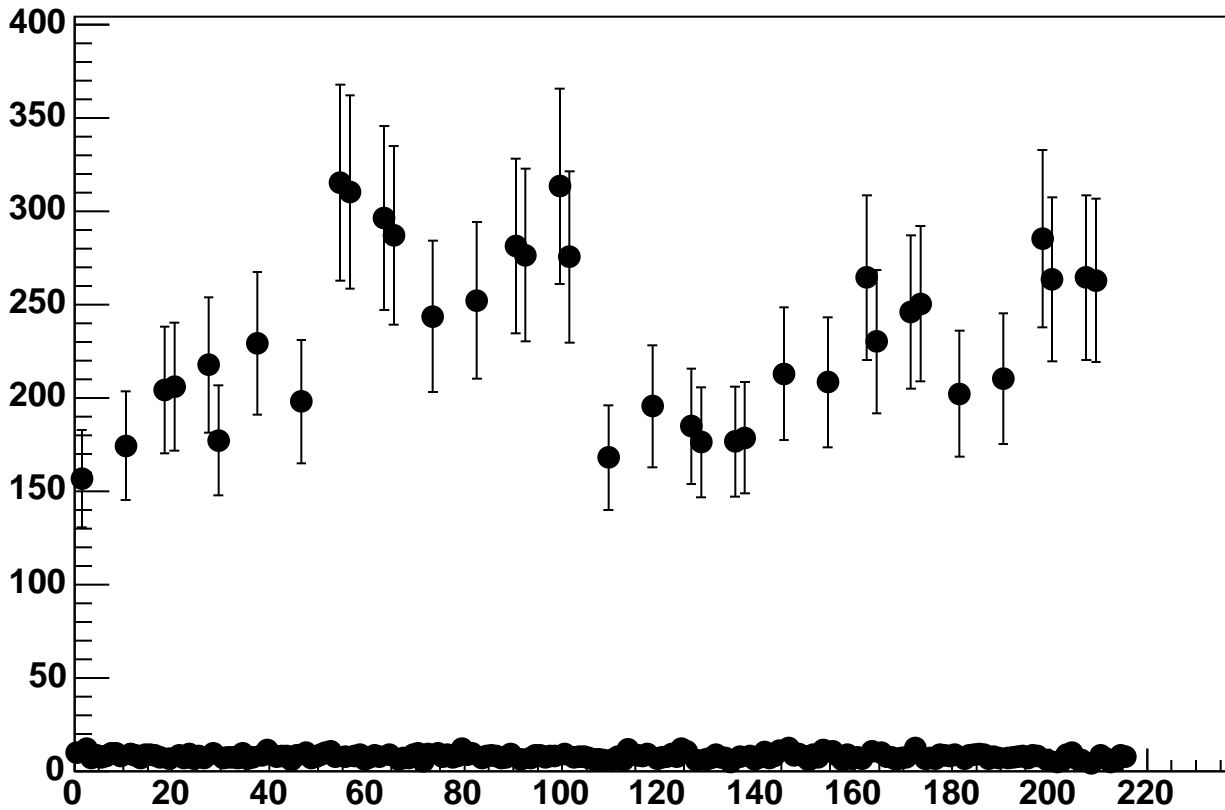
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18\*Chip+Chan



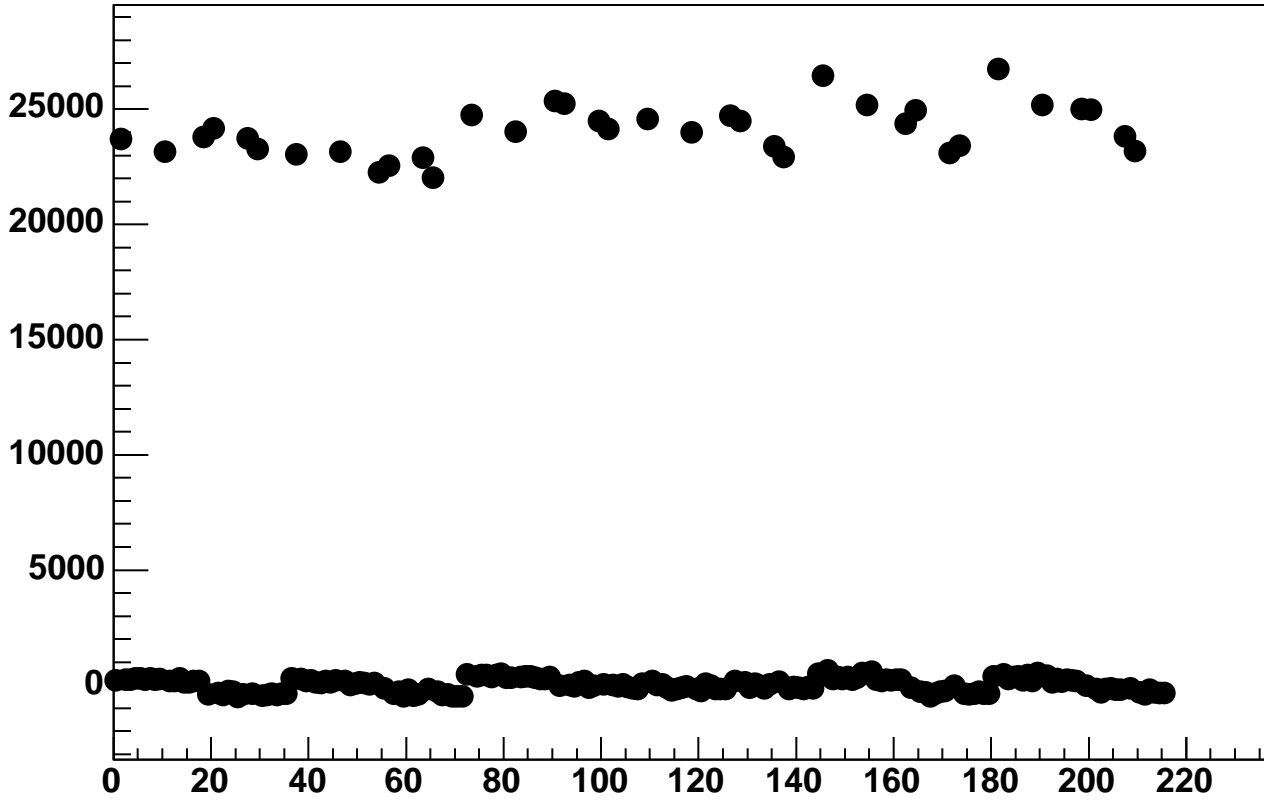
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18\*Chip+Chan



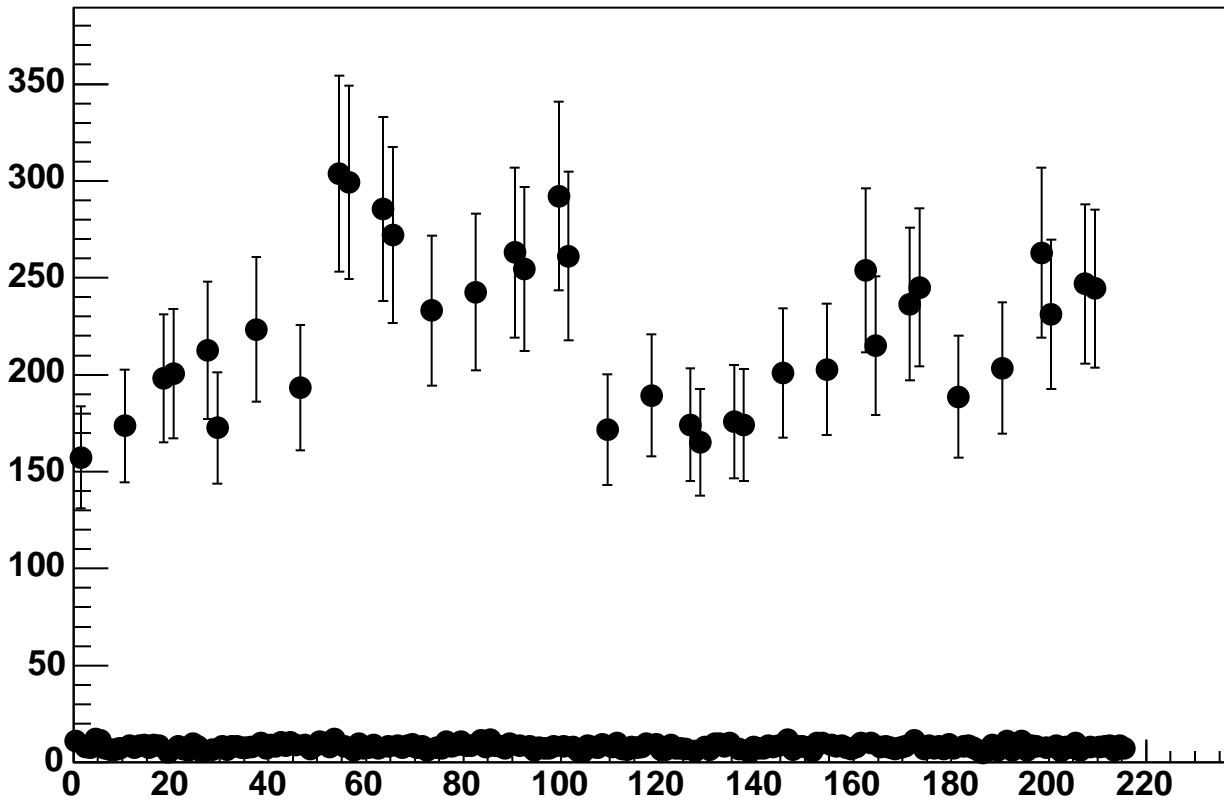
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18\*Chip+Chan



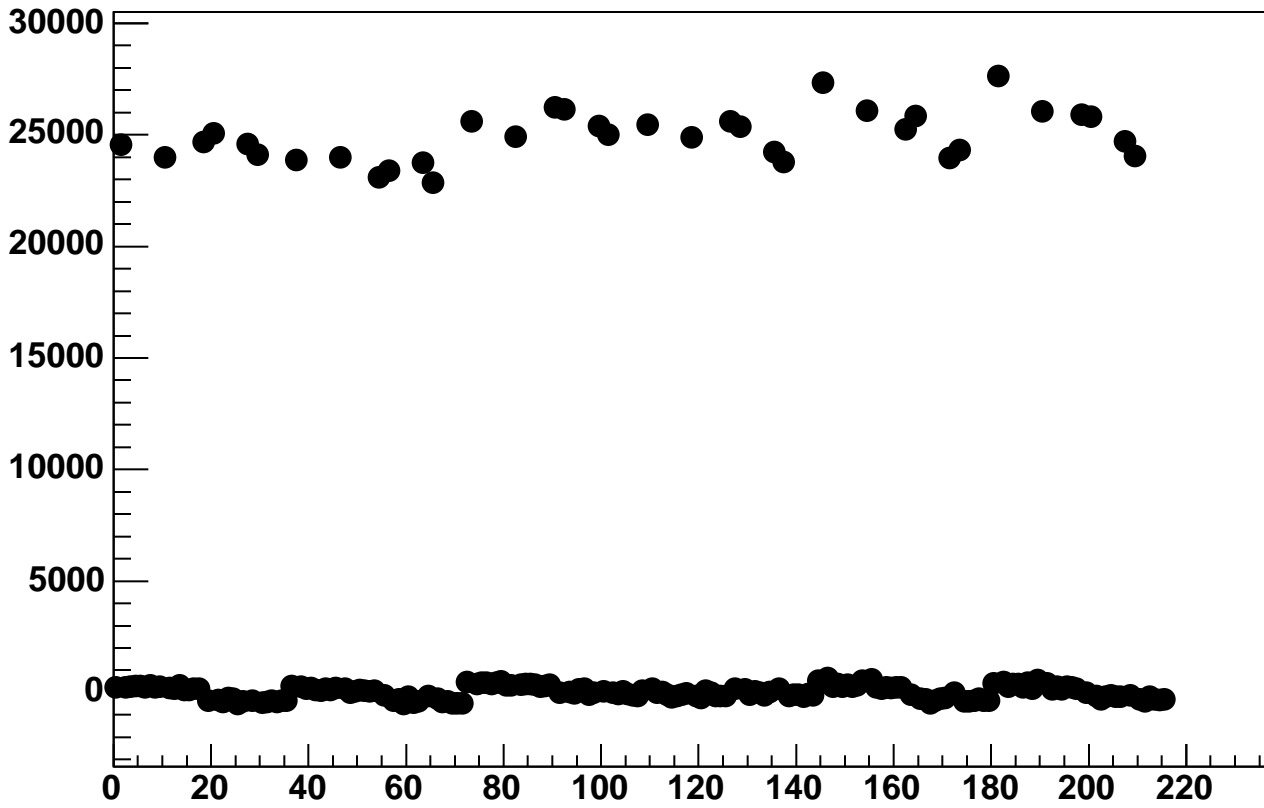
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18\*Chip+Chan



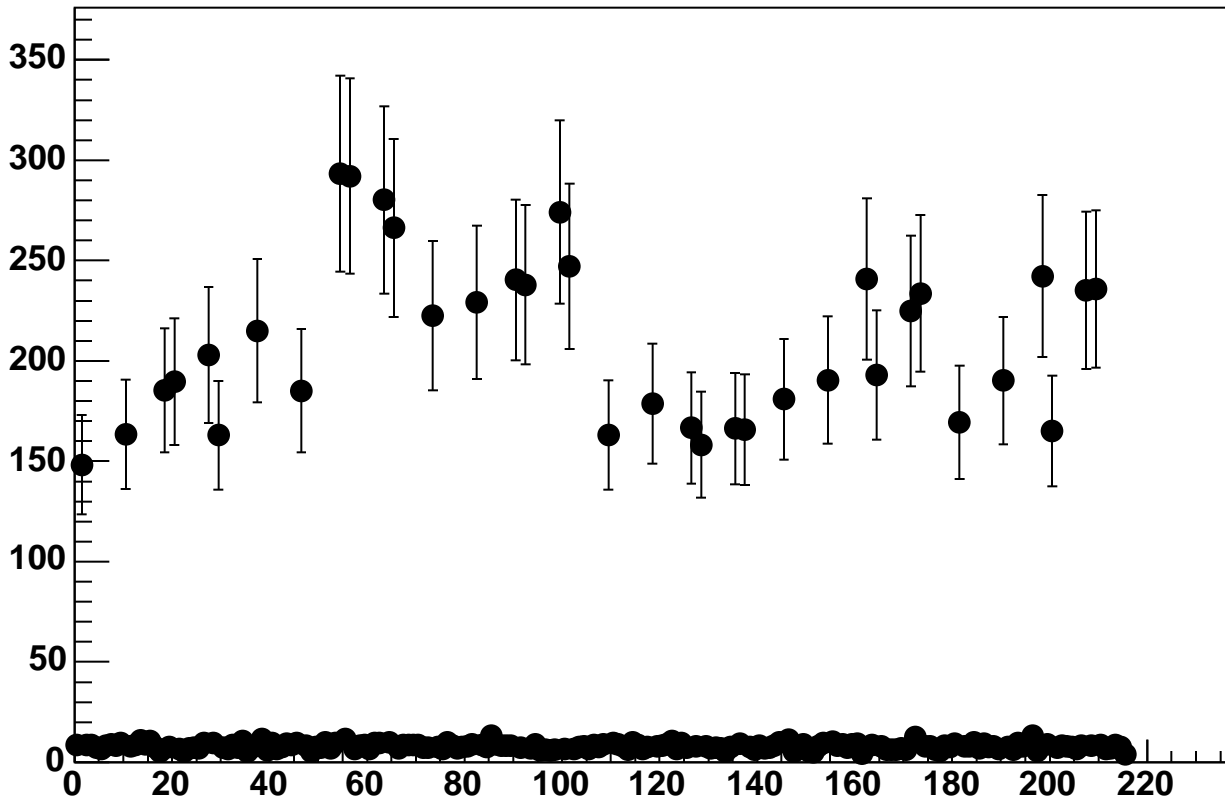
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18\*Chip+Chan



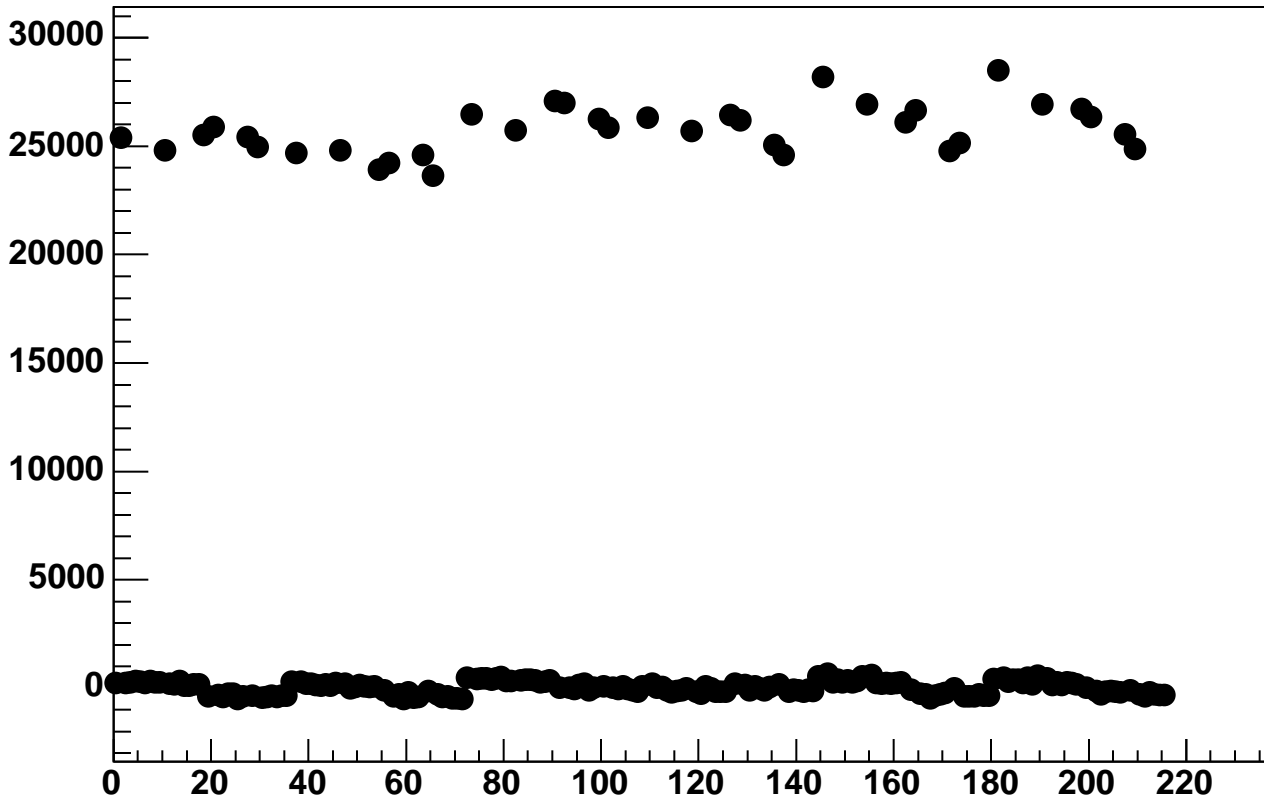
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18\*Chip+Chan



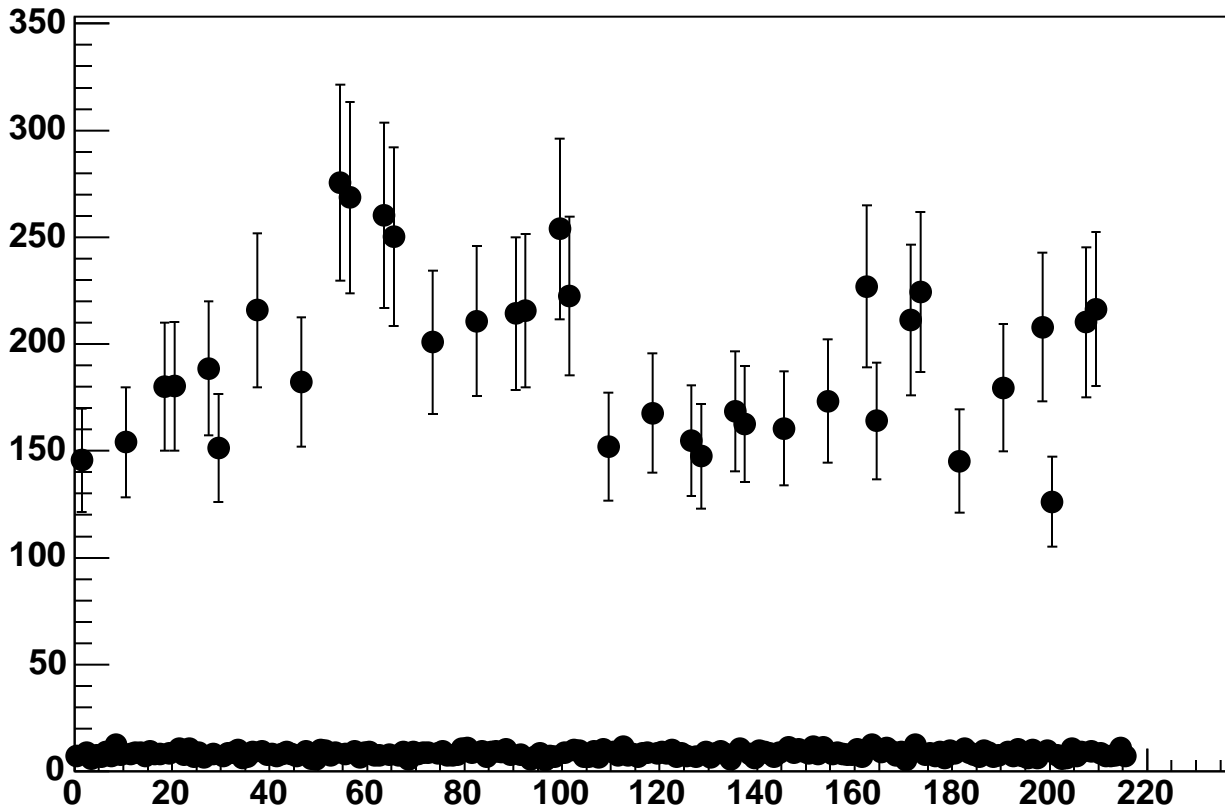
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18\*Chip+Chan



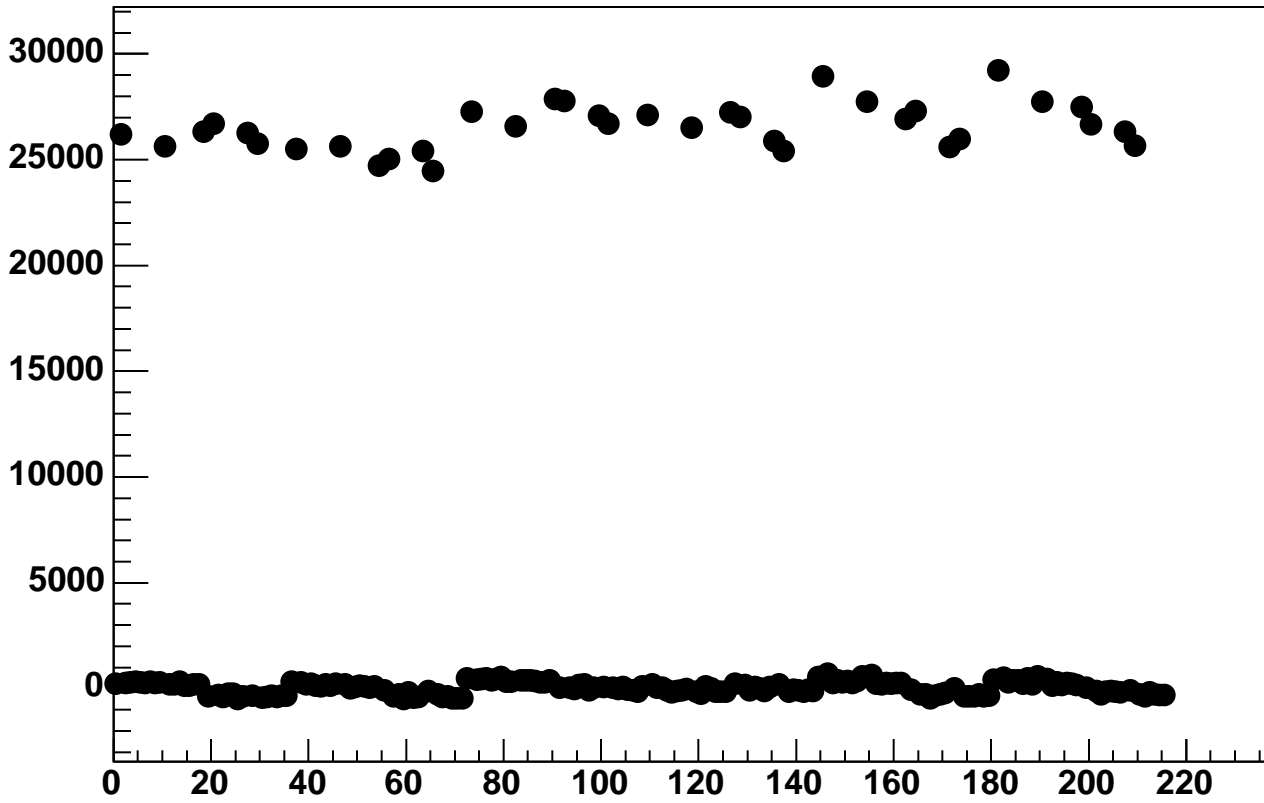
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18\*Chip+Chan



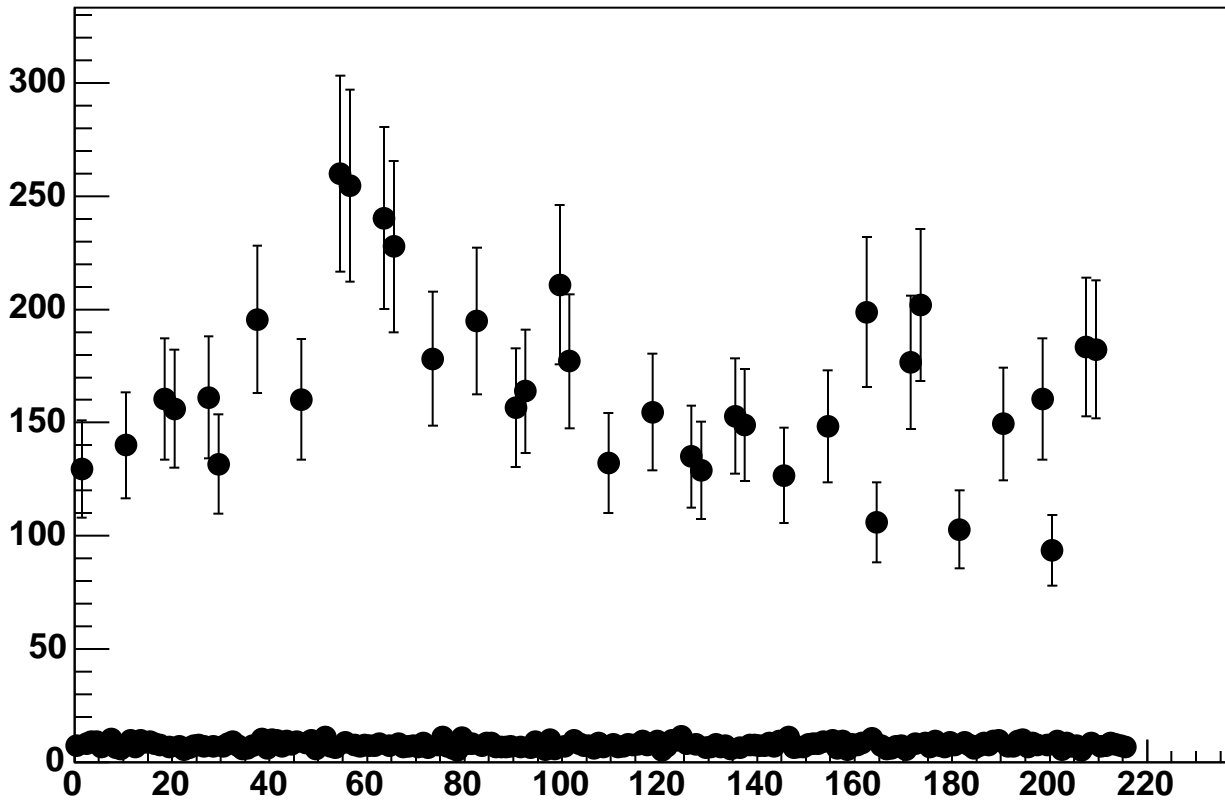
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18\*Chip+Chan



Enable 3, Hold=35, DAC=12000, ADC Mean vs 18\*Chip+Chan

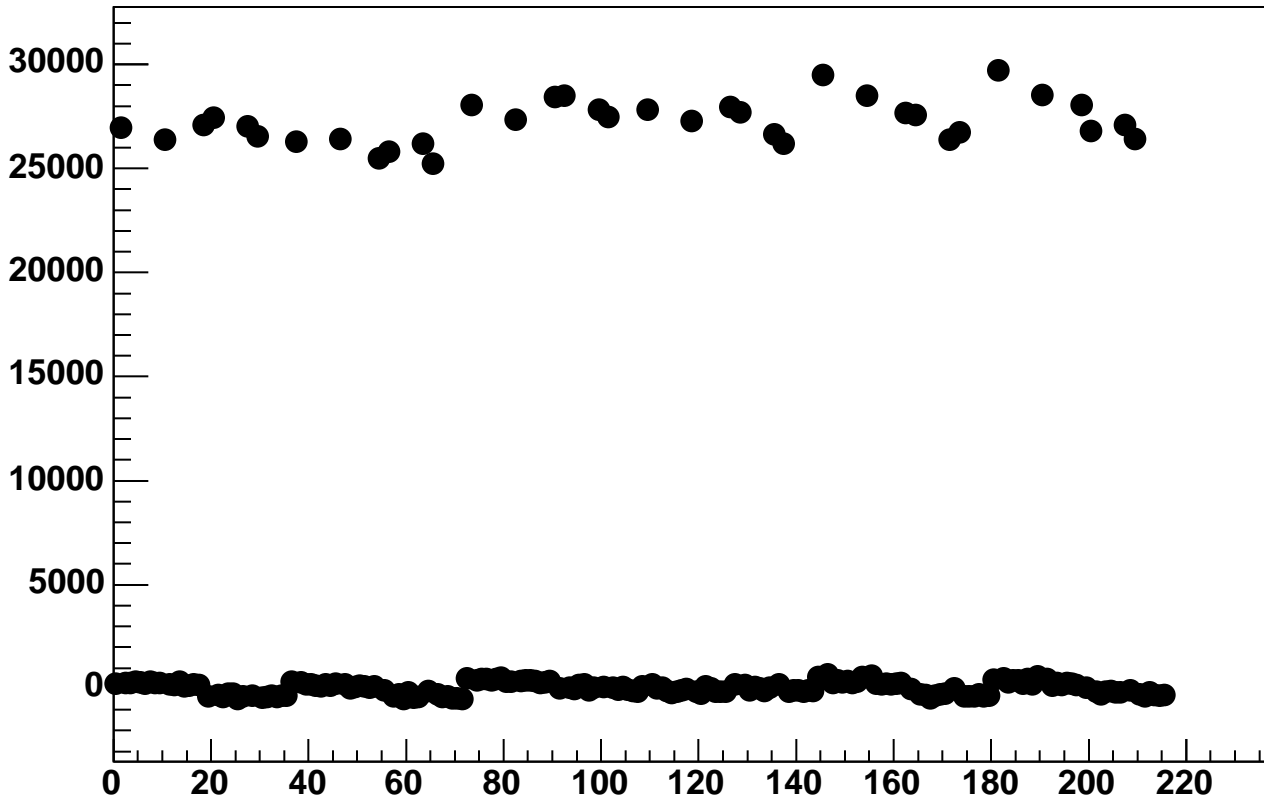


Enable 3, Hold=35, DAC=12000, ADC Noise vs 18\*Chip+Chan

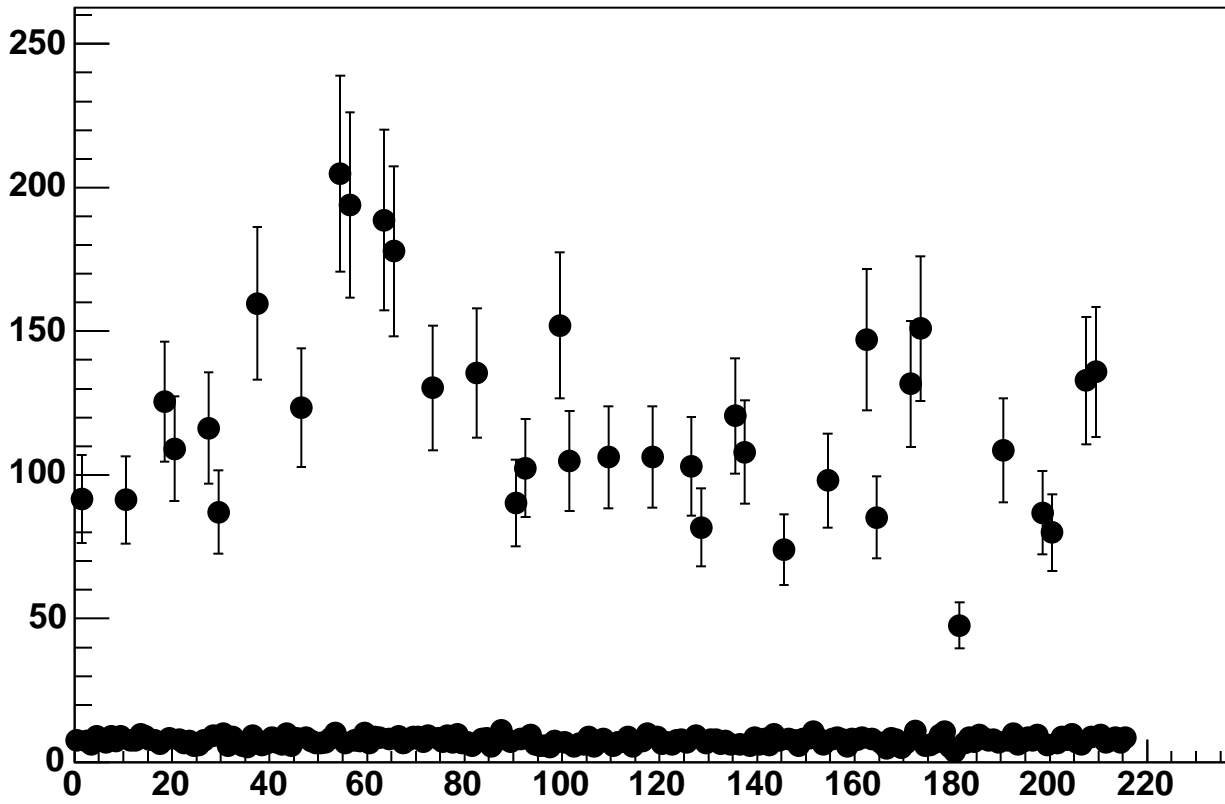




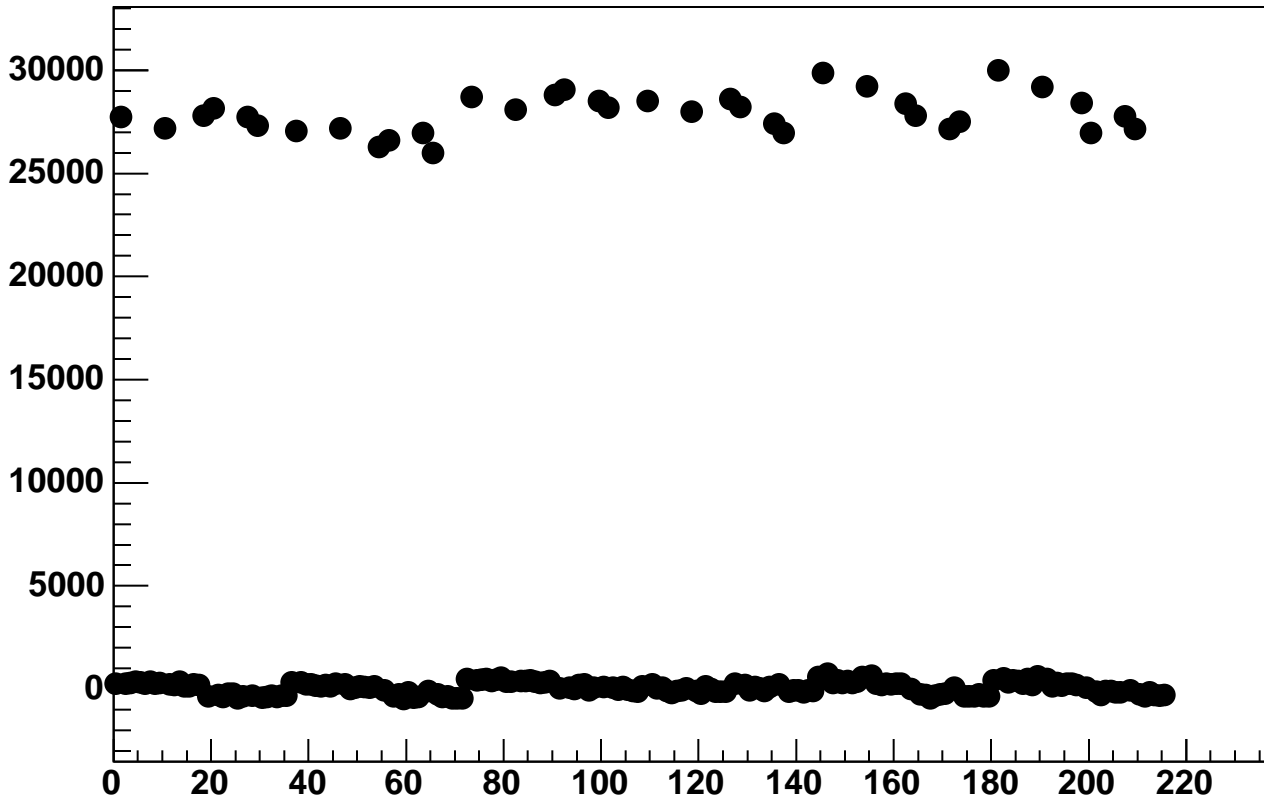
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18\*Chip+Chan



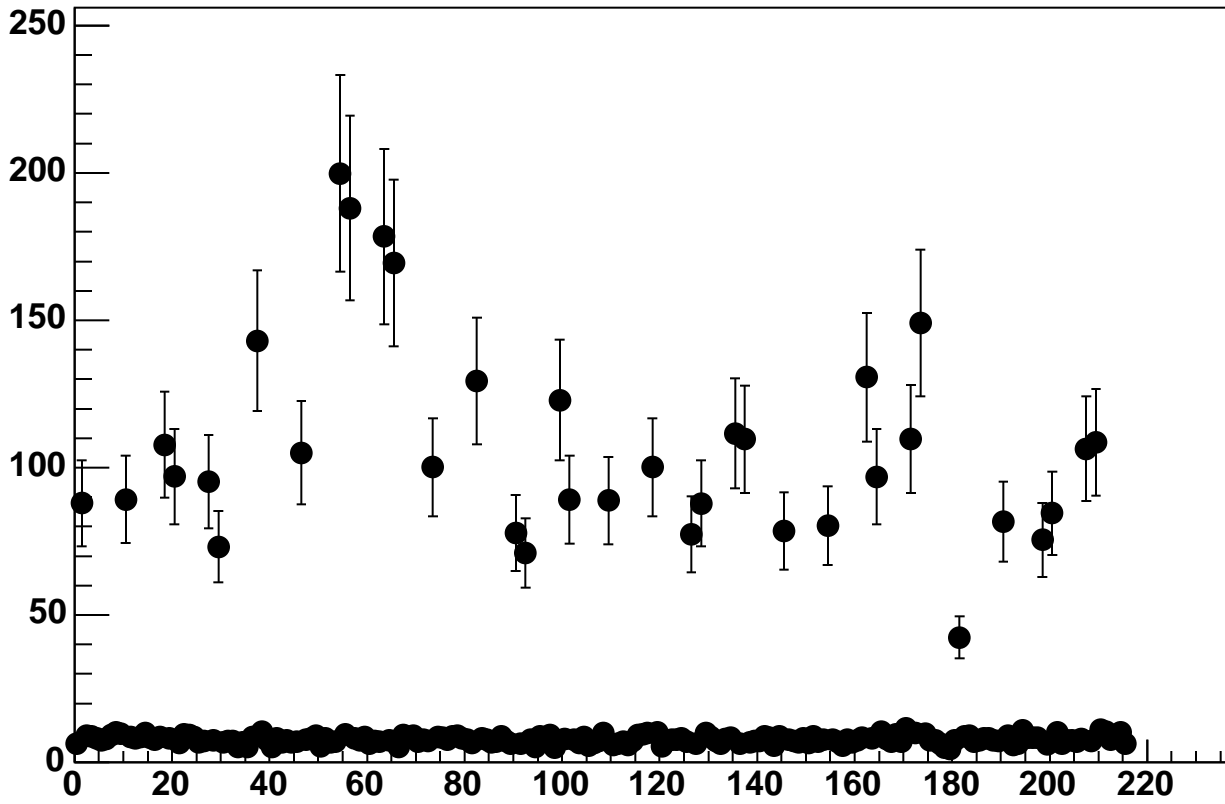
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18\*Chip+Chan



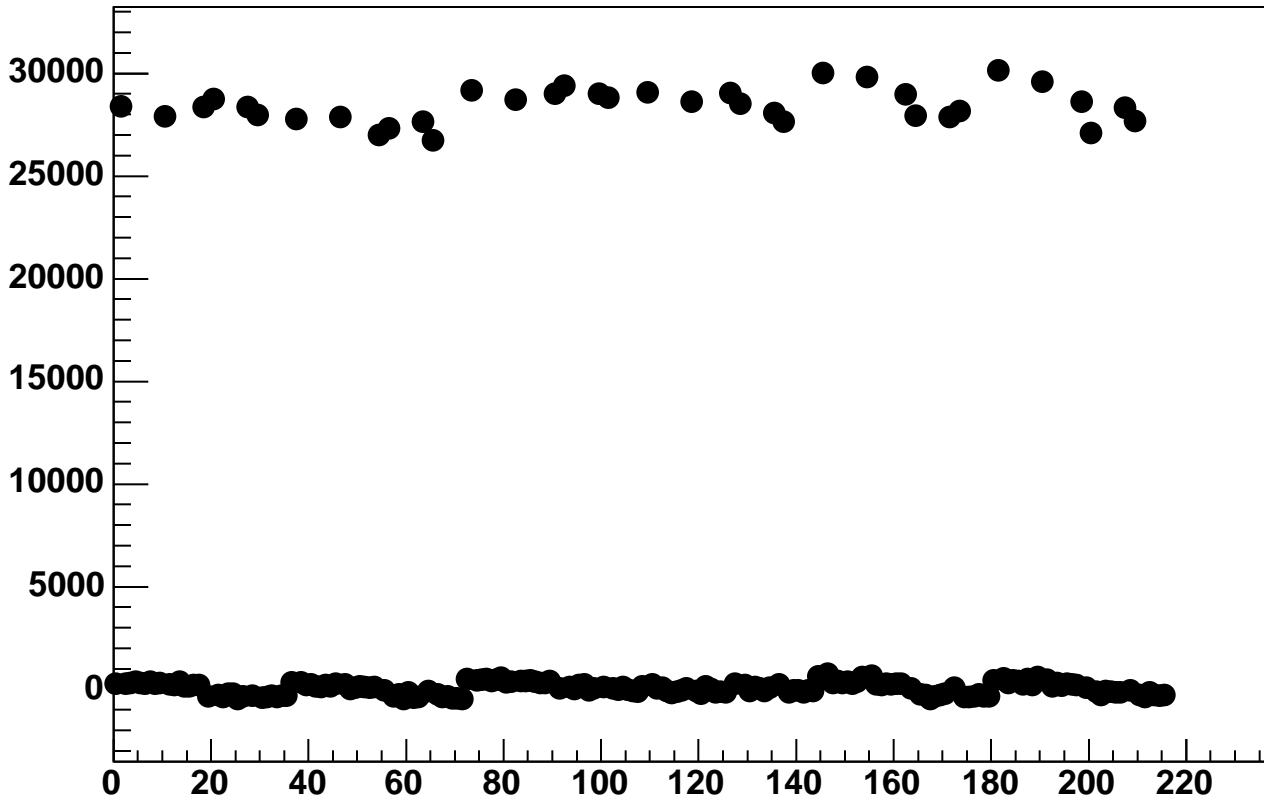
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18\*Chip+Chan



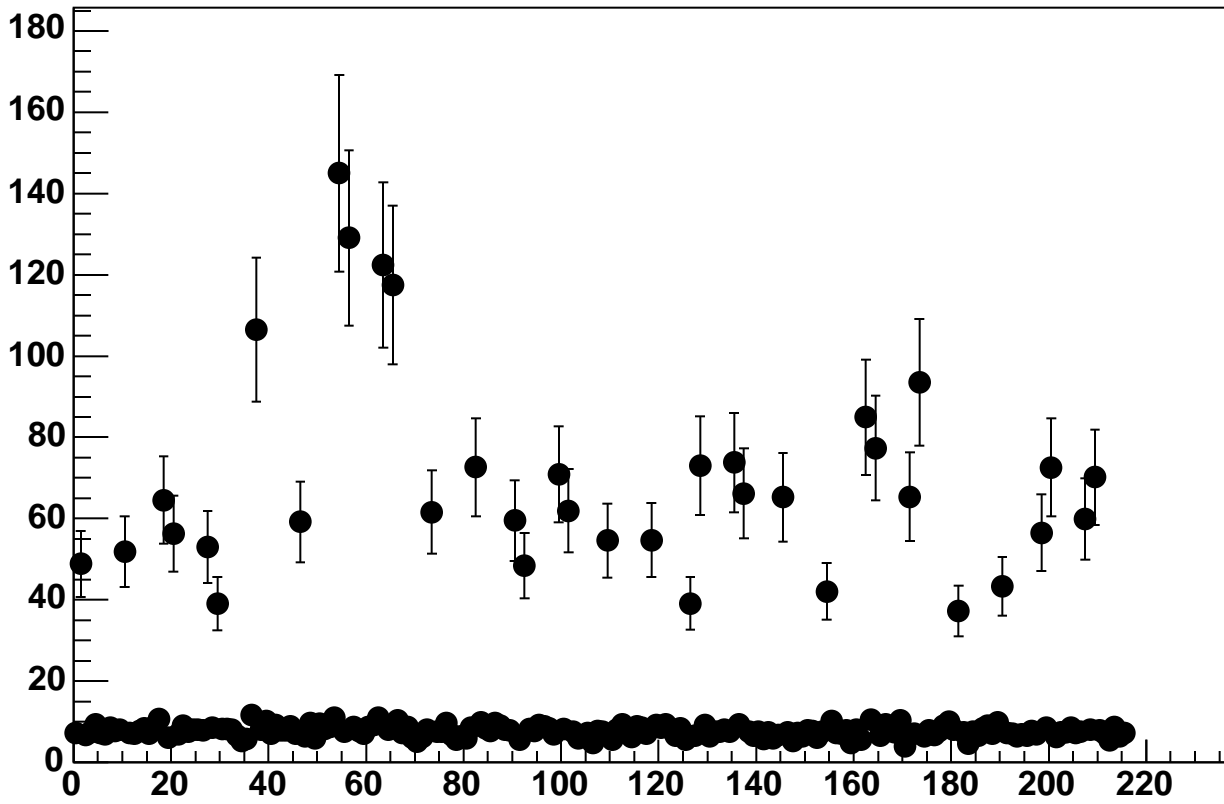
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18\*Chip+Chan



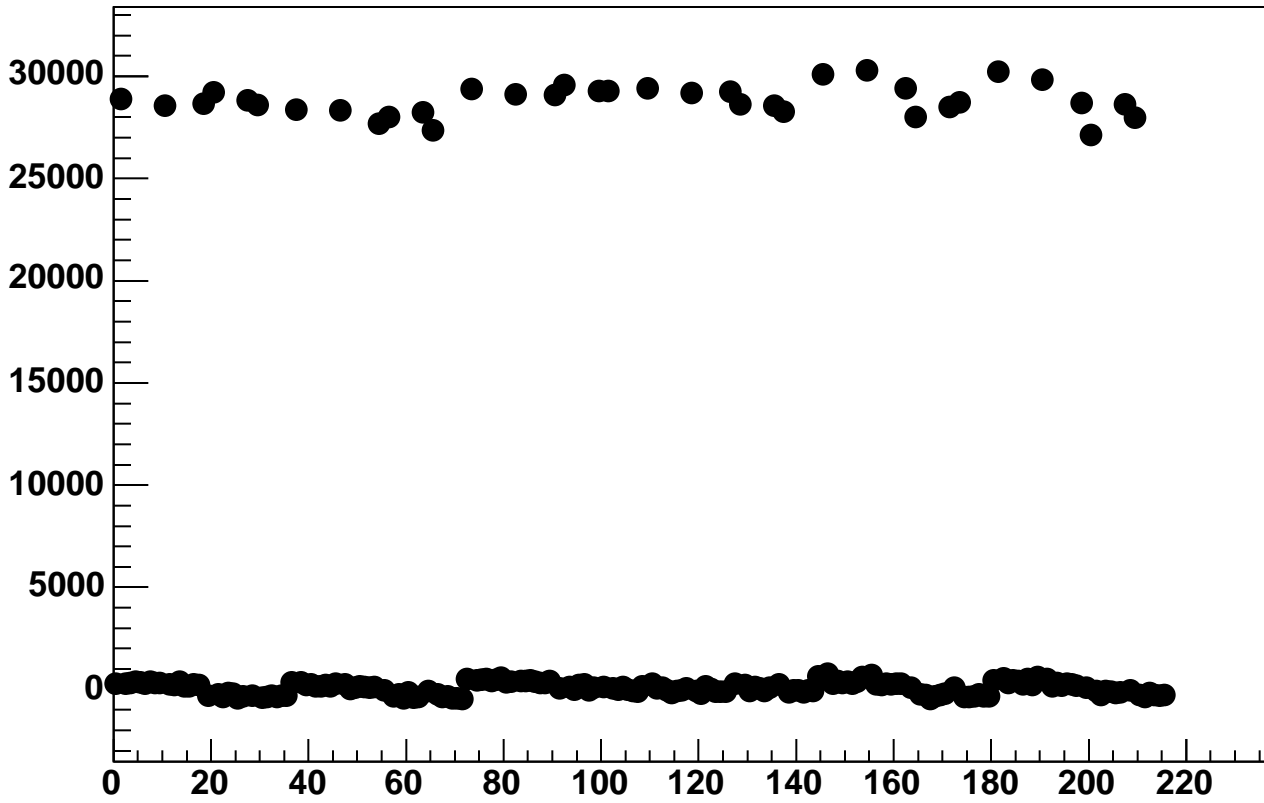
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18\*Chip+Chan



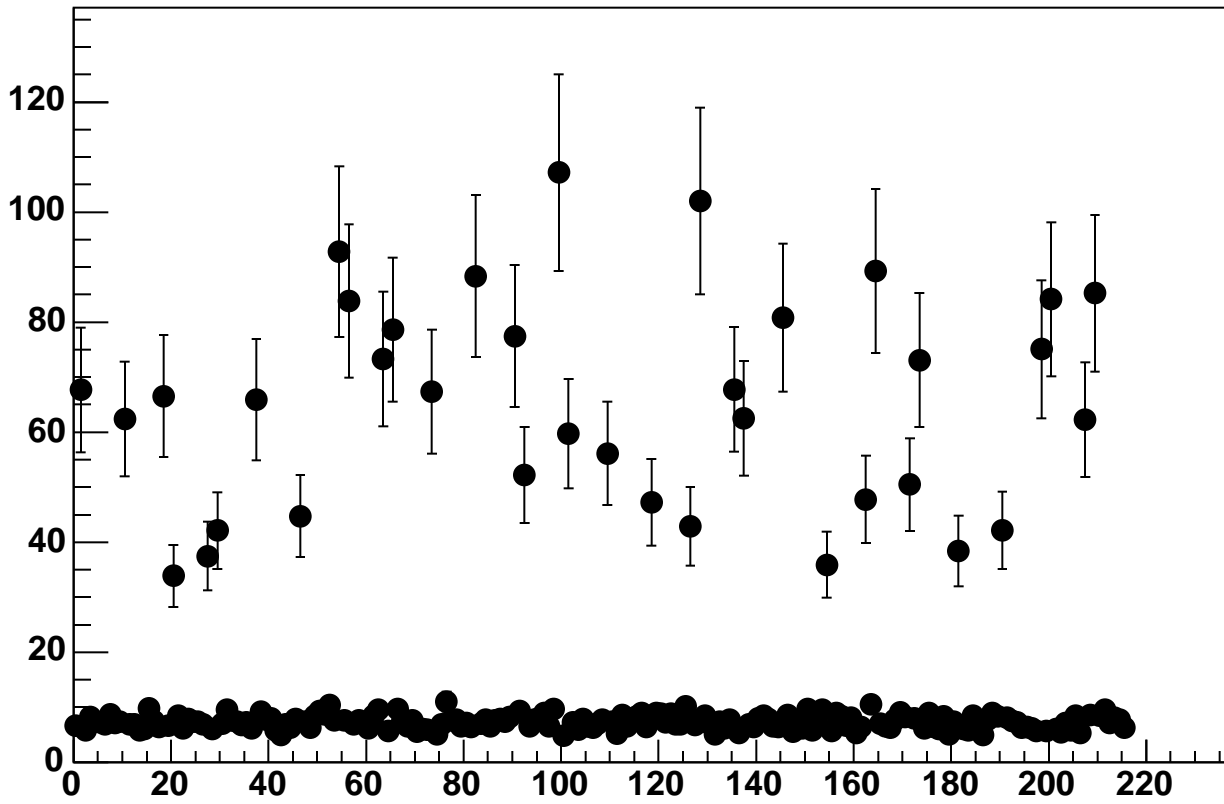
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18\*Chip+Chan



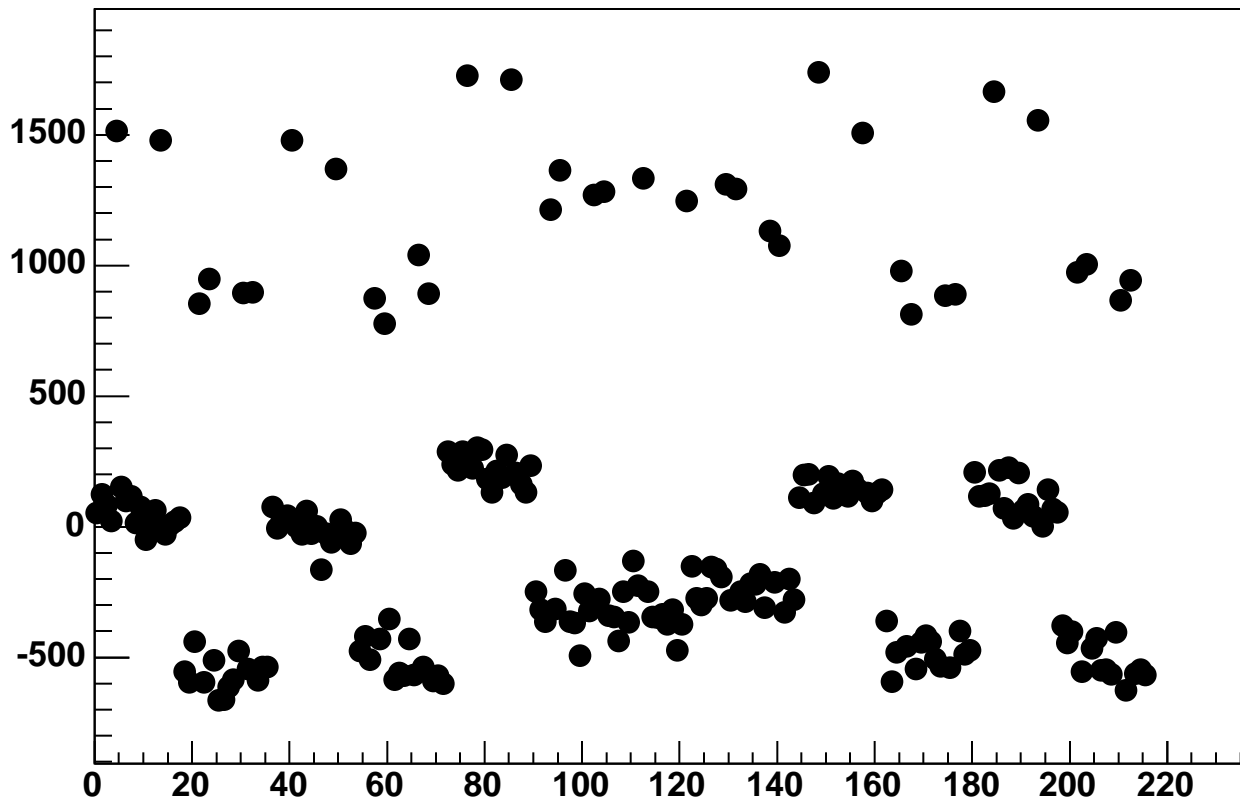
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18\*Chip+Chan



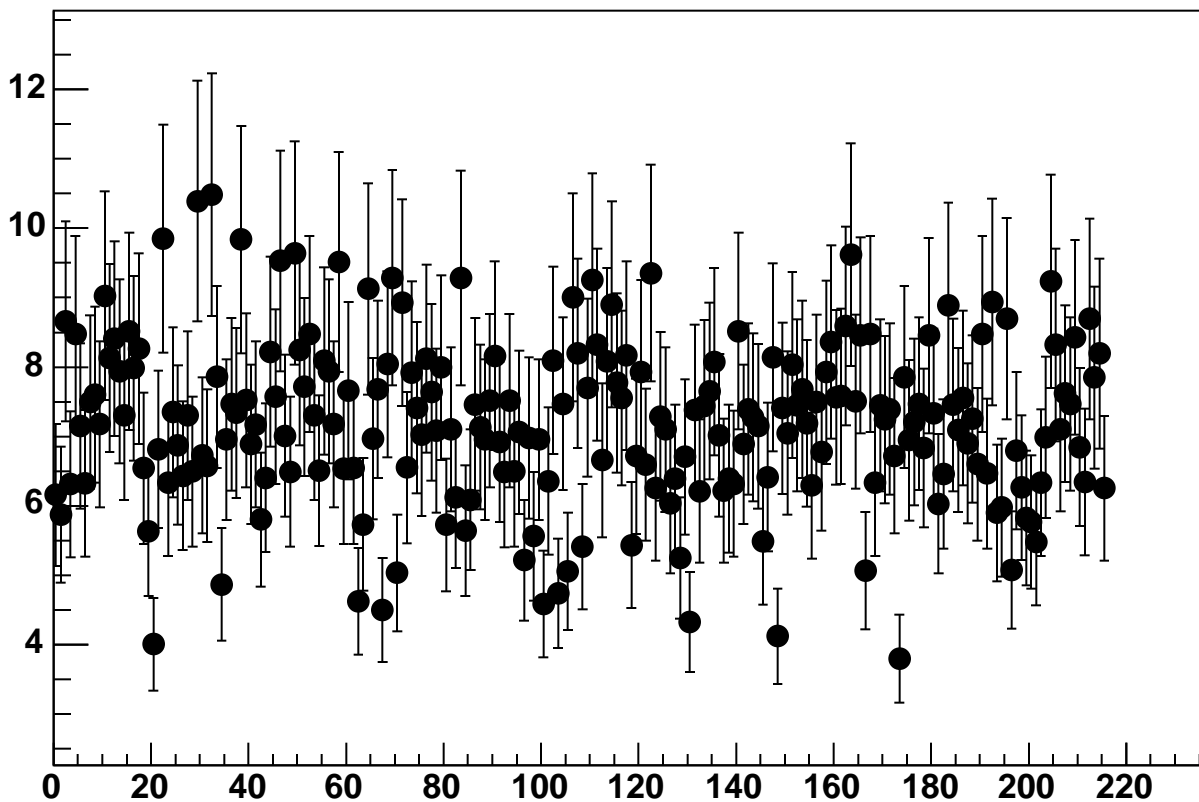
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18\*Chip+Chan



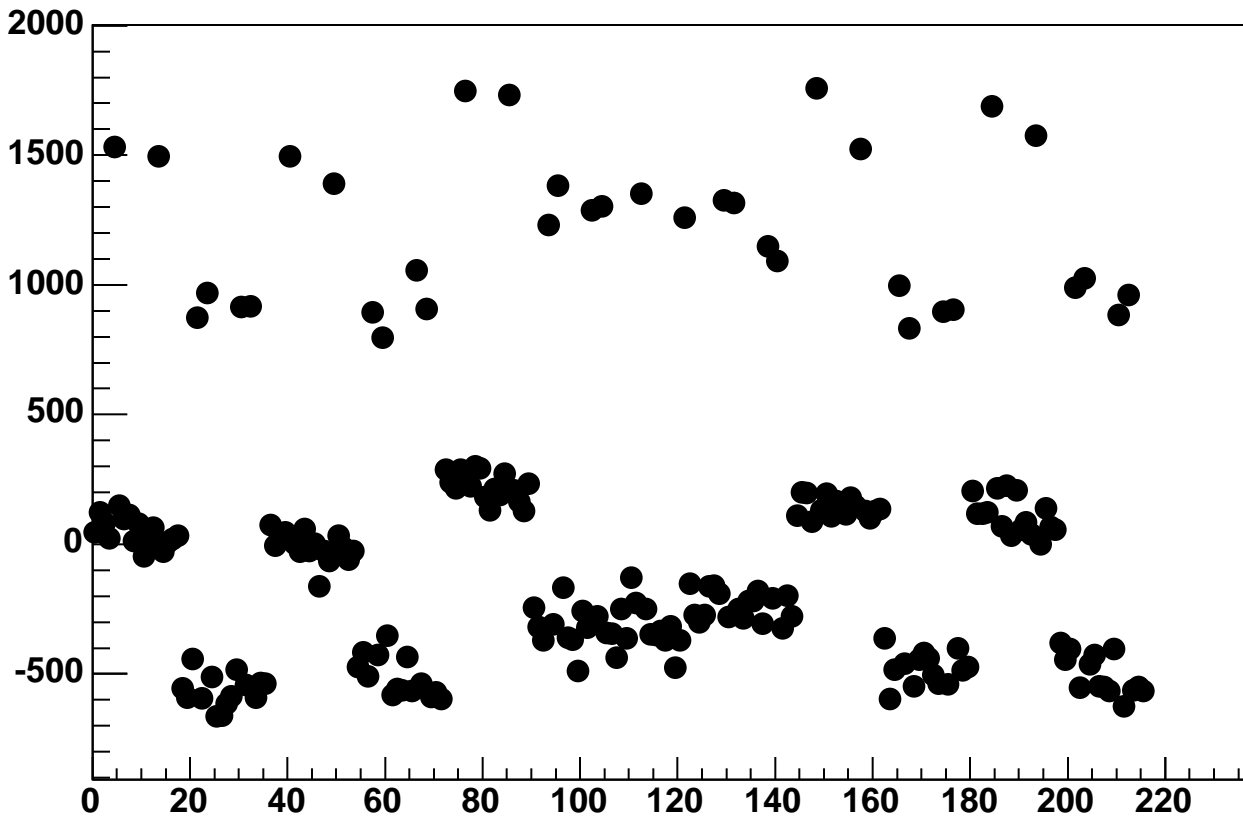
Enable 4, Hold=35, DAC=0, ADC Mean vs 18\*Chip+Chan



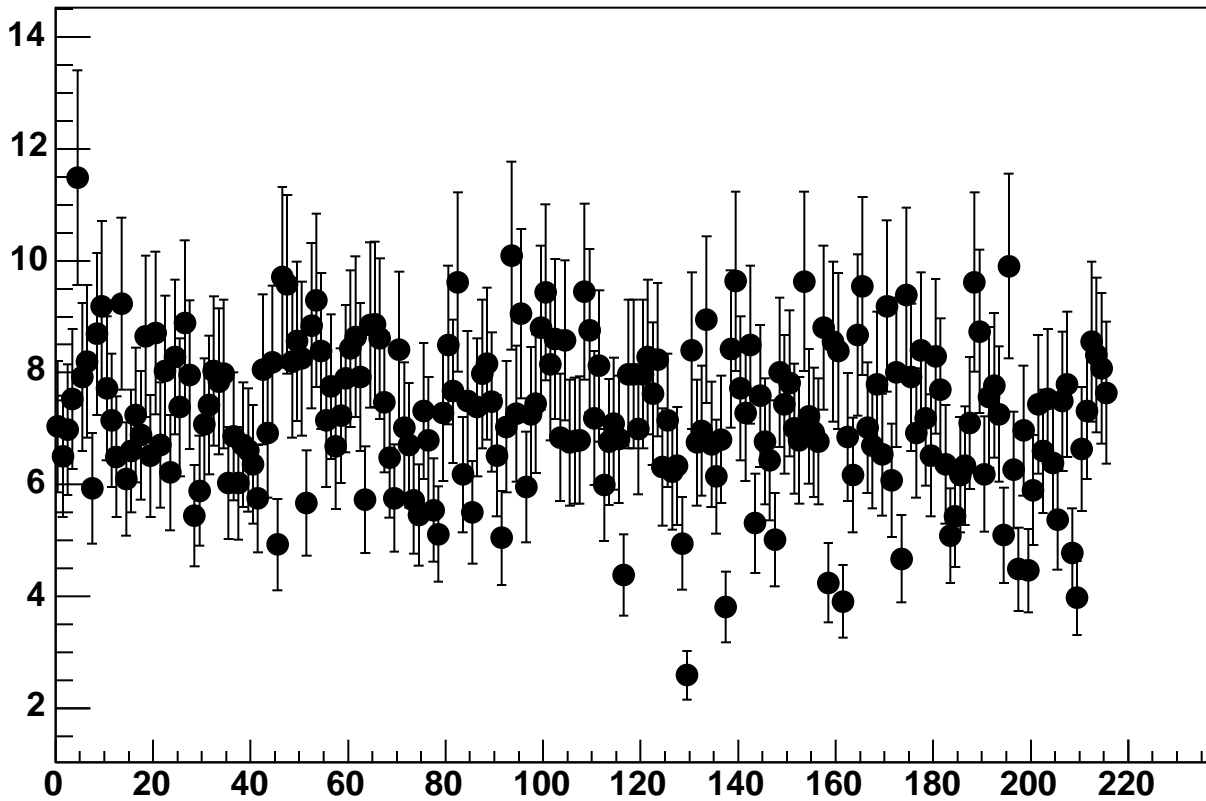
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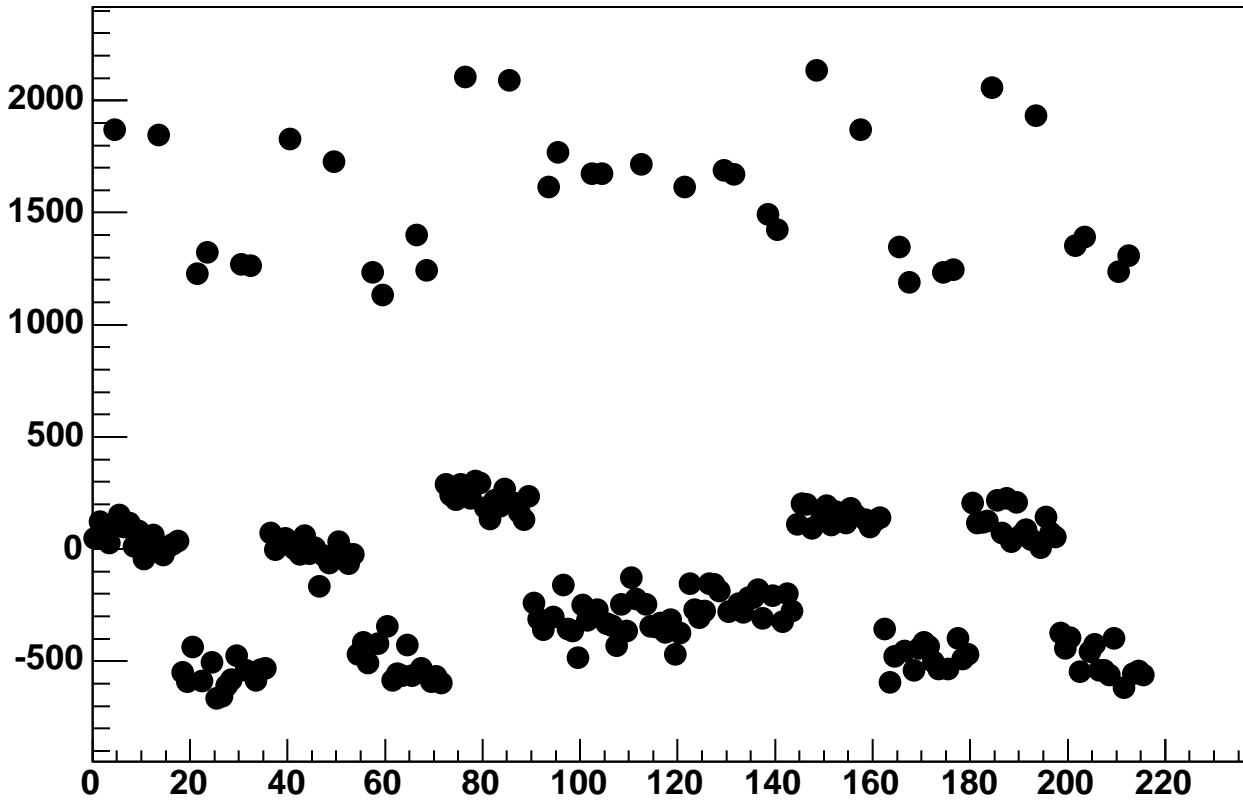
Enable 4, Hold=35, DAC=400, ADC Mean vs 18\*Chip+Chan



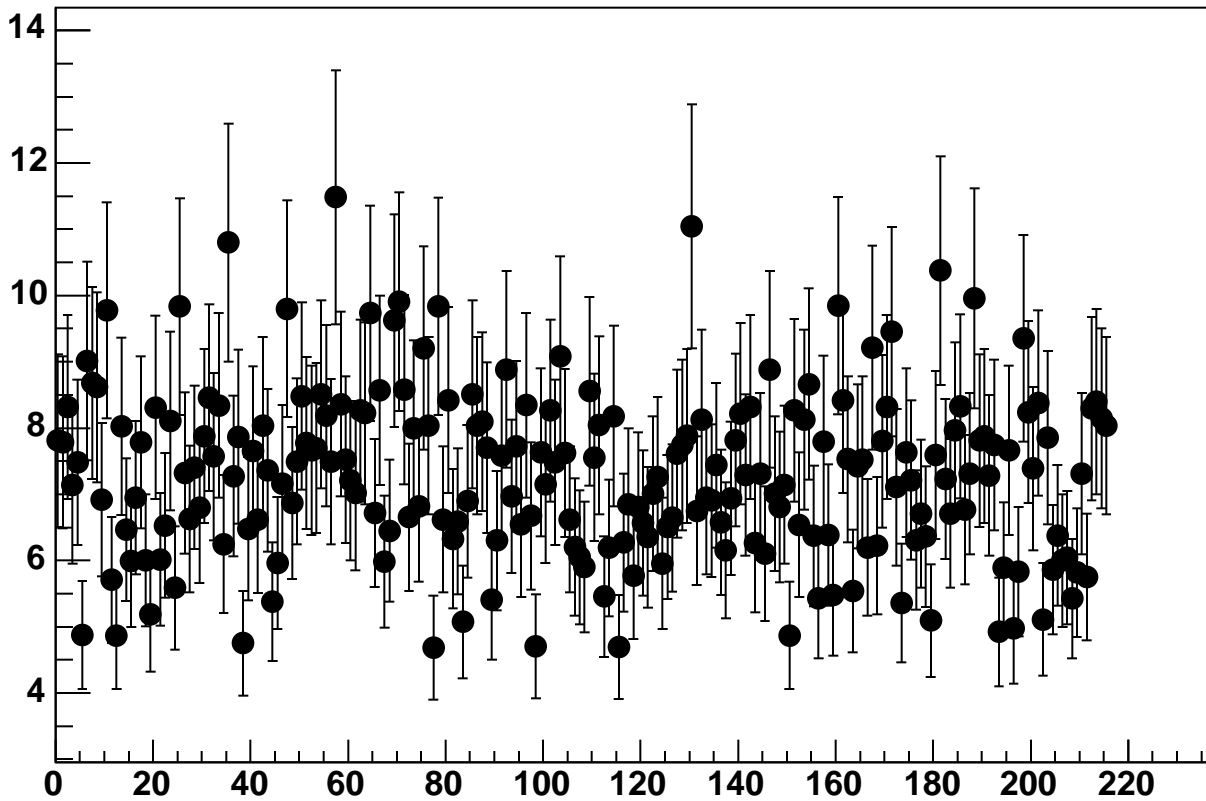
Enable 4, Hold=35, DAC=400, ADC Noise vs 18\*Chip+Chan



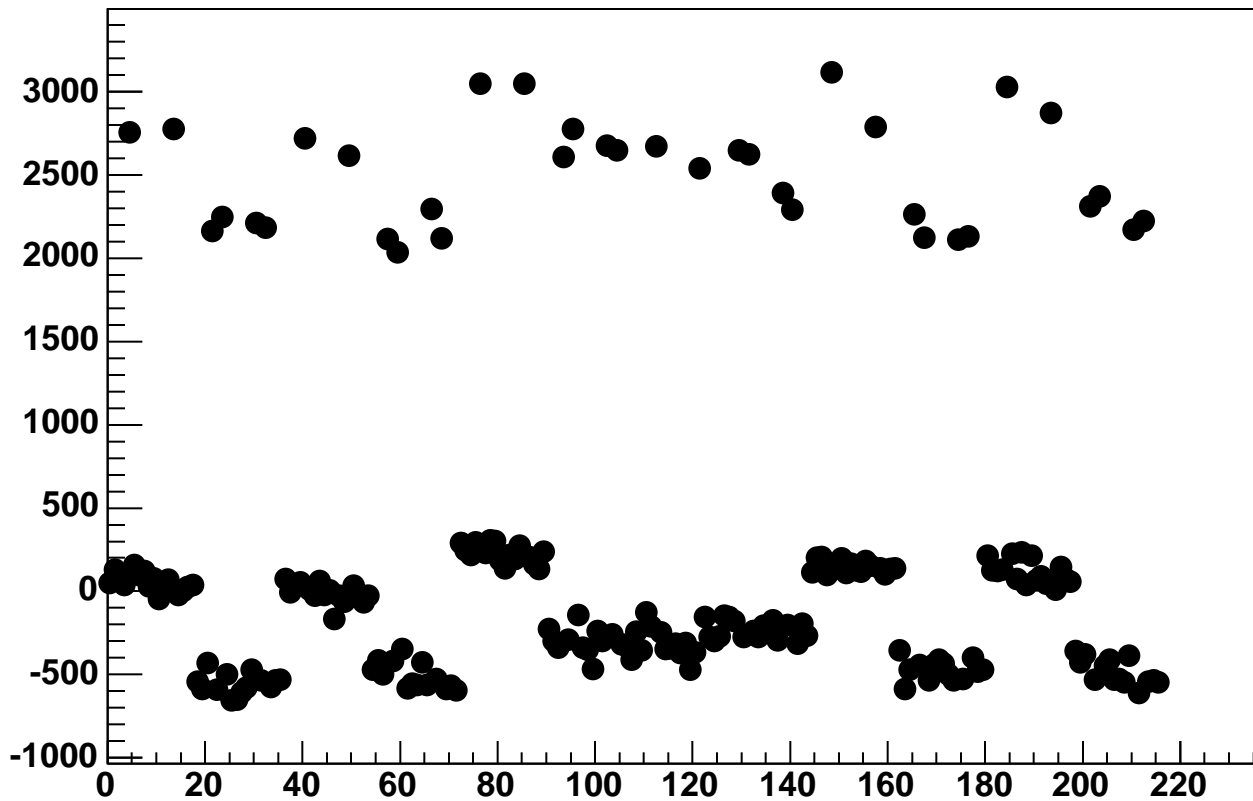
Enable 4, Hold=35, DAC=800, ADC Mean vs 18\*Chip+Chan



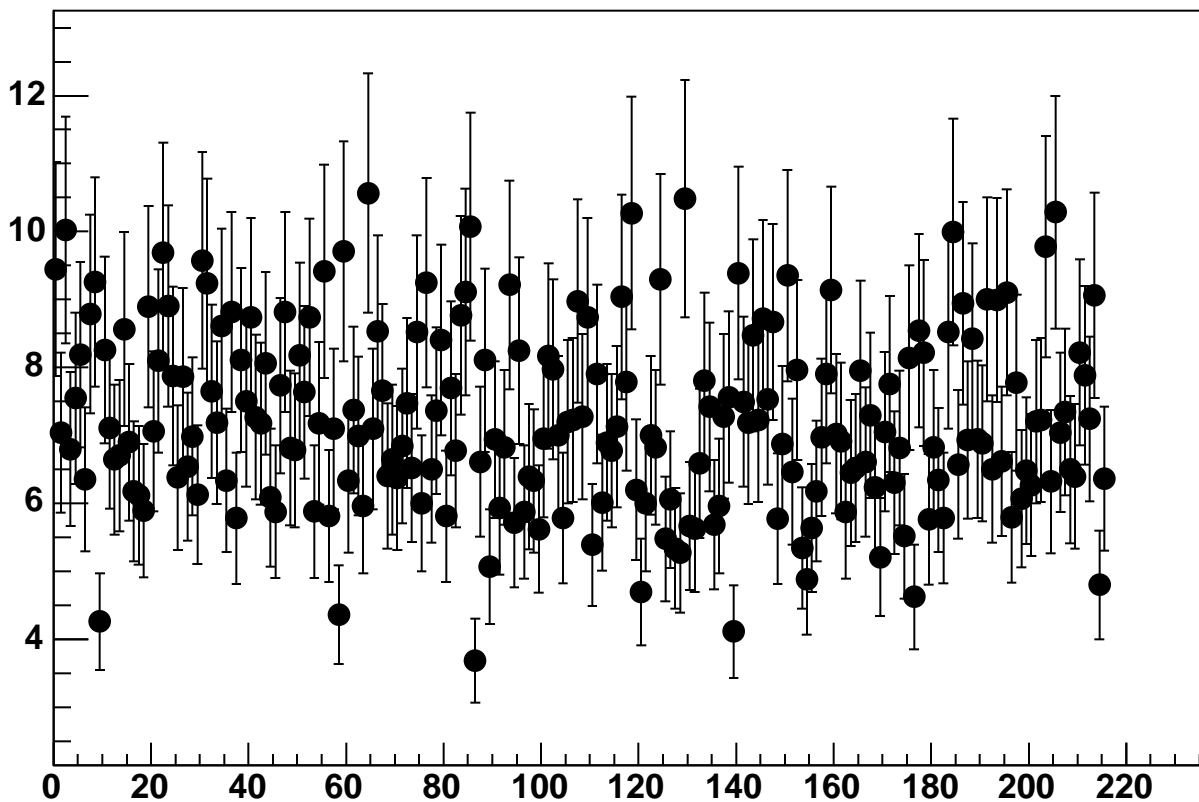
Enable 4, Hold=35, DAC=800, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=35, DAC=1200, ADC Mean vs 18\*Chip+Chan

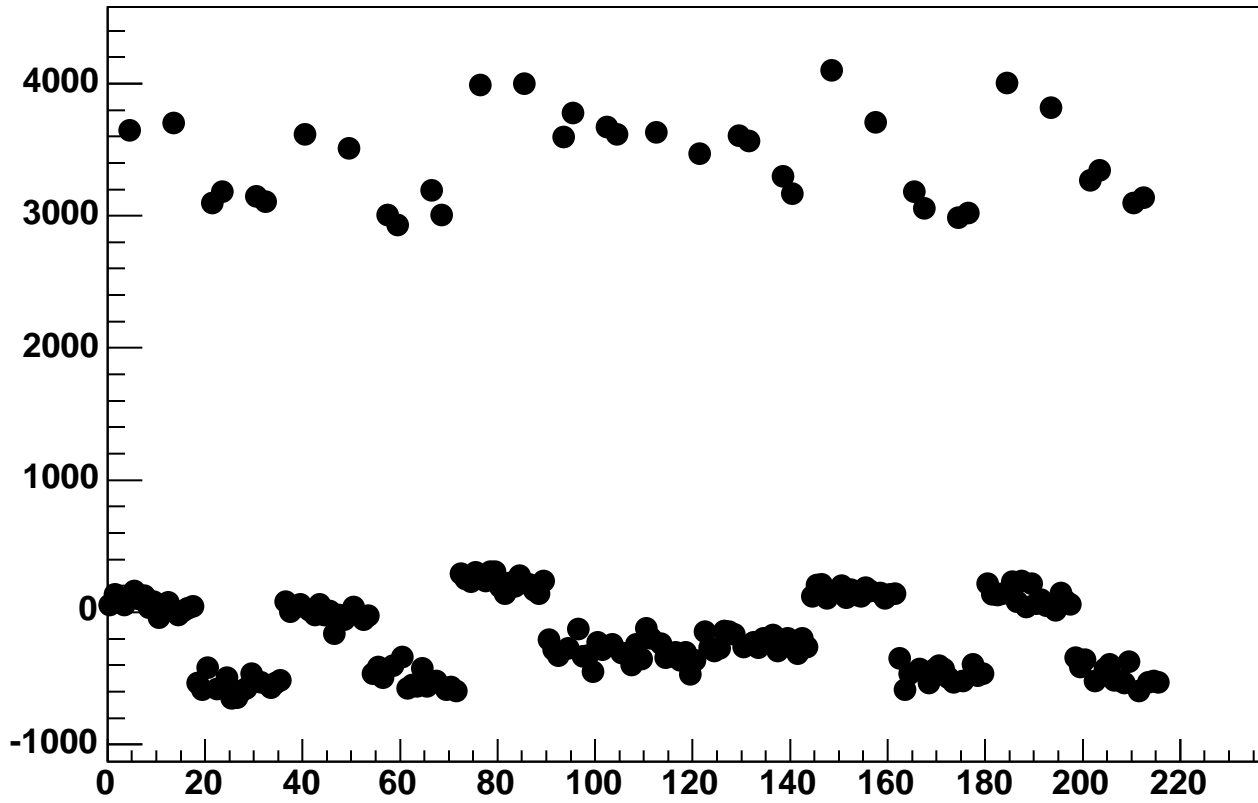


Enable 4, Hold=35, DAC=1200, ADC Noise vs 18\*Chip+Chan

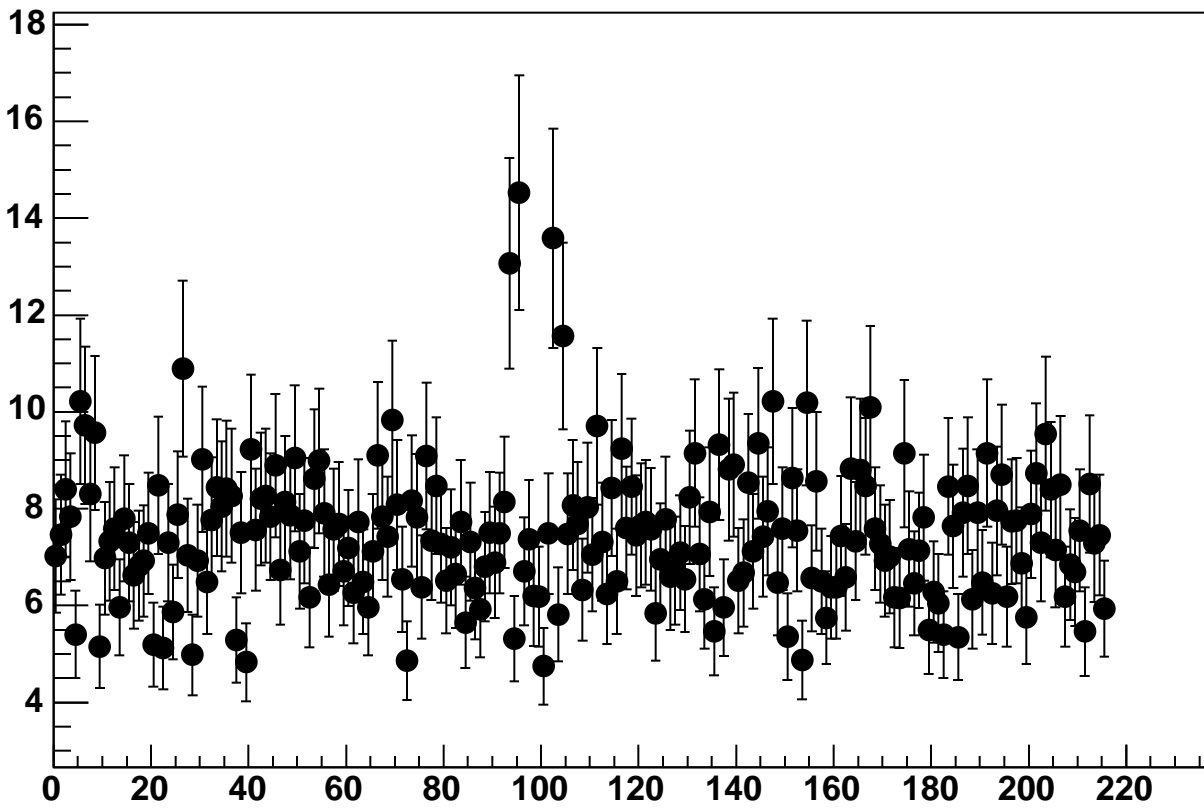




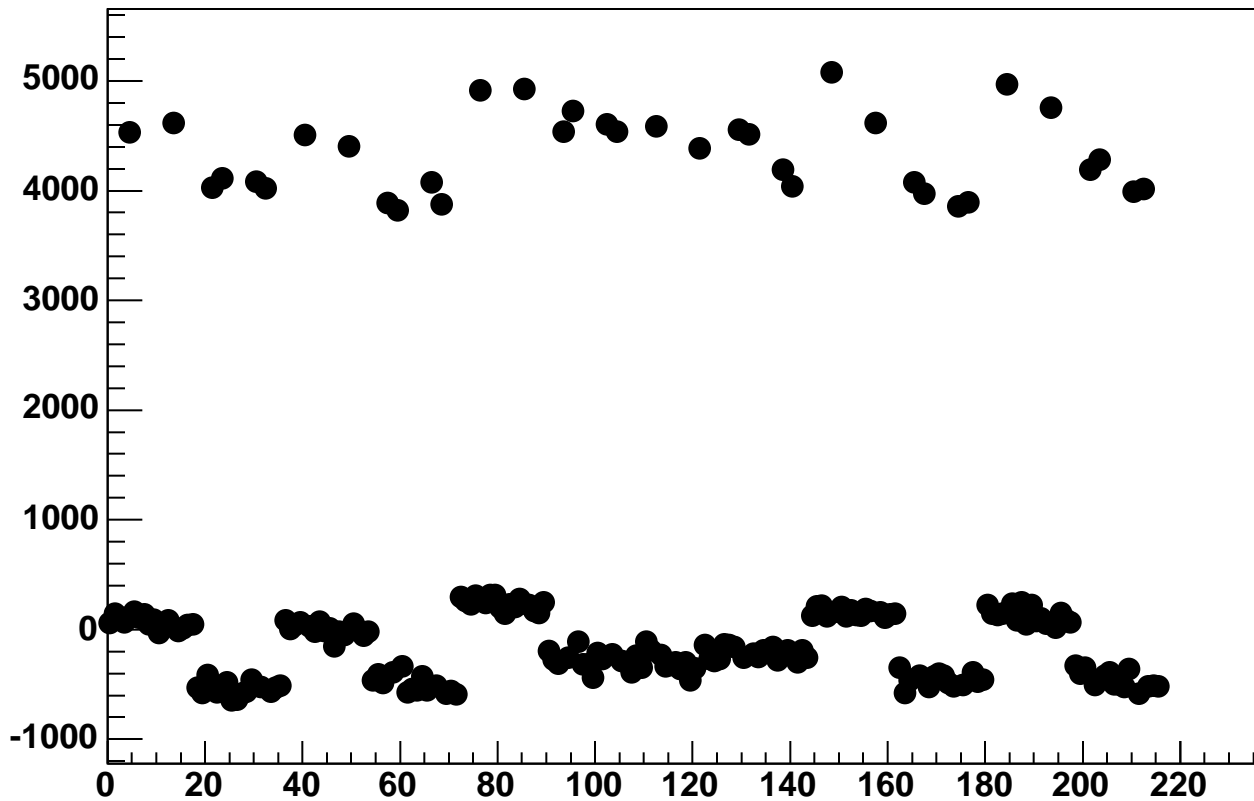
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18\*Chip+Chan



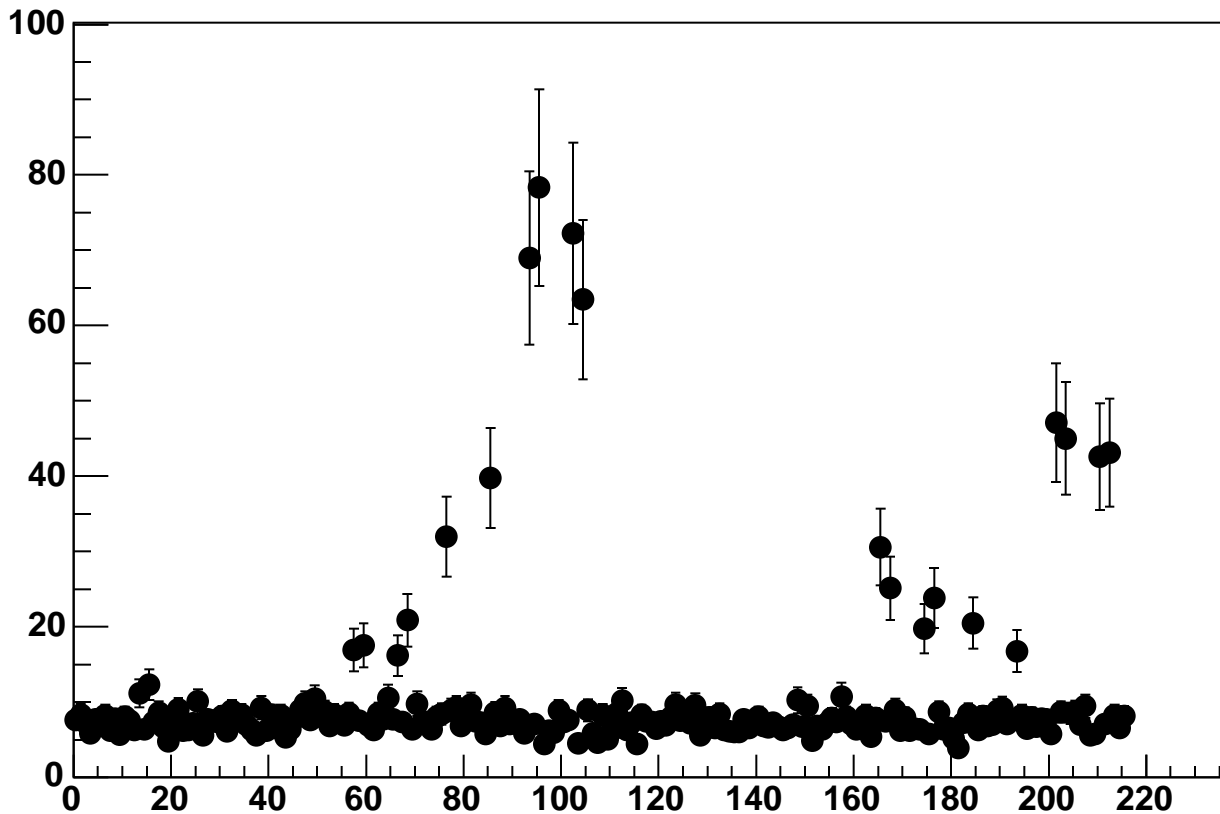
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18\*Chip+Chan



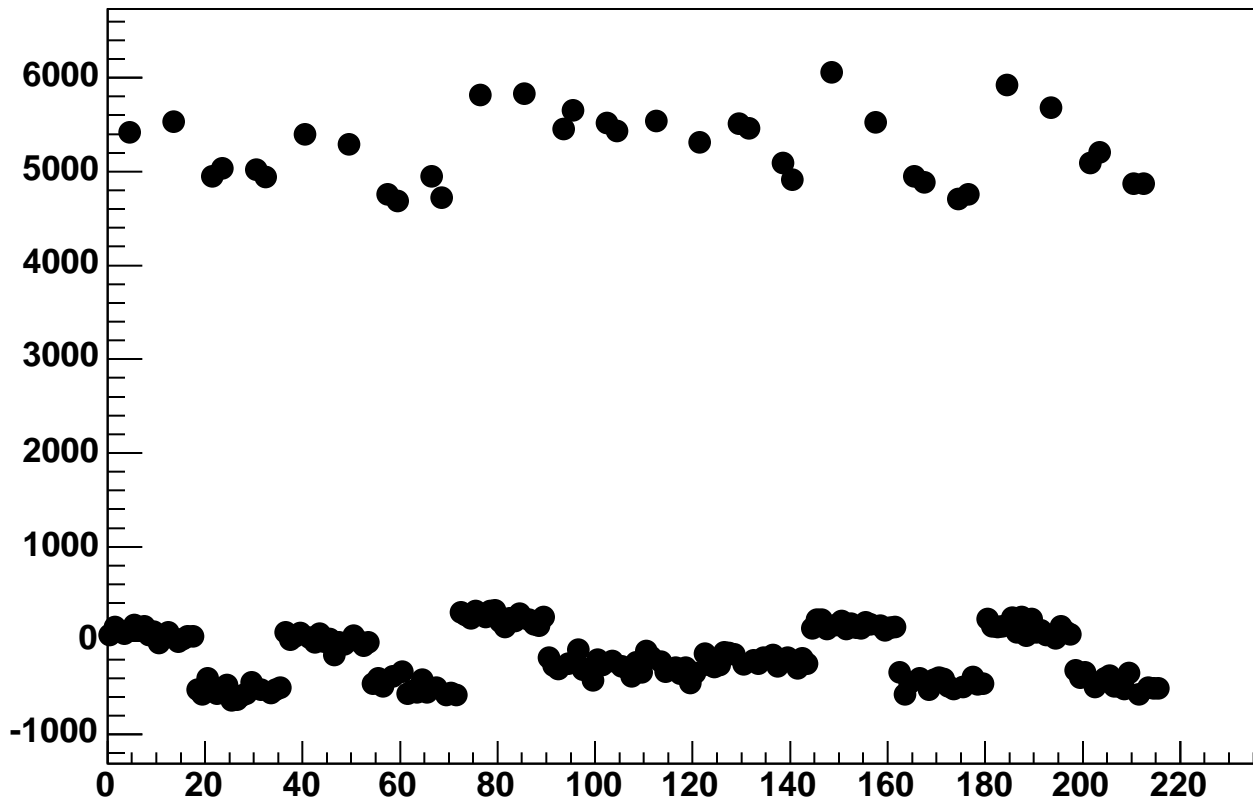
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18\*Chip+Chan



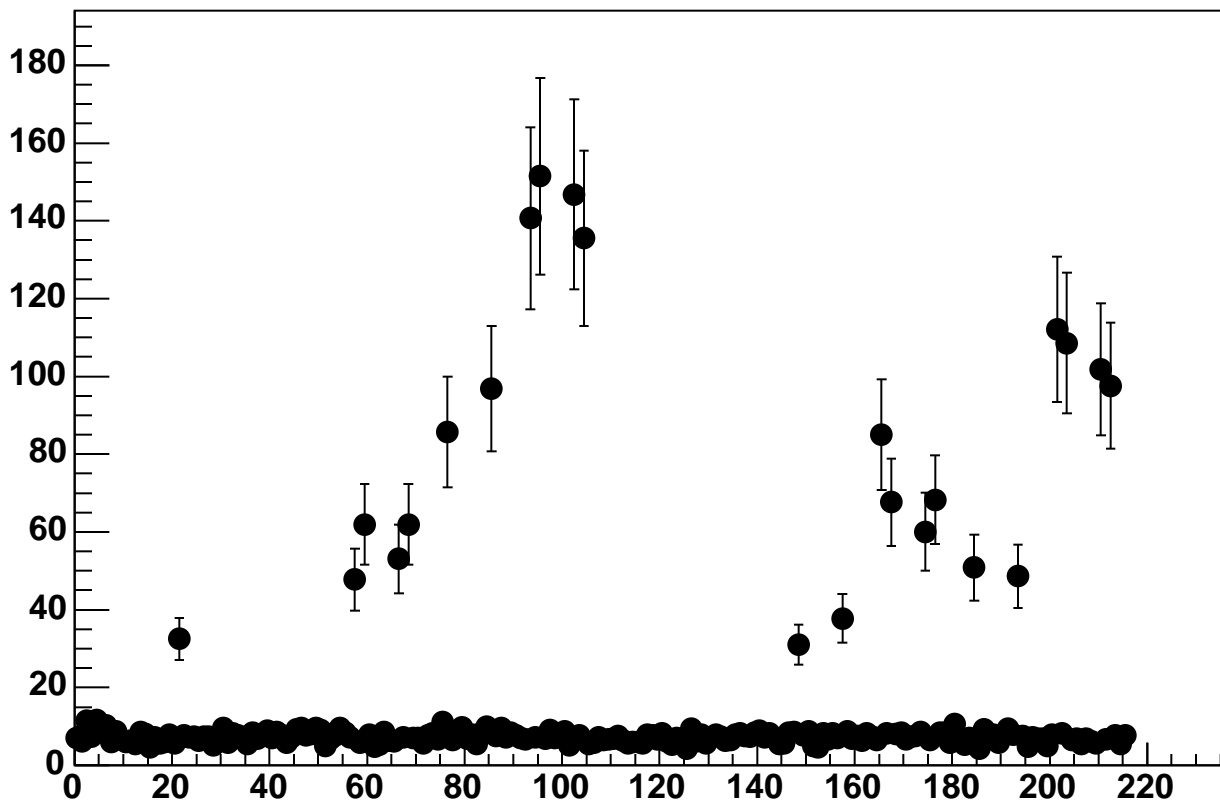
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18\*Chip+Chan



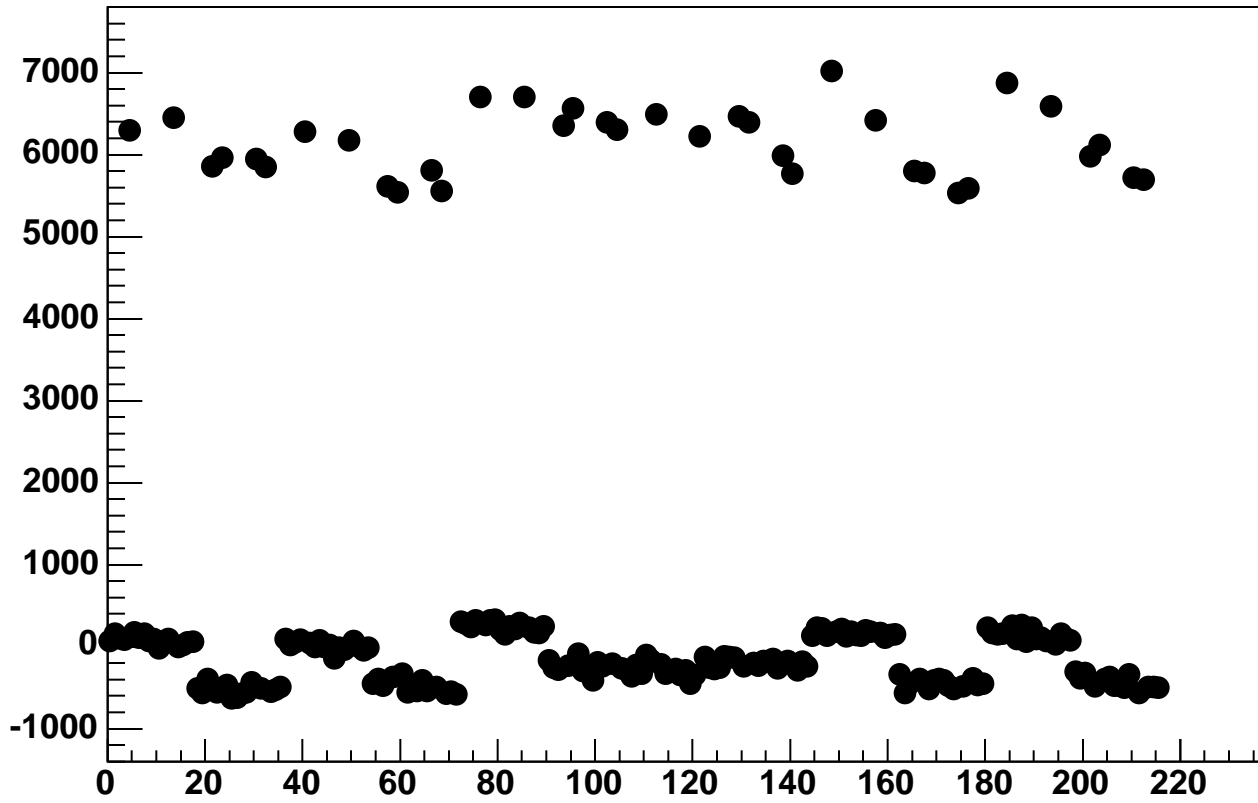
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18\*Chip+Chan



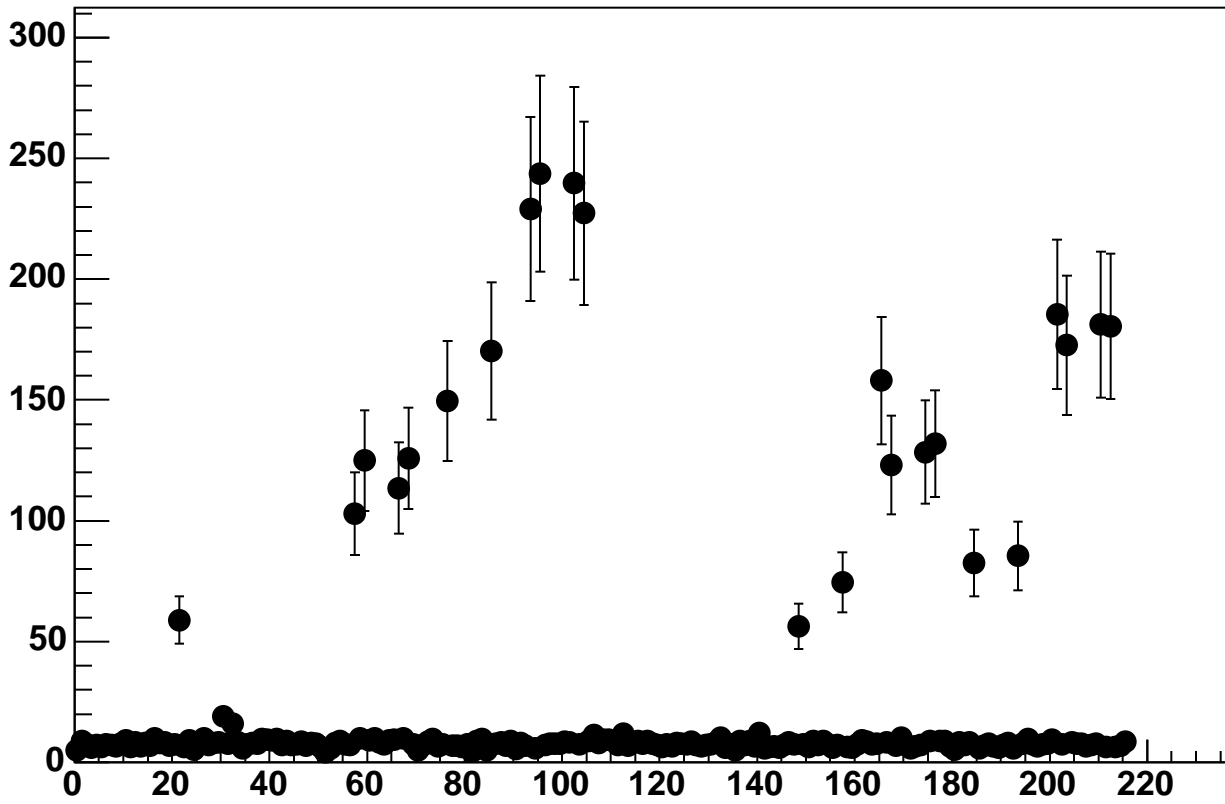
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18\*Chip+Chan



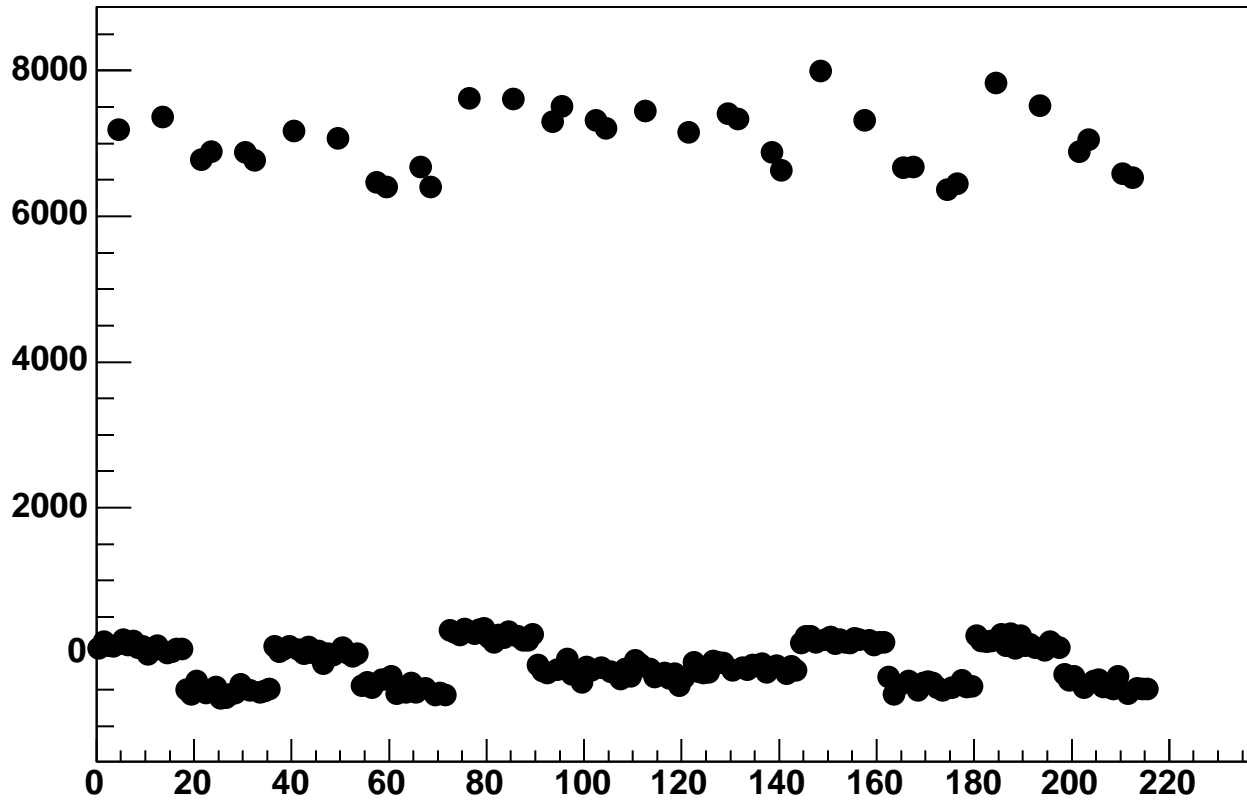
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18\*Chip+Chan



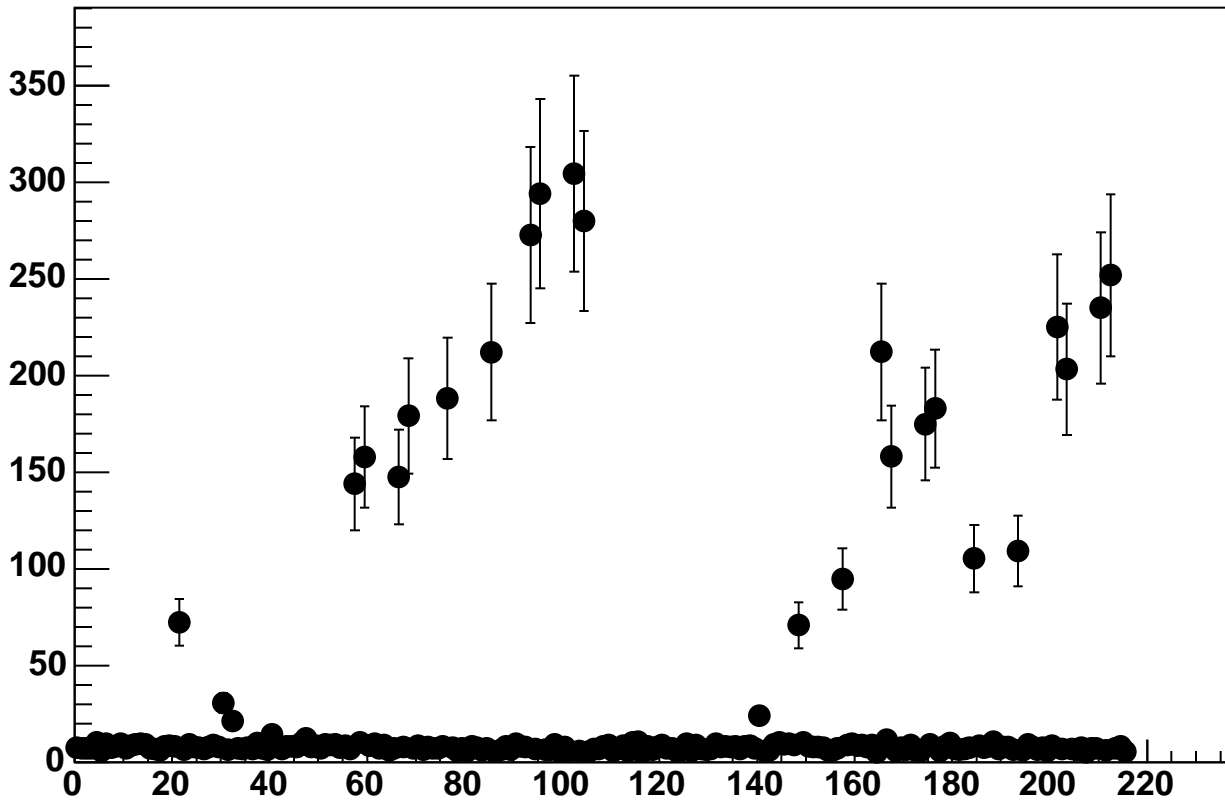
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18\*Chip+Chan



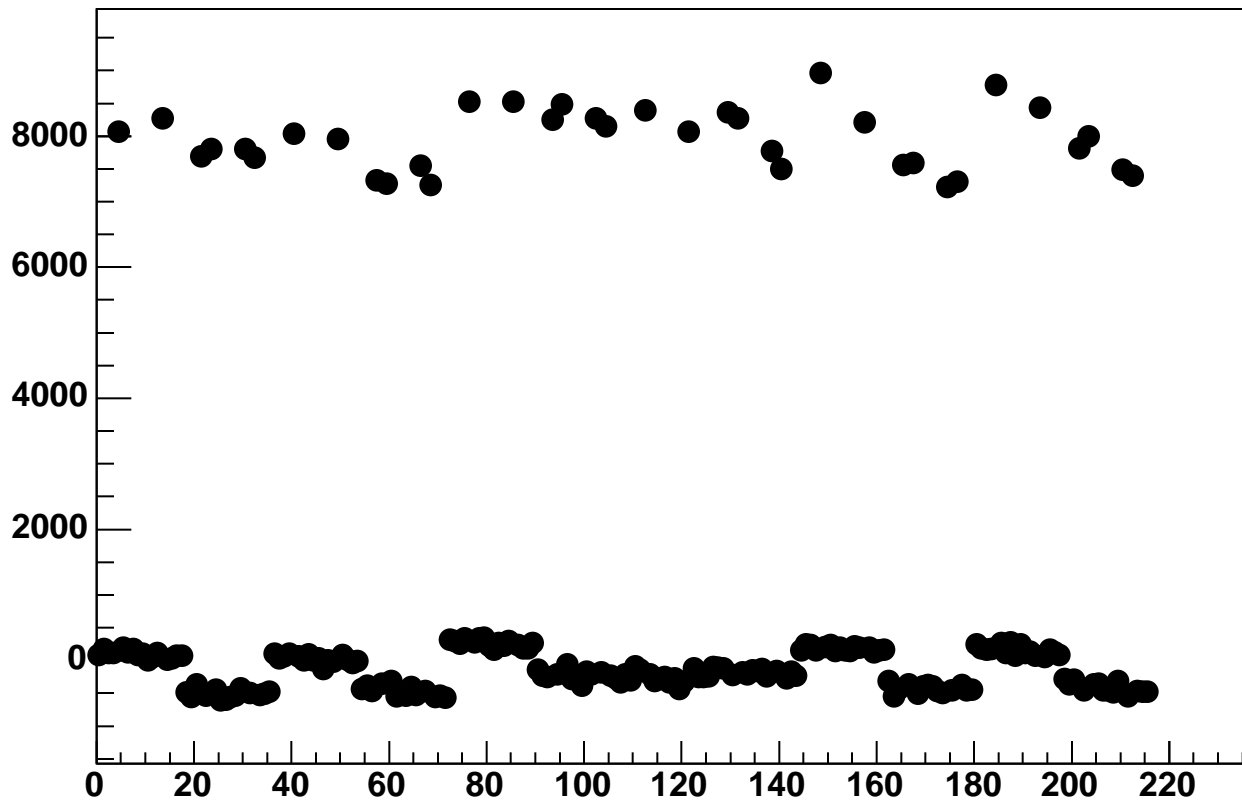
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18\*Chip+Chan



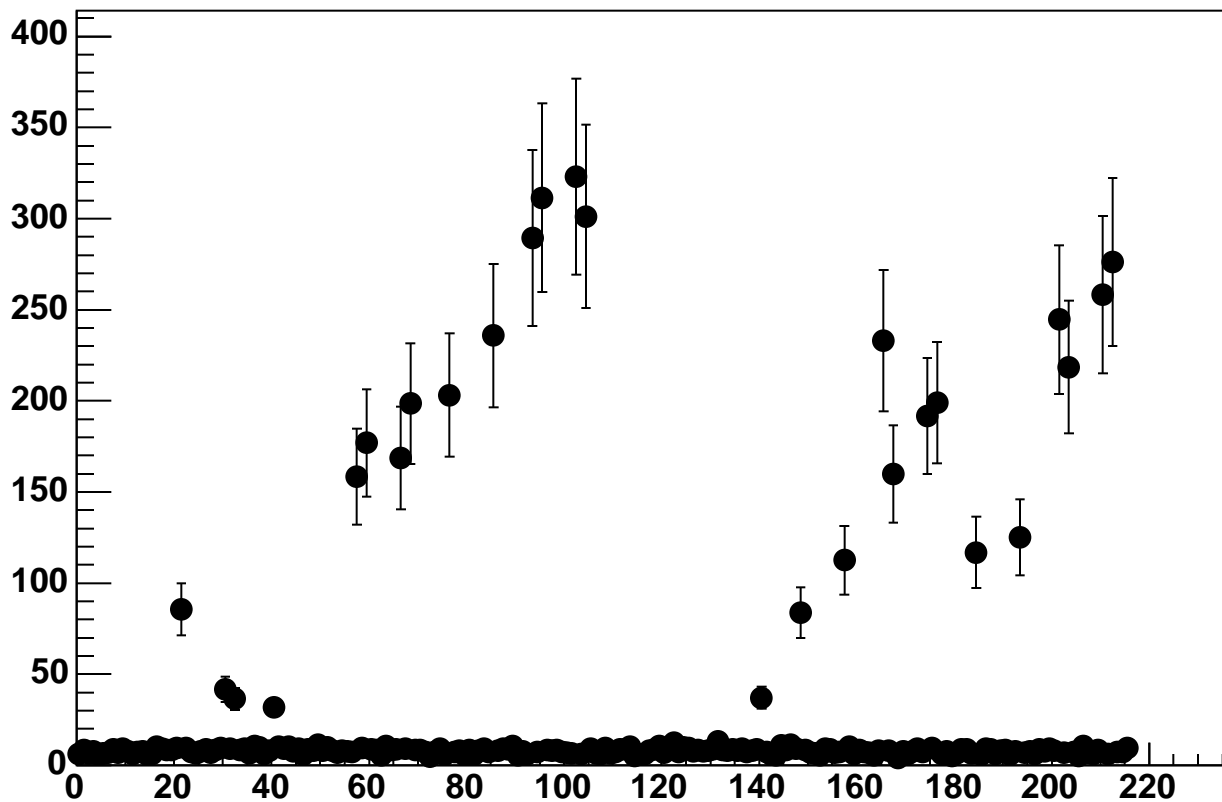
Enable 4, Hold=35, DAC=3200, ADC Noise vs 18\*Chip+Chan



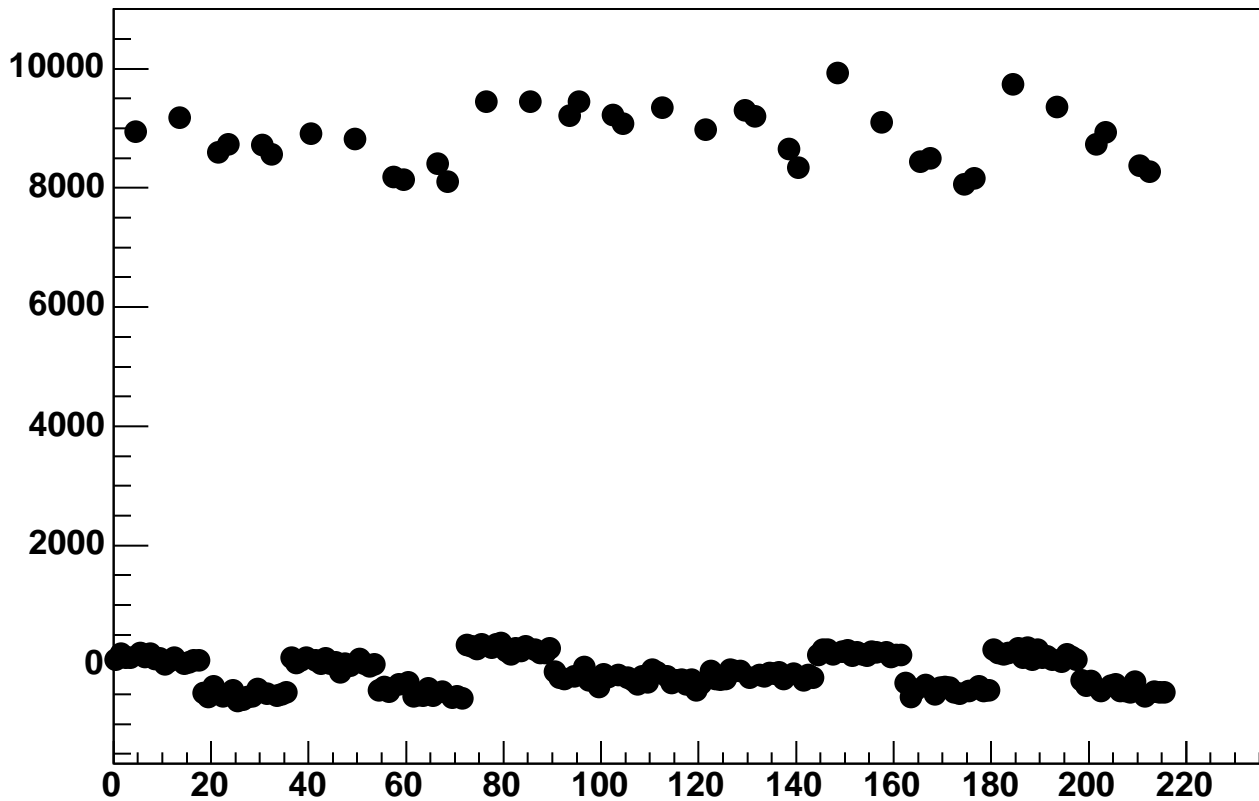
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18\*Chip+Chan



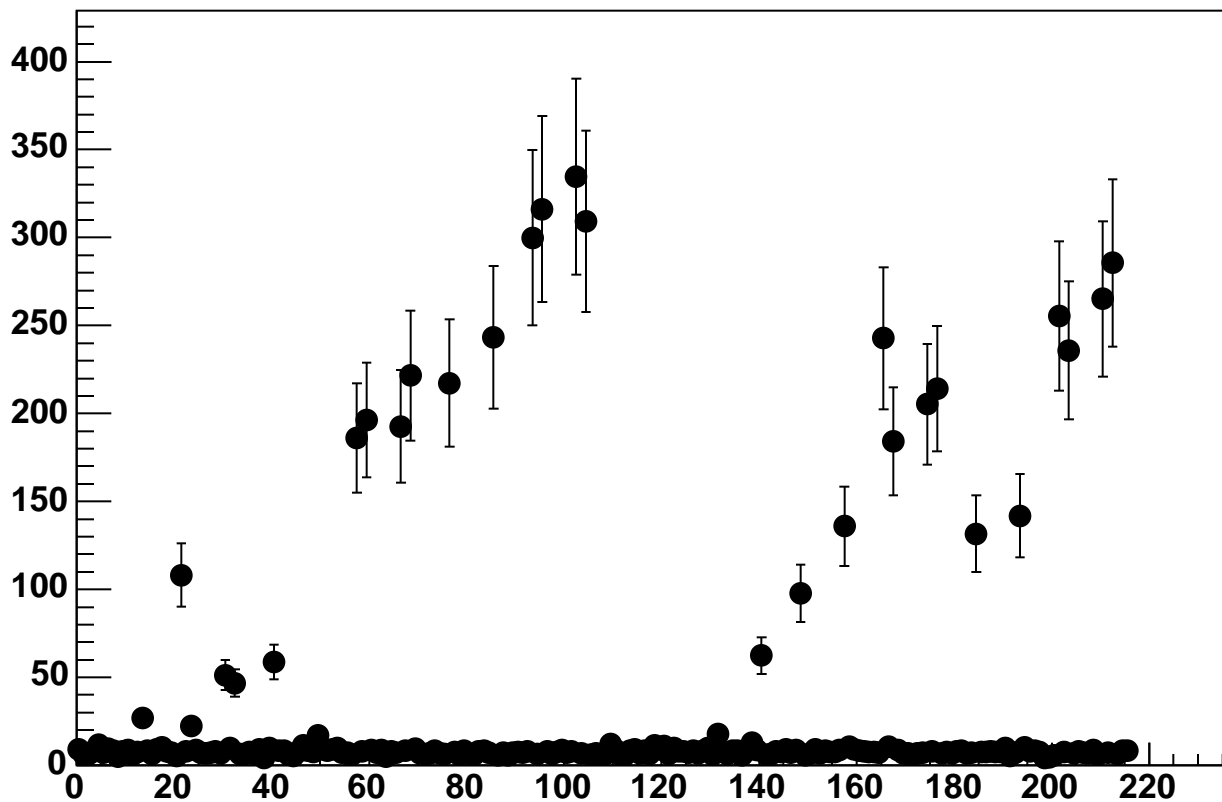
Enable 4, Hold=35, DAC=3600, ADC Noise vs 18\*Chip+Chan



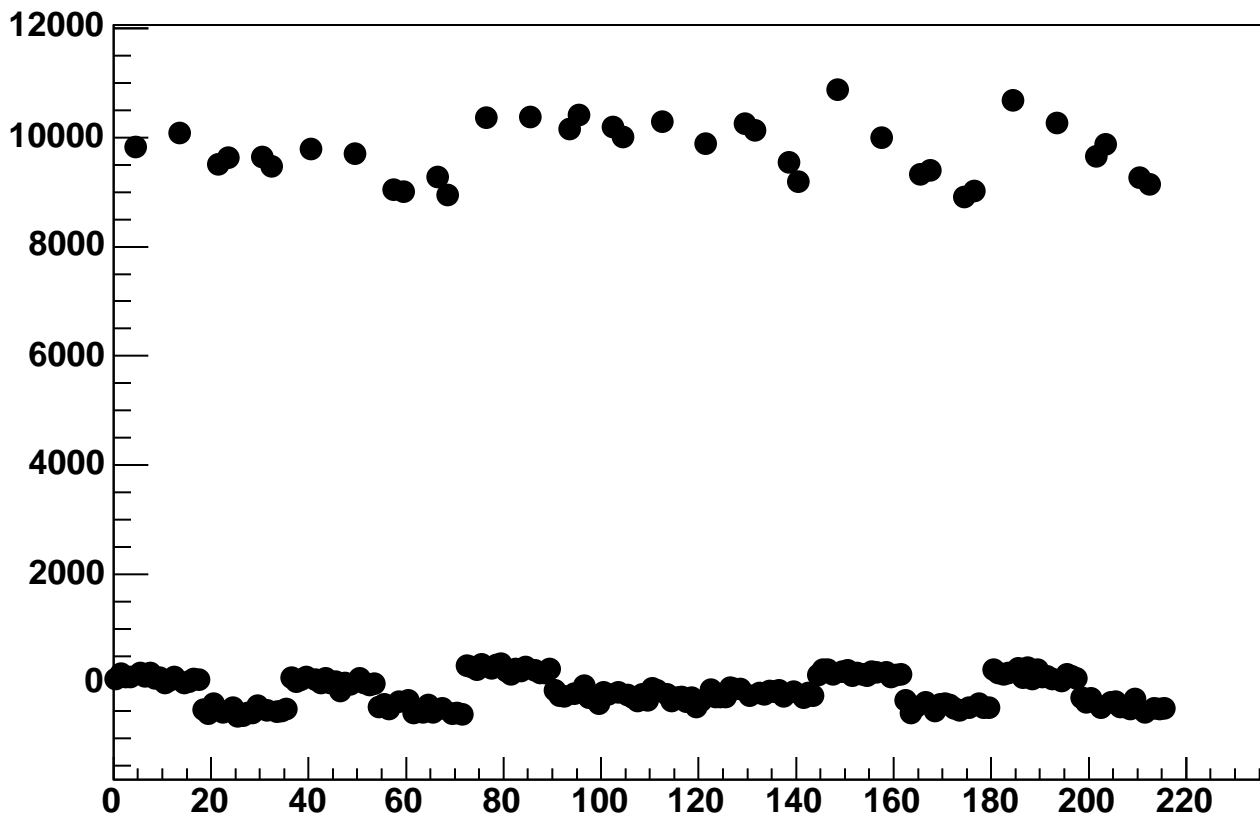
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18\*Chip+Chan



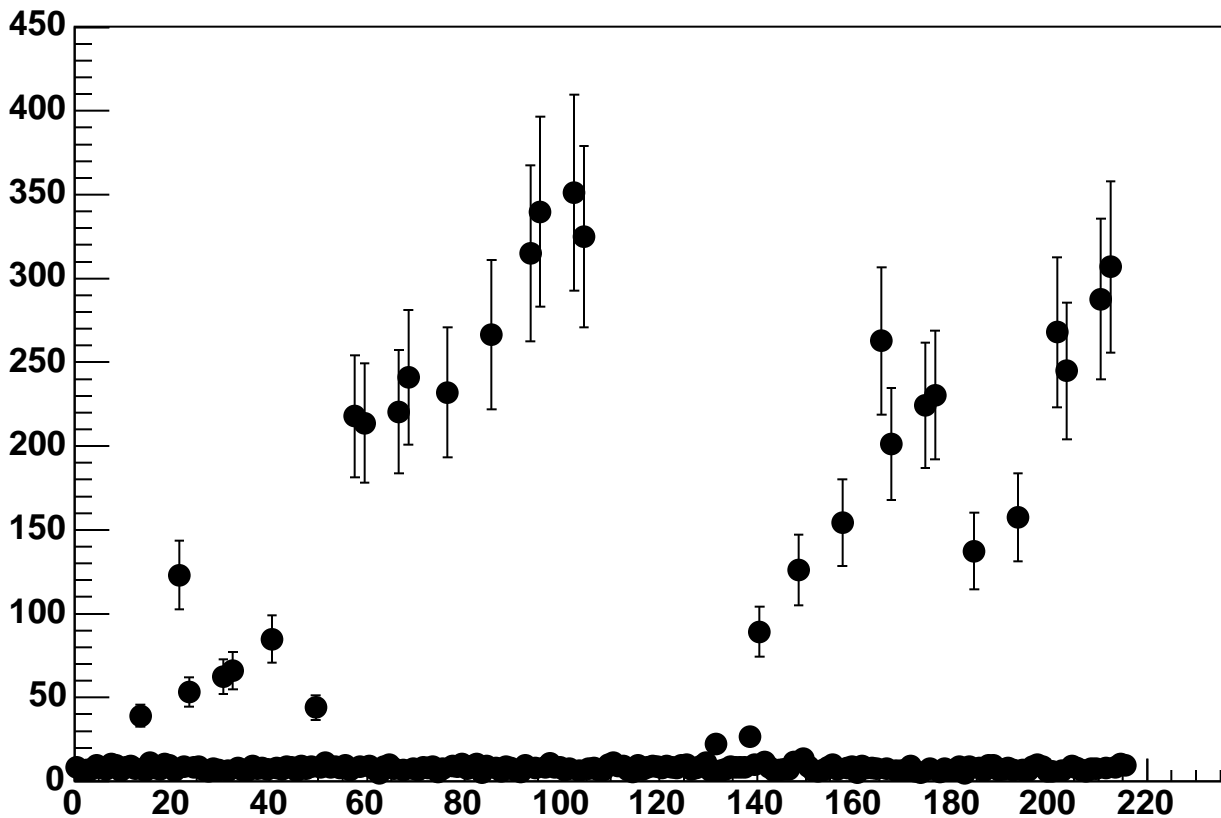
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=35, DAC=4400, ADC Mean vs 18\*Chip+Chan

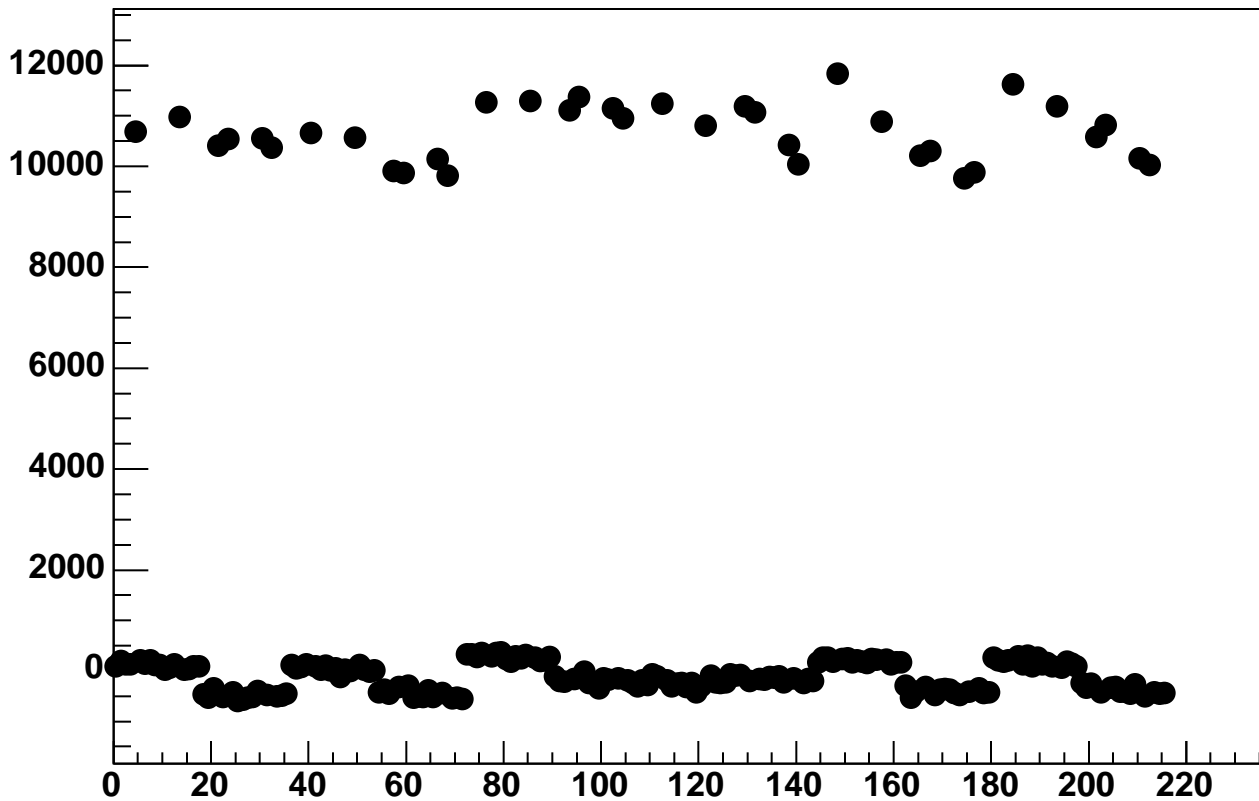


Enable 4, Hold=35, DAC=4400, ADC Noise vs 18\*Chip+Chan

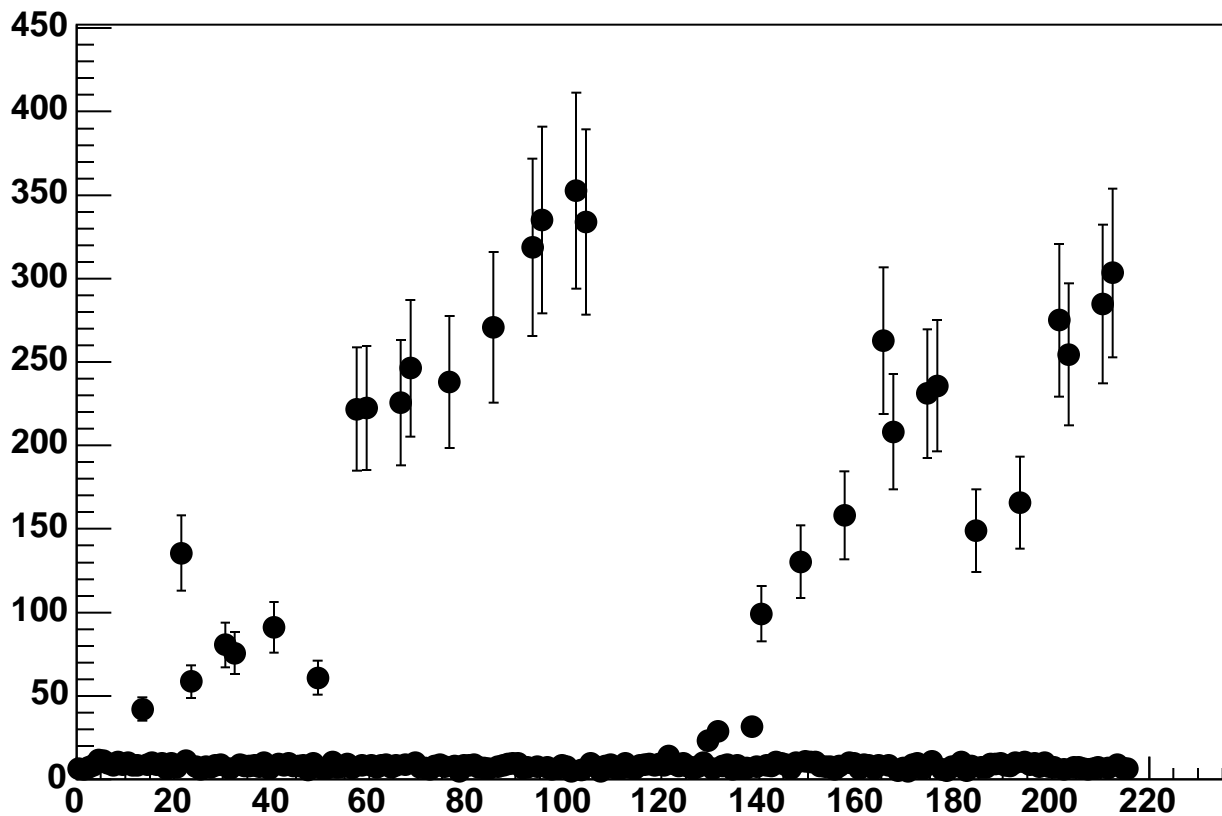




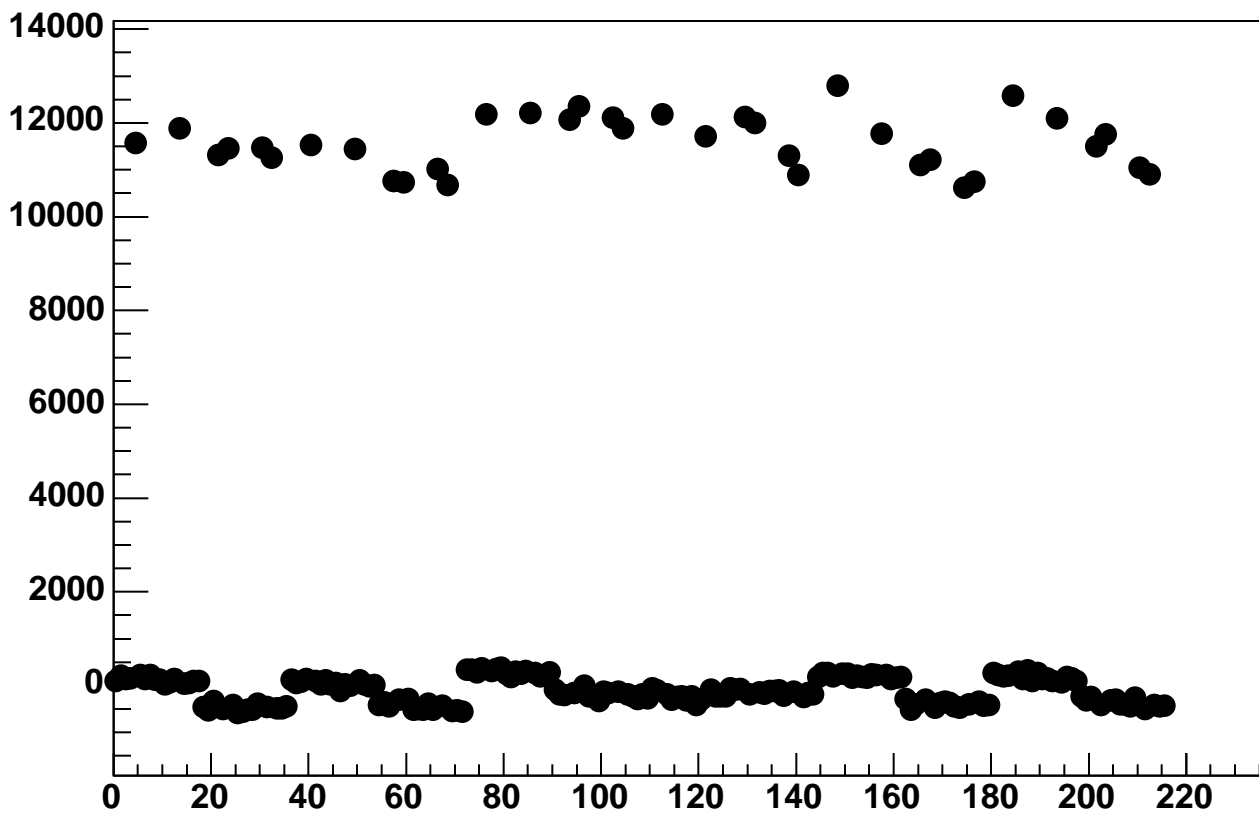
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18\*Chip+Chan



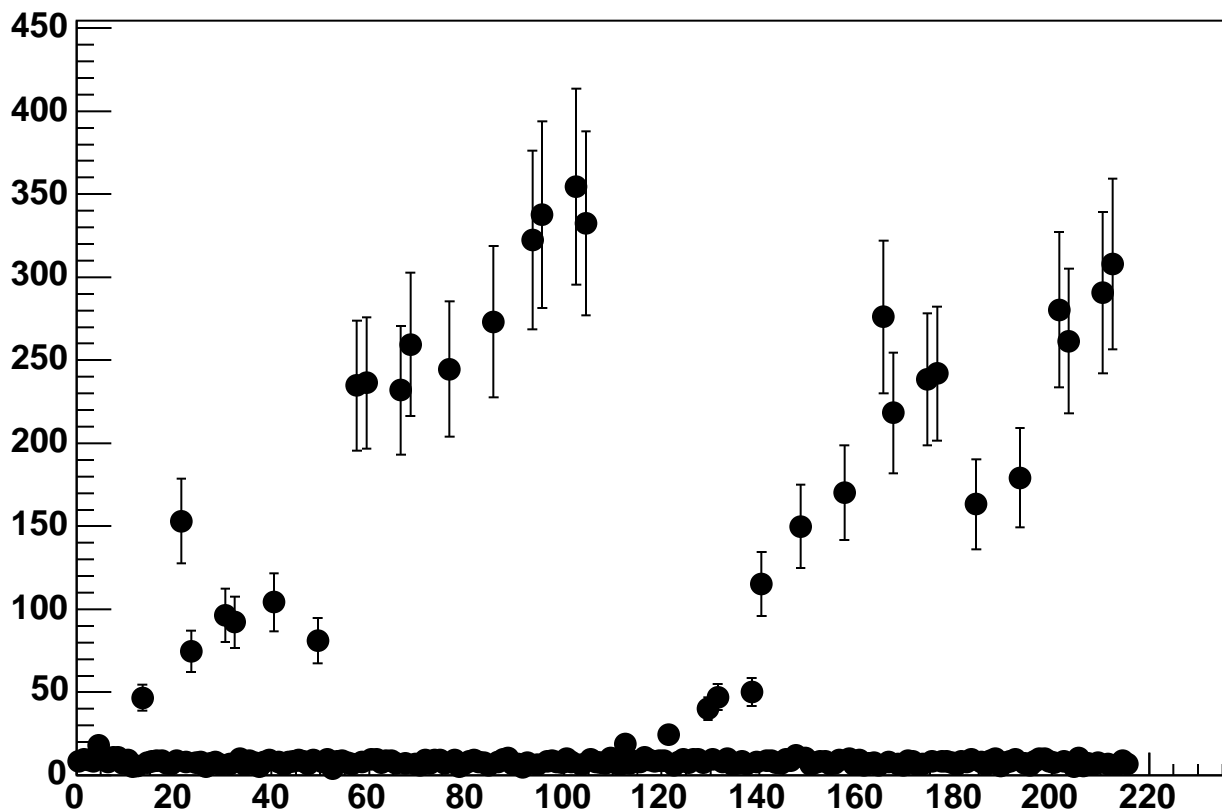
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18\*Chip+Chan



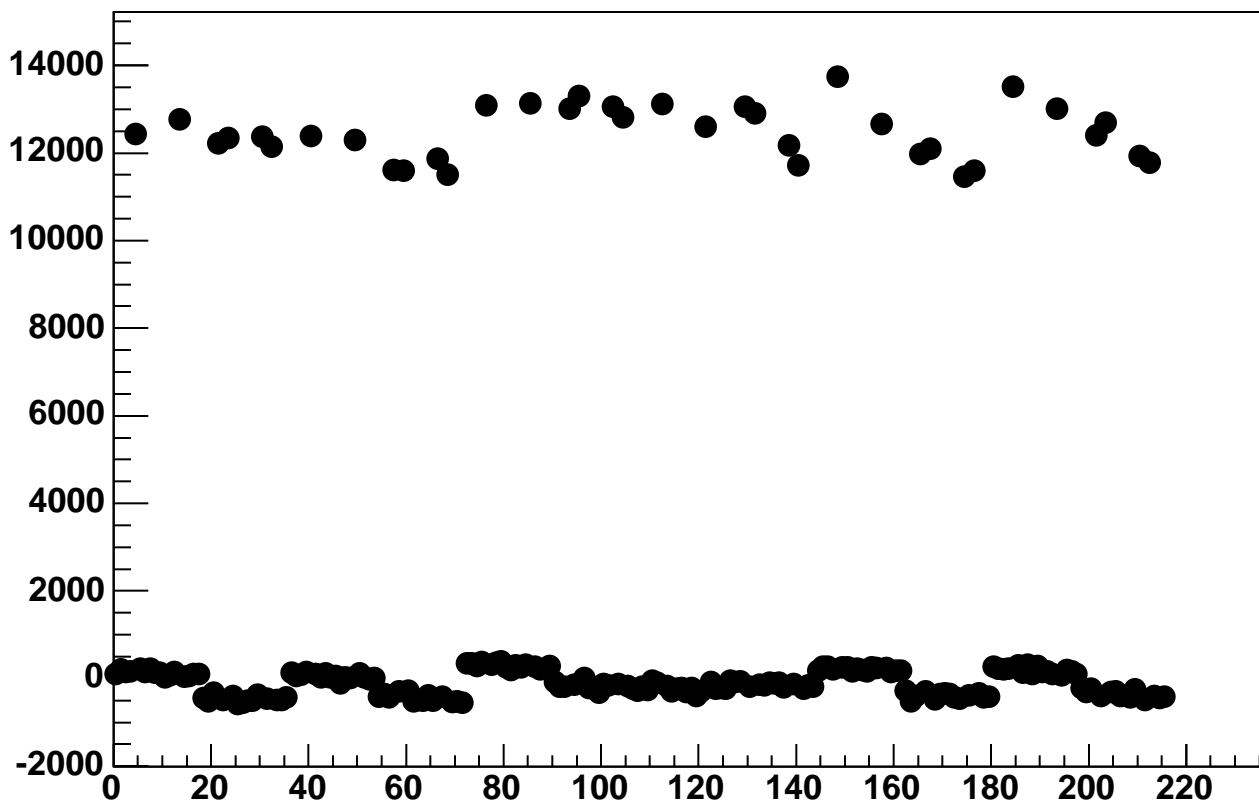
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18\*Chip+Chan



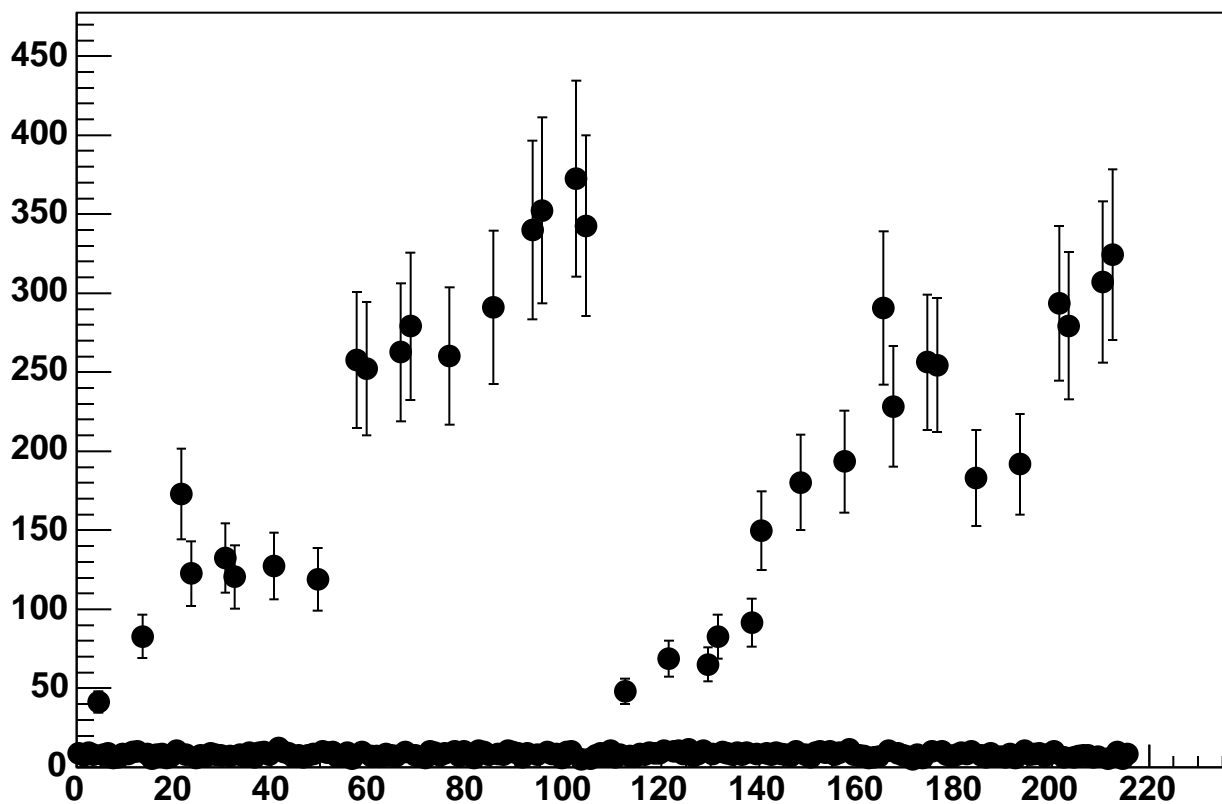
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18\*Chip+Chan



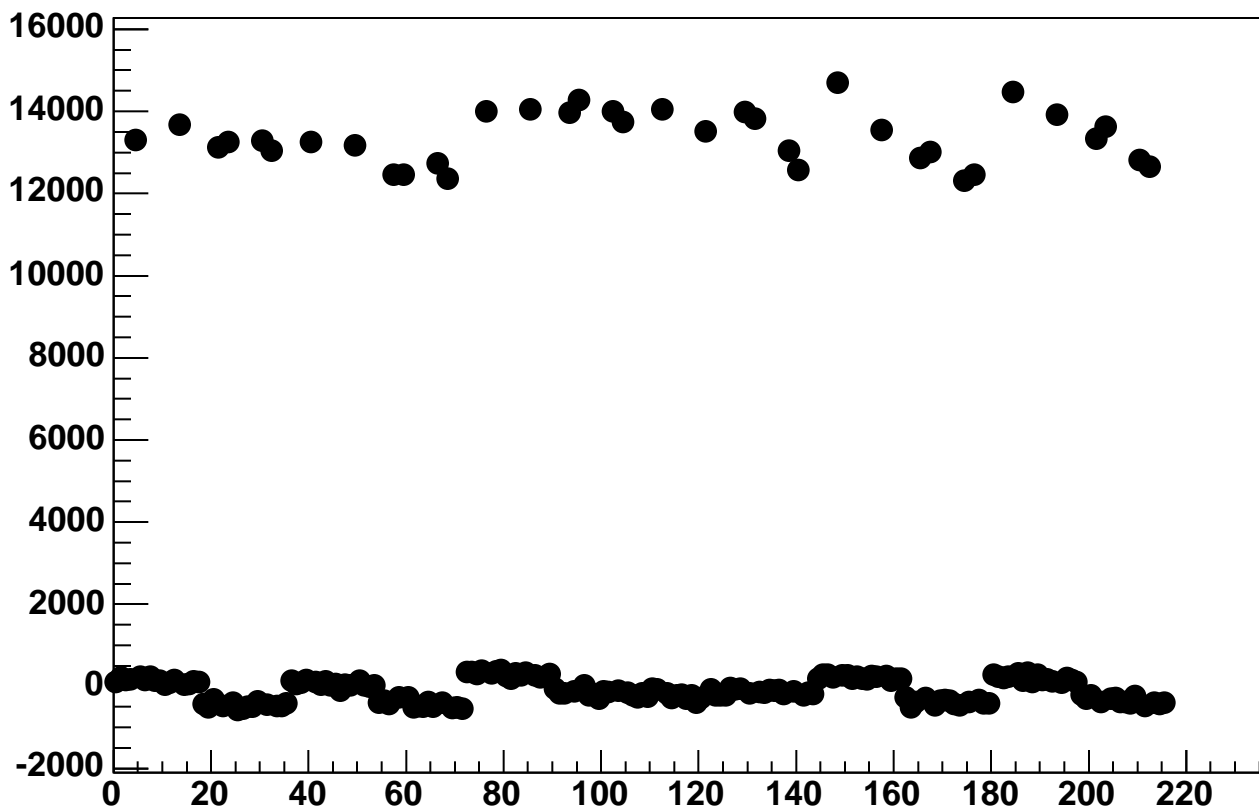
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18\*Chip+Chan



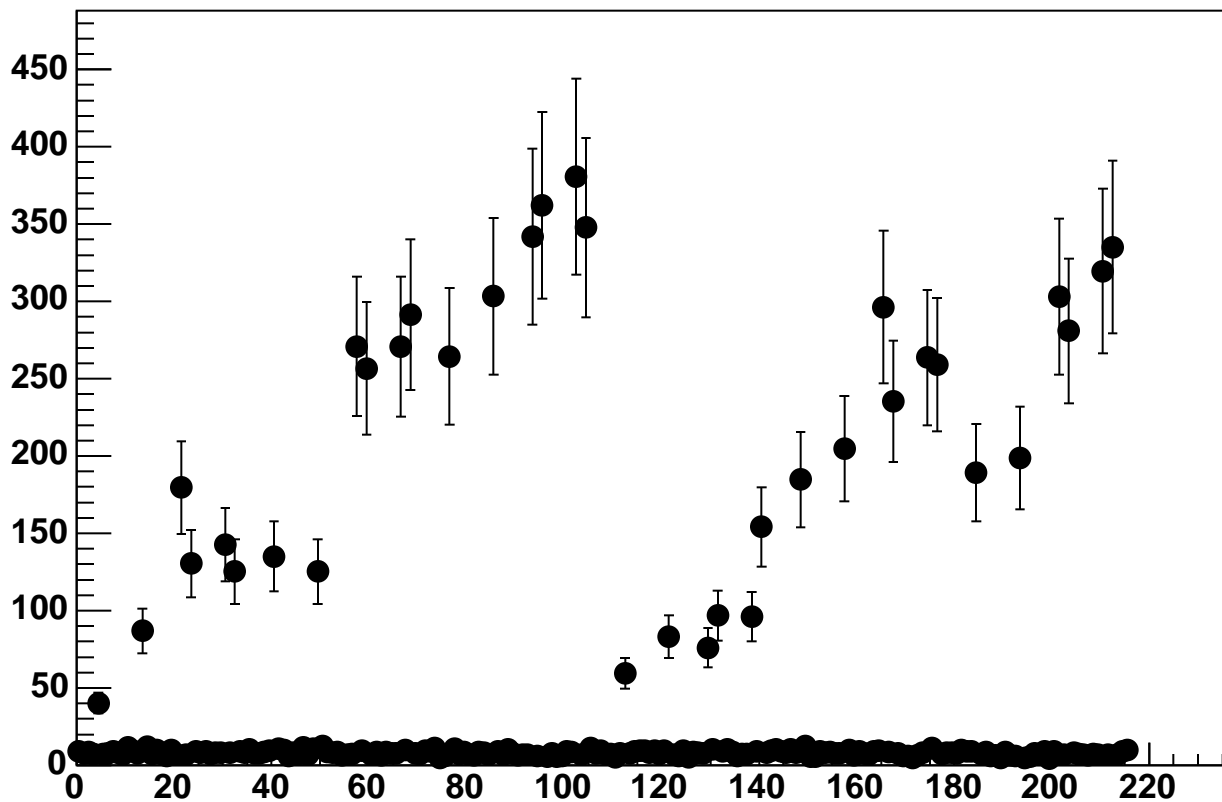
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18\*Chip+Chan



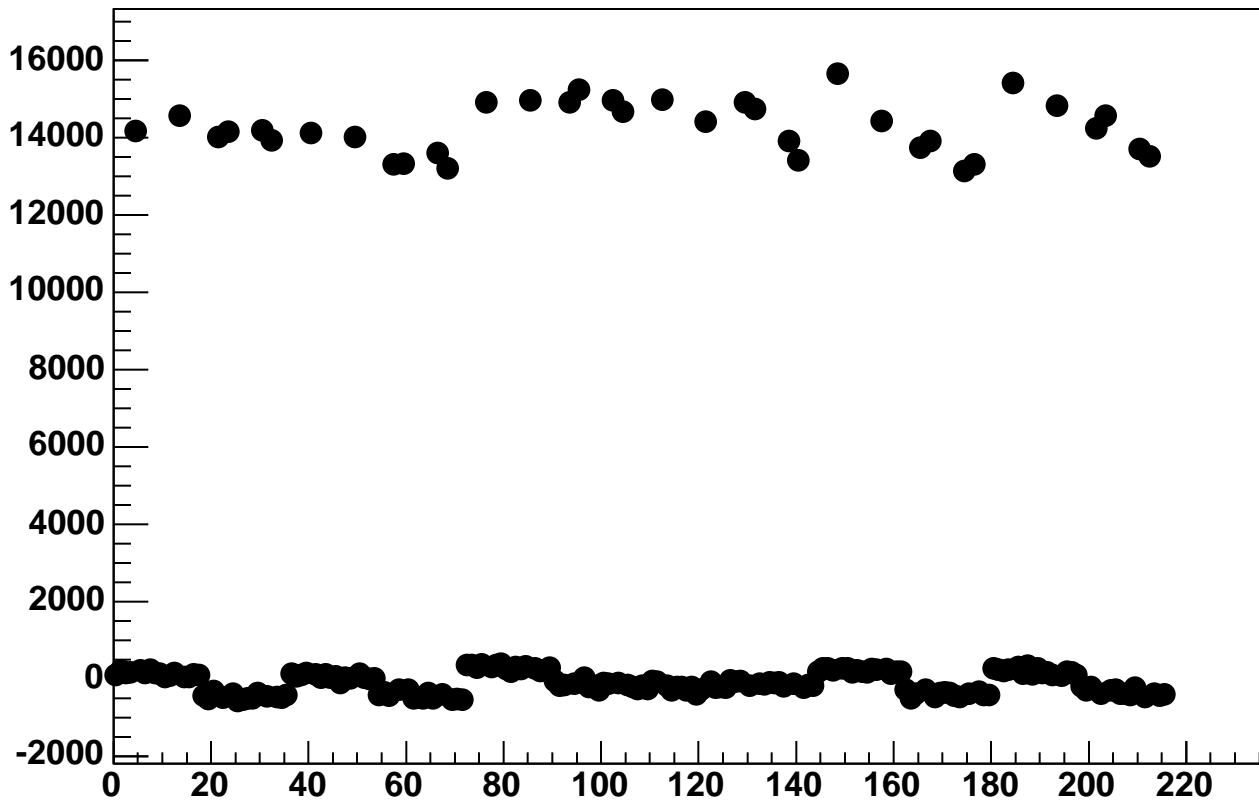
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18\*Chip+Chan



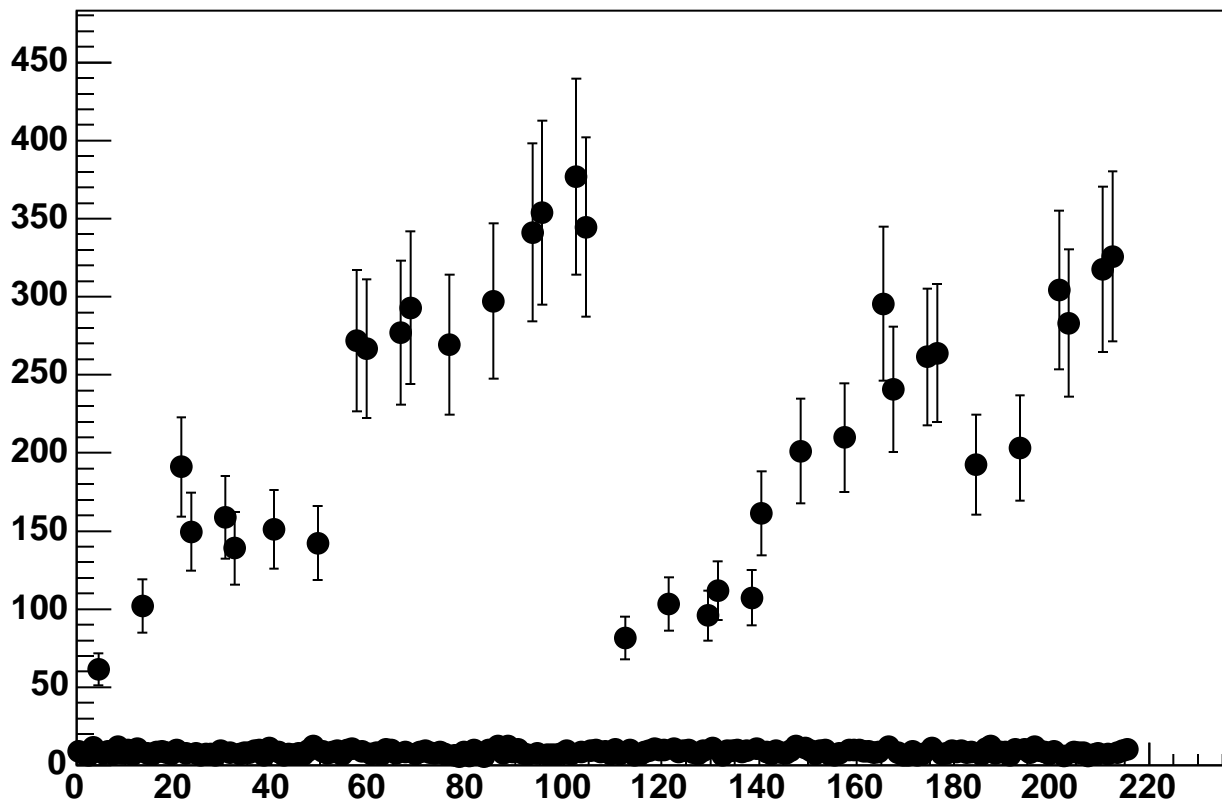
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18\*Chip+Chan



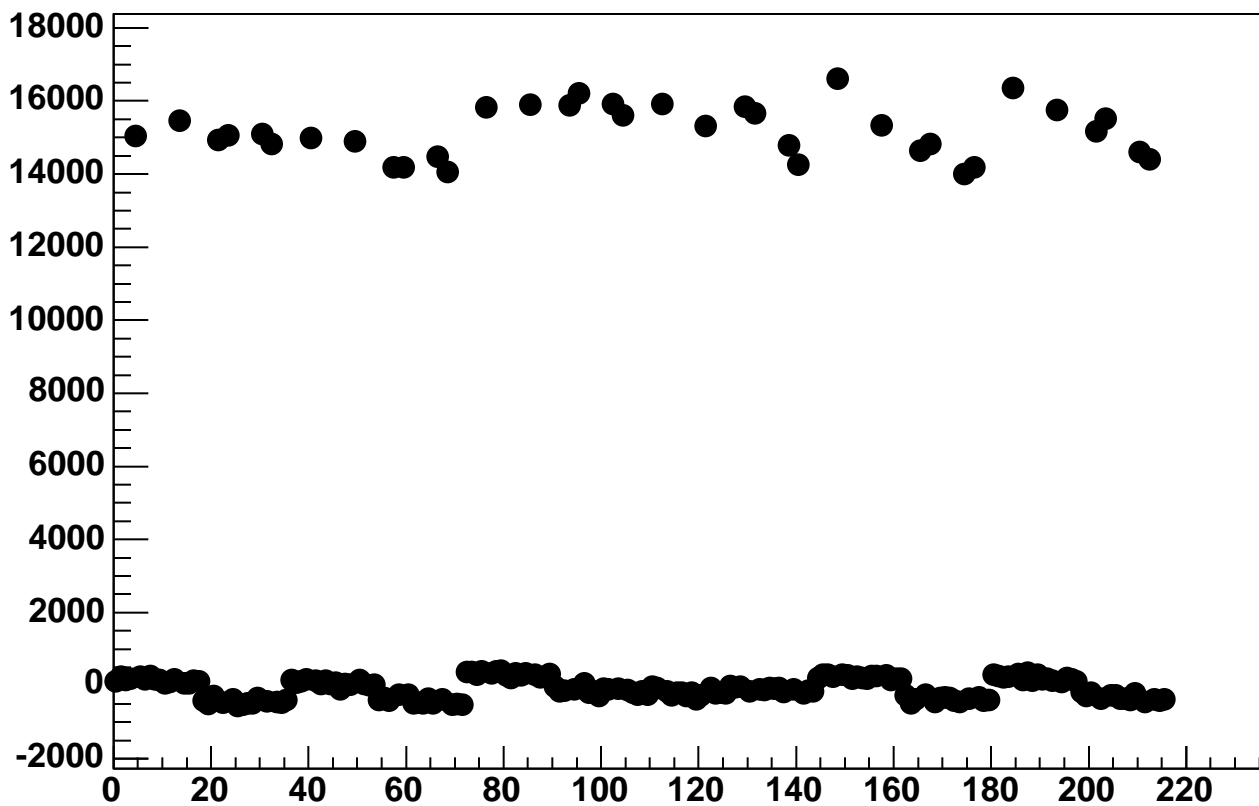
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18\*Chip+Chan



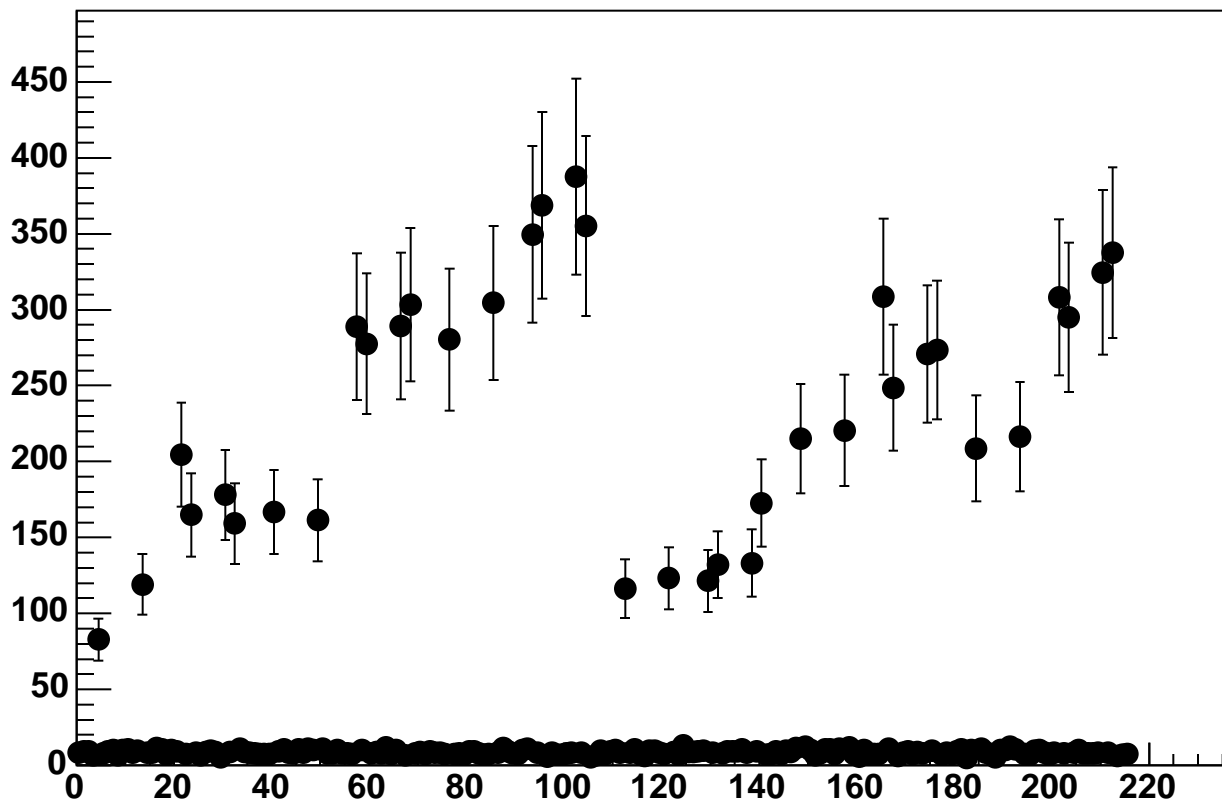
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18\*Chip+Chan



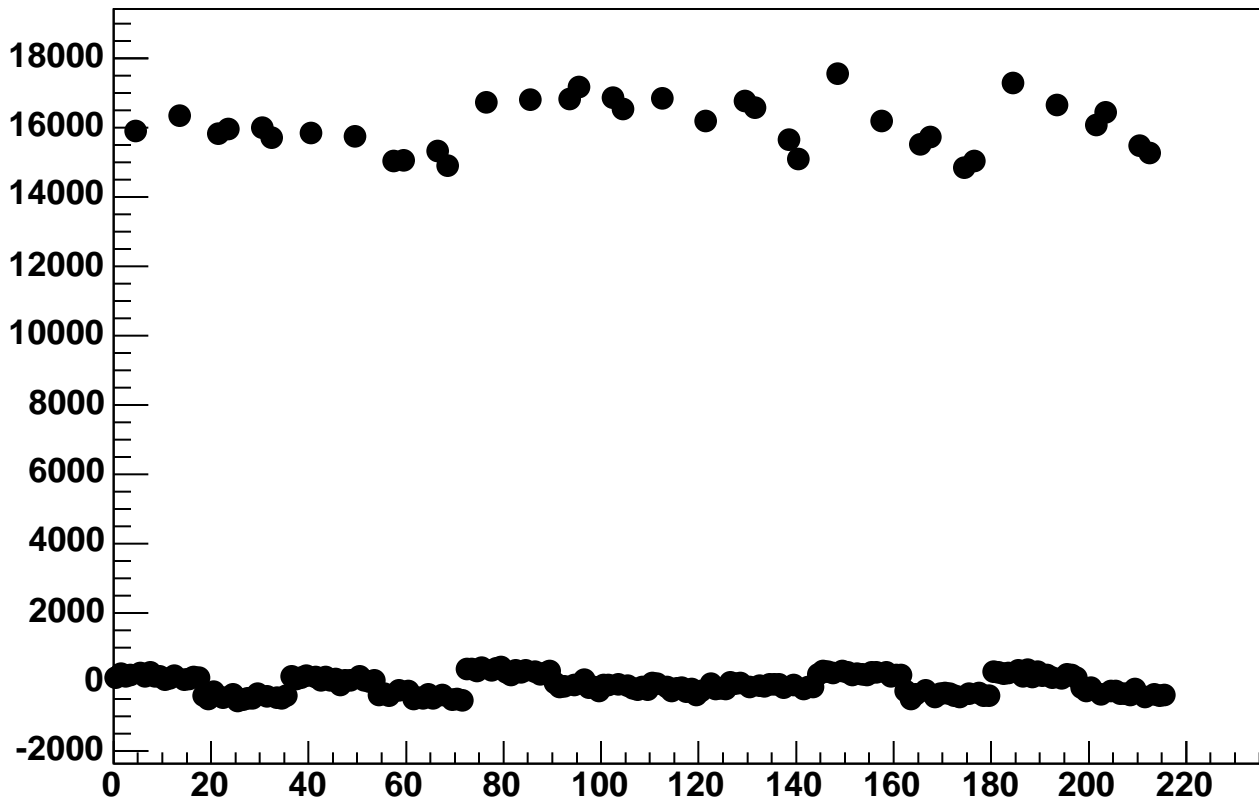
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18\*Chip+Chan



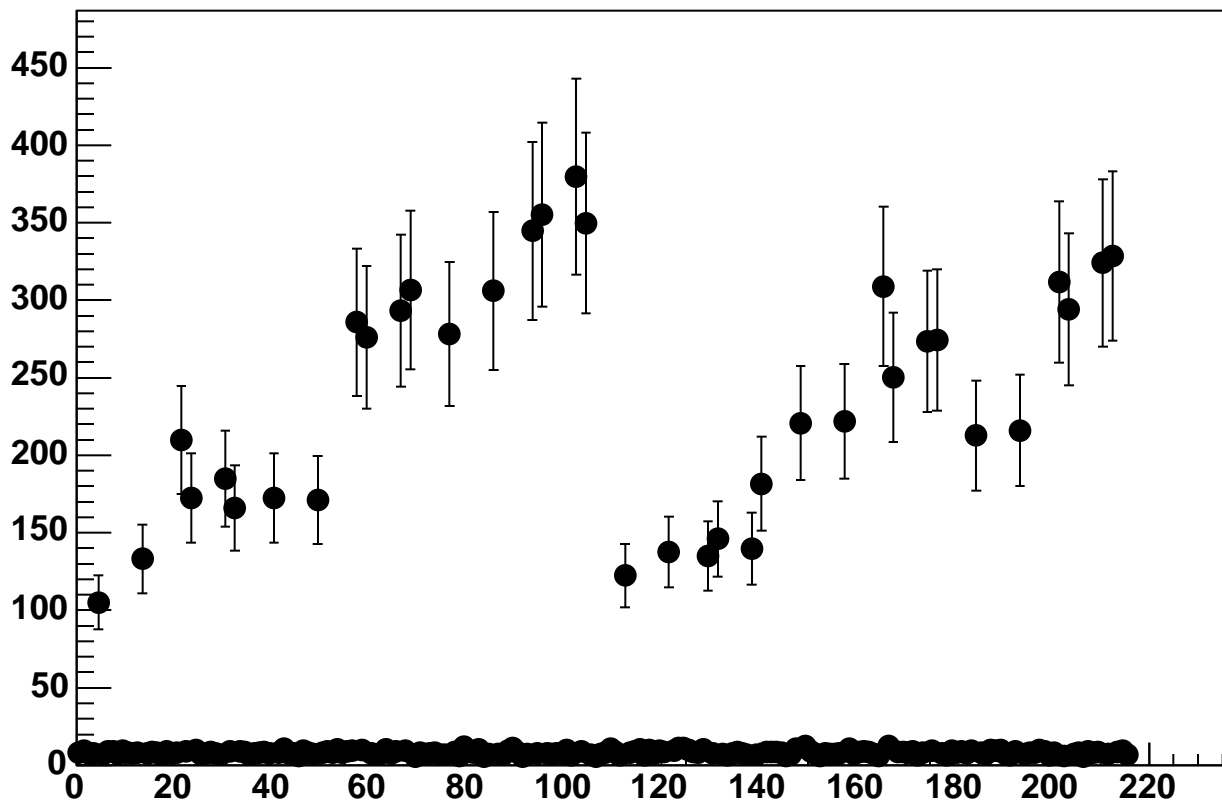
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18\*Chip+Chan



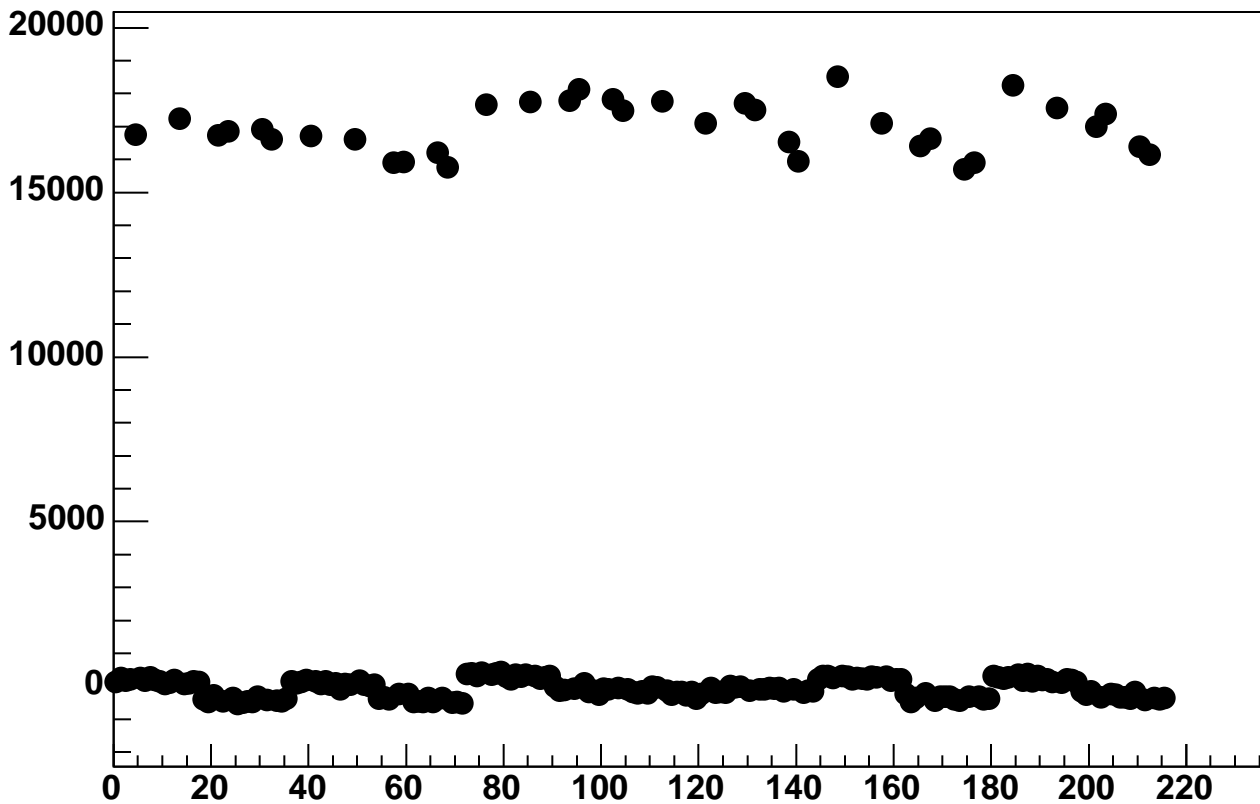
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18\*Chip+Chan



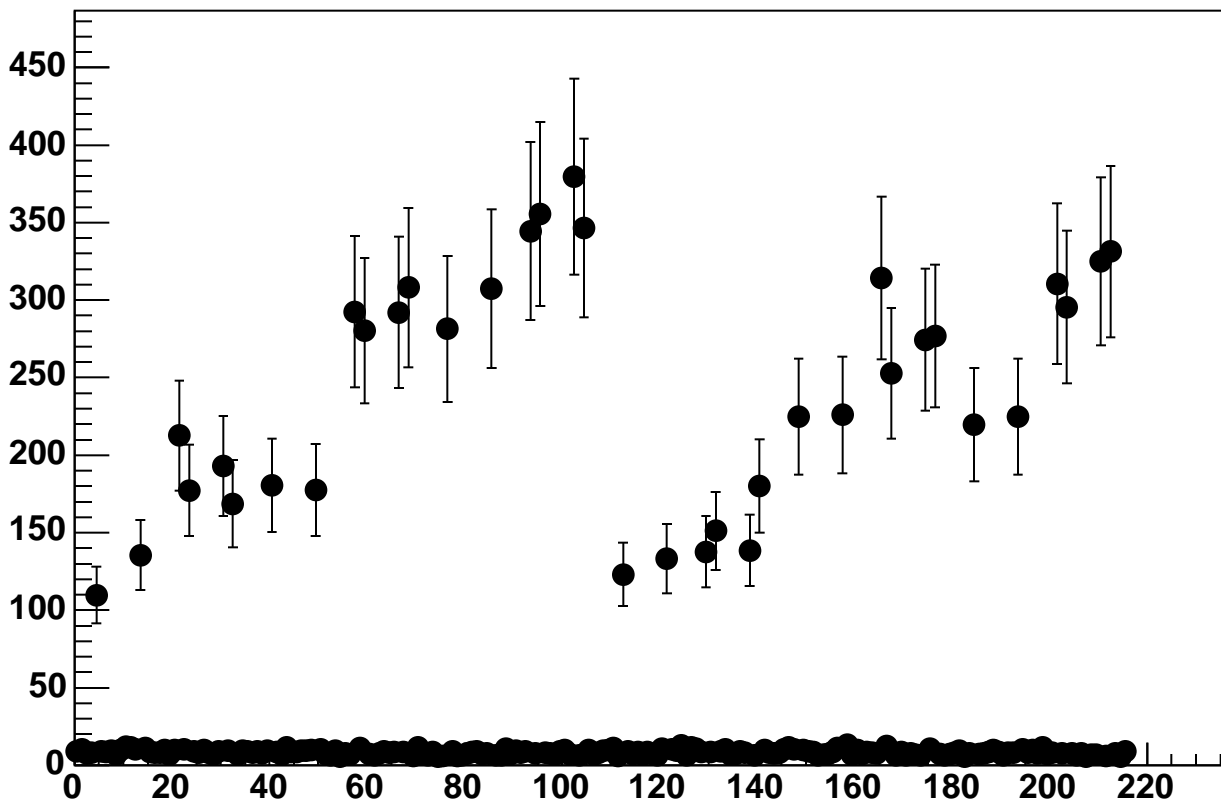
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=35, DAC=7600, ADC Mean vs 18\*Chip+Chan

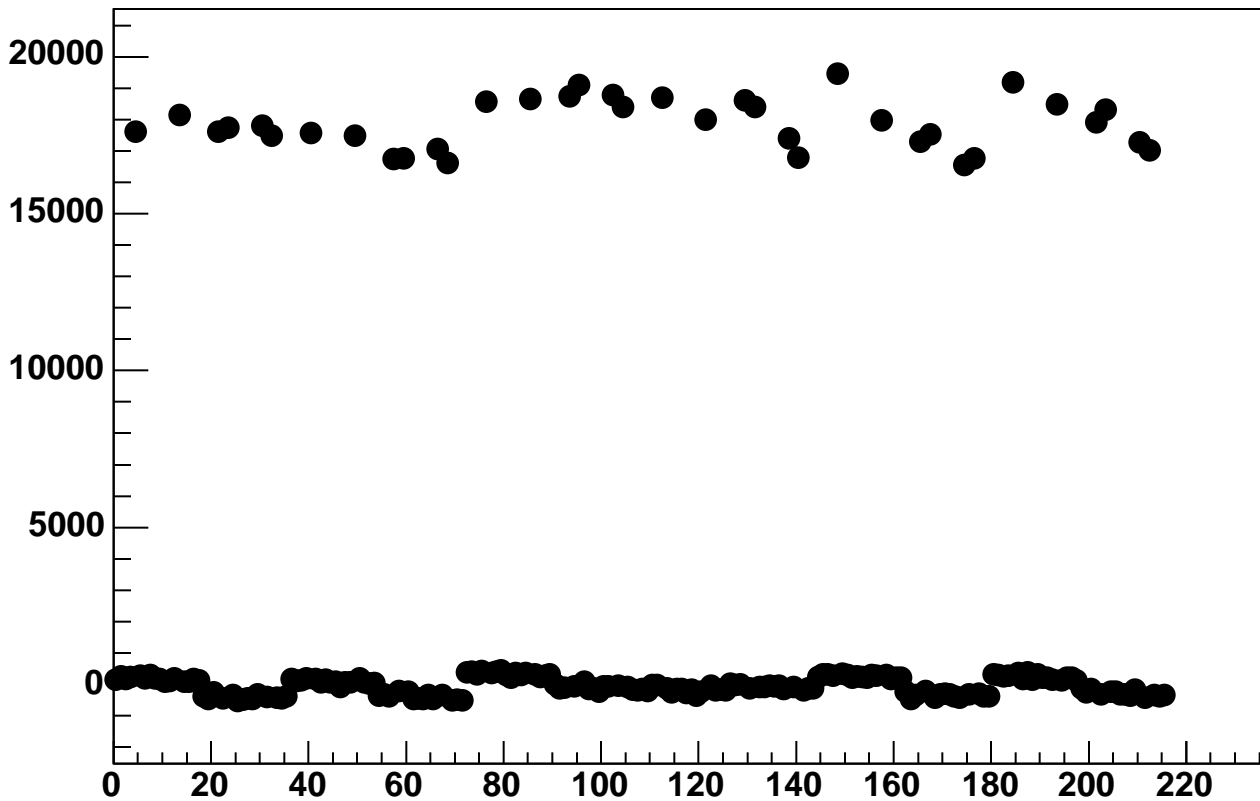


Enable 4, Hold=35, DAC=7600, ADC Noise vs 18\*Chip+Chan

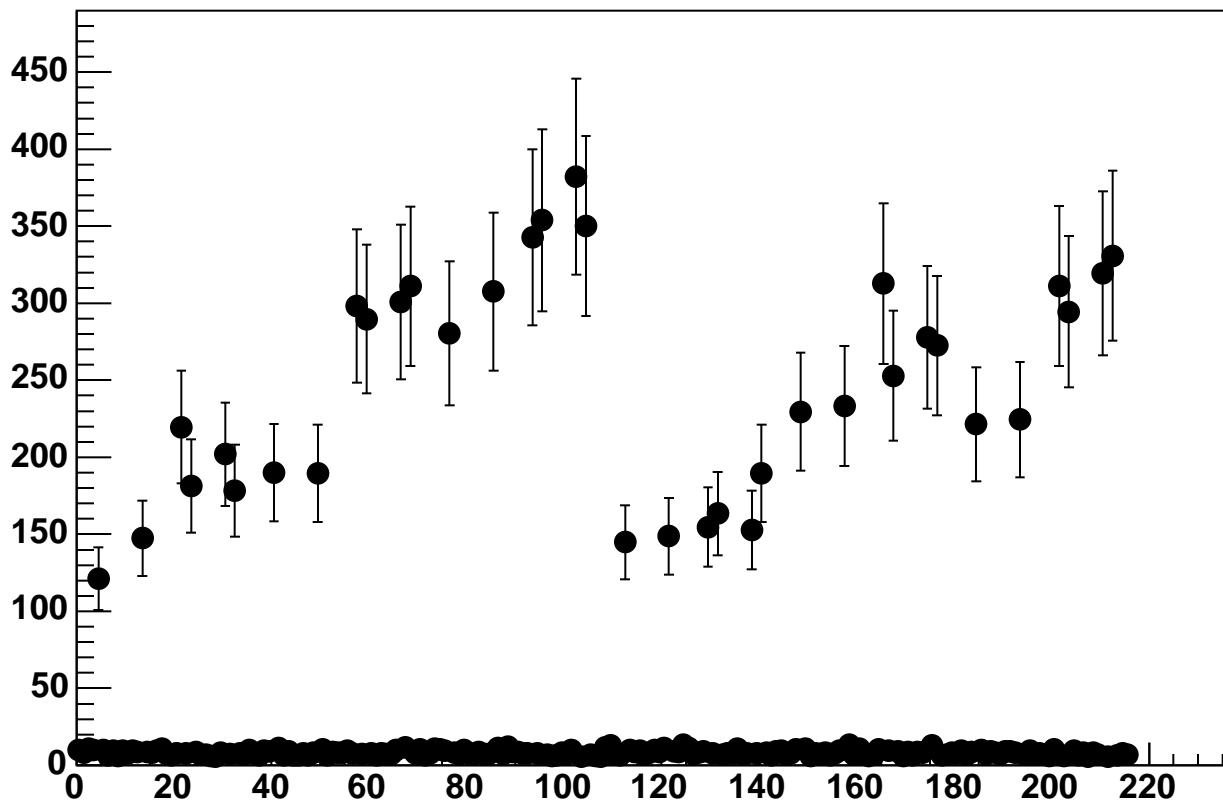




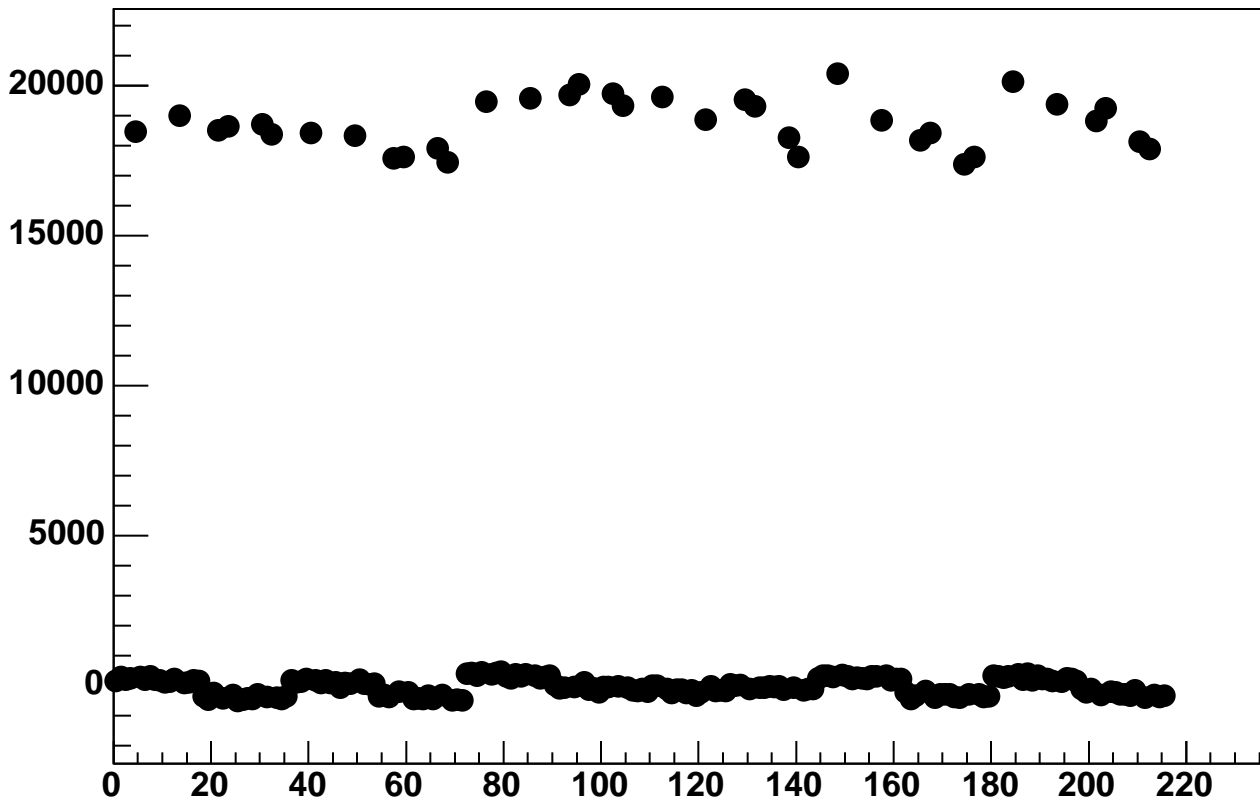
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18\*Chip+Chan



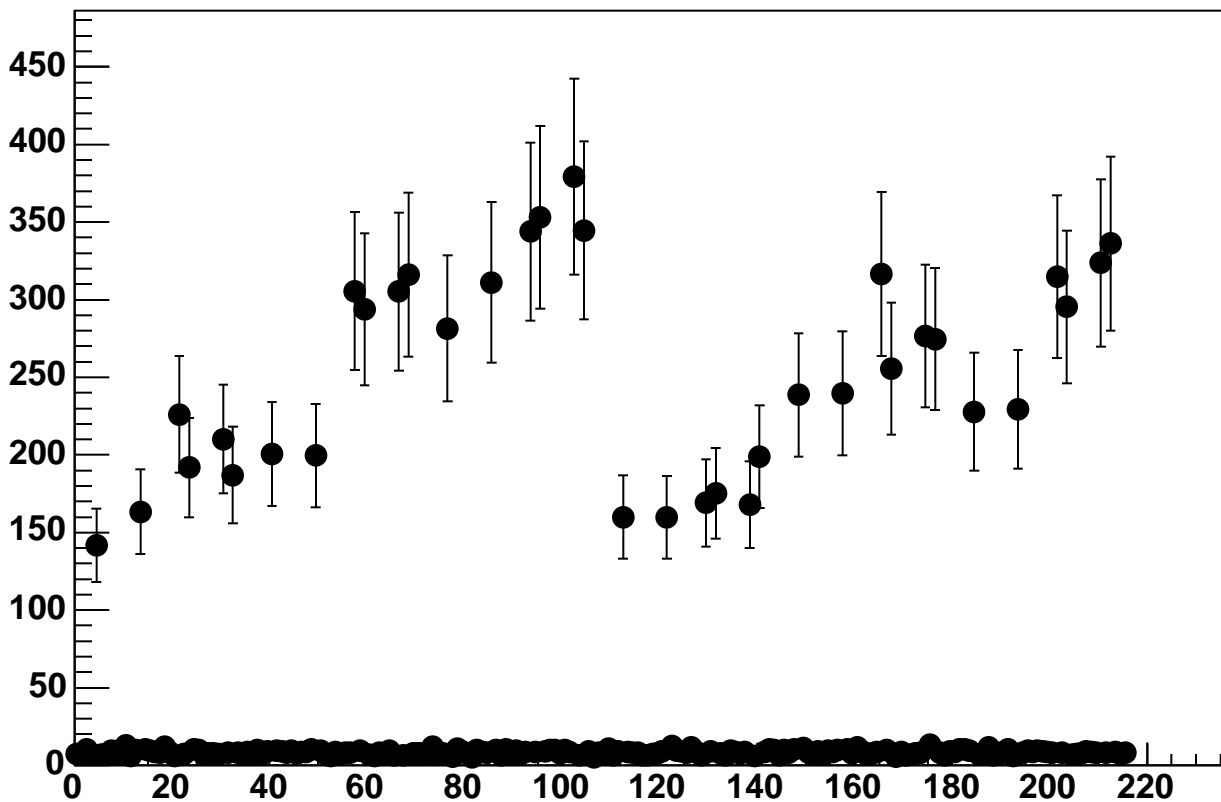
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18\*Chip+Chan



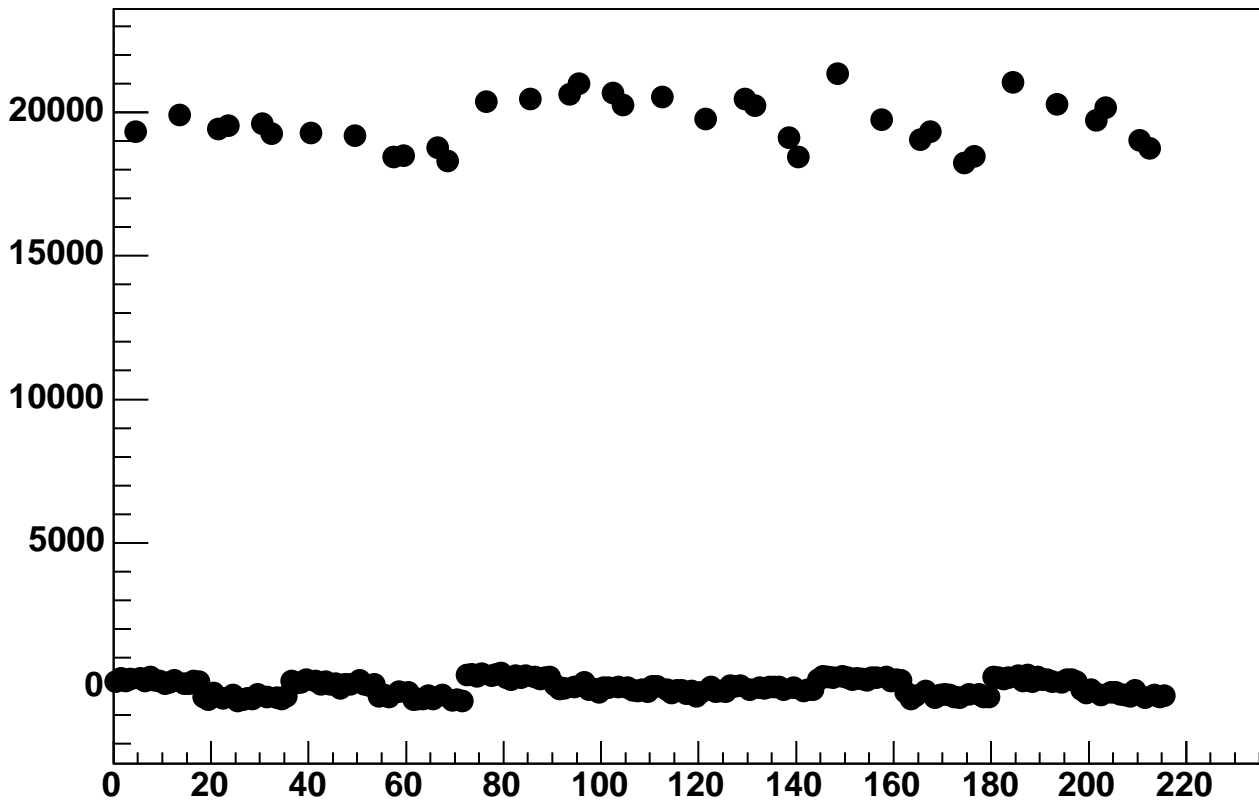
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18\*Chip+Chan



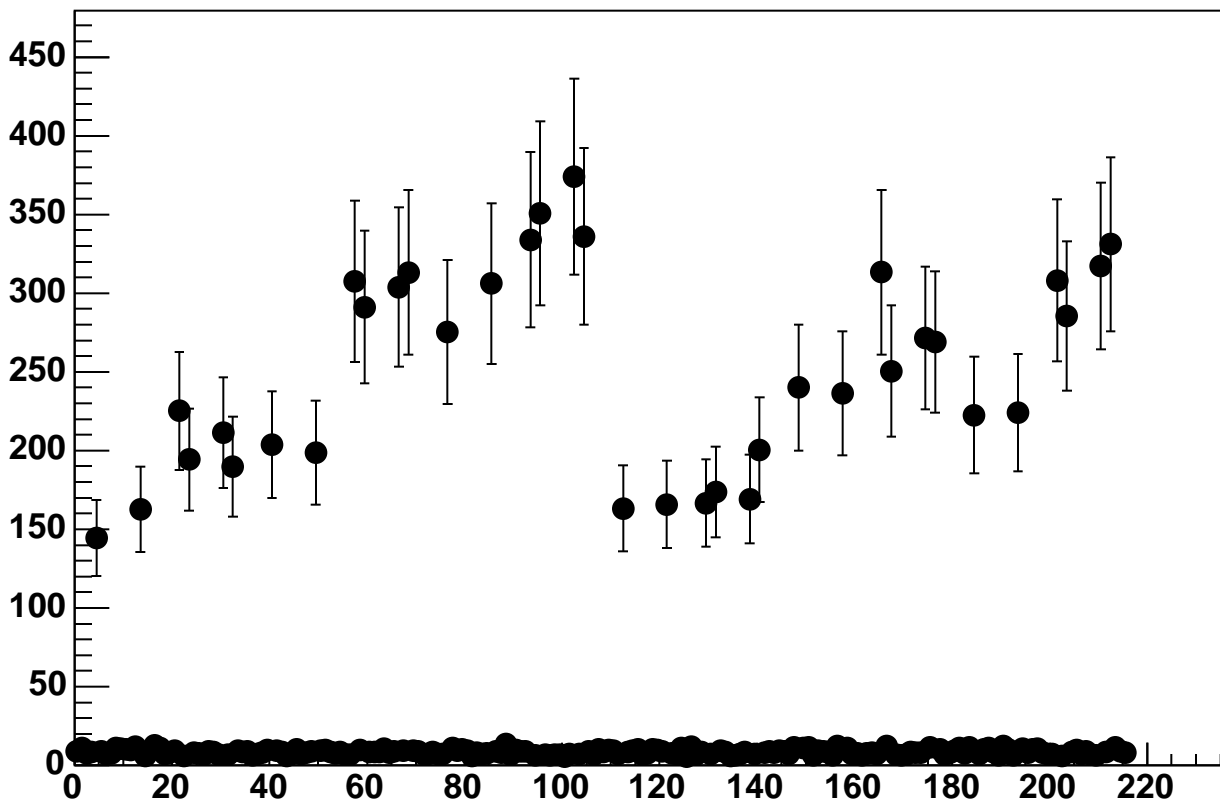
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18\*Chip+Chan



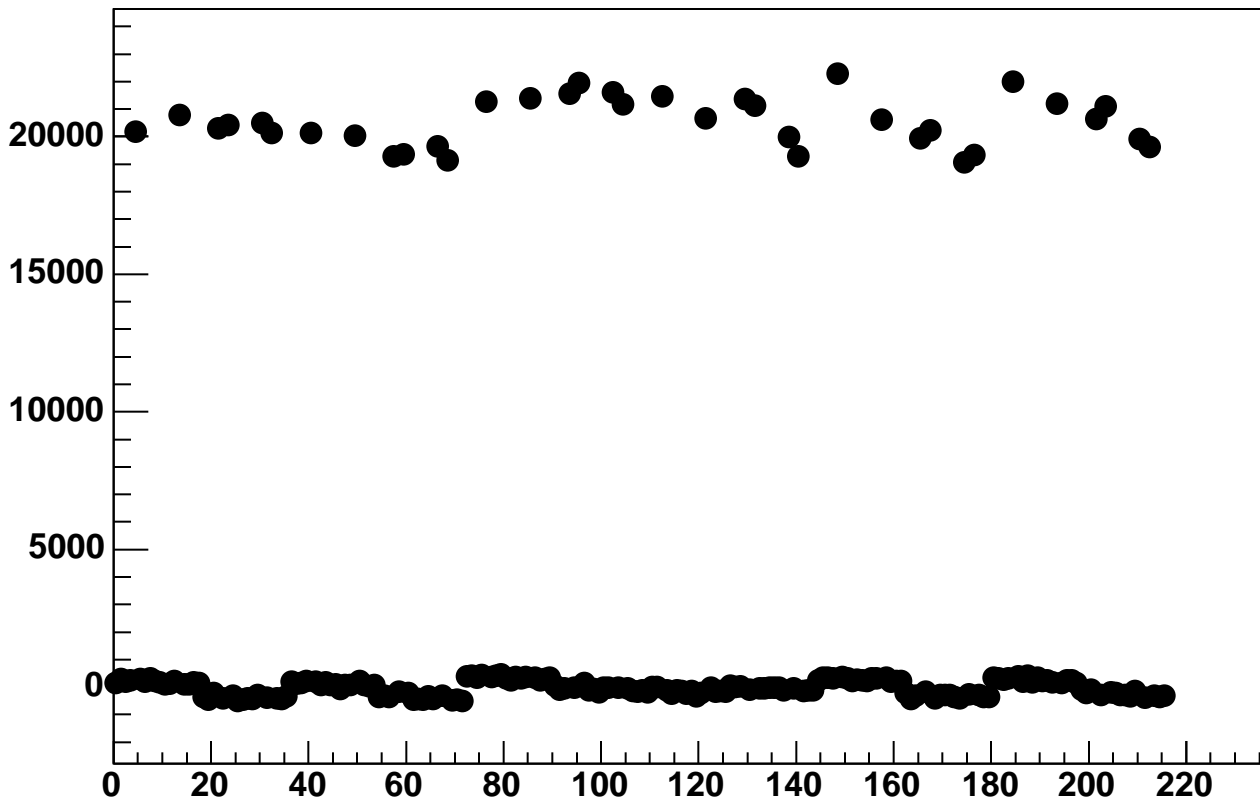
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18\*Chip+Chan



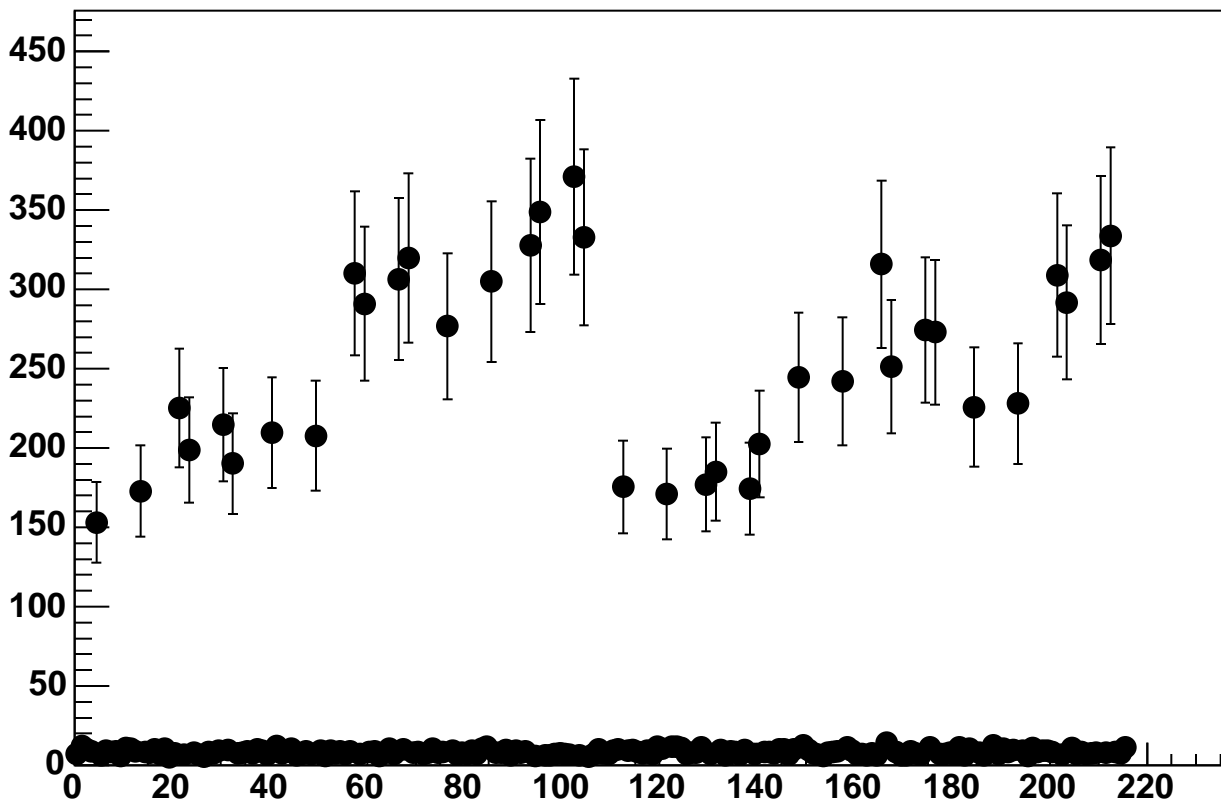
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18\*Chip+Chan



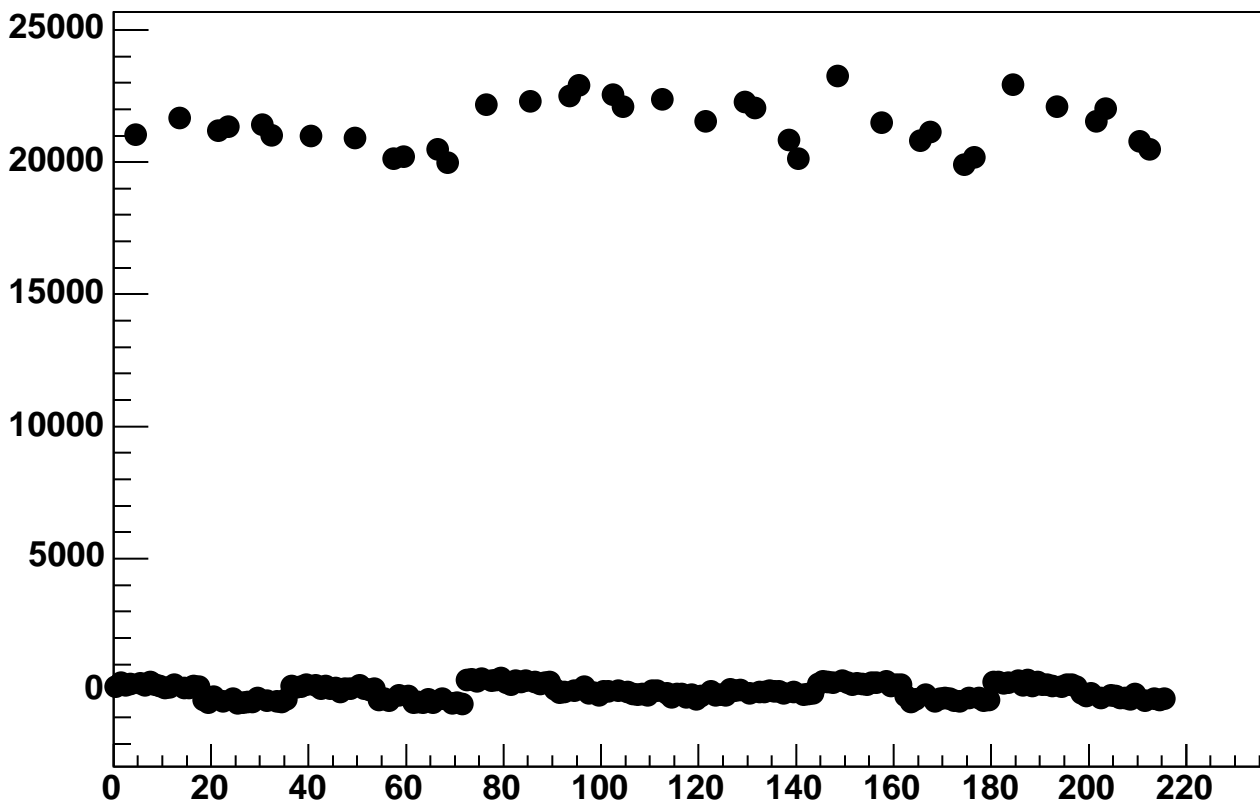
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18\*Chip+Chan



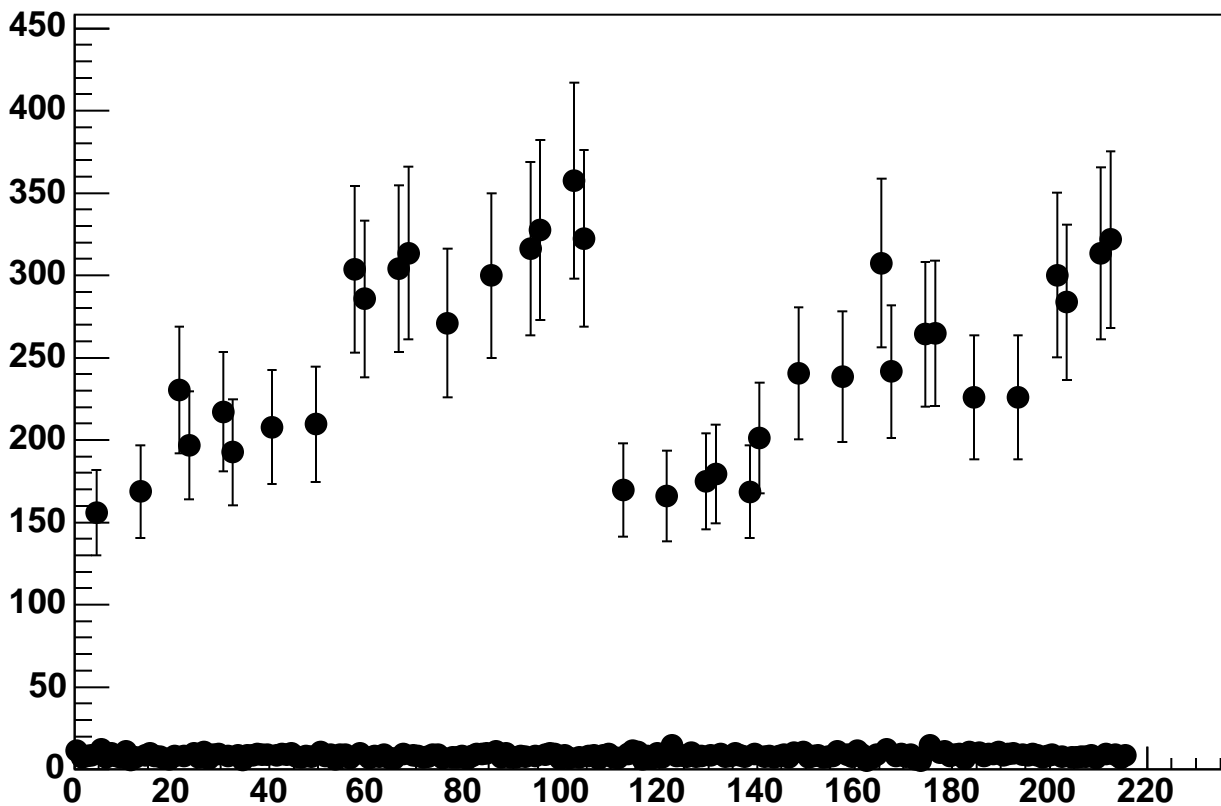
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18\*Chip+Chan



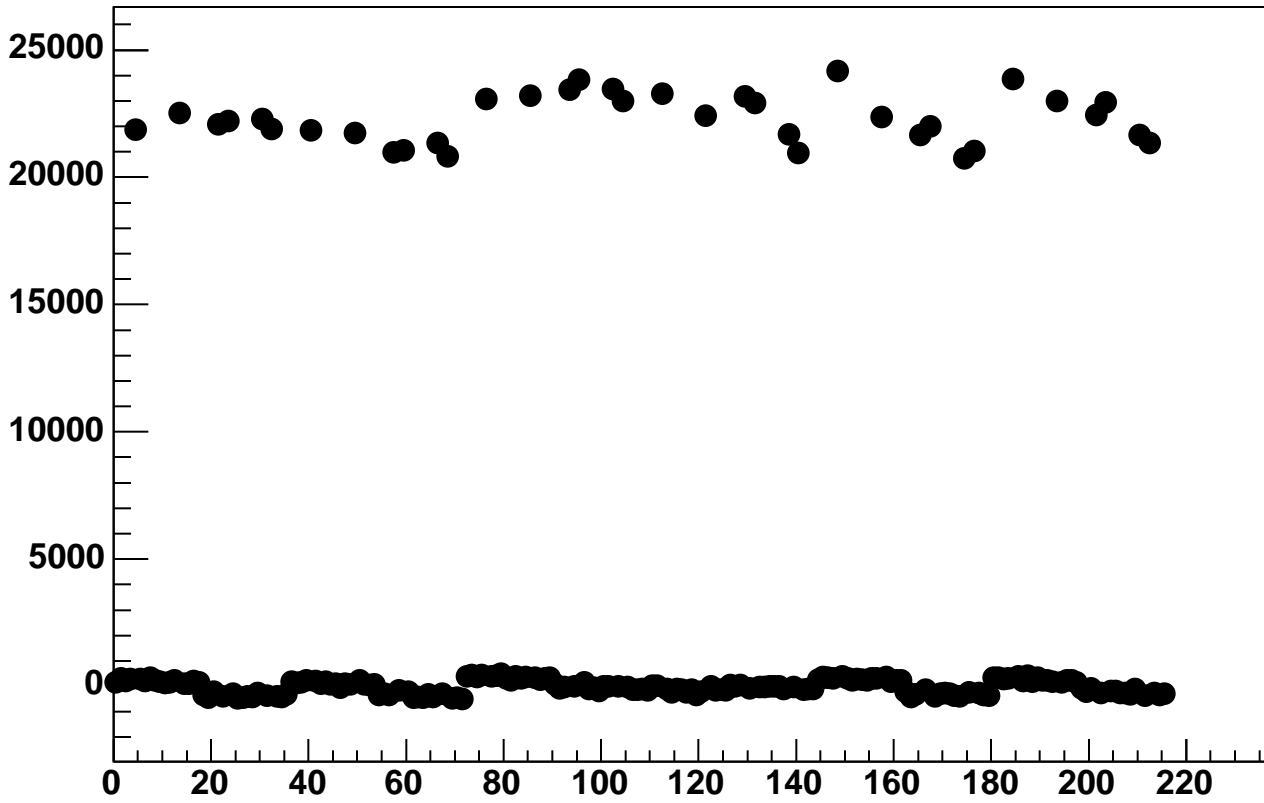
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18\*Chip+Chan



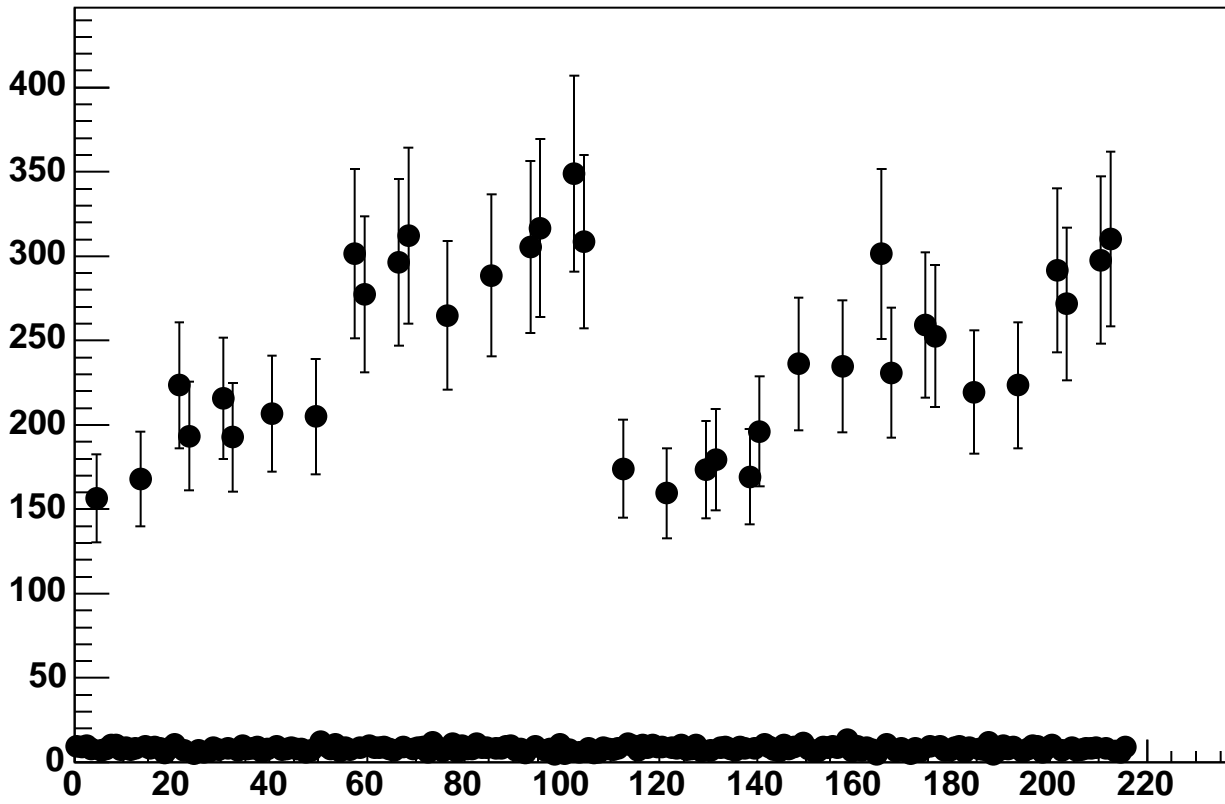
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18\*Chip+Chan



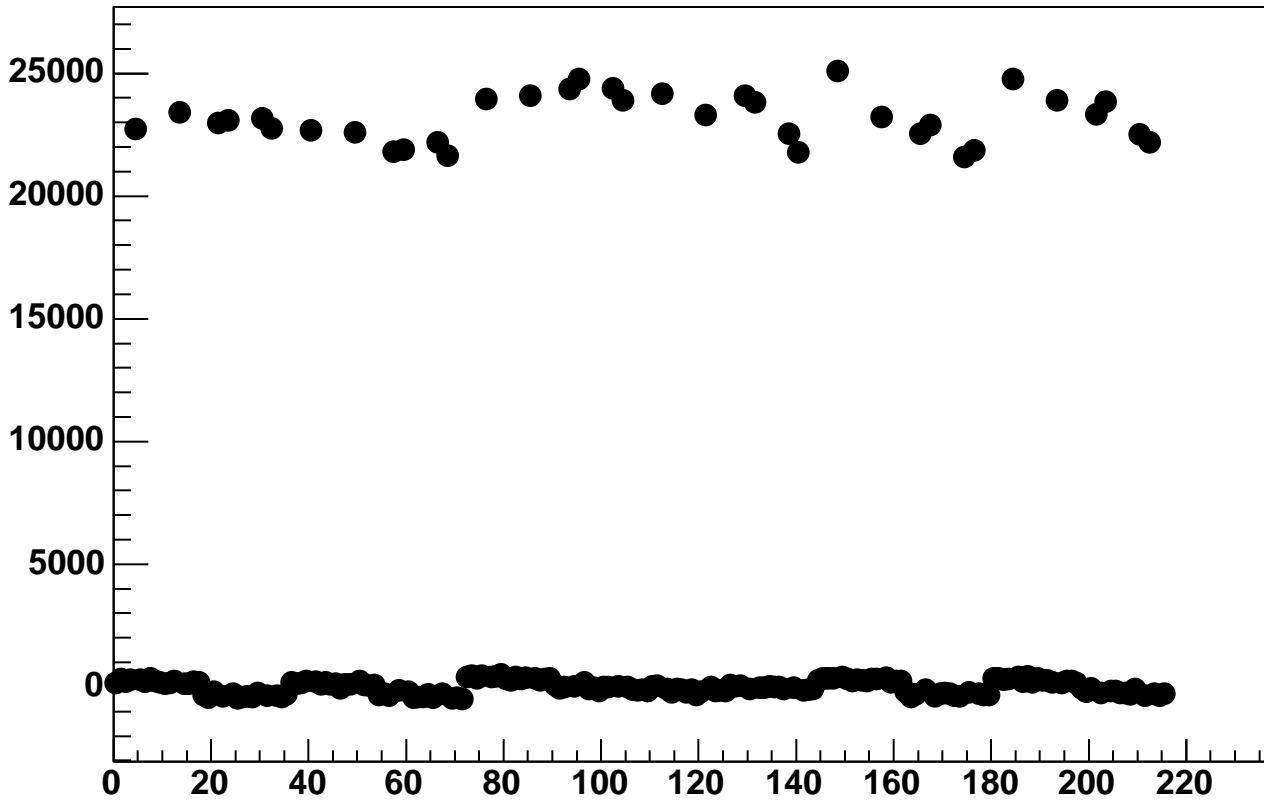
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18\*Chip+Chan



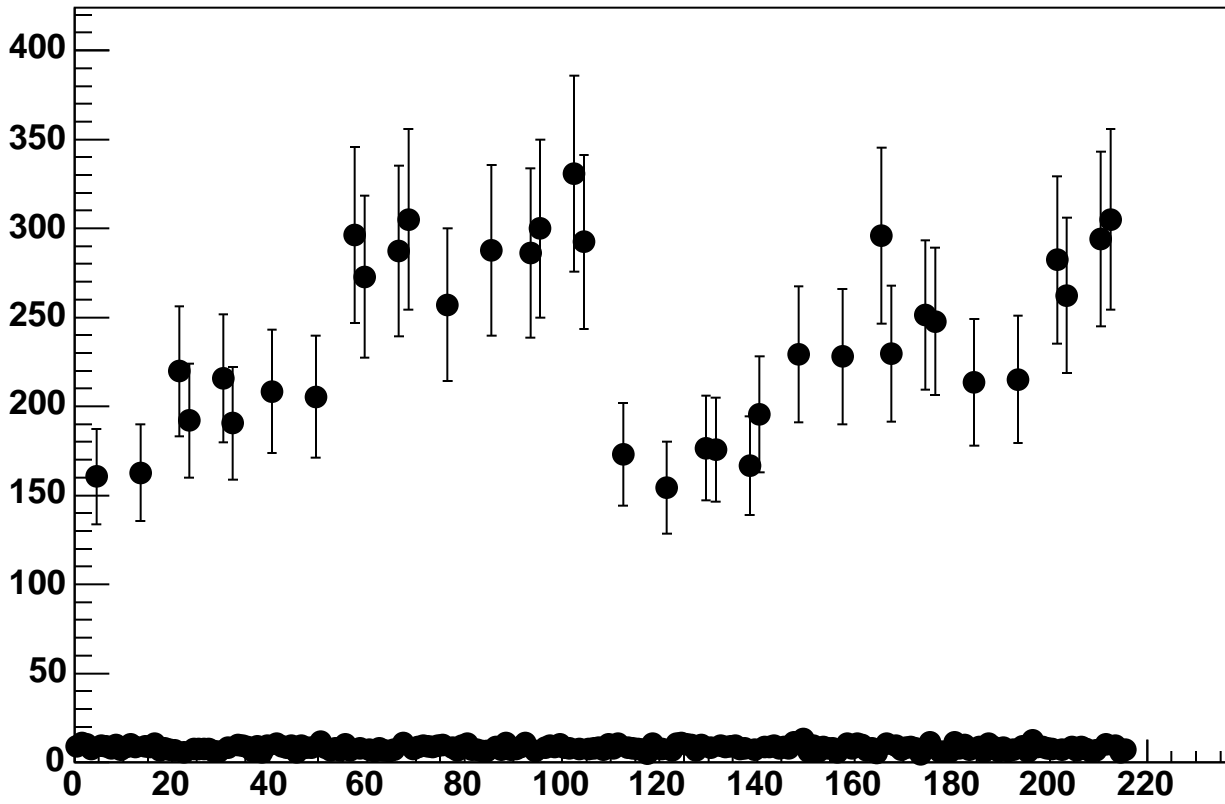
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18\*Chip+Chan



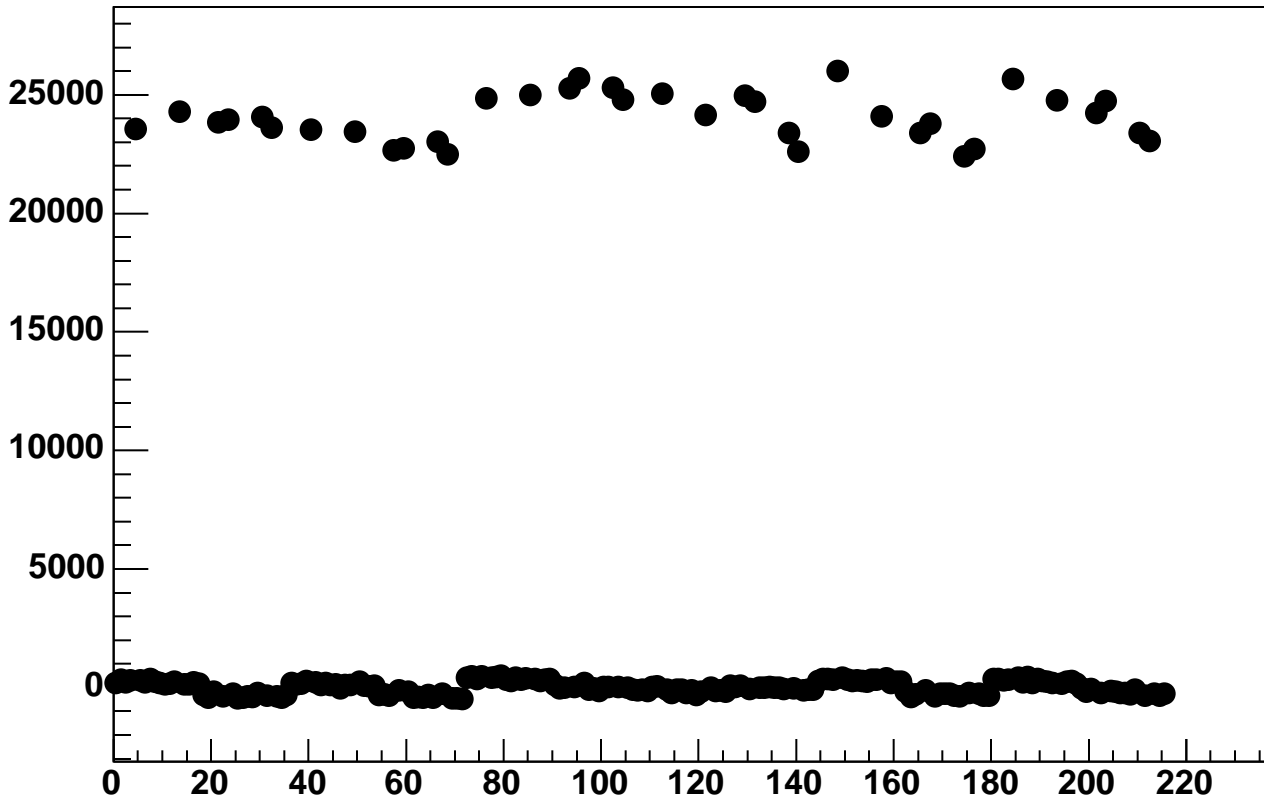
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18\*Chip+Chan



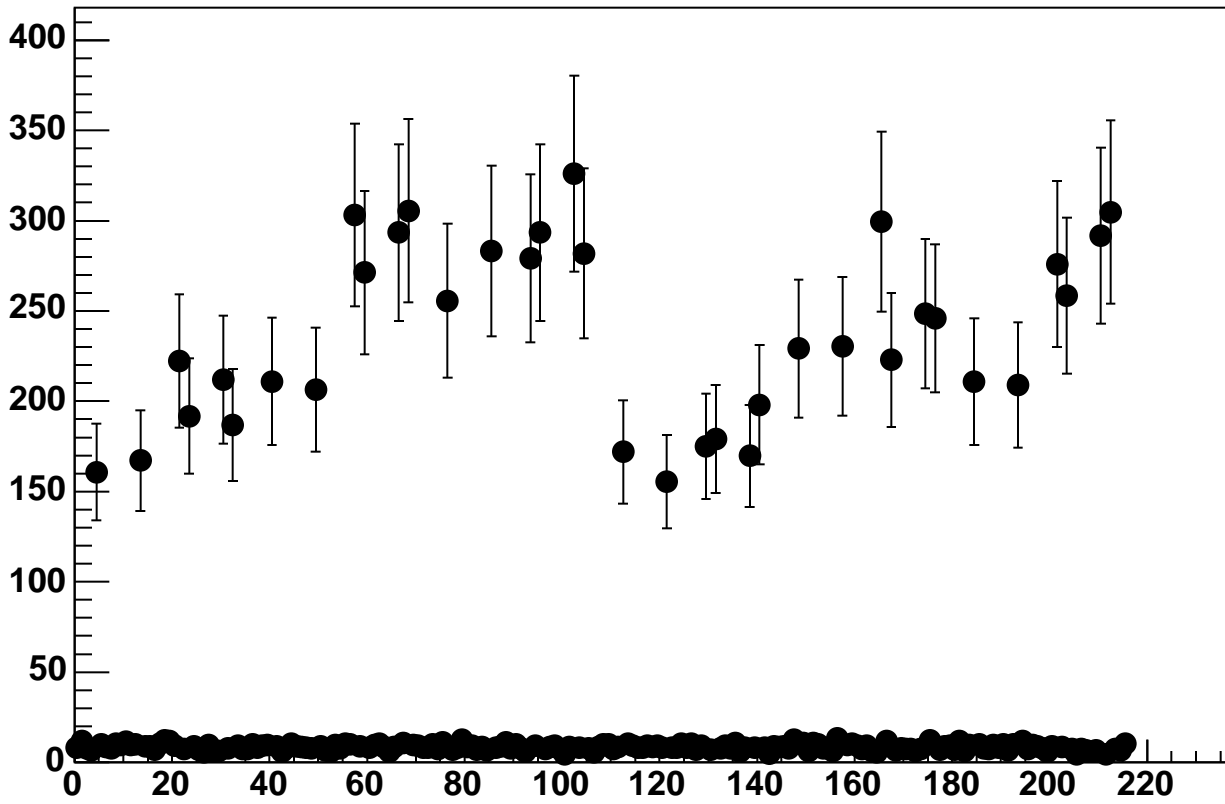
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18\*Chip+Chan



Enable 4, Hold=35, DAC=10800, ADC Mean vs 18\*Chip+Chan

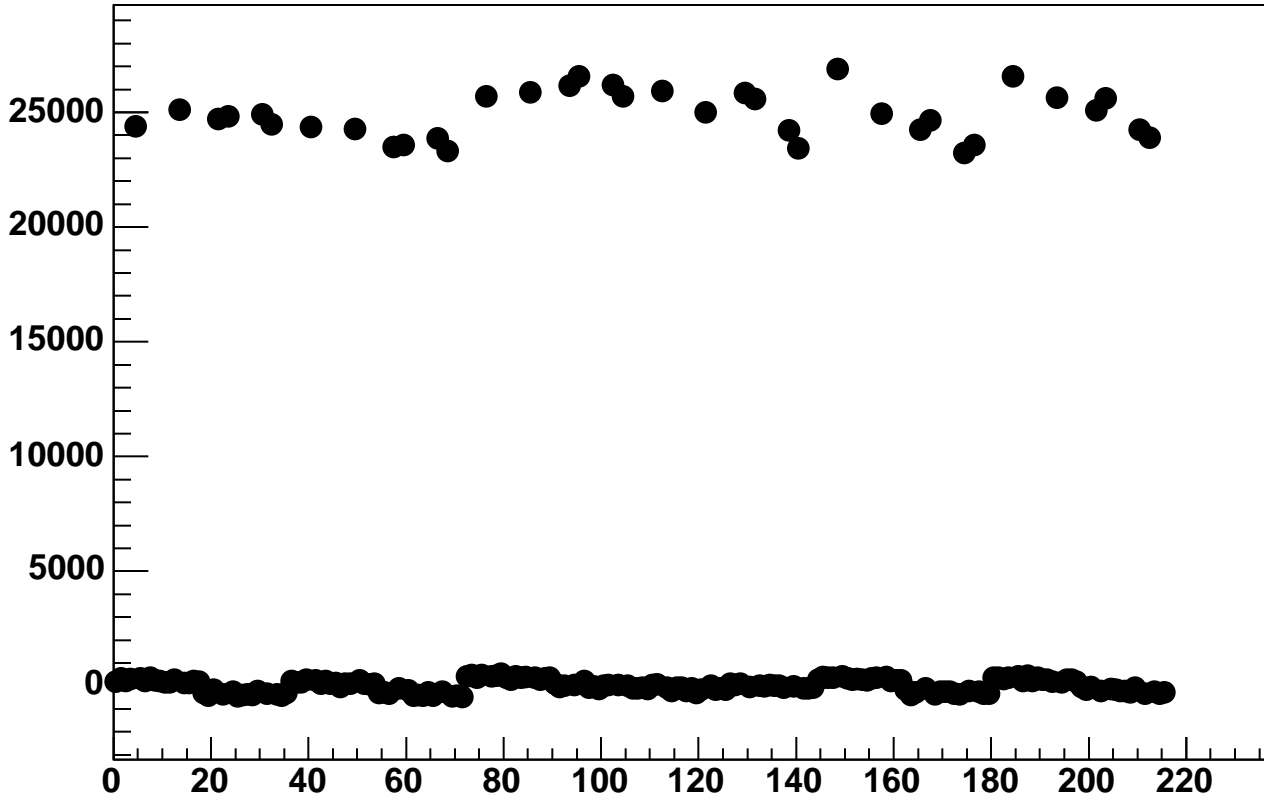


Enable 4, Hold=35, DAC=10800, ADC Noise vs 18\*Chip+Chan

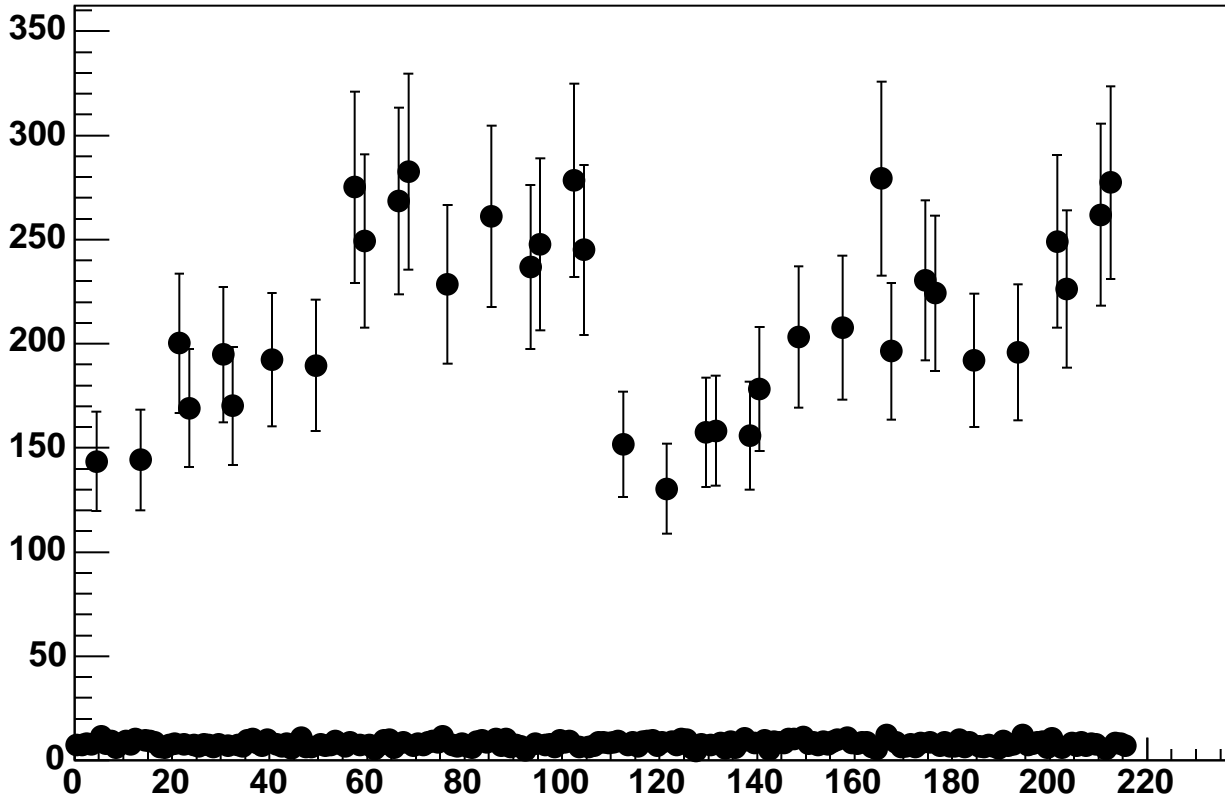




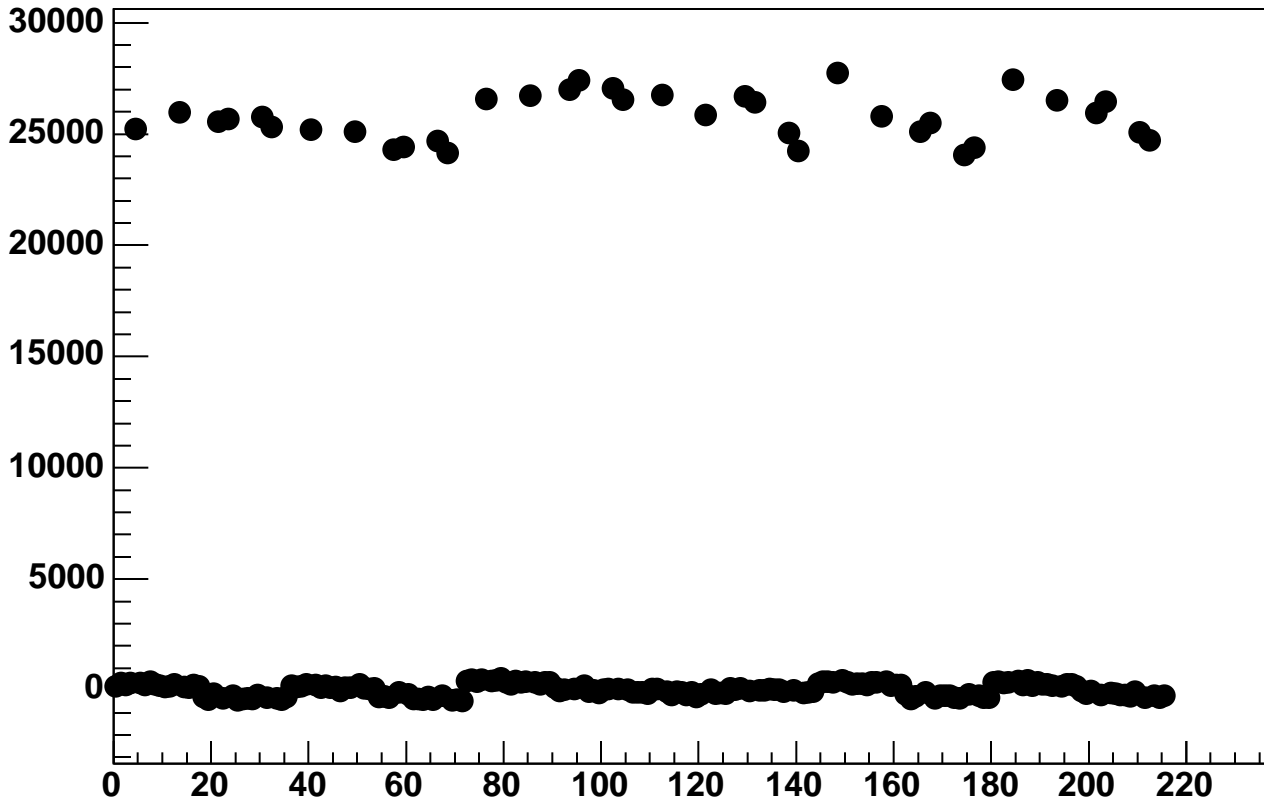
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18\*Chip+Chan



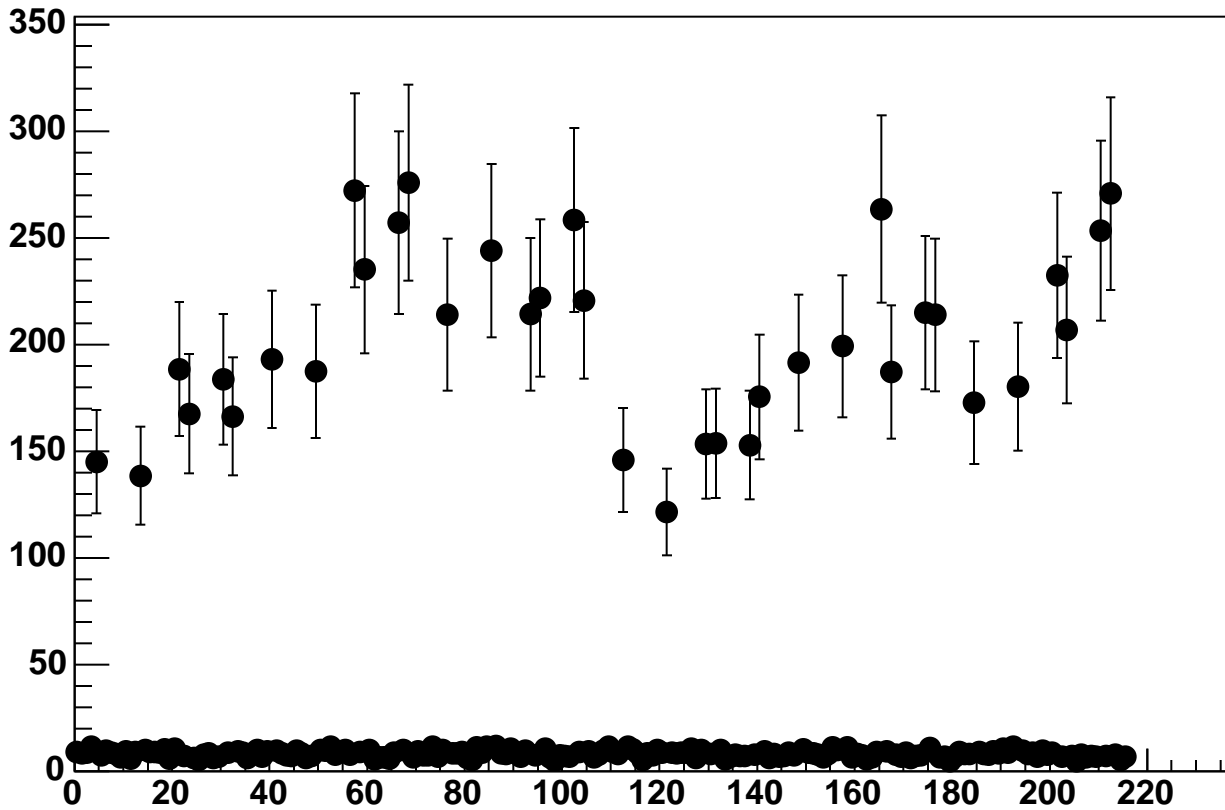
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18\*Chip+Chan



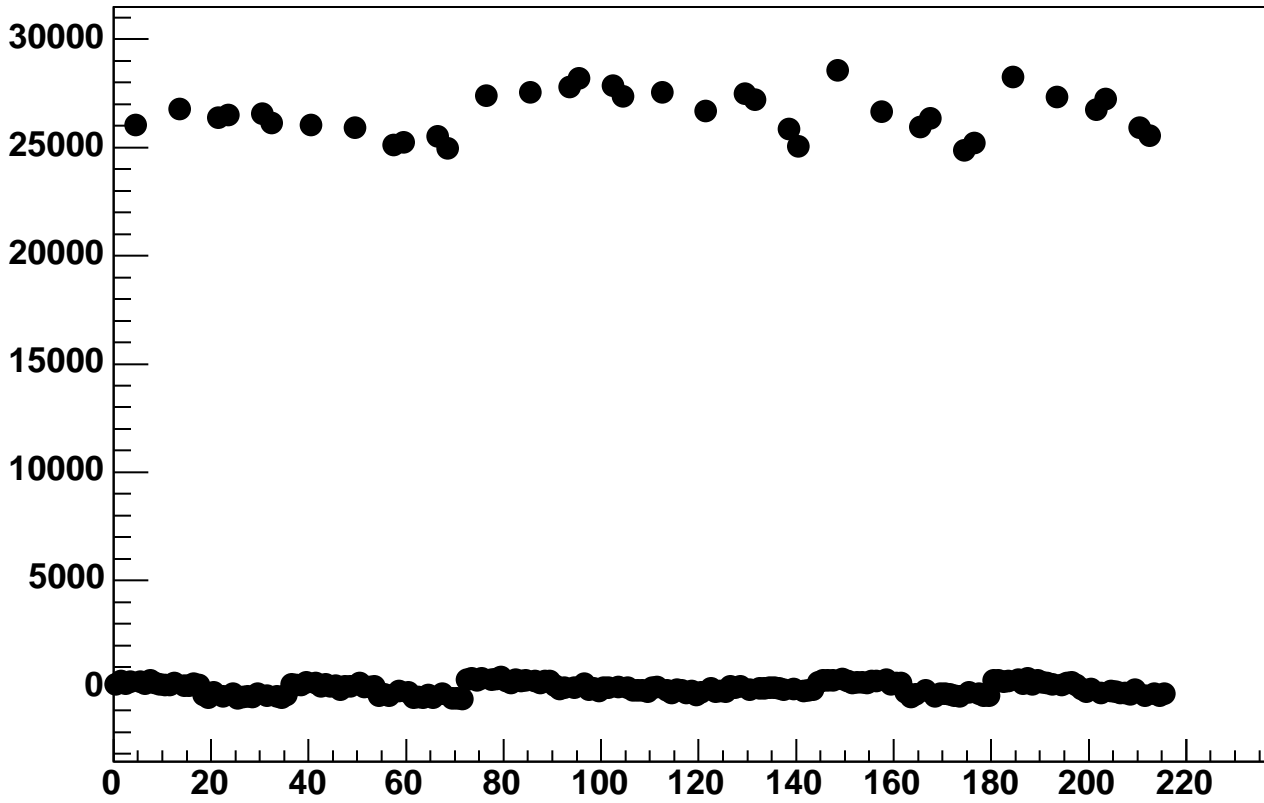
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18\*Chip+Chan



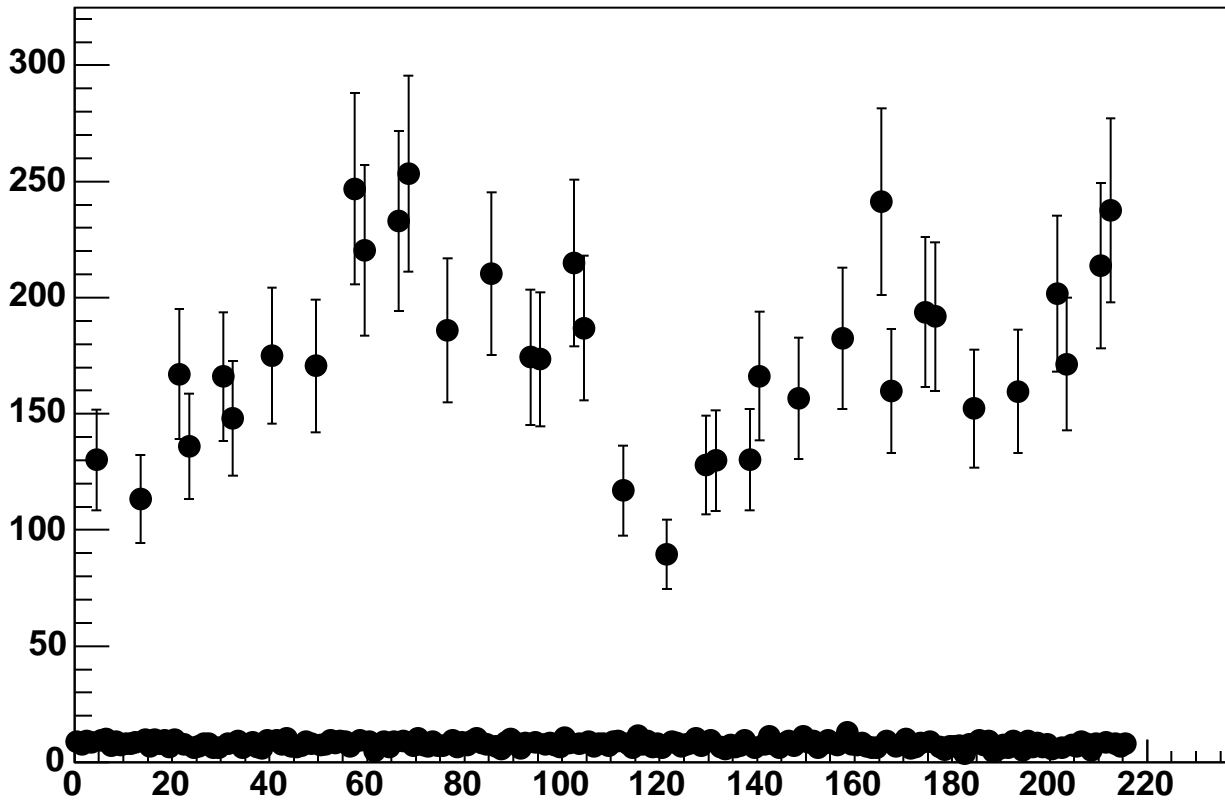
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18\*Chip+Chan



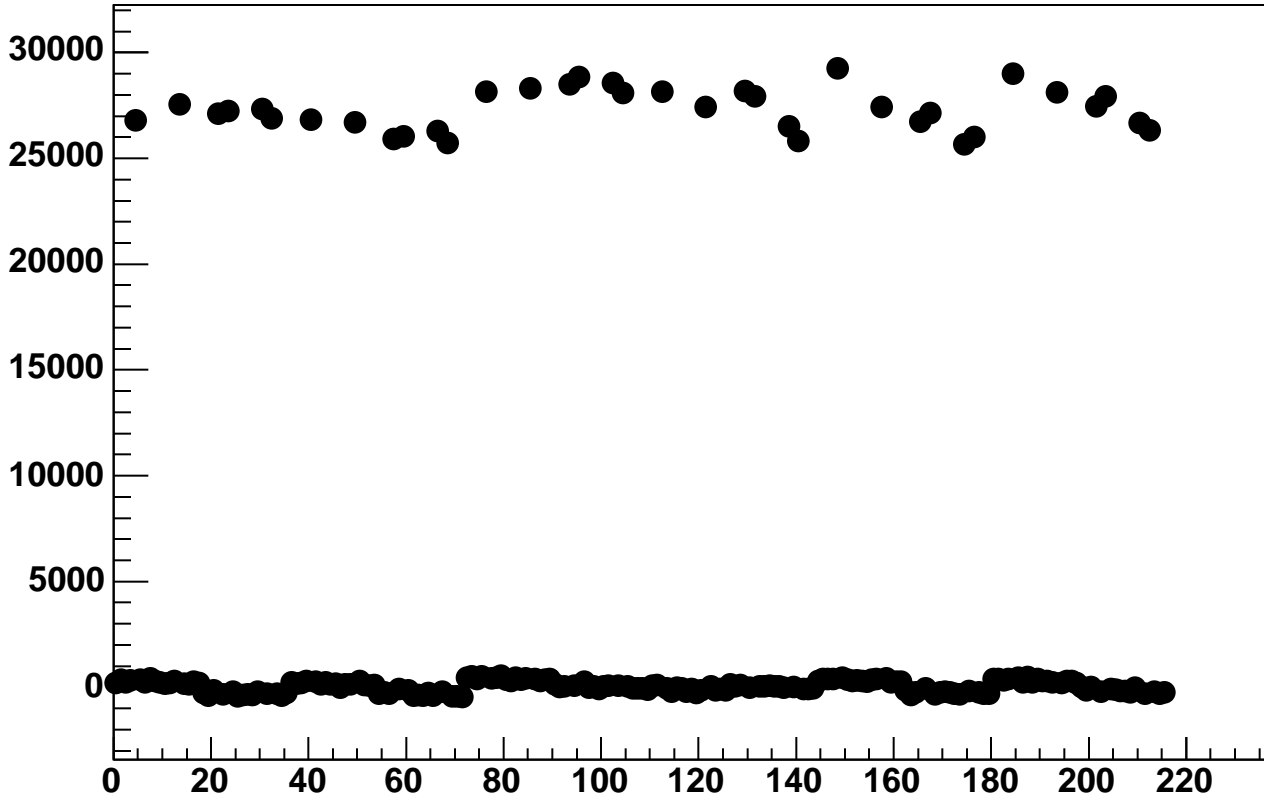
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18\*Chip+Chan



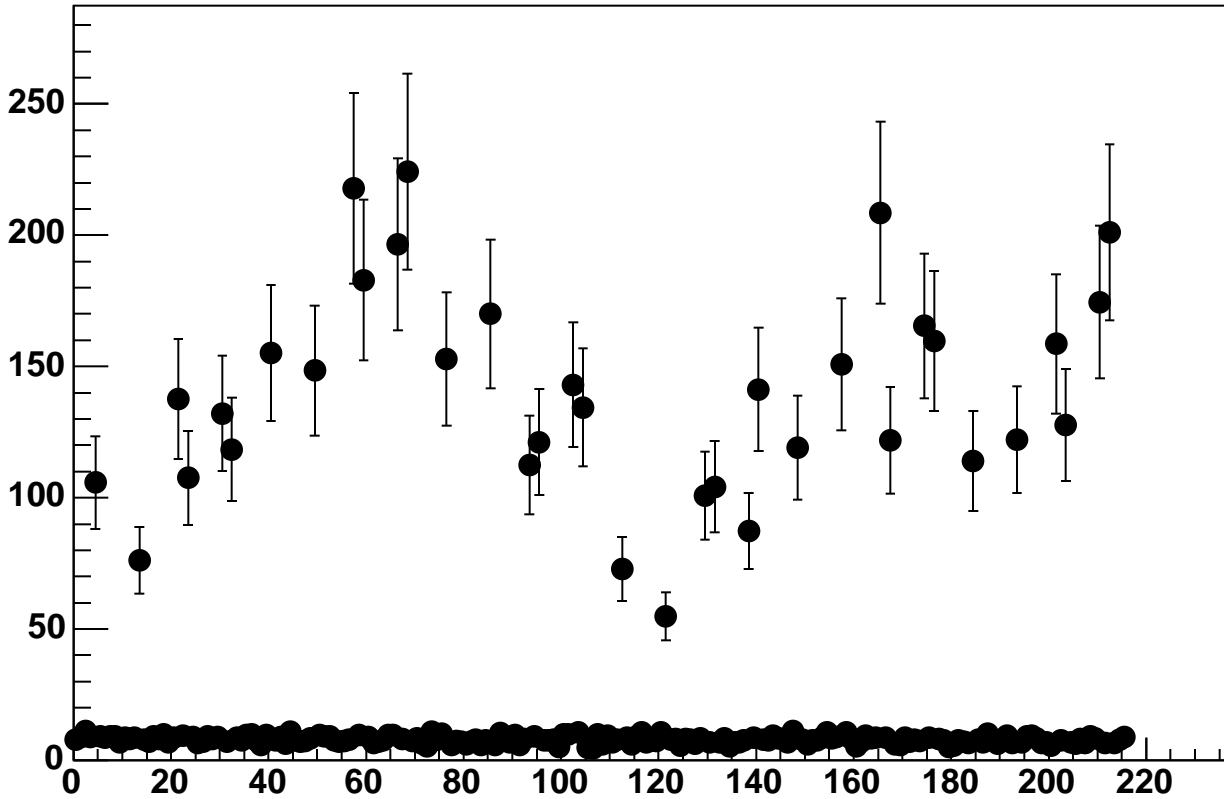
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18\*Chip+Chan



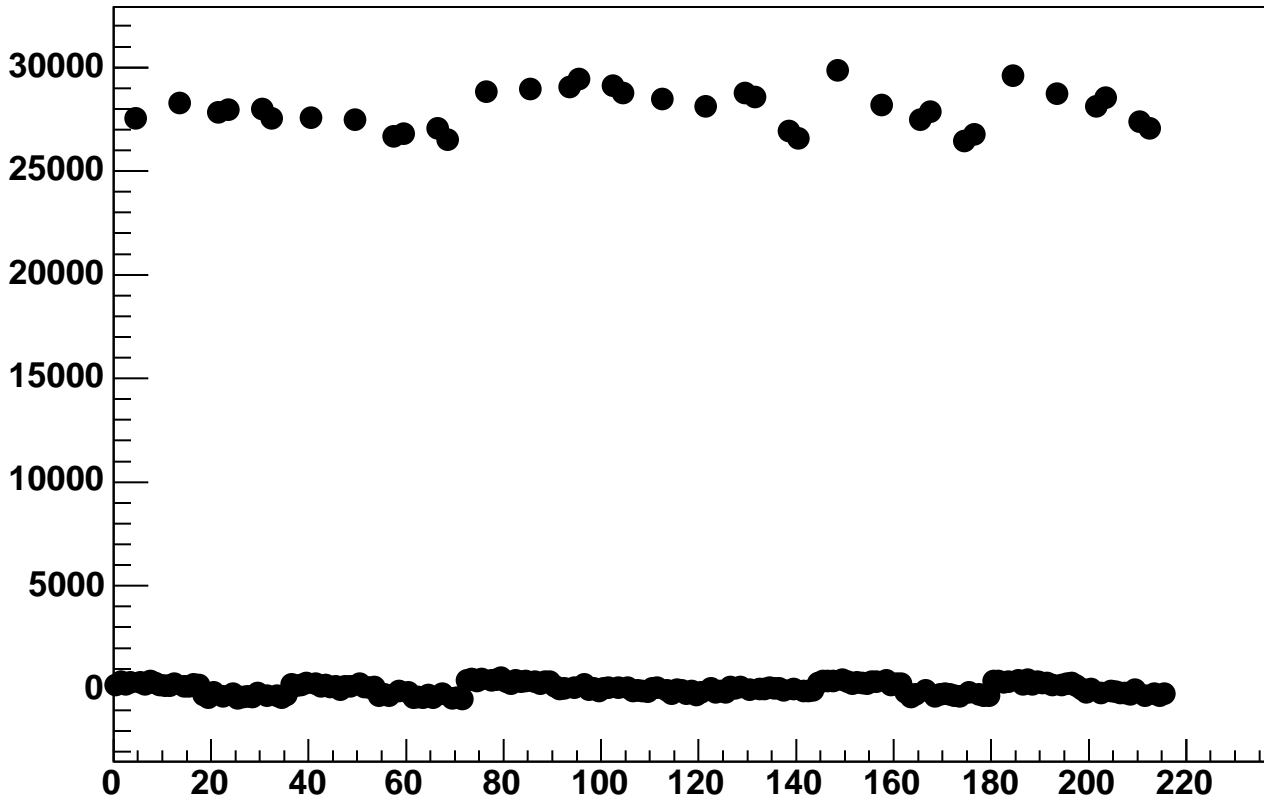
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18\*Chip+Chan



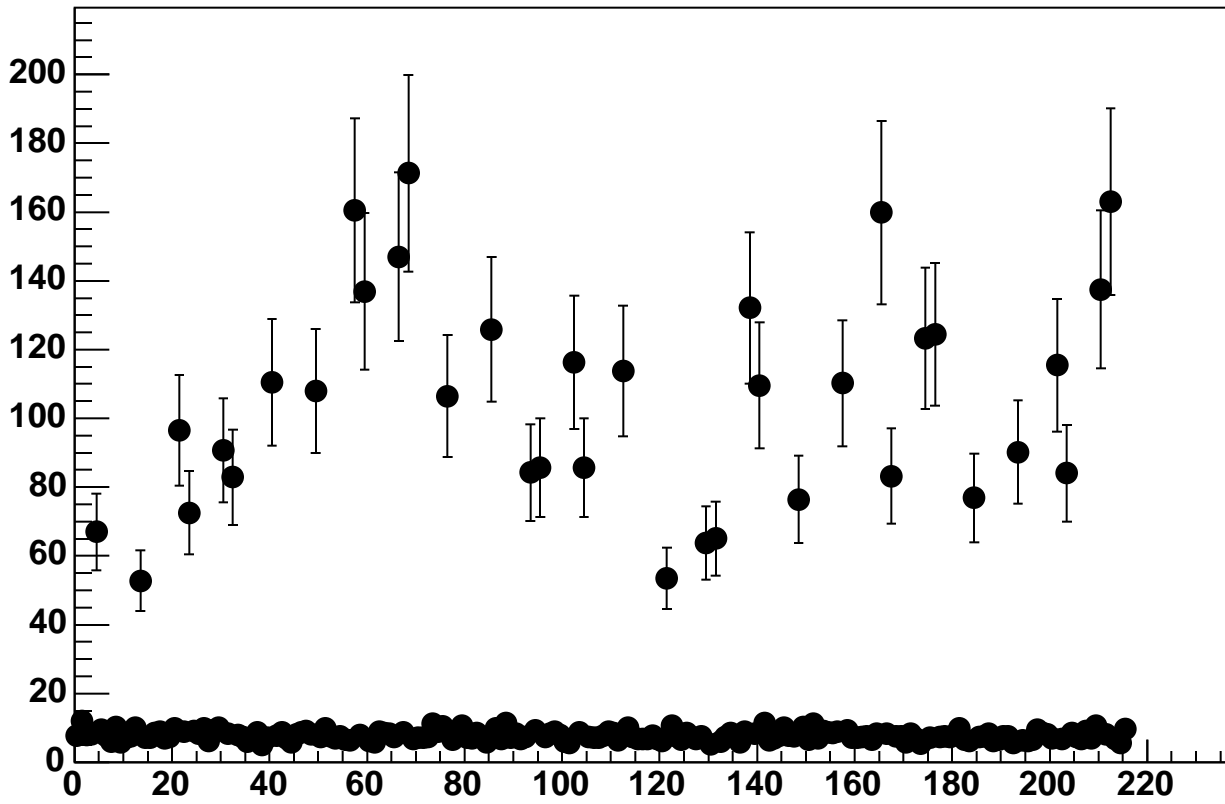
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18\*Chip+Chan



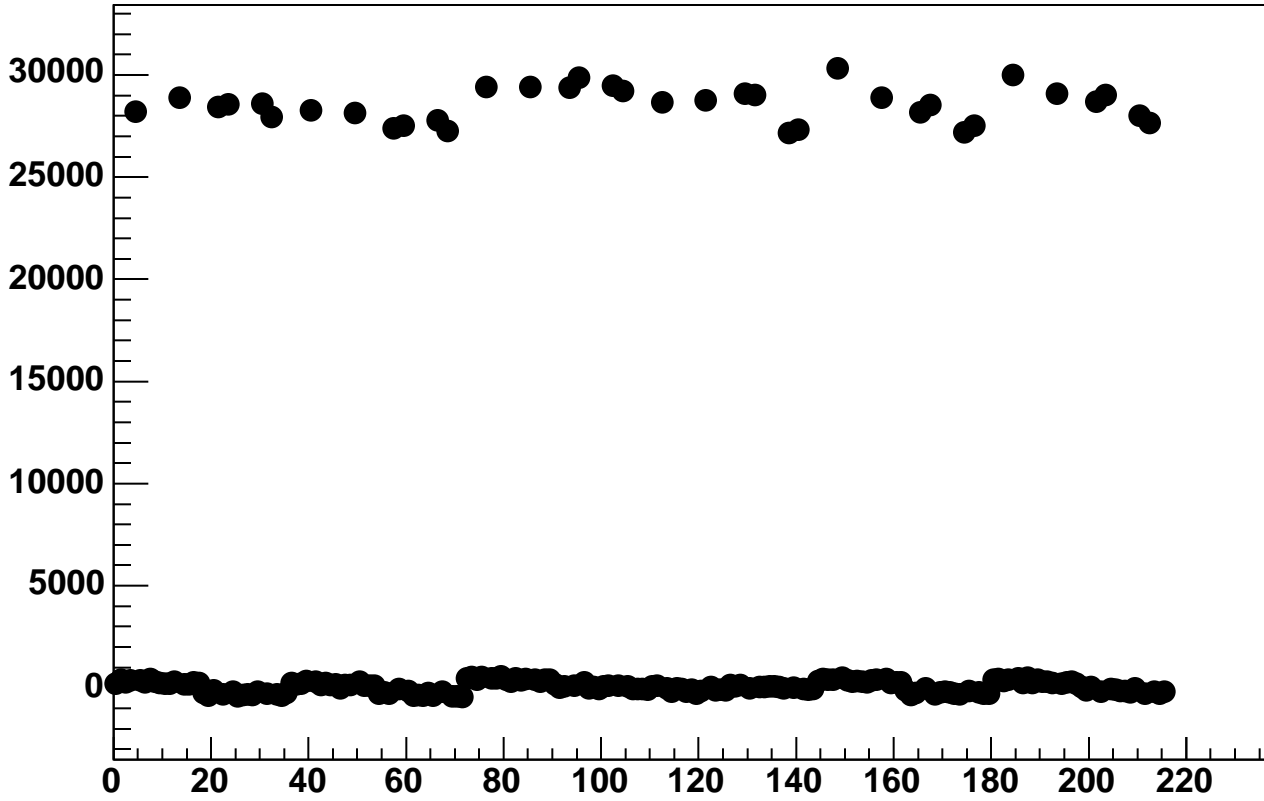
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18\*Chip+Chan



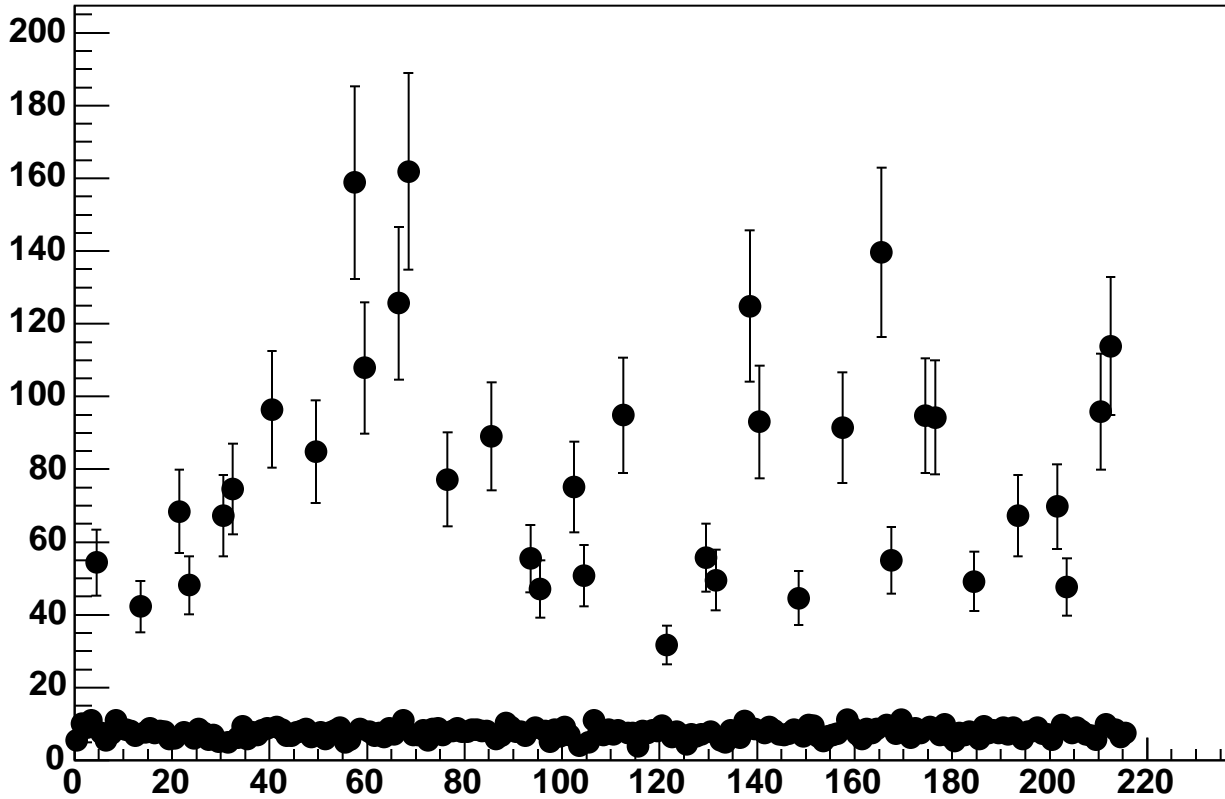
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18\*Chip+Chan



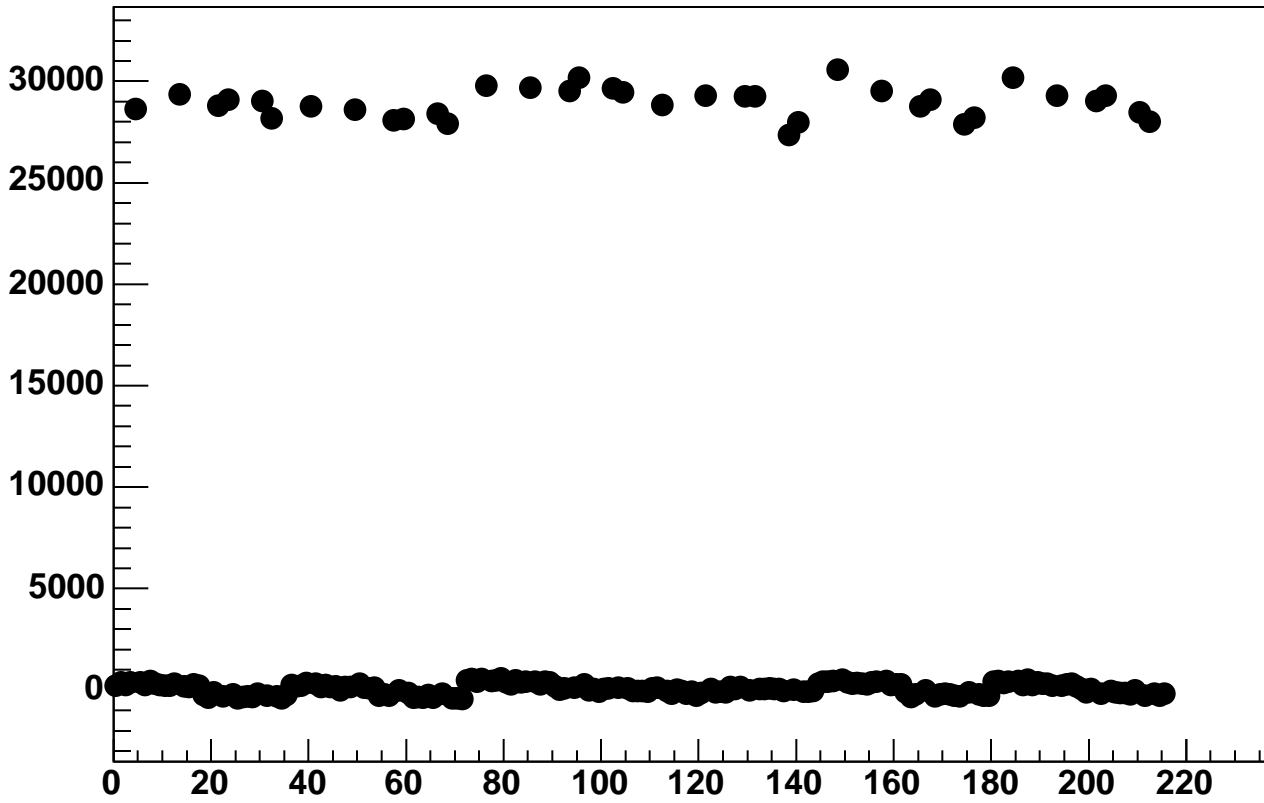
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18\*Chip+Chan



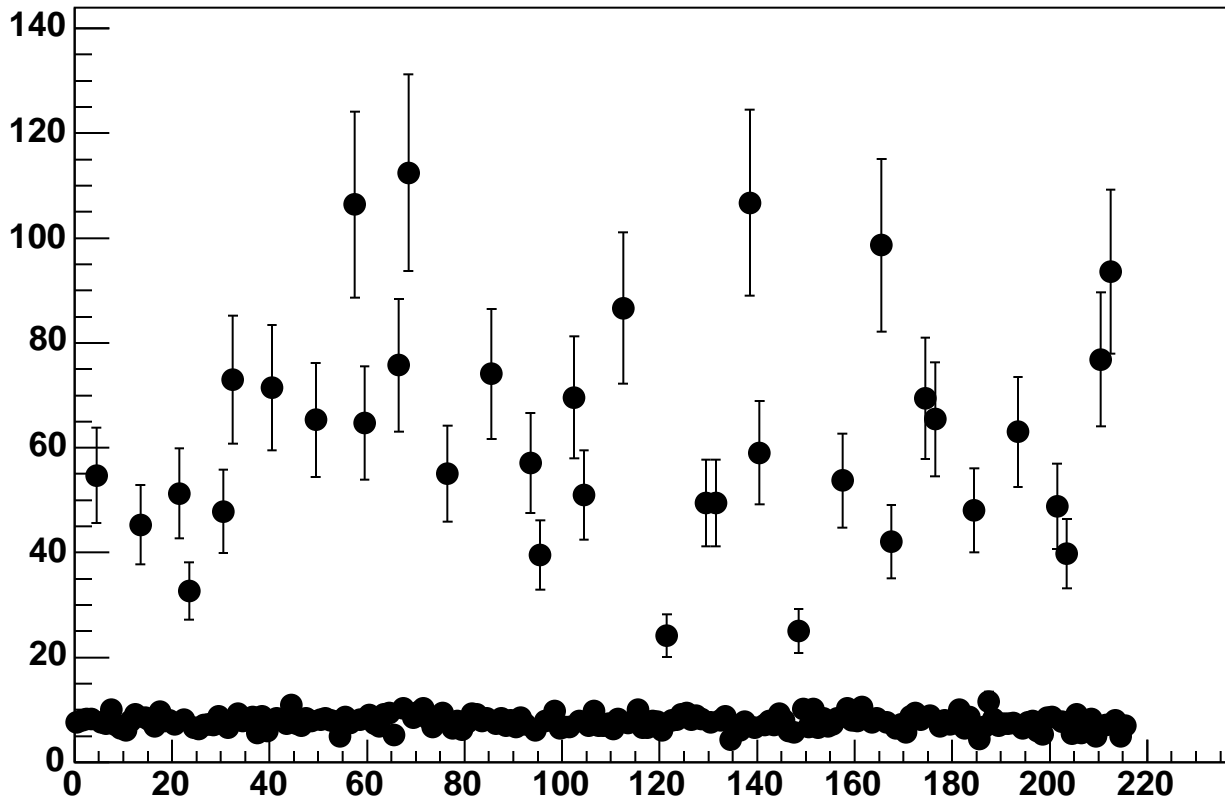
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18\*Chip+Chan



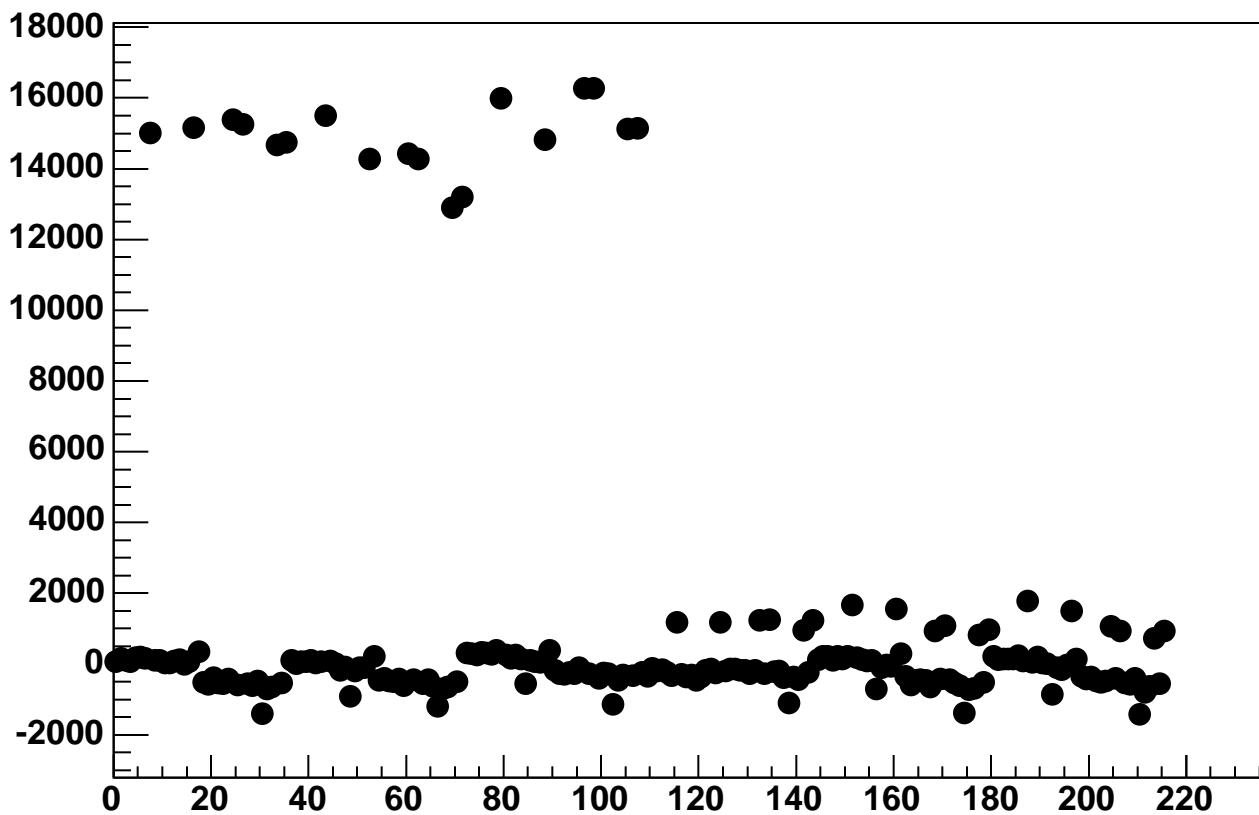
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18\*Chip+Chan



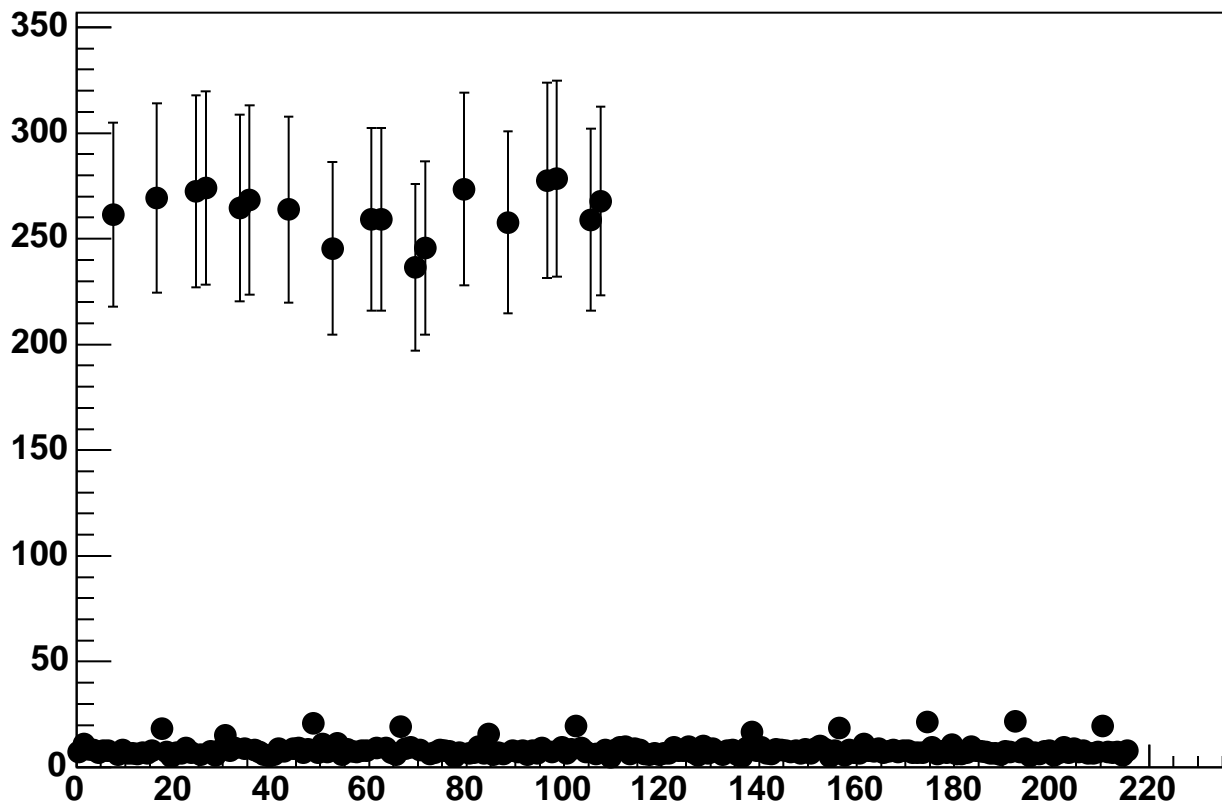
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=35, DAC=0, ADC Mean vs 18\*Chip+Chan

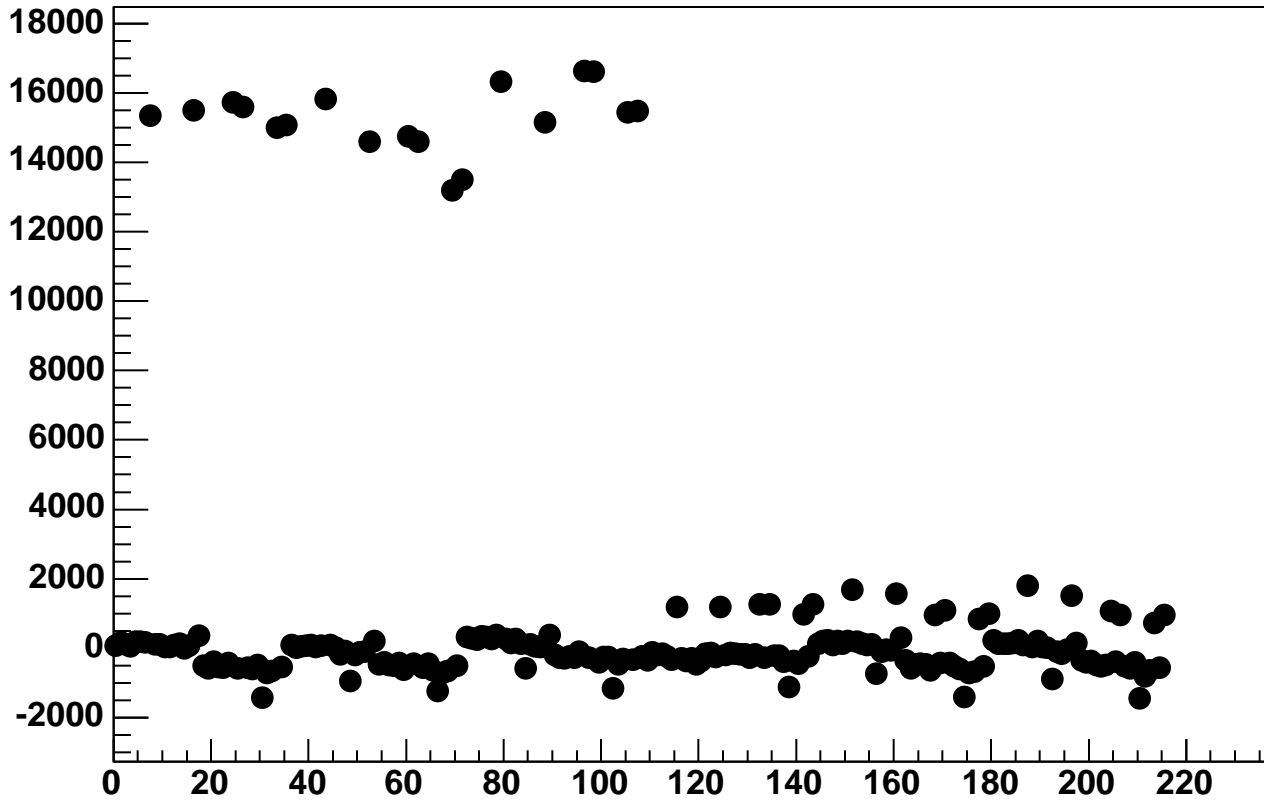


Enable 5, Hold=35, DAC=0, ADC Noise vs 18\*Chip+Chan

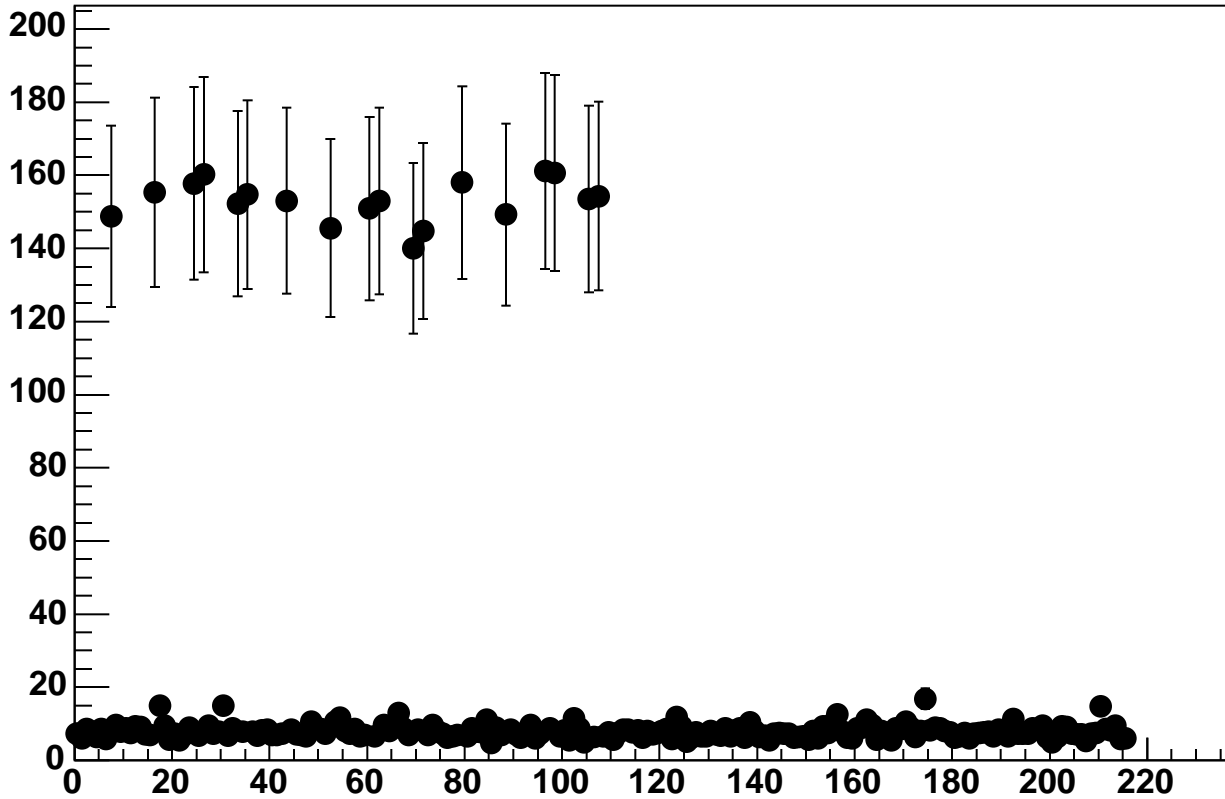




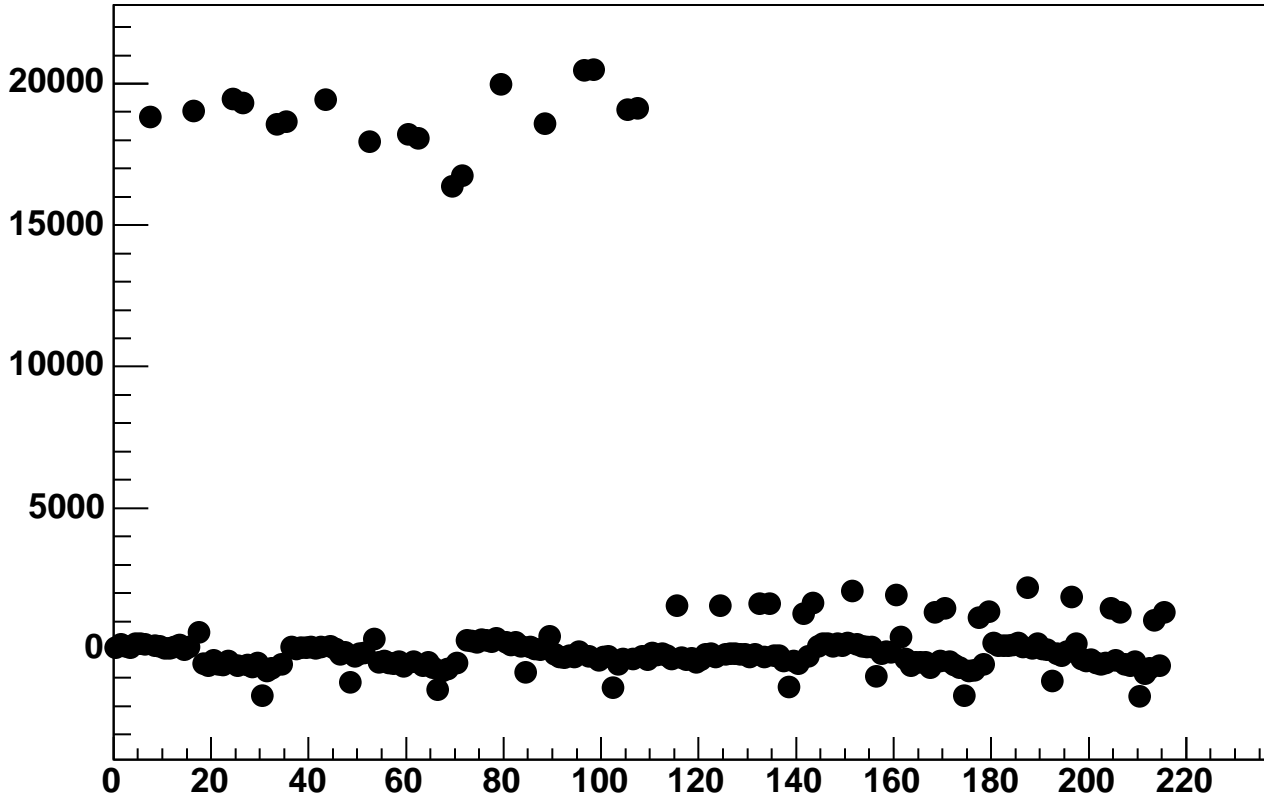
Enable 5, Hold=35, DAC=400, ADC Mean vs 18\*Chip+Chan



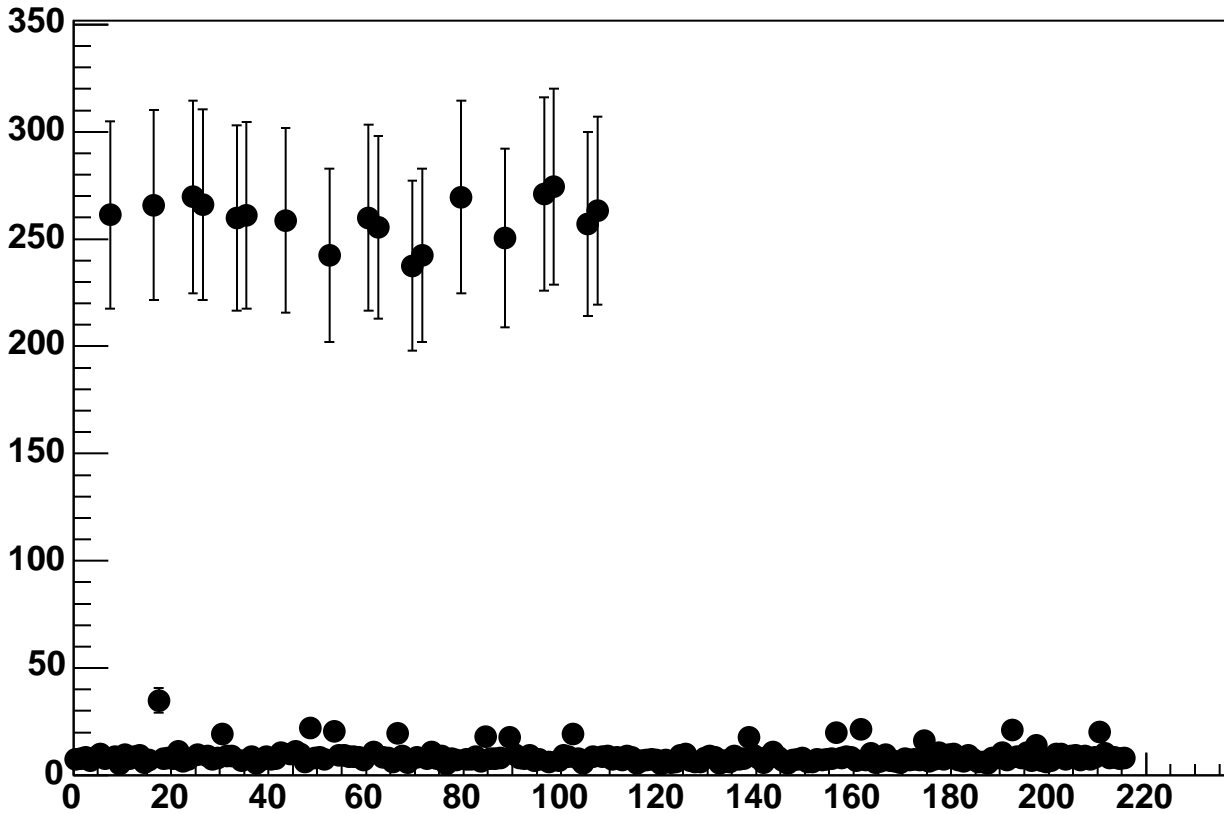
Enable 5, Hold=35, DAC=400, ADC Noise vs 18\*Chip+Chan



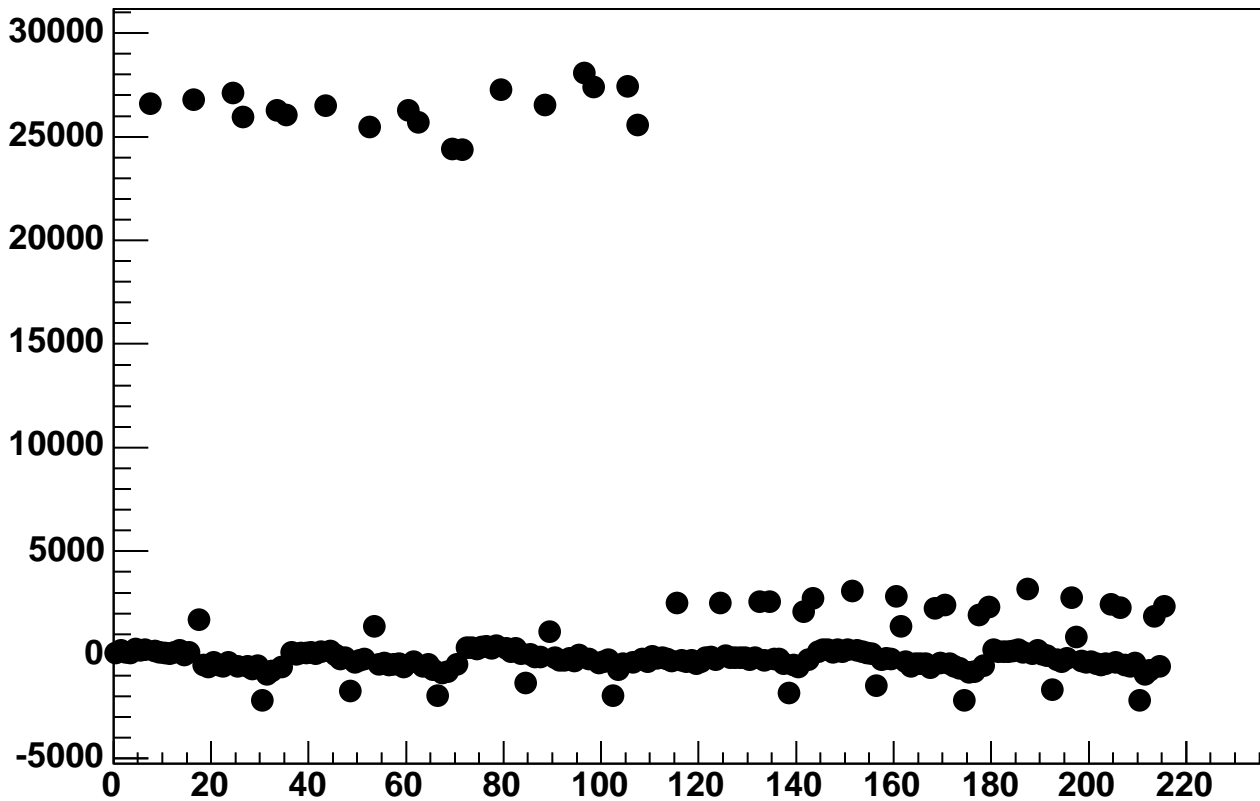
Enable 5, Hold=35, DAC=800, ADC Mean vs 18\*Chip+Chan



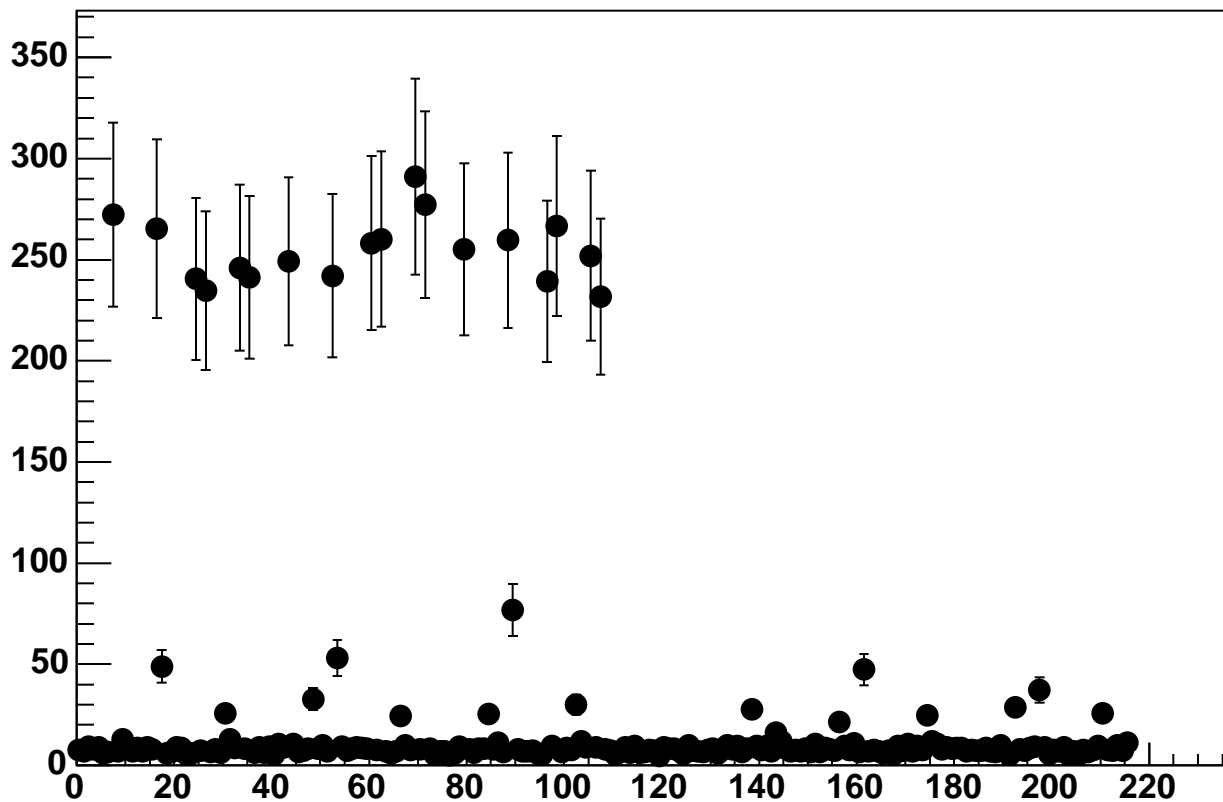
Enable 5, Hold=35, DAC=800, ADC Noise vs 18\*Chip+Chan



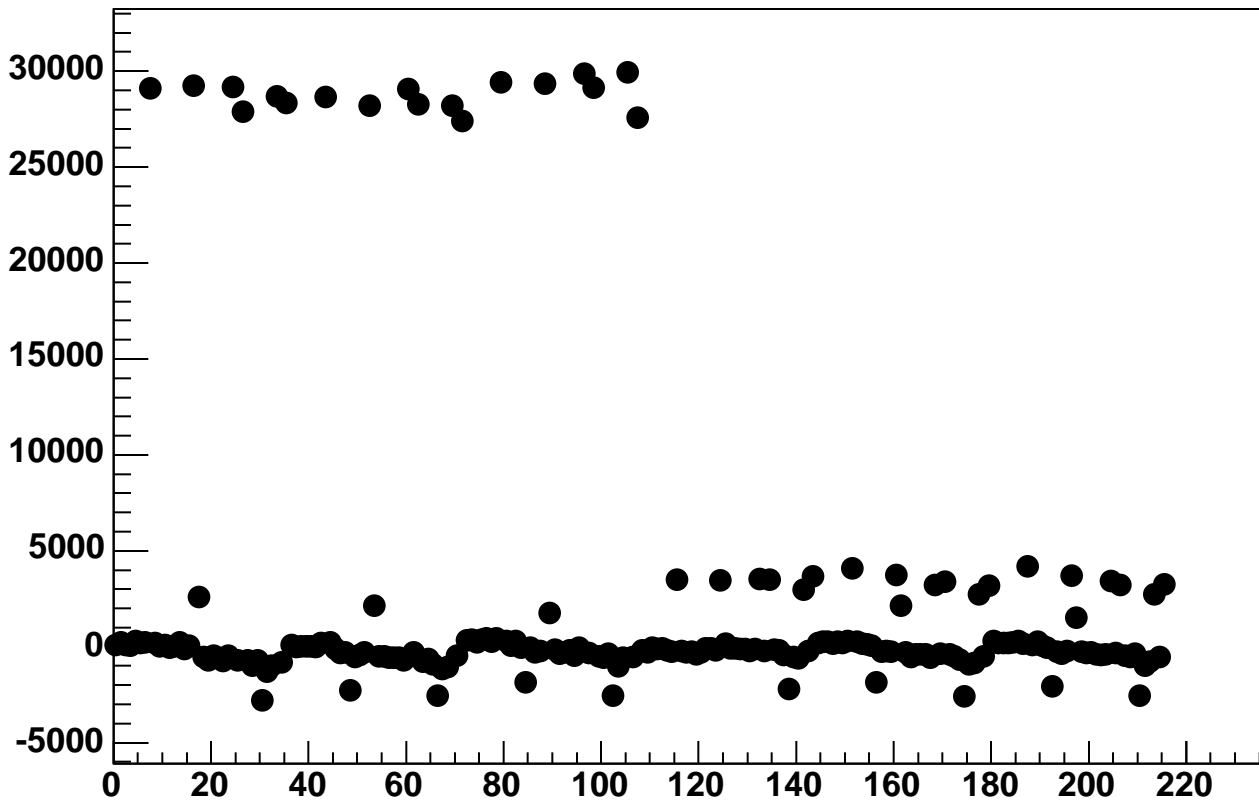
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18\*Chip+Chan



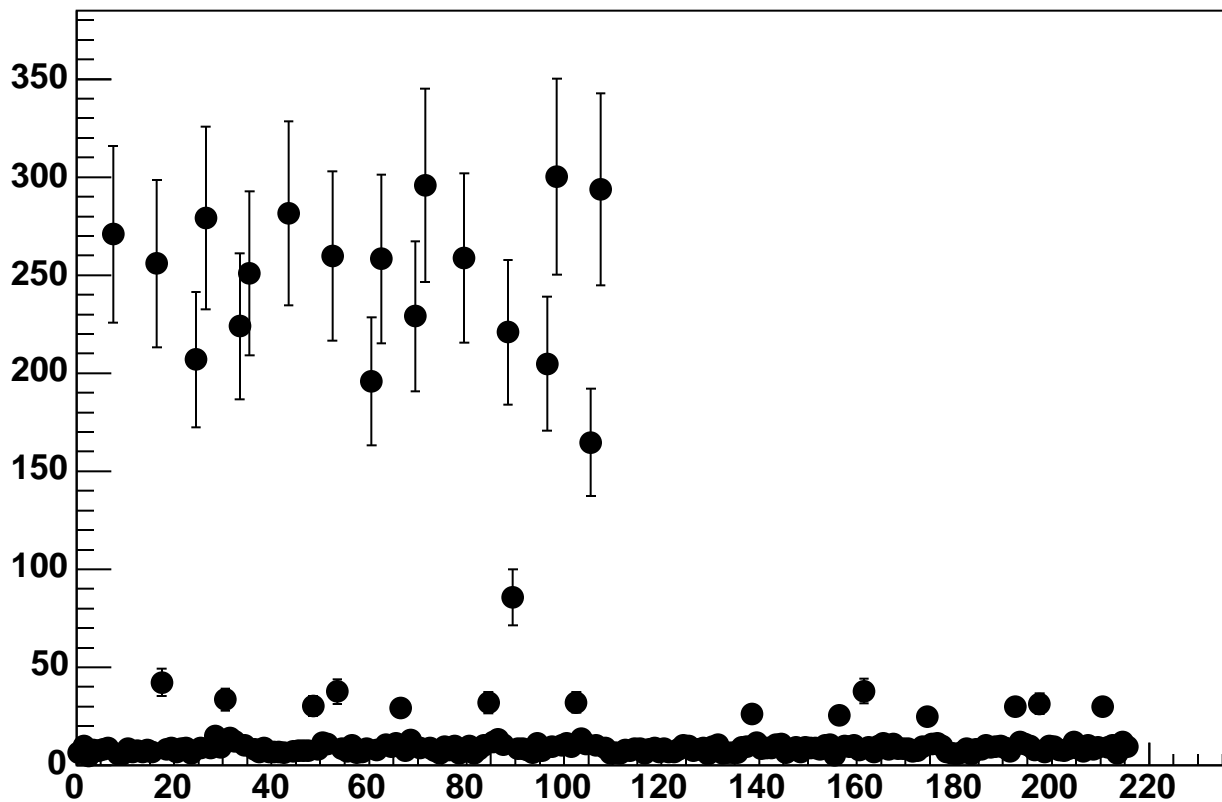
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18\*Chip+Chan



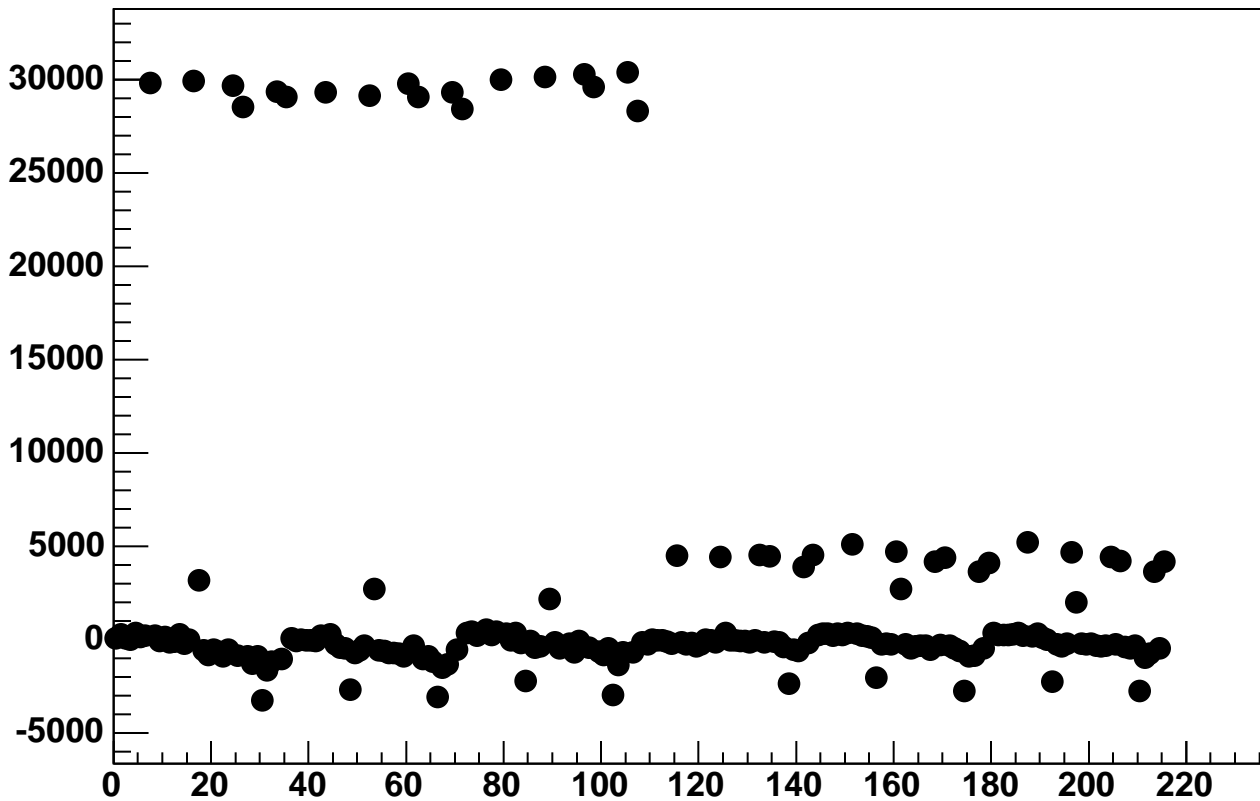
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18\*Chip+Chan



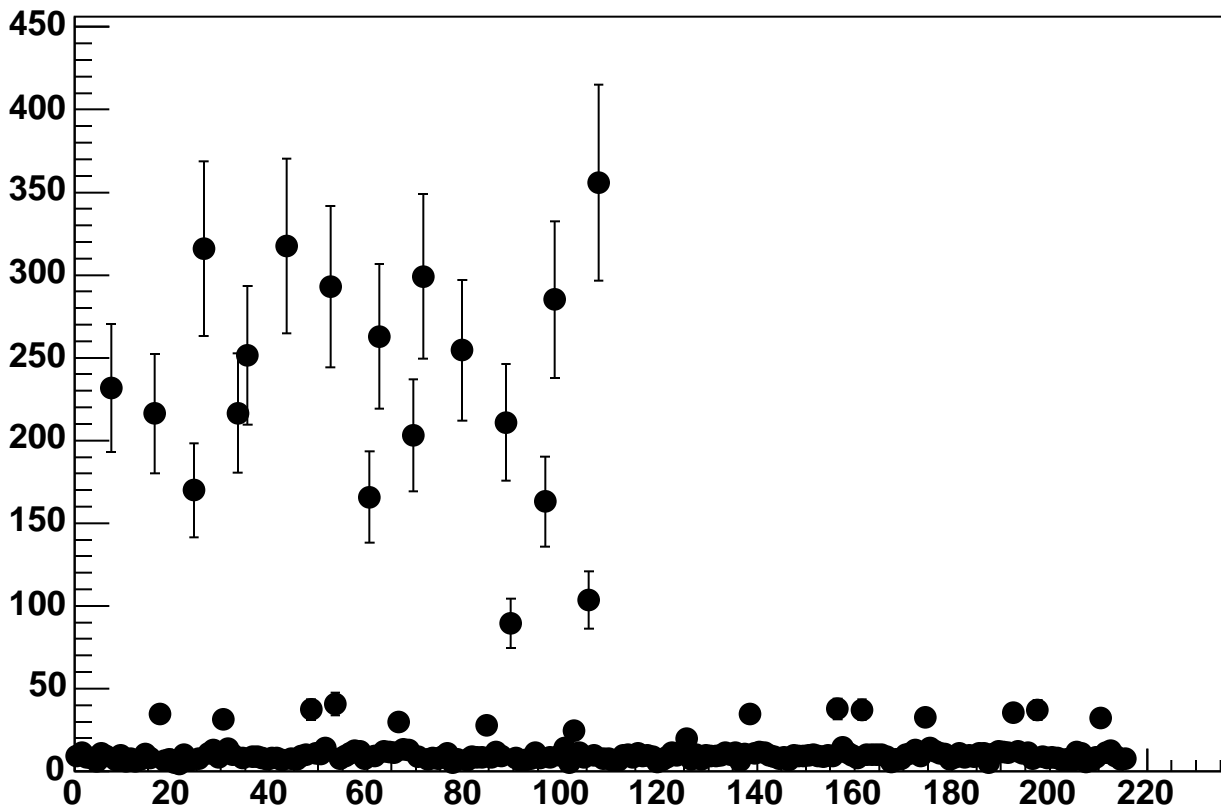
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18\*Chip+Chan



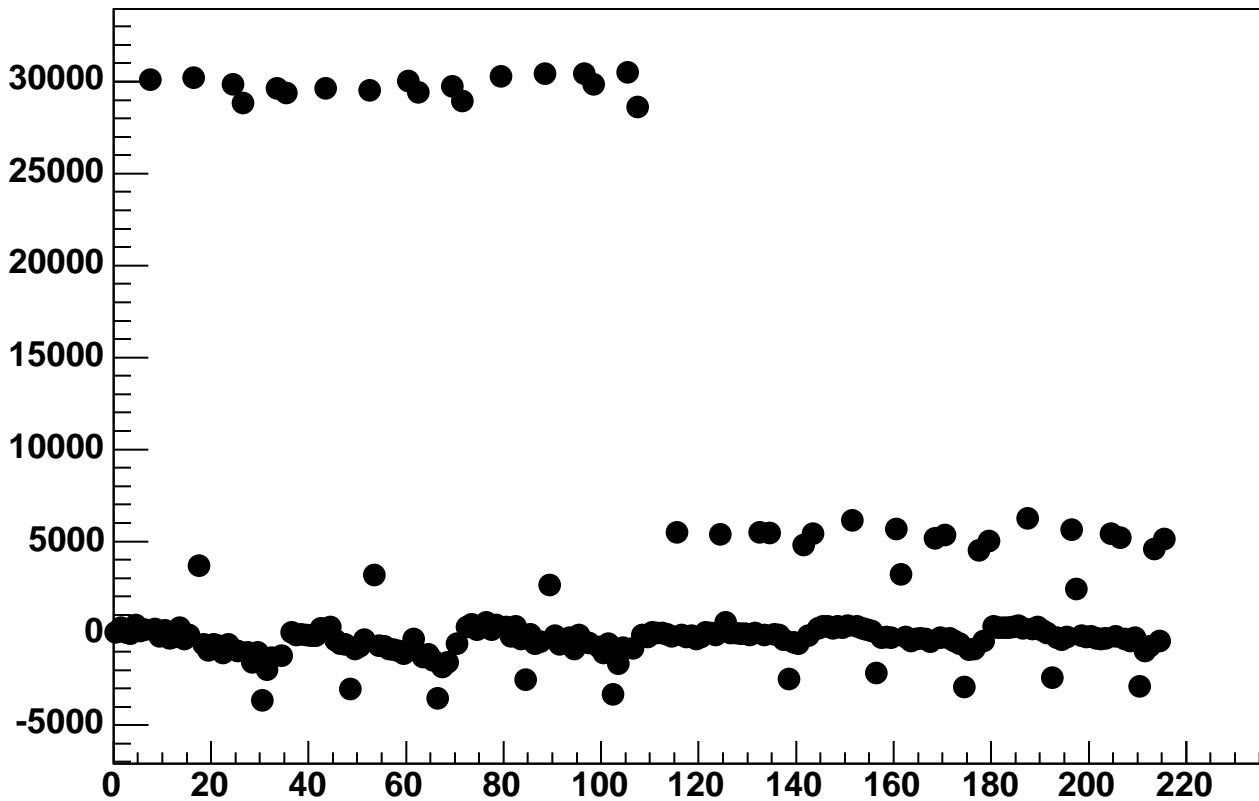
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18\*Chip+Chan



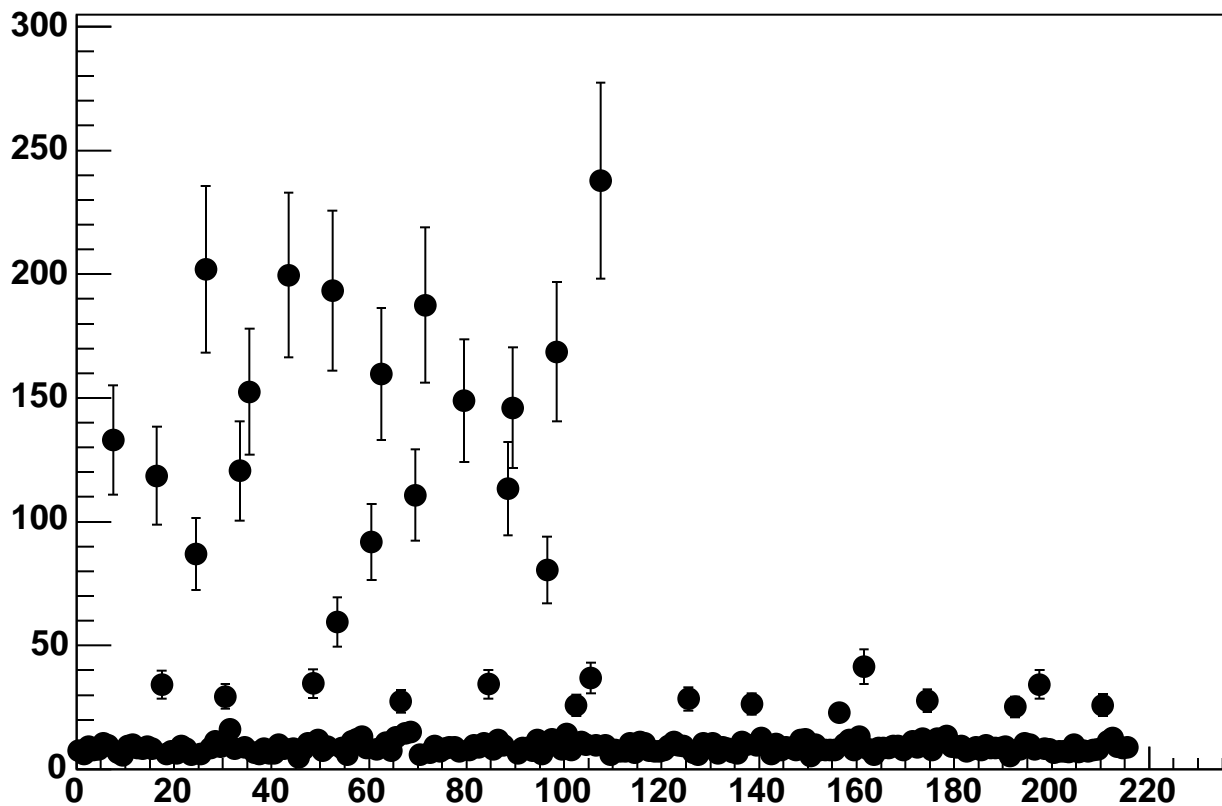
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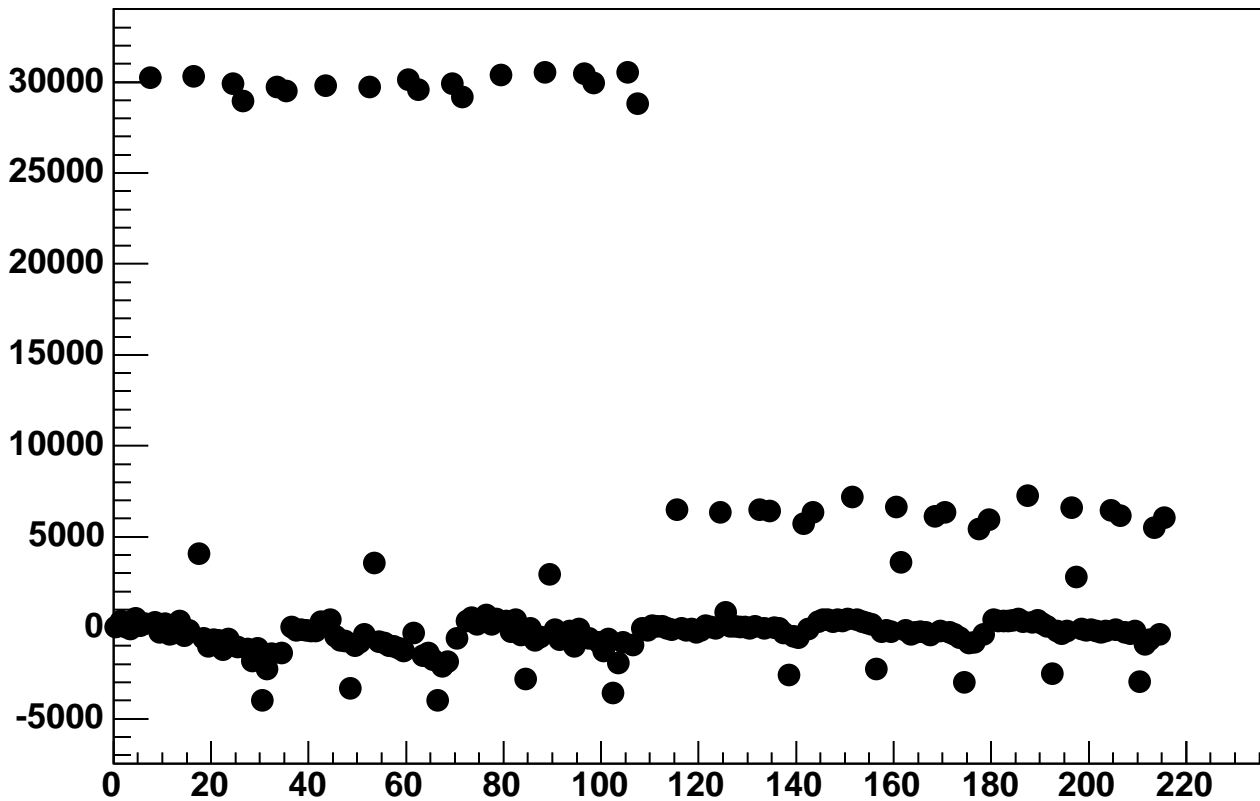
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18\*Chip+Chan



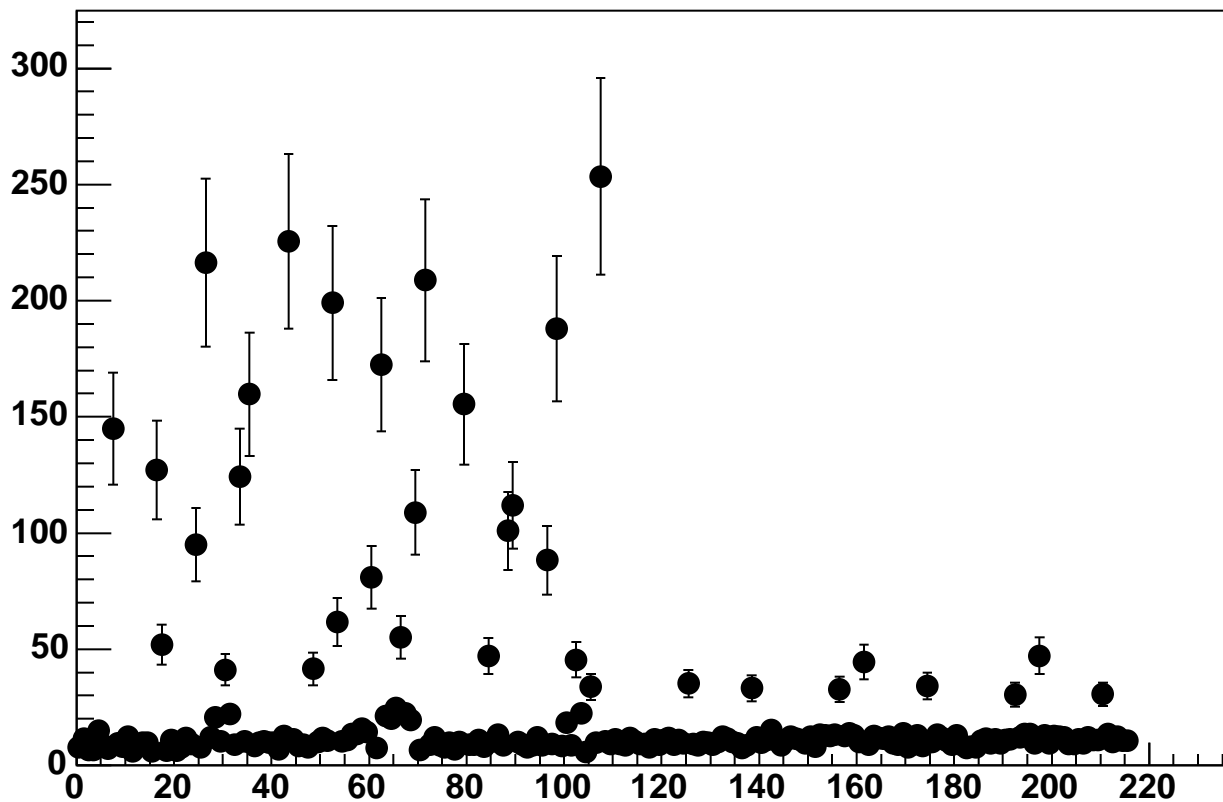
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18\*Chip+Chan



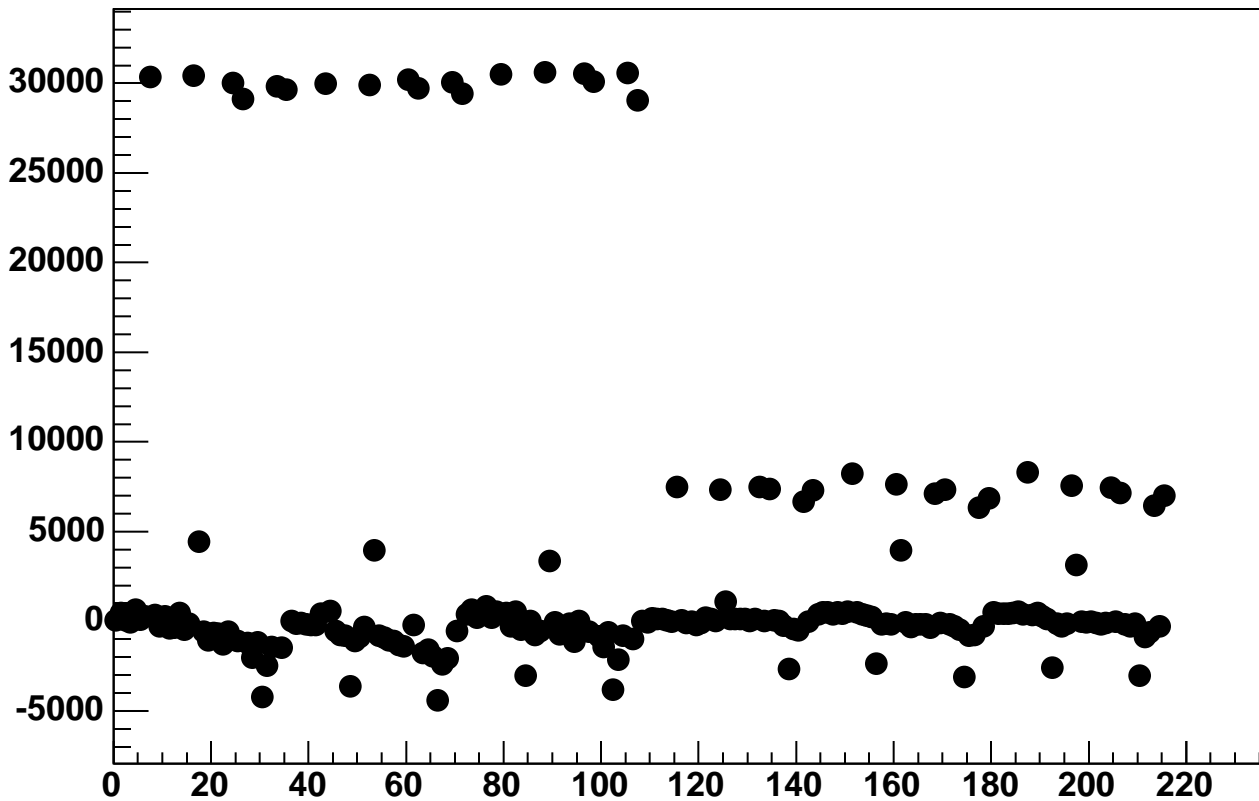
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18\*Chip+Chan



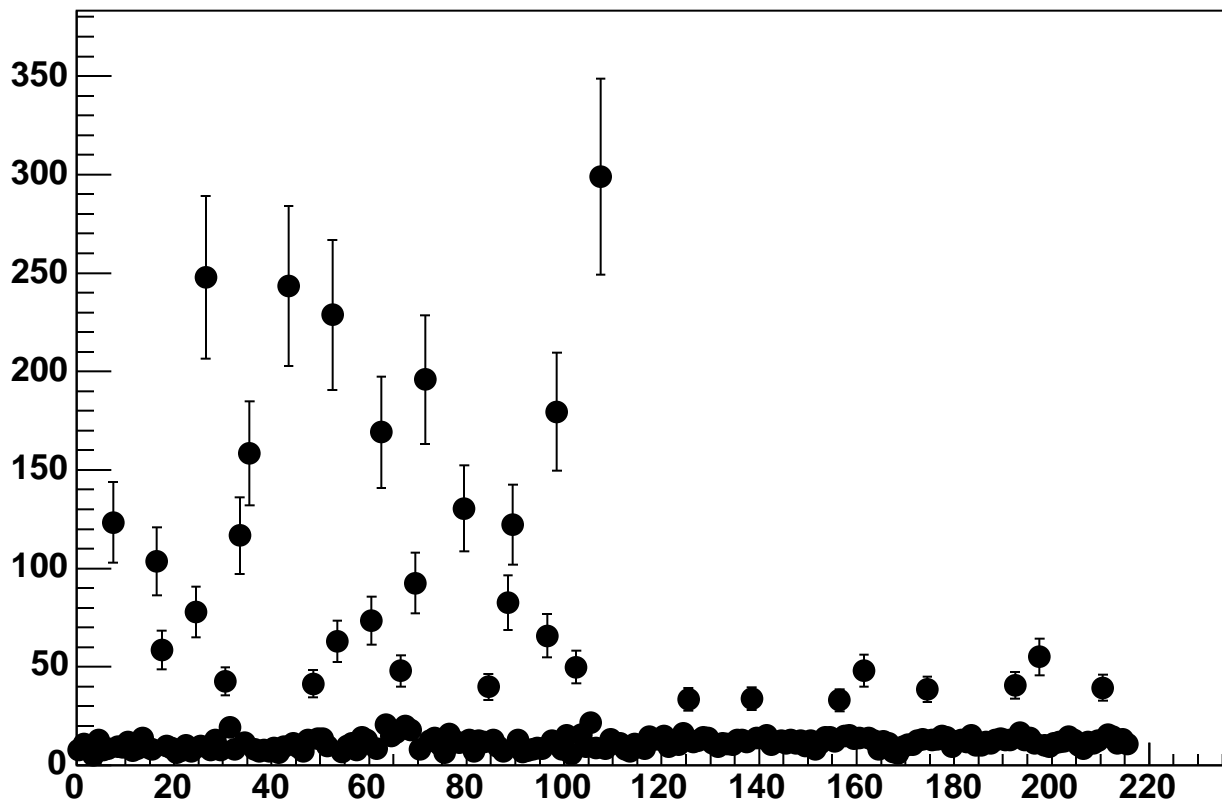
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=35, DAC=3200, ADC Mean vs 18\*Chip+Chan

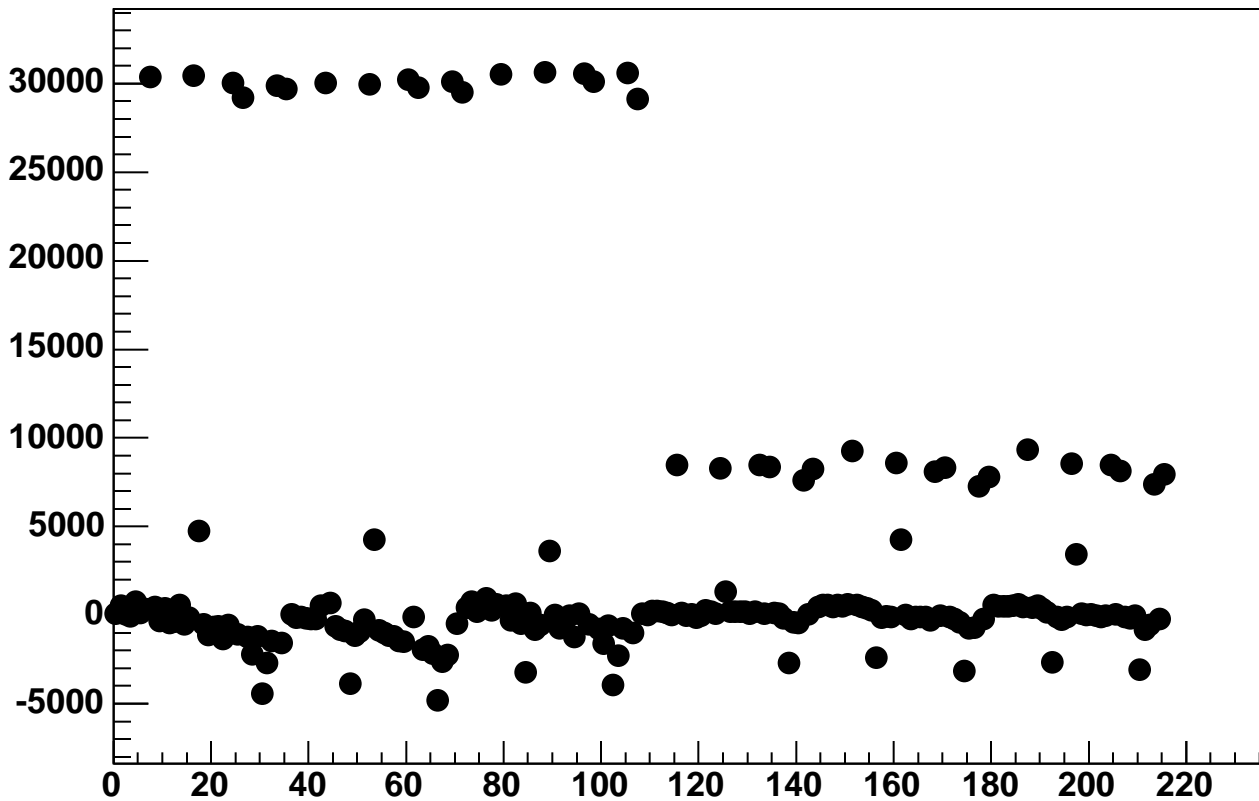


Enable 5, Hold=35, DAC=3200, ADC Noise vs 18\*Chip+Chan

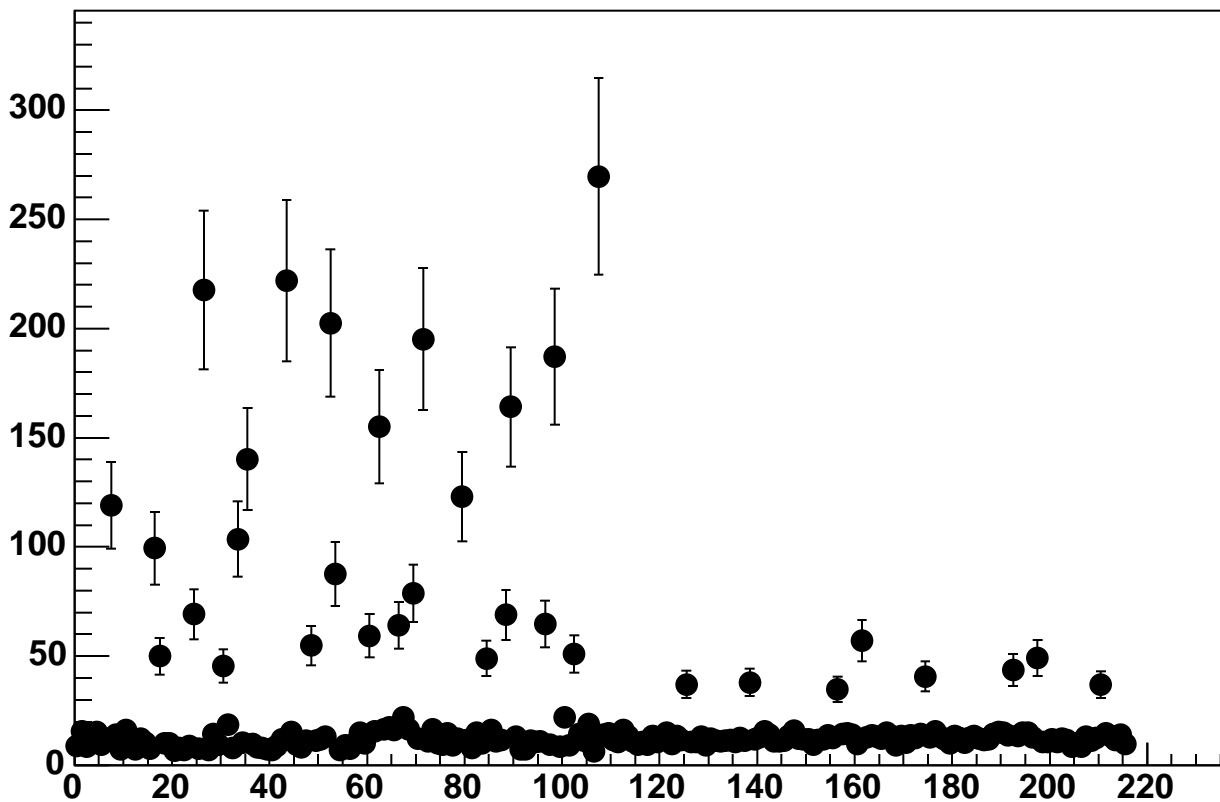




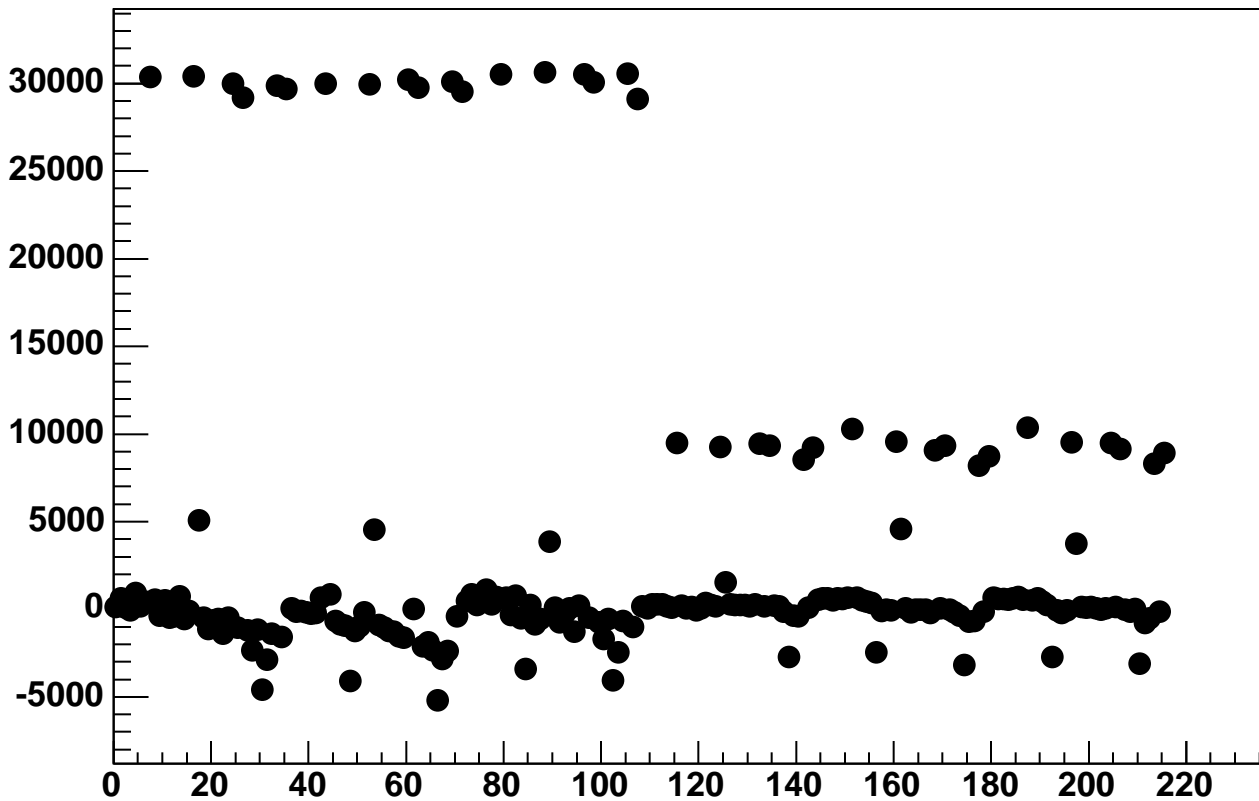
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18\*Chip+Chan



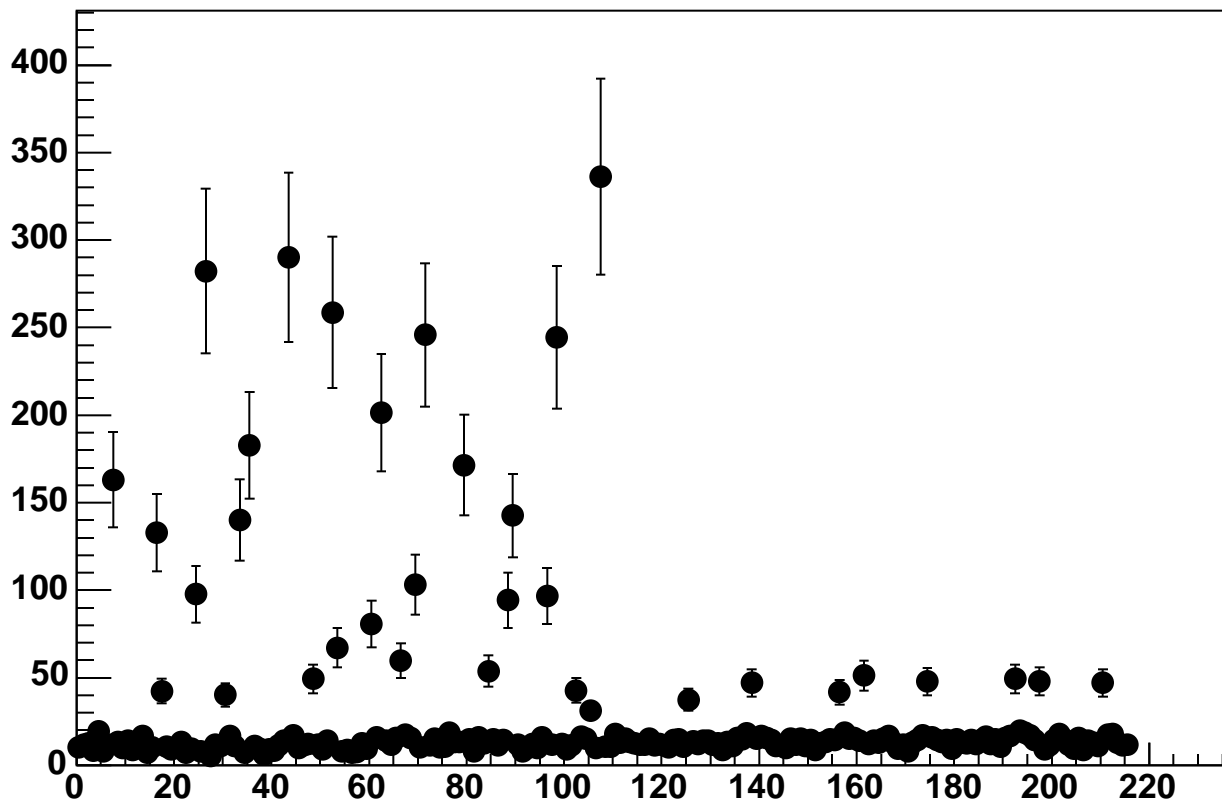
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18\*Chip+Chan



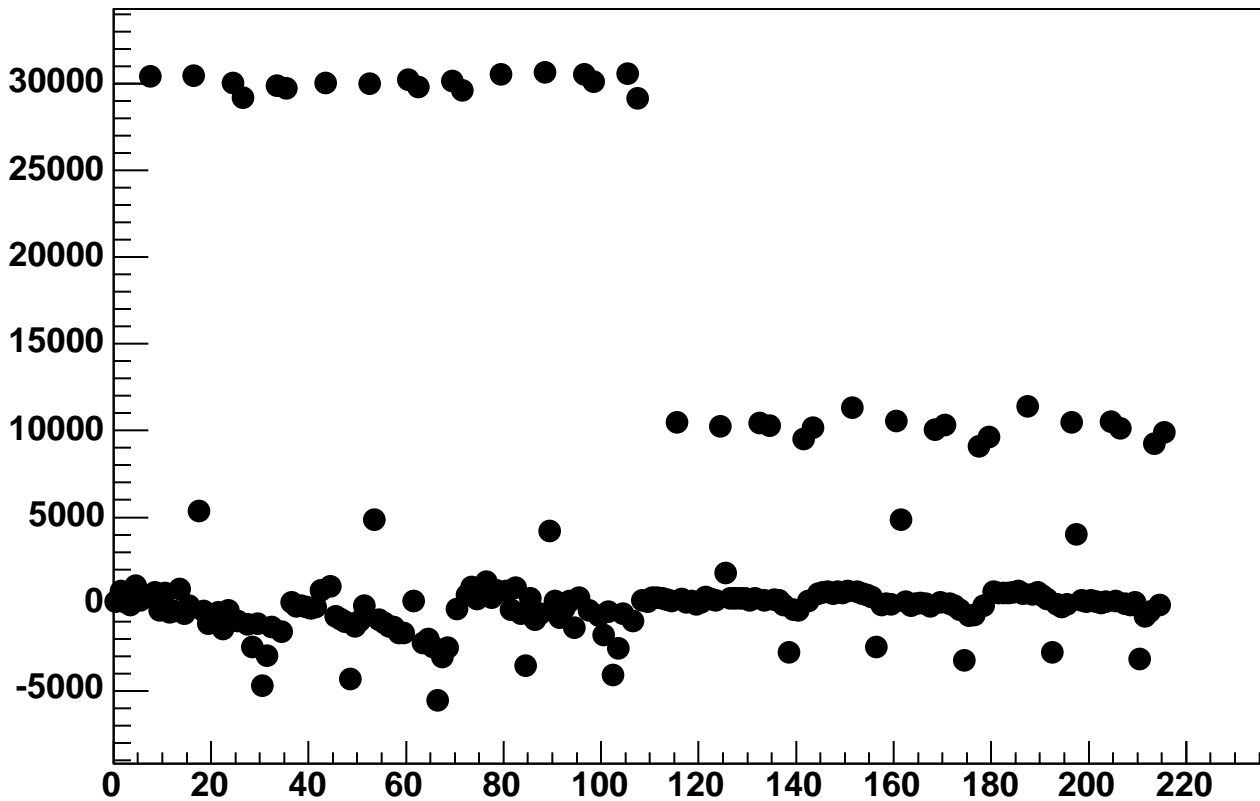
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18\*Chip+Chan



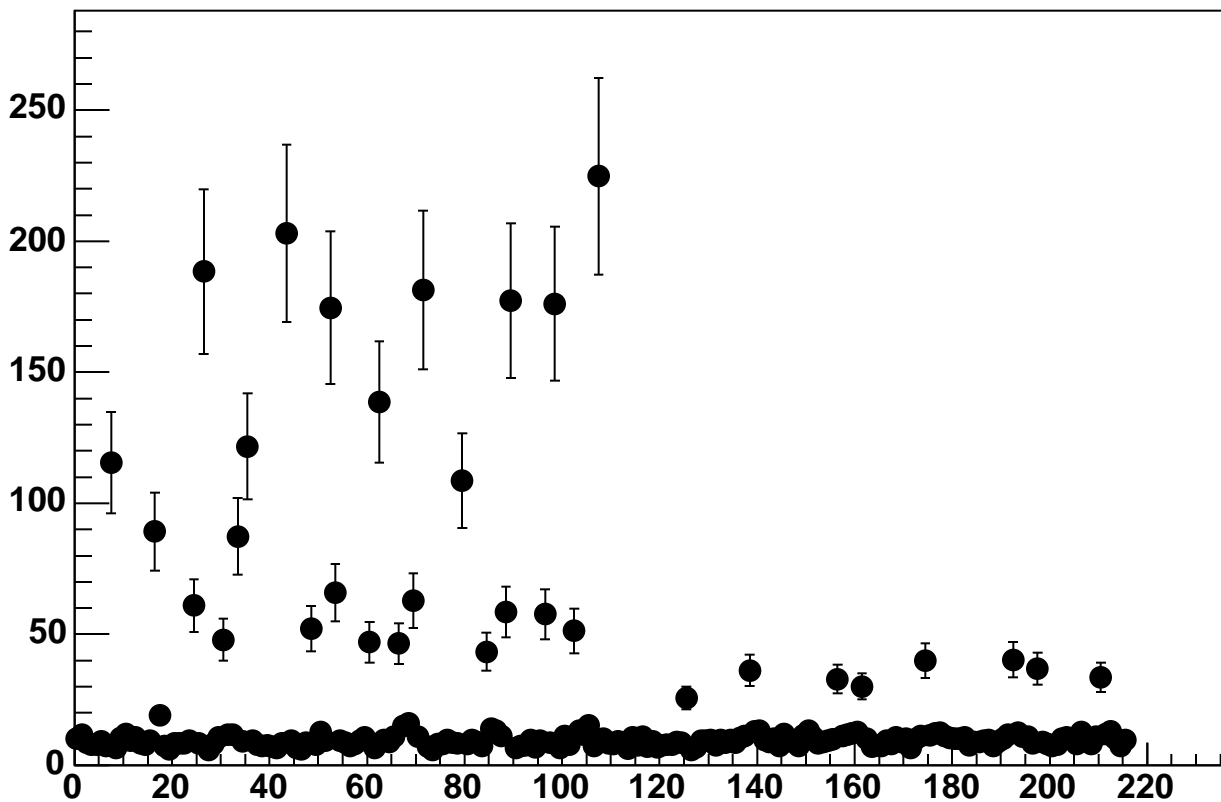
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18\*Chip+Chan



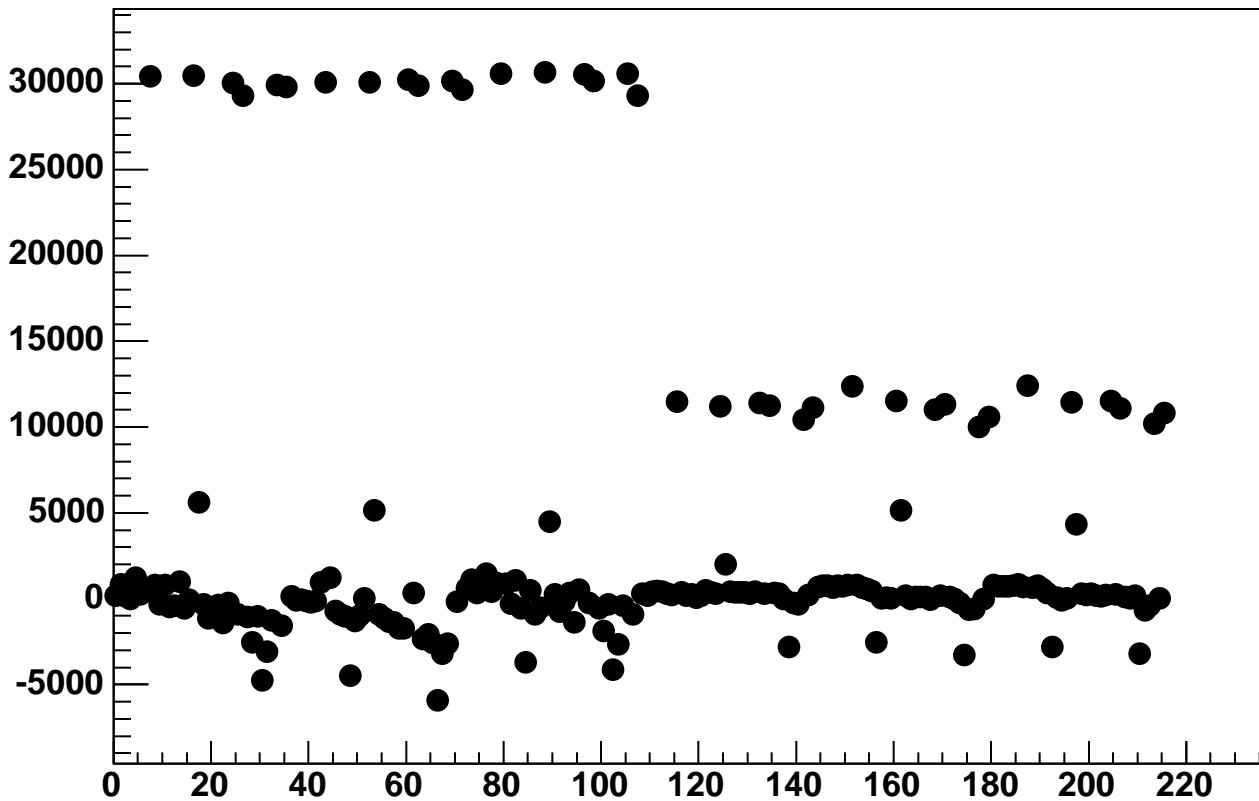
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18\*Chip+Chan



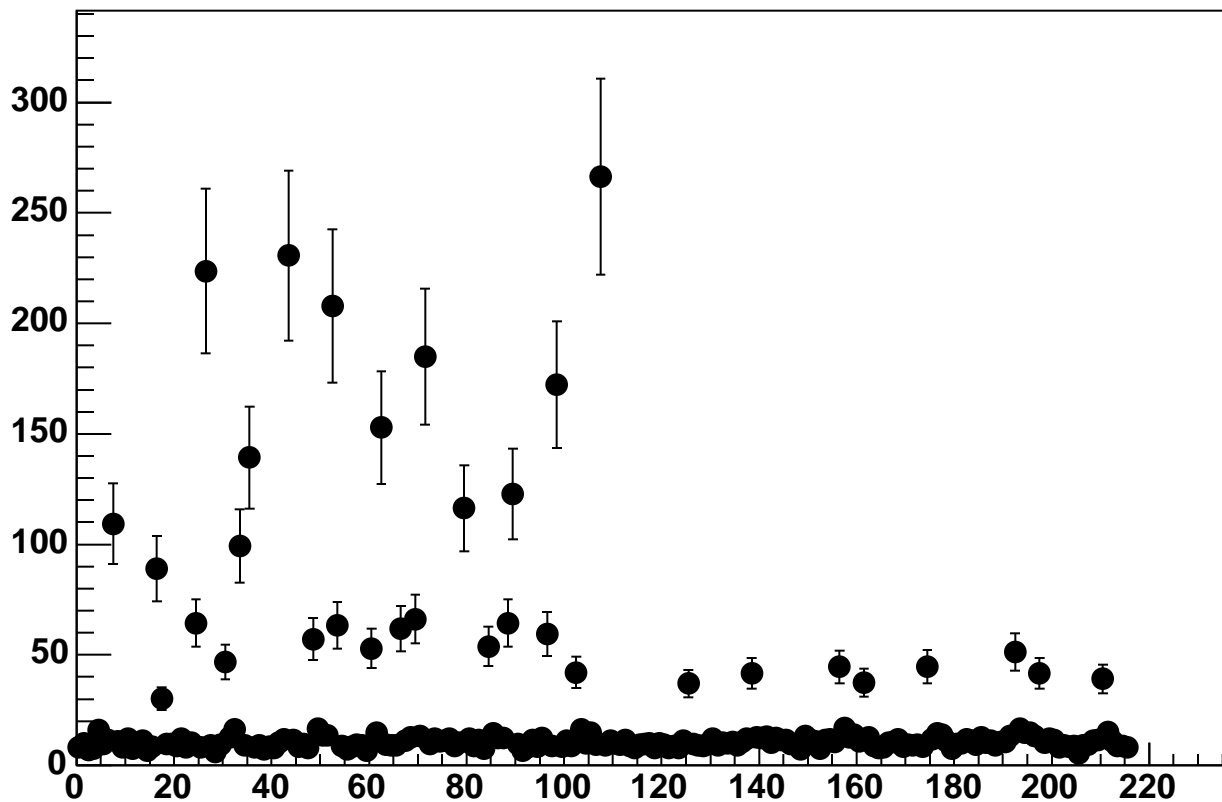
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18\*Chip+Chan



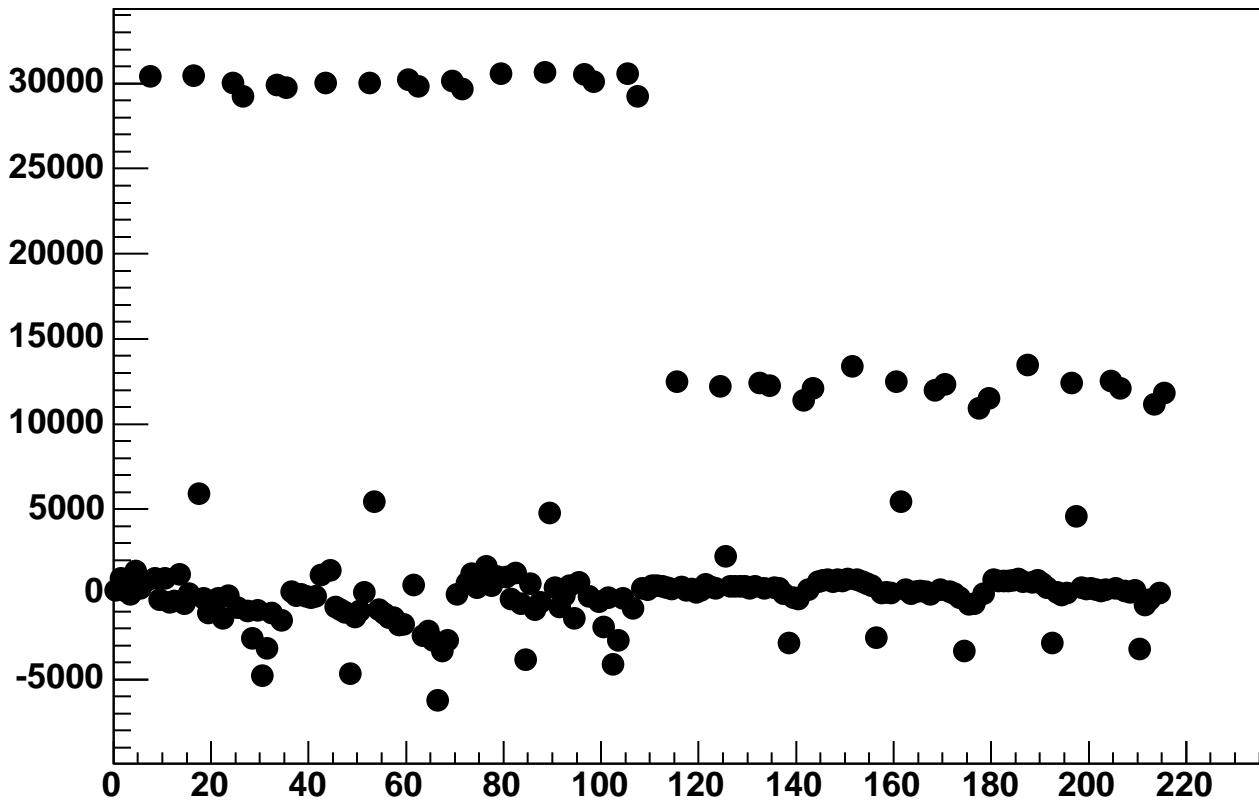
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18\*Chip+Chan



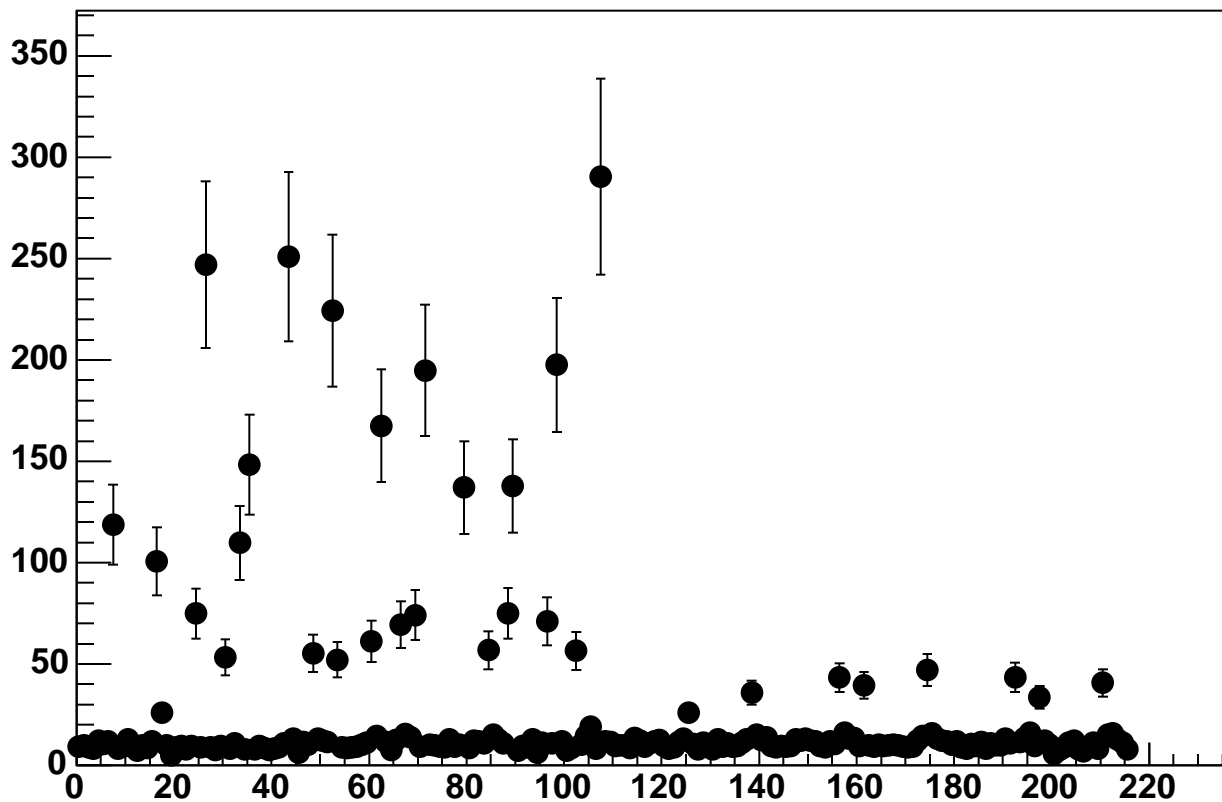
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18\*Chip+Chan



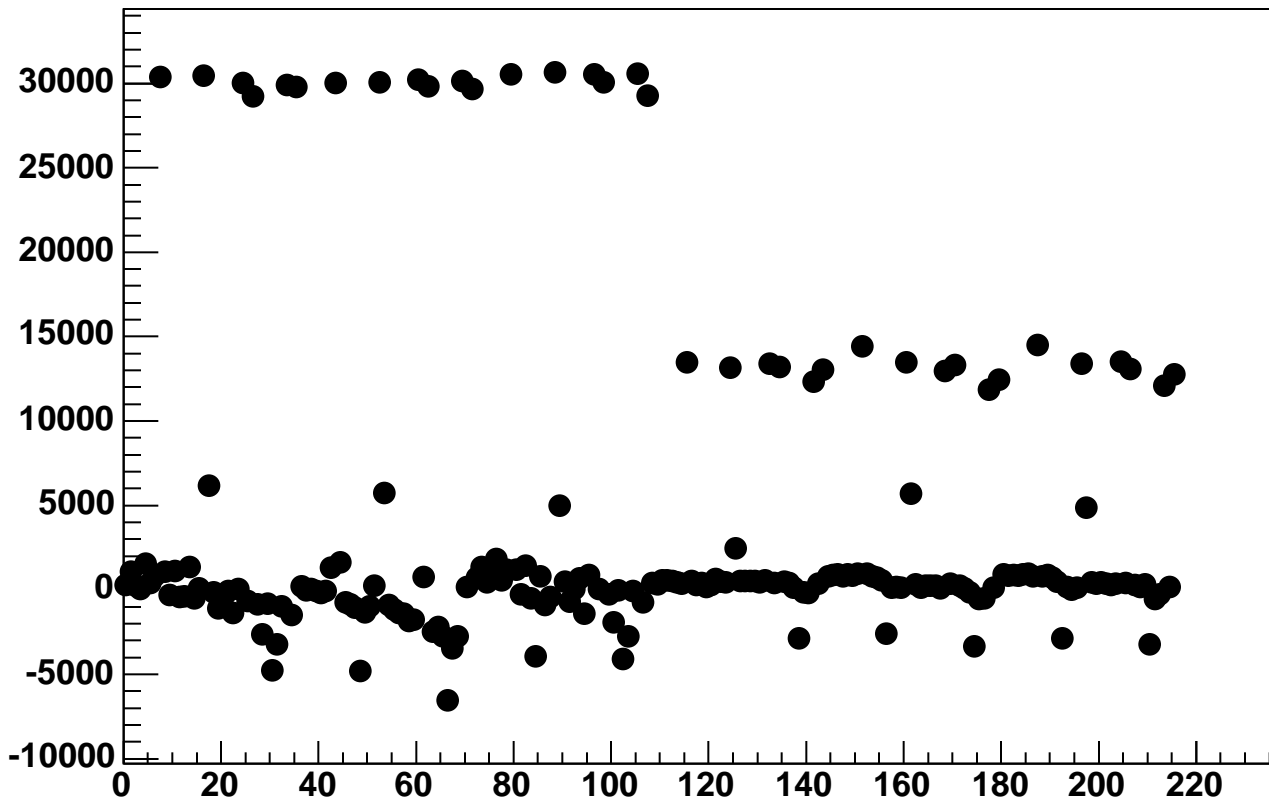
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18\*Chip+Chan



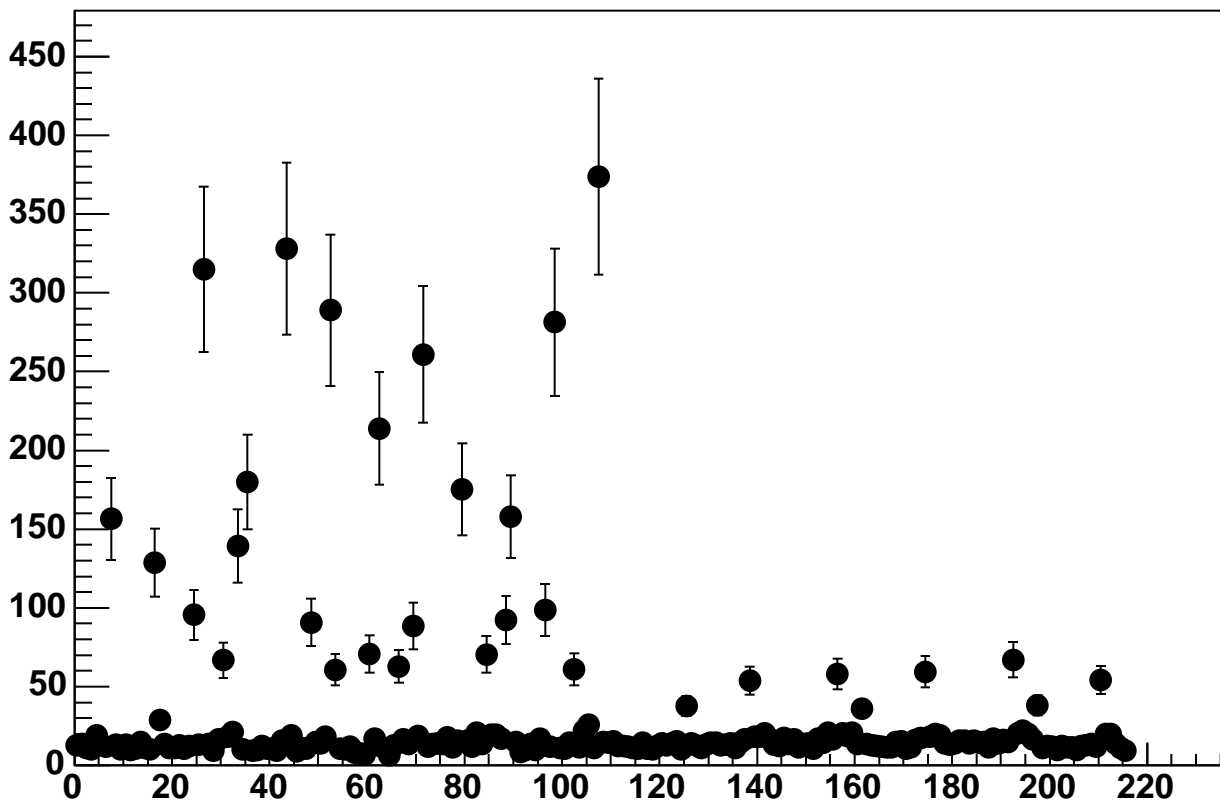
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18\*Chip+Chan



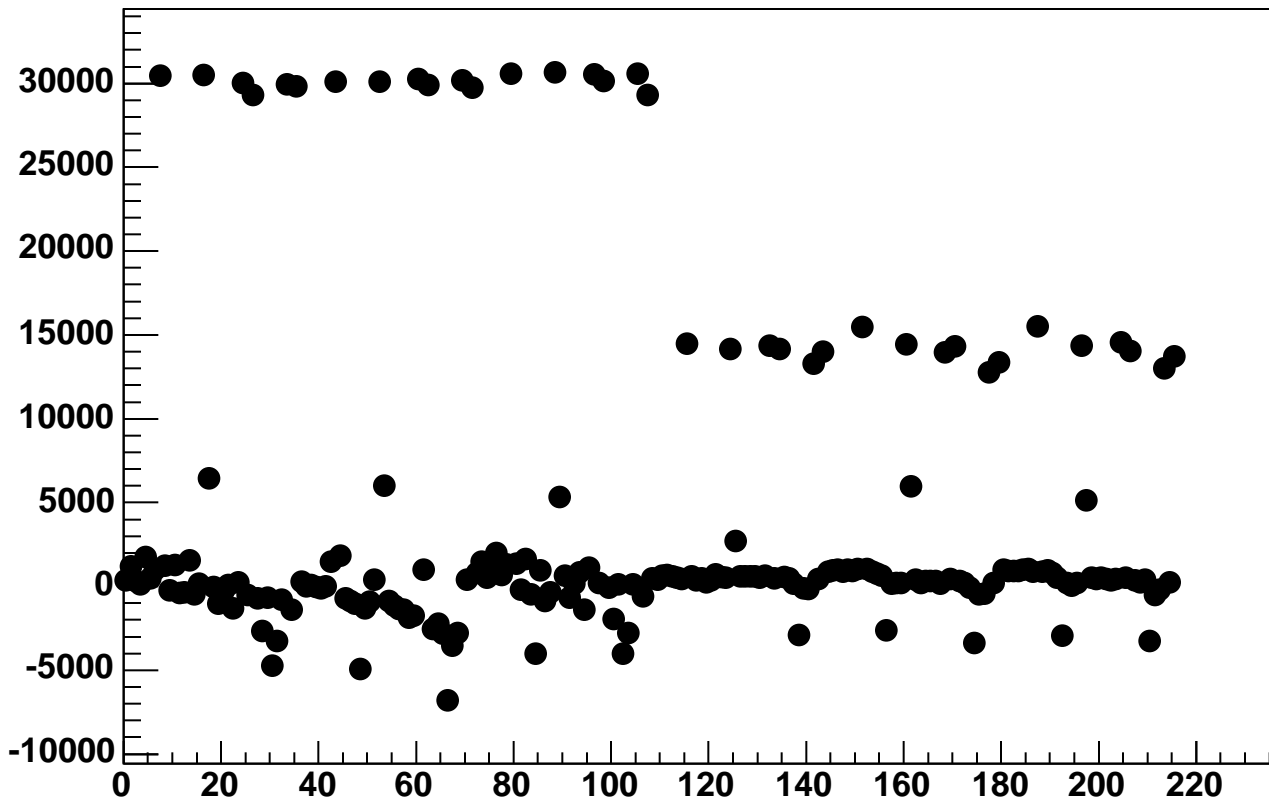
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18\*Chip+Chan



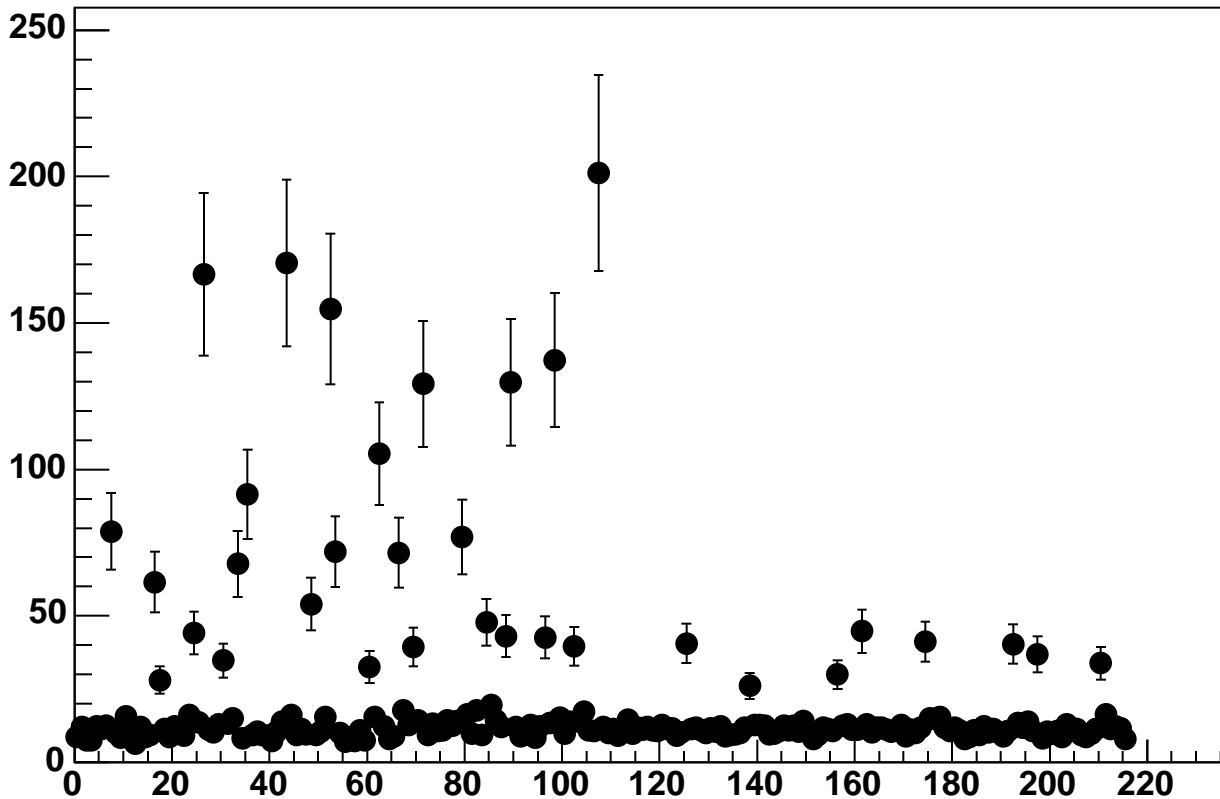
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18\*Chip+Chan



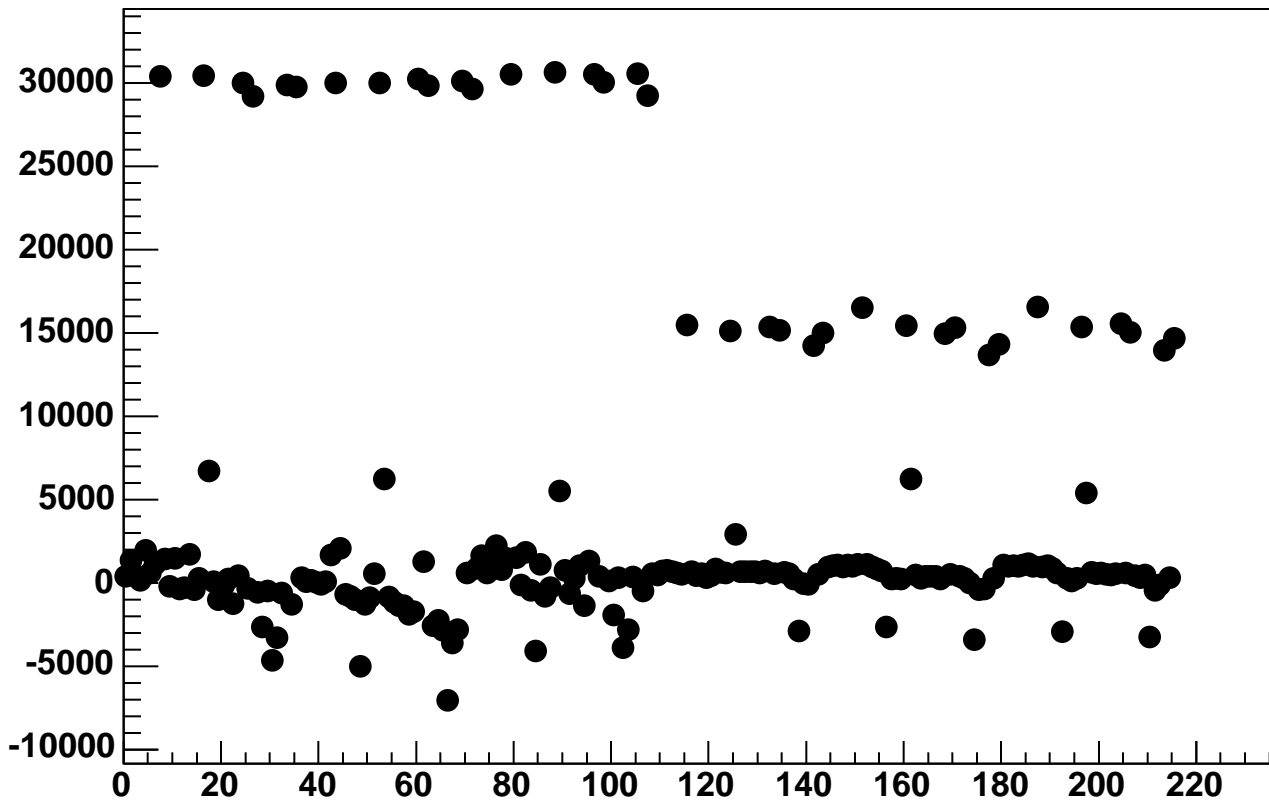
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18\*Chip+Chan



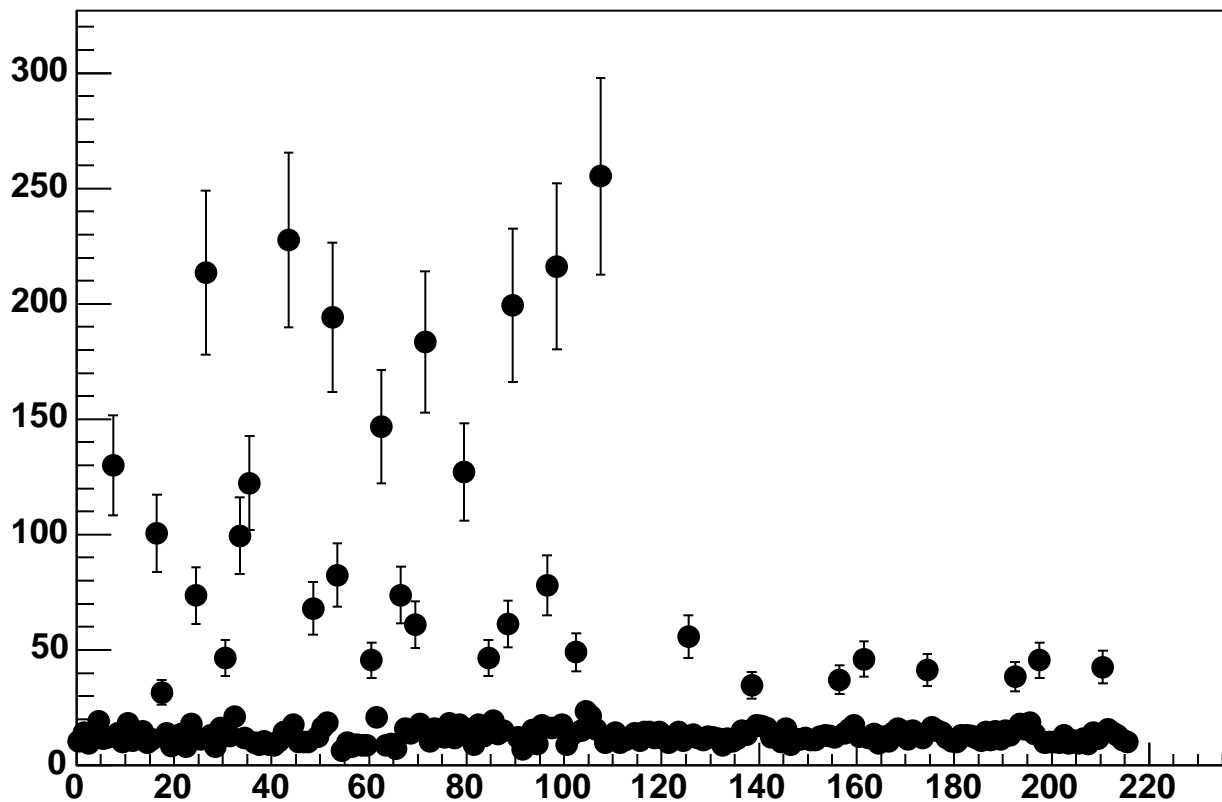
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=35, DAC=6400, ADC Mean vs 18\*Chip+Chan

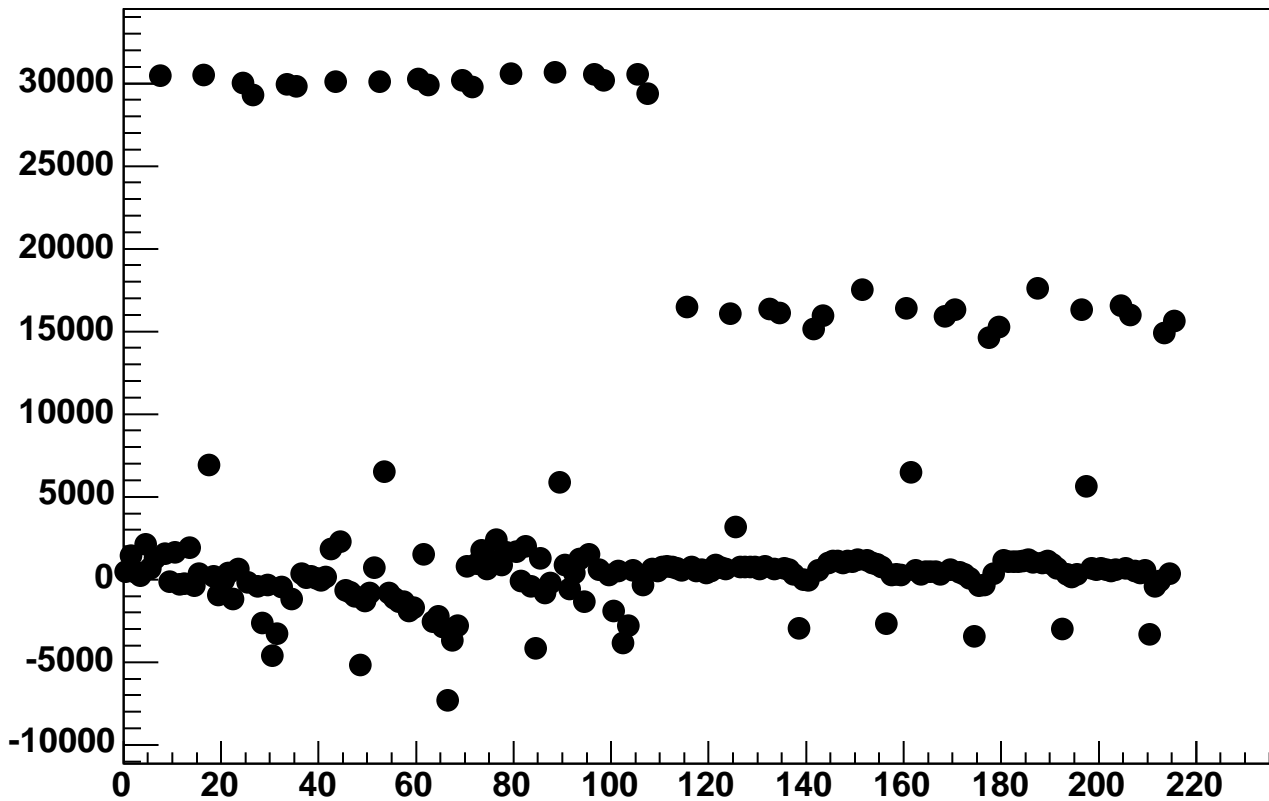


Enable 5, Hold=35, DAC=6400, ADC Noise vs 18\*Chip+Chan

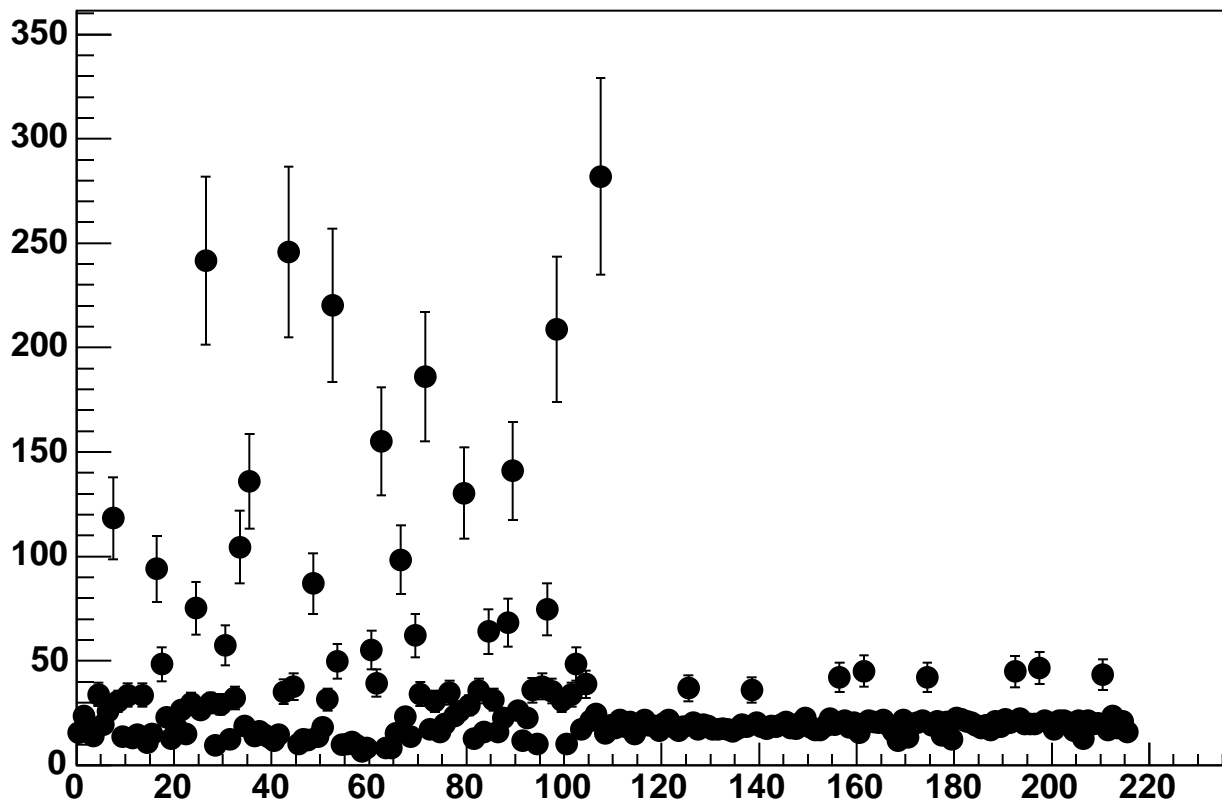




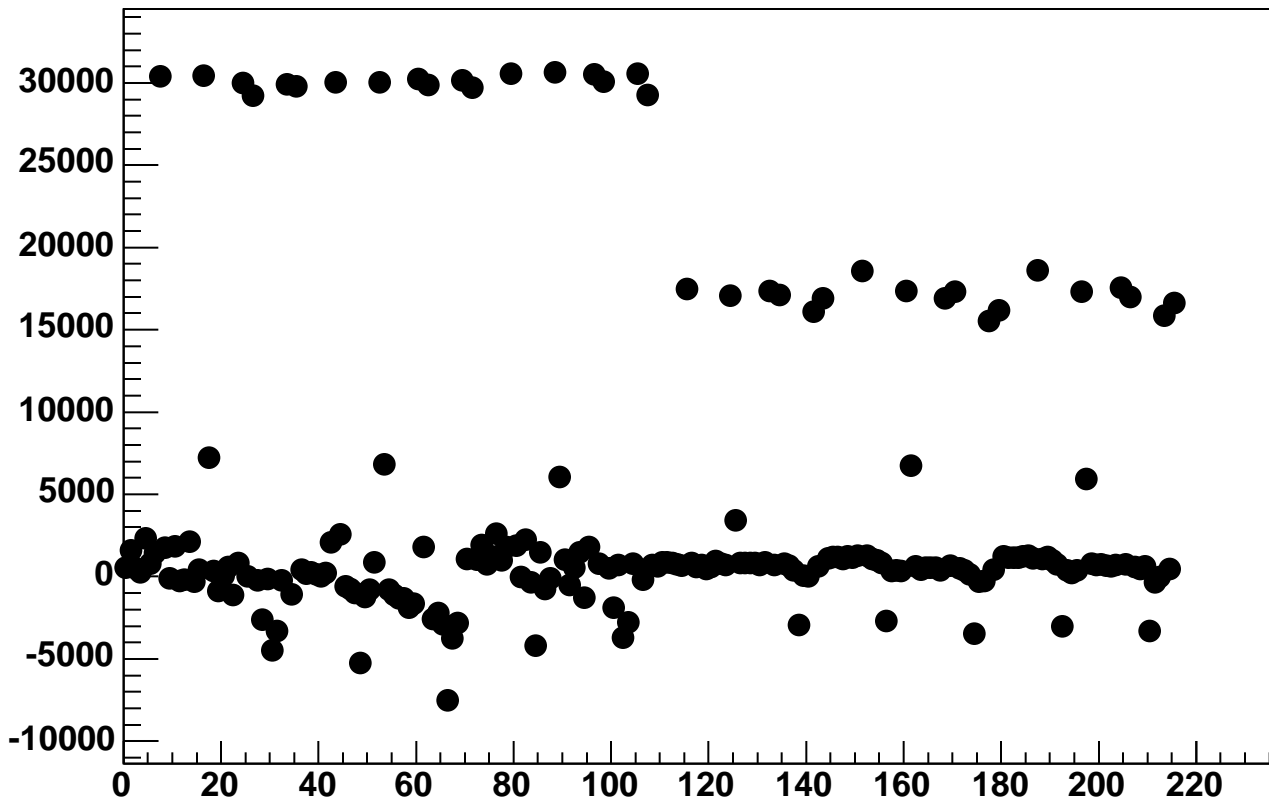
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18\*Chip+Chan



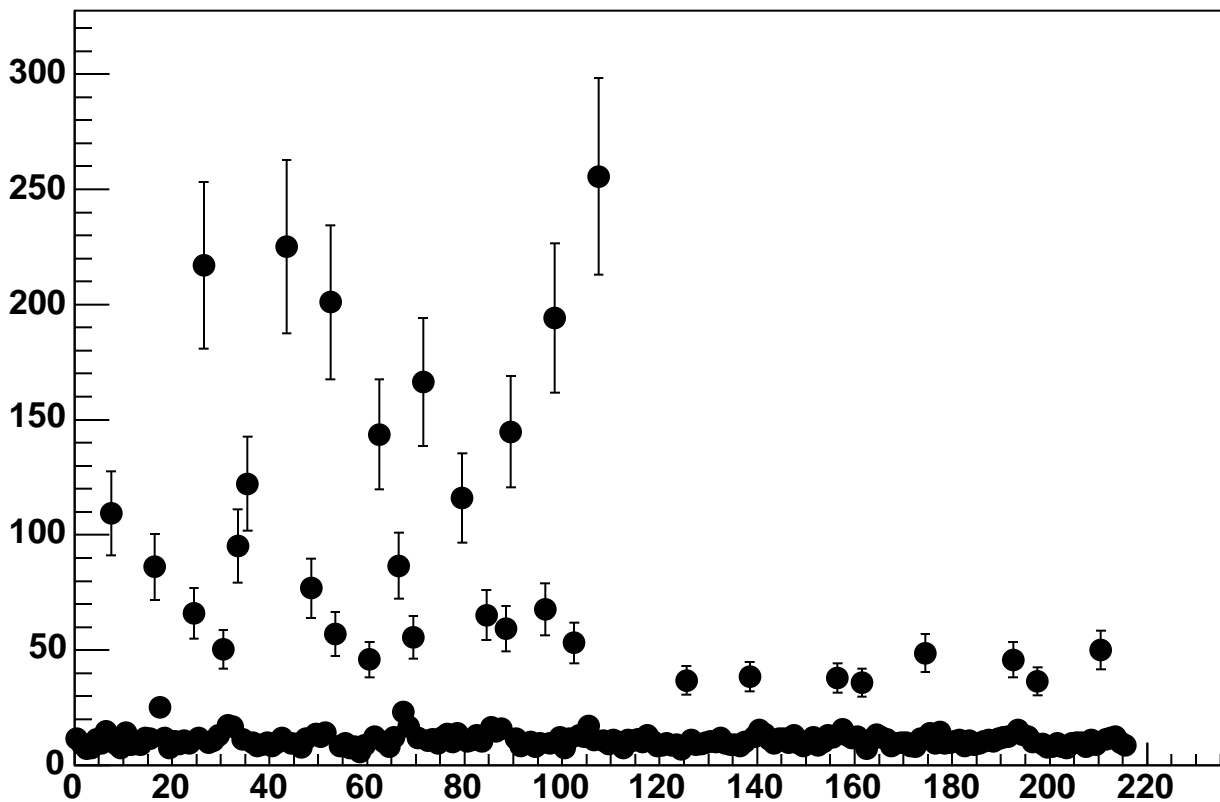
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18\*Chip+Chan



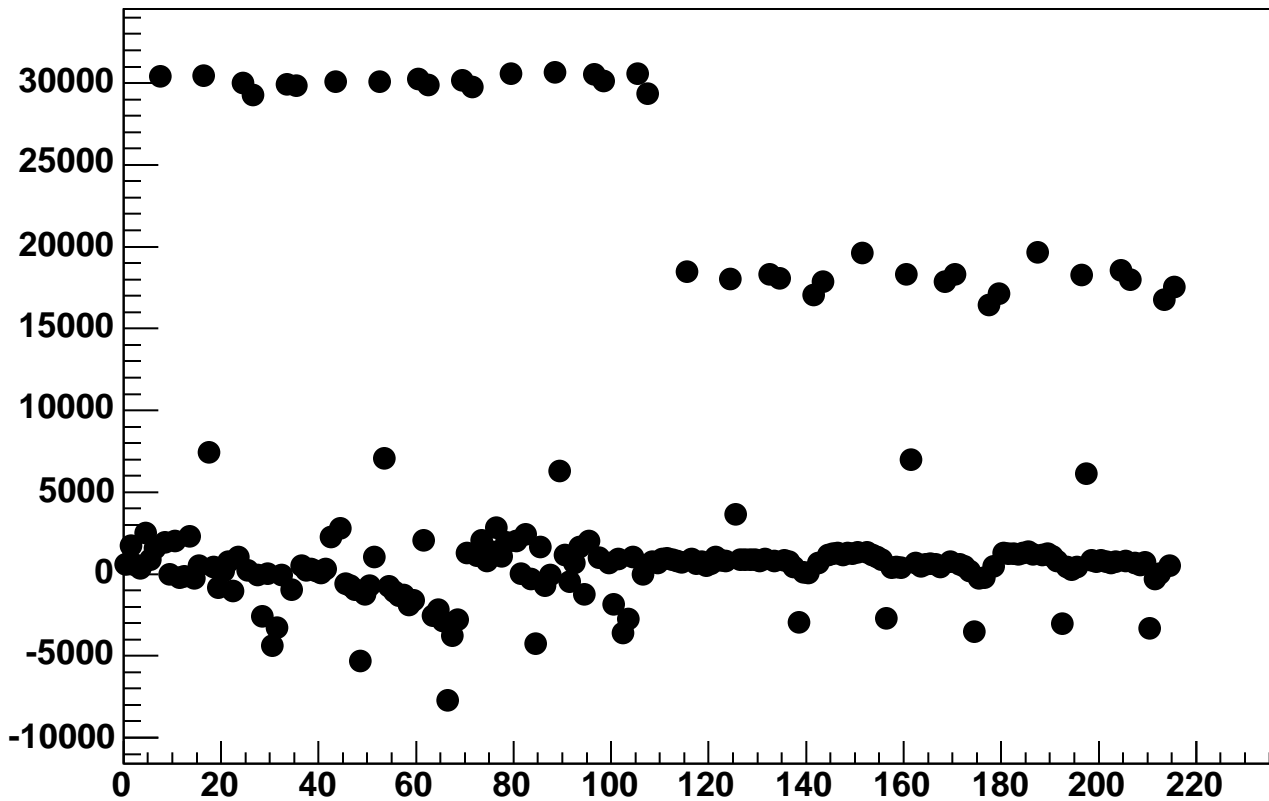
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18\*Chip+Chan



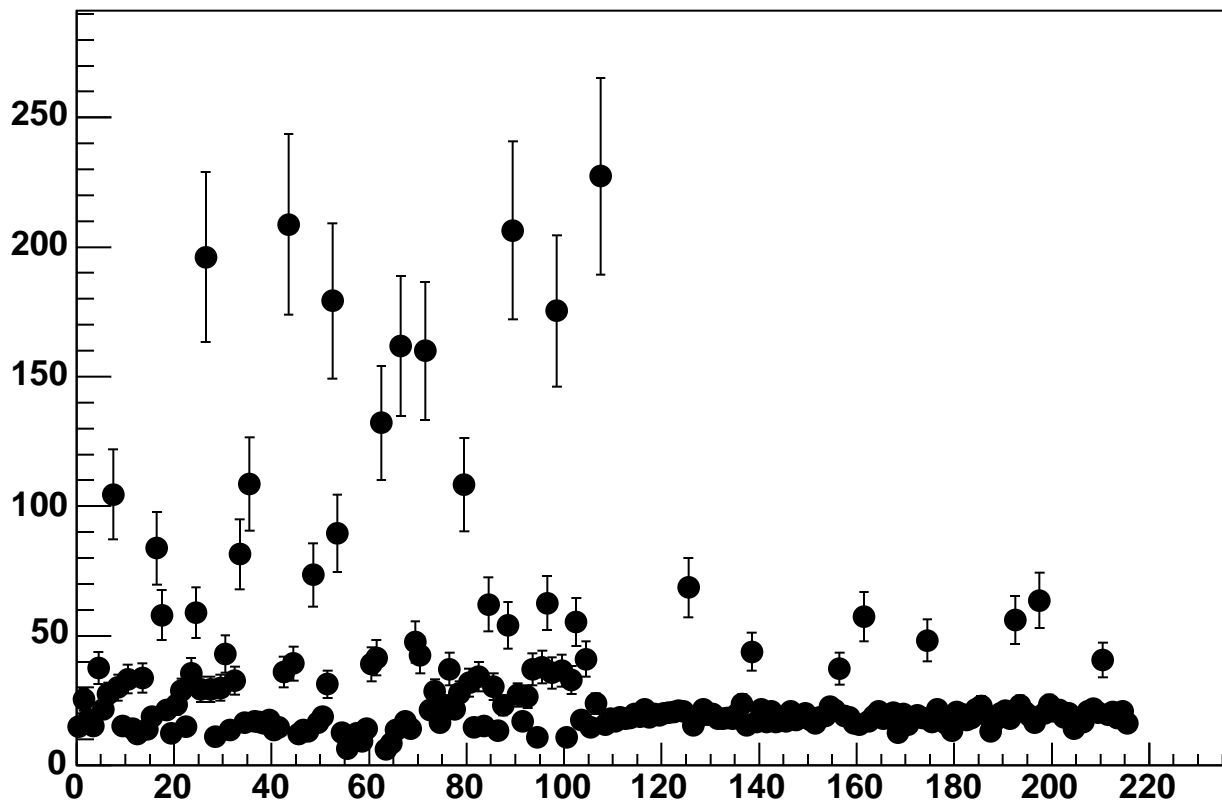
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18\*Chip+Chan



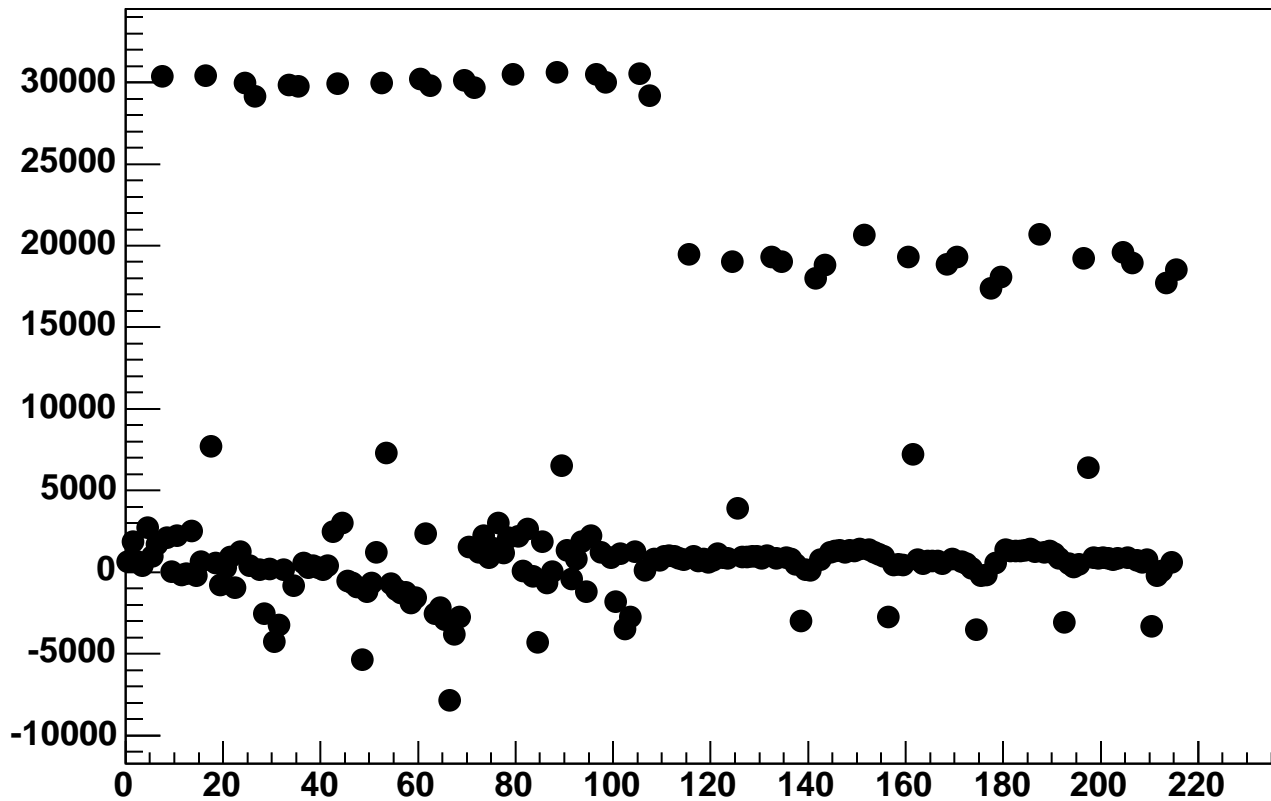
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18\*Chip+Chan



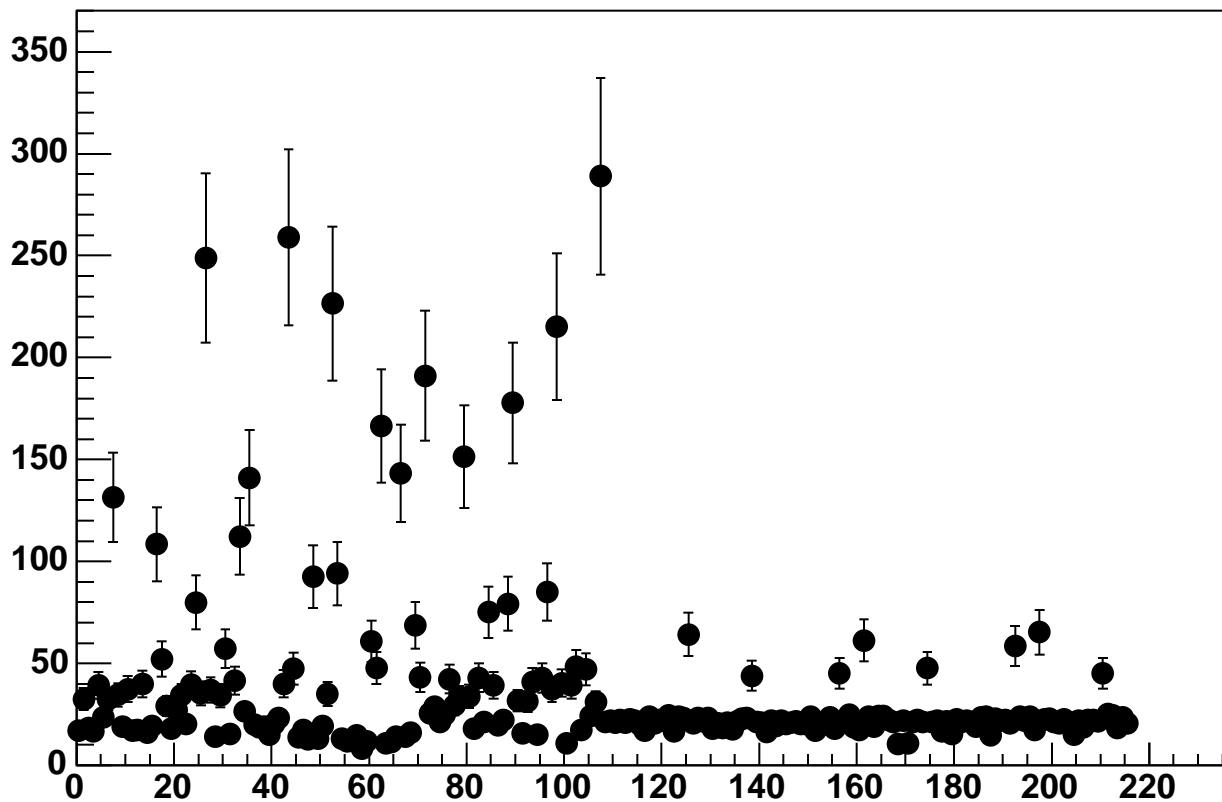
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18\*Chip+Chan



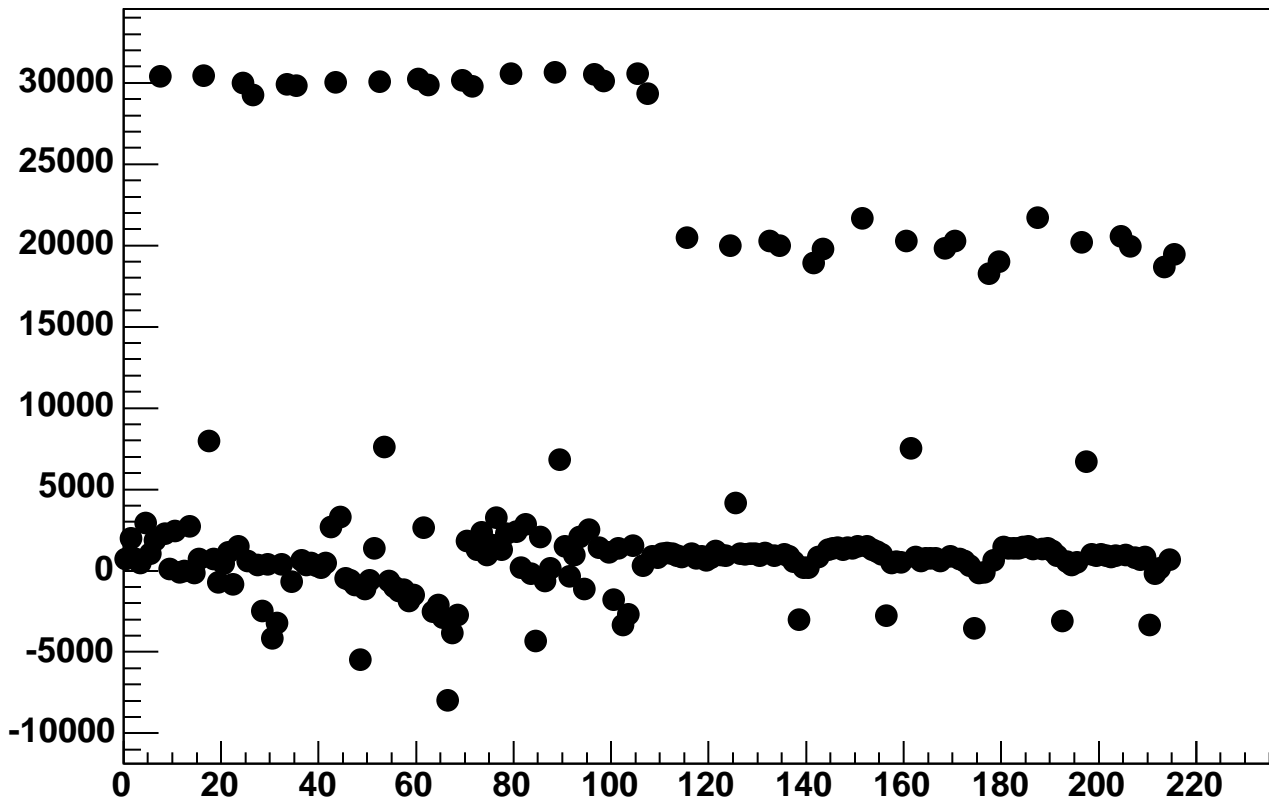
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18\*Chip+Chan



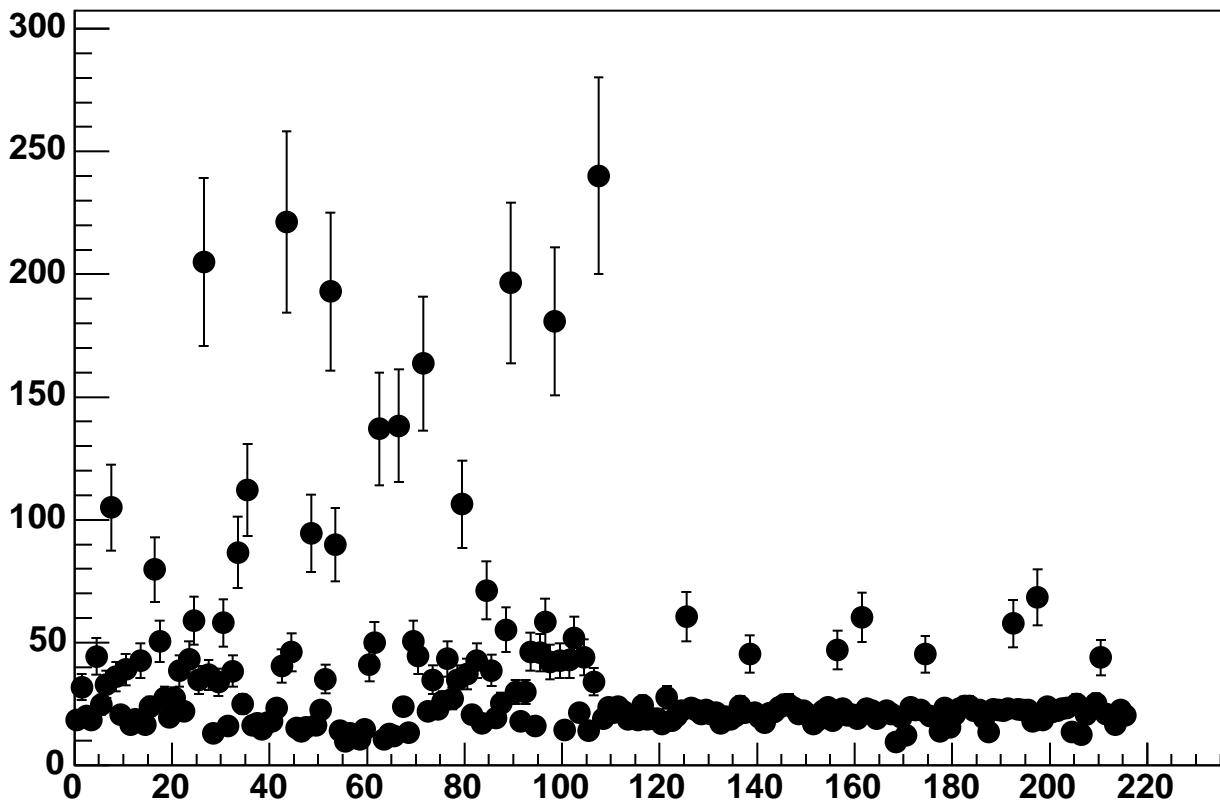
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18\*Chip+Chan



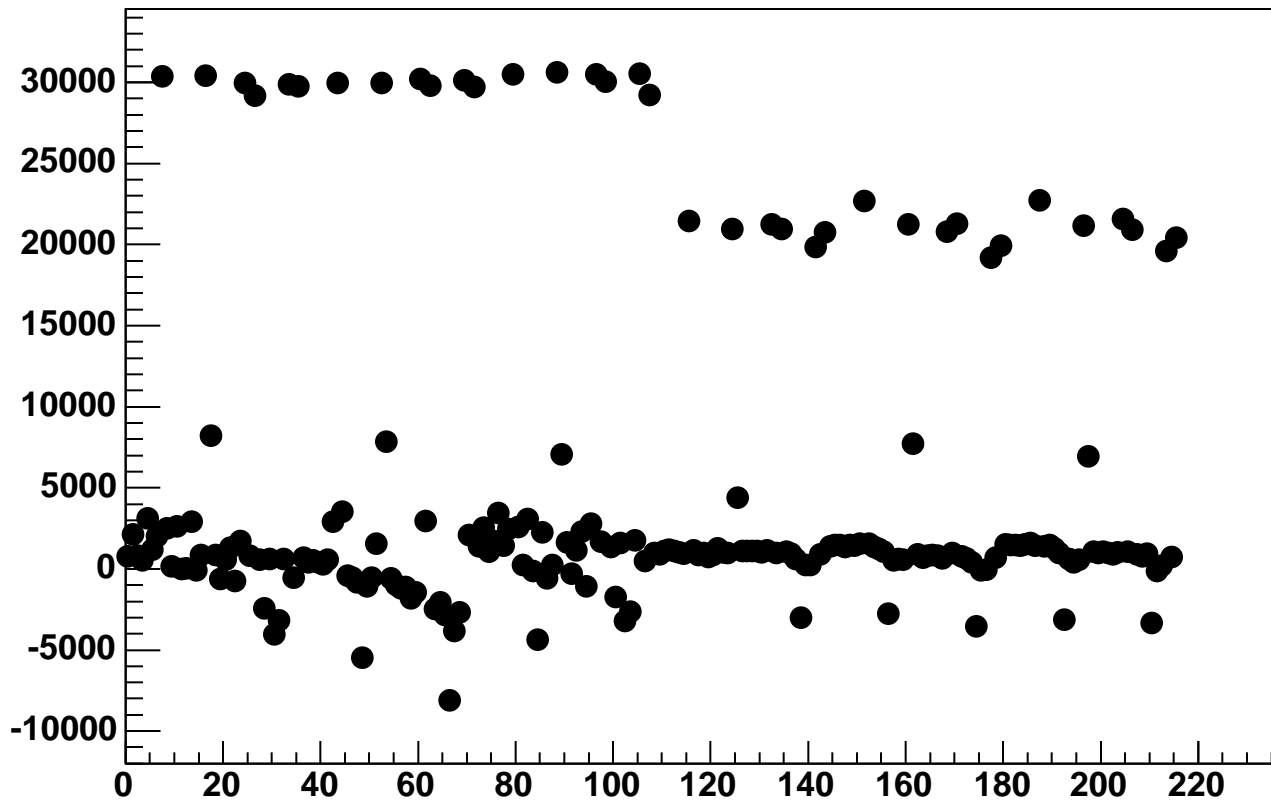
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18\*Chip+Chan



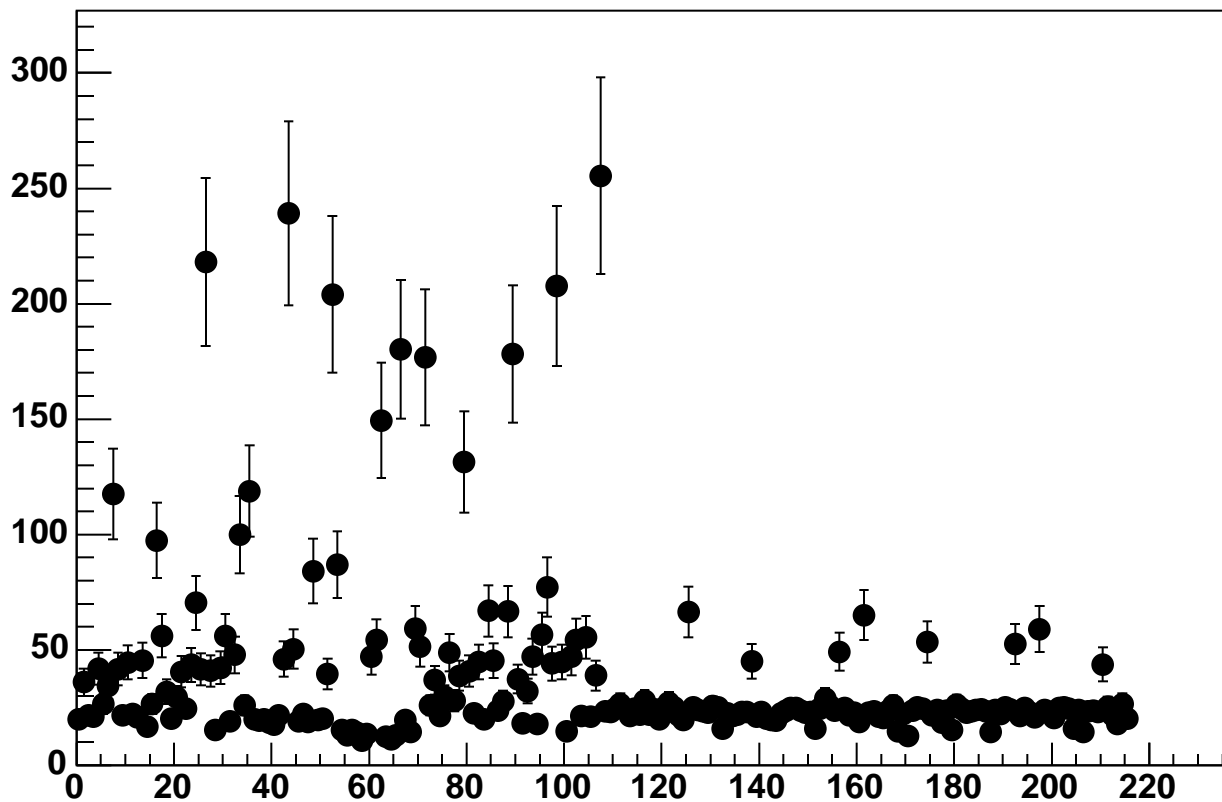
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18\*Chip+Chan



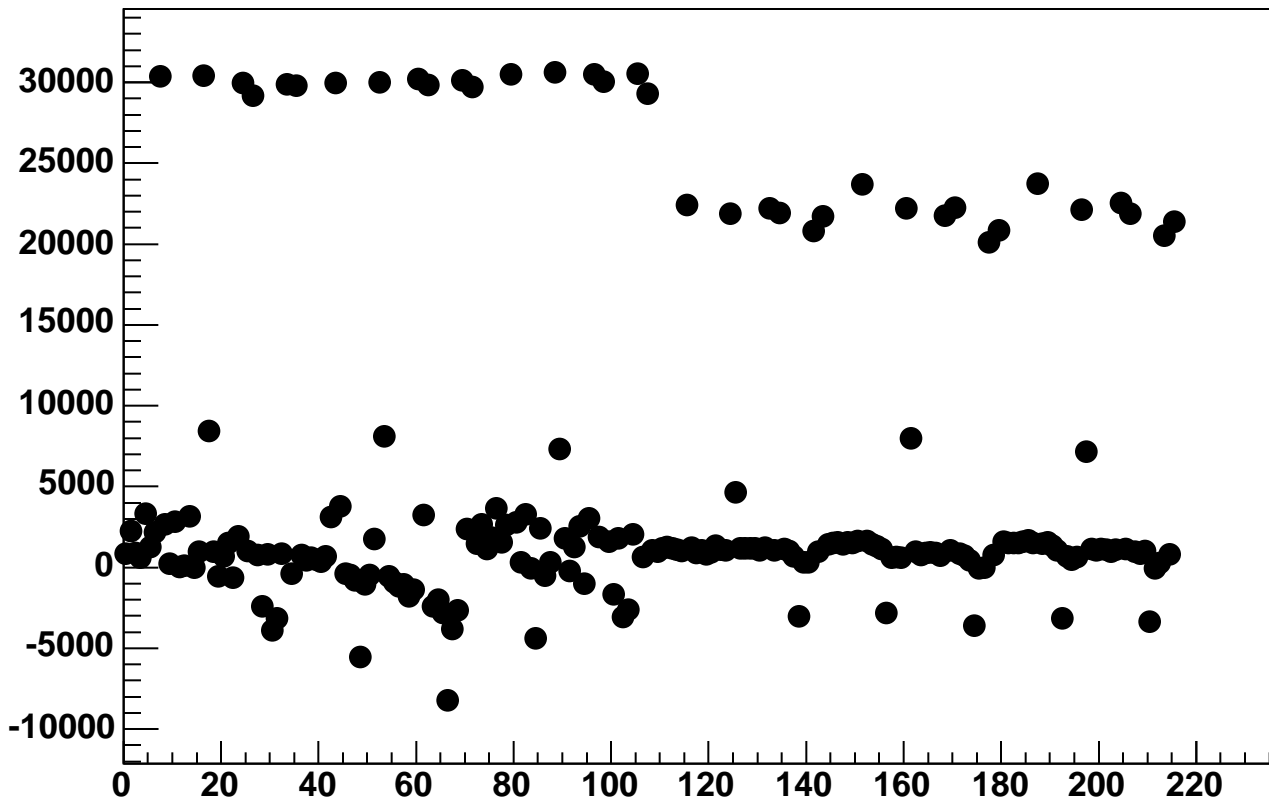
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18\*Chip+Chan



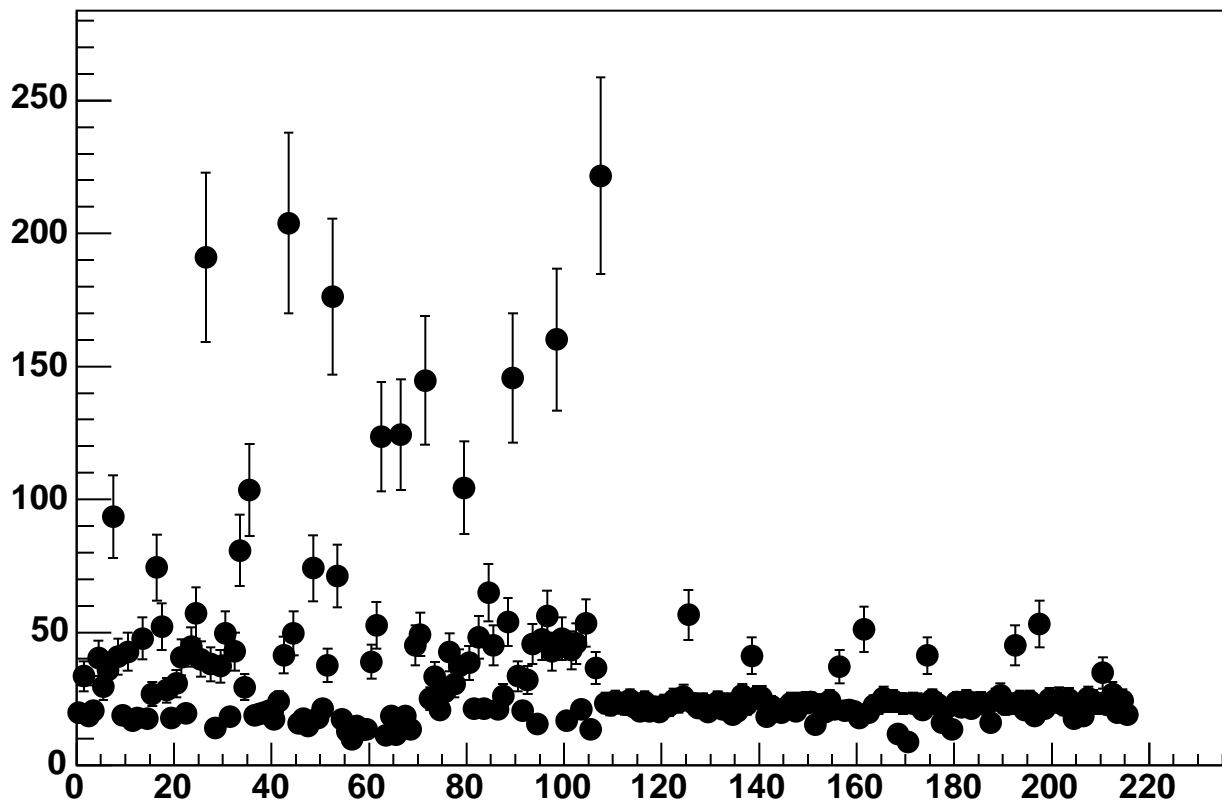
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18\*Chip+Chan



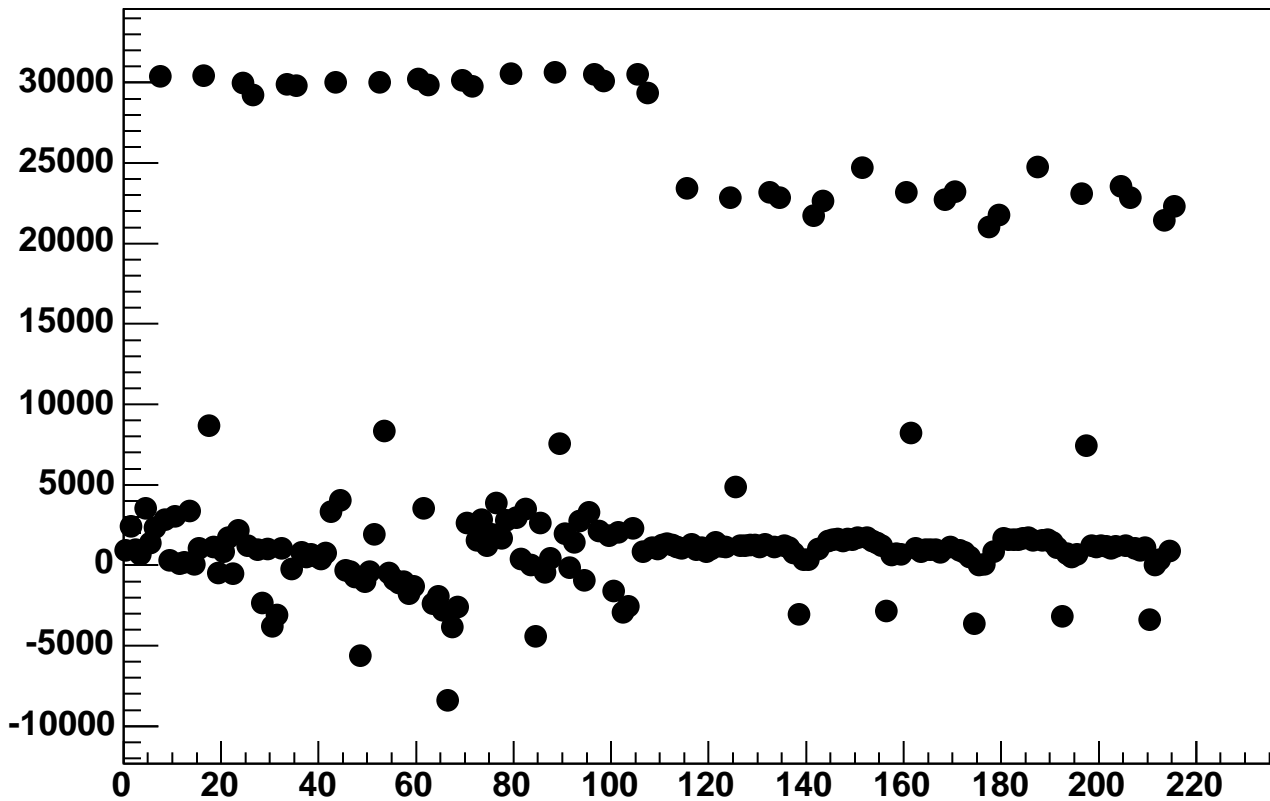
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18\*Chip+Chan



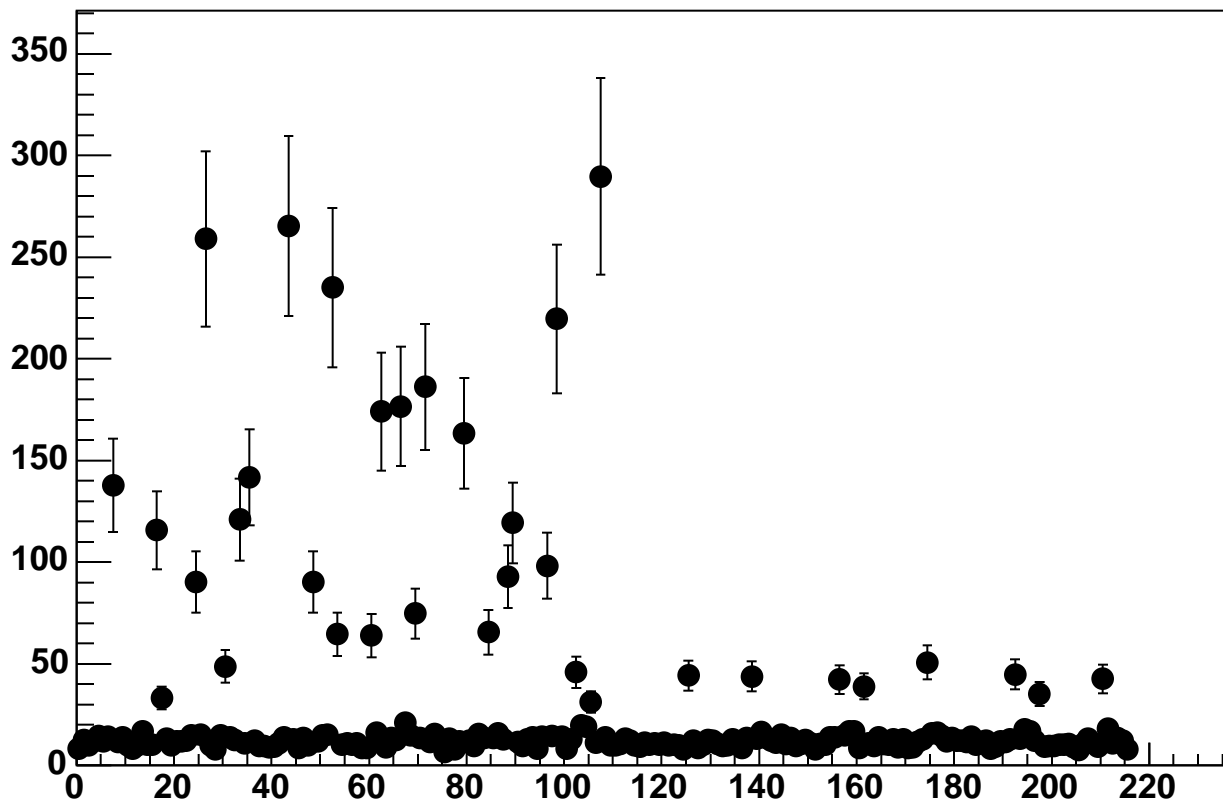
Enable 5, Hold=35, DAC=9200, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=35, DAC=9600, ADC Mean vs 18\*Chip+Chan

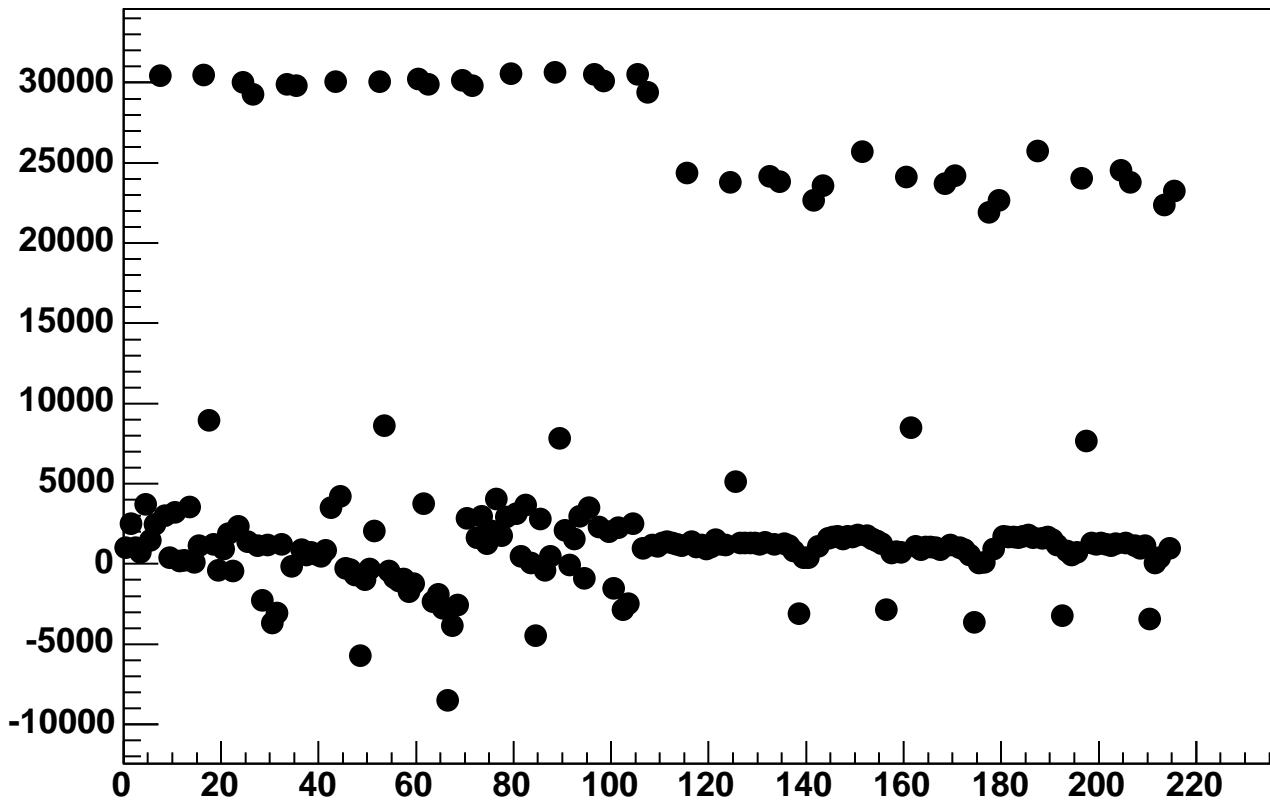


Enable 5, Hold=35, DAC=9600, ADC Noise vs 18\*Chip+Chan

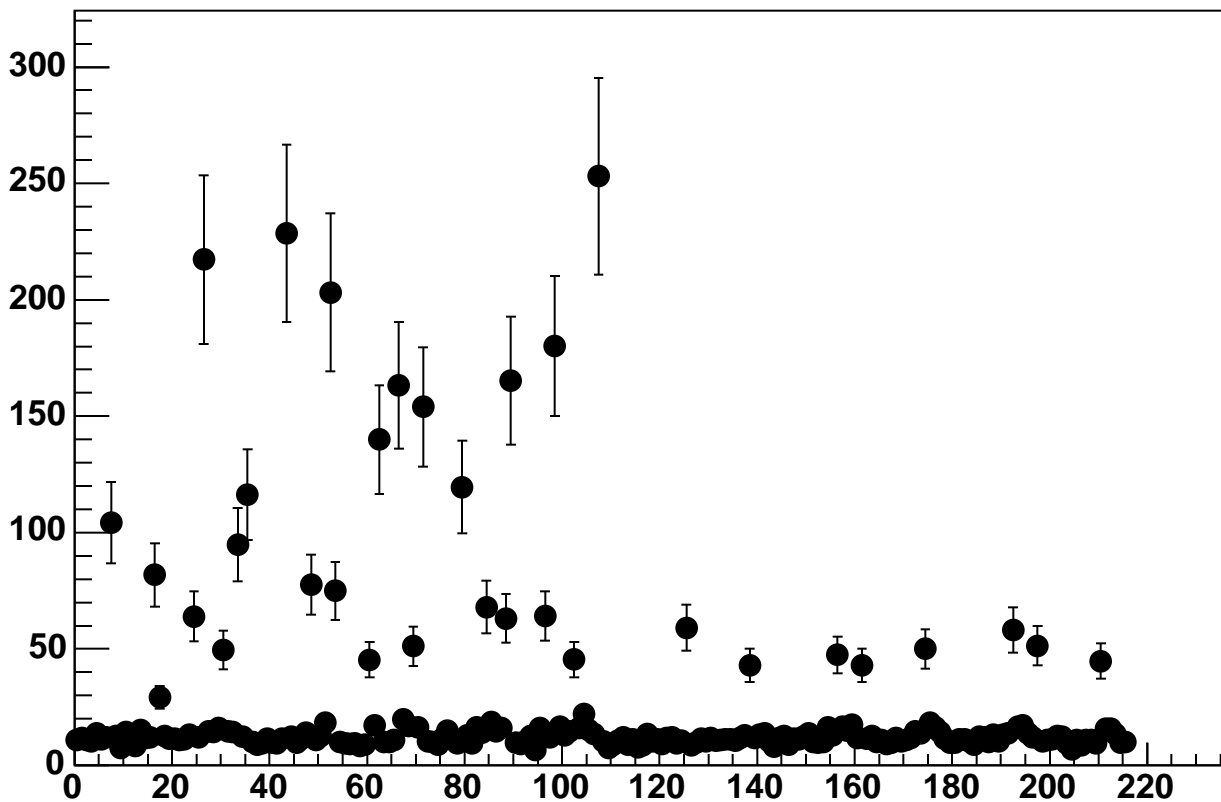




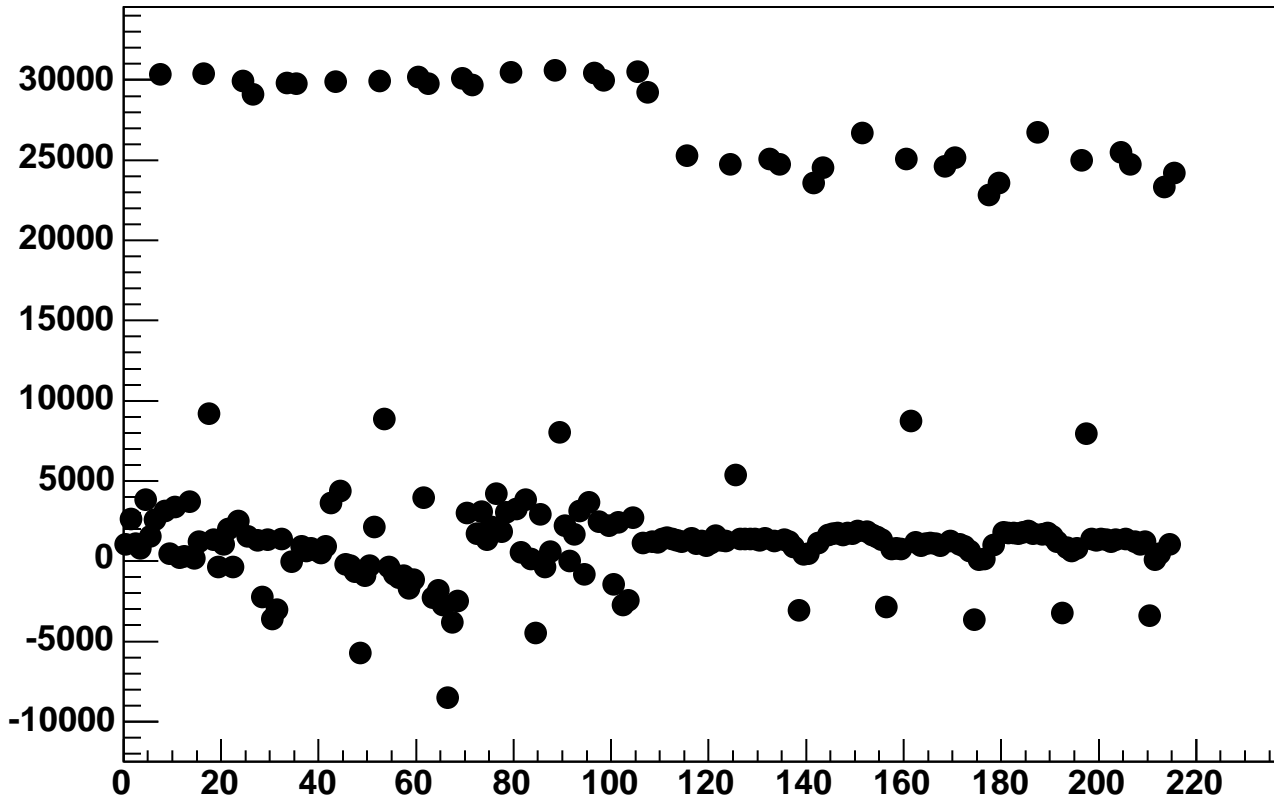
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18\*Chip+Chan



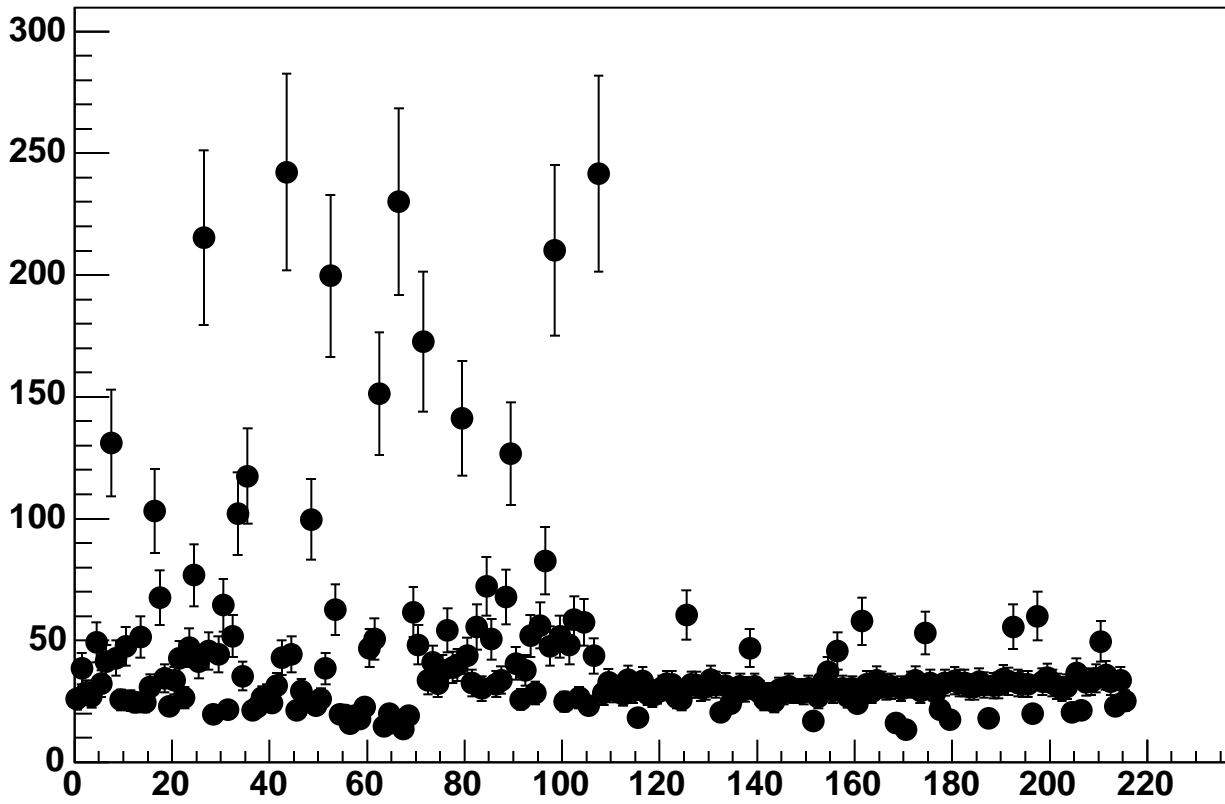
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18\*Chip+Chan



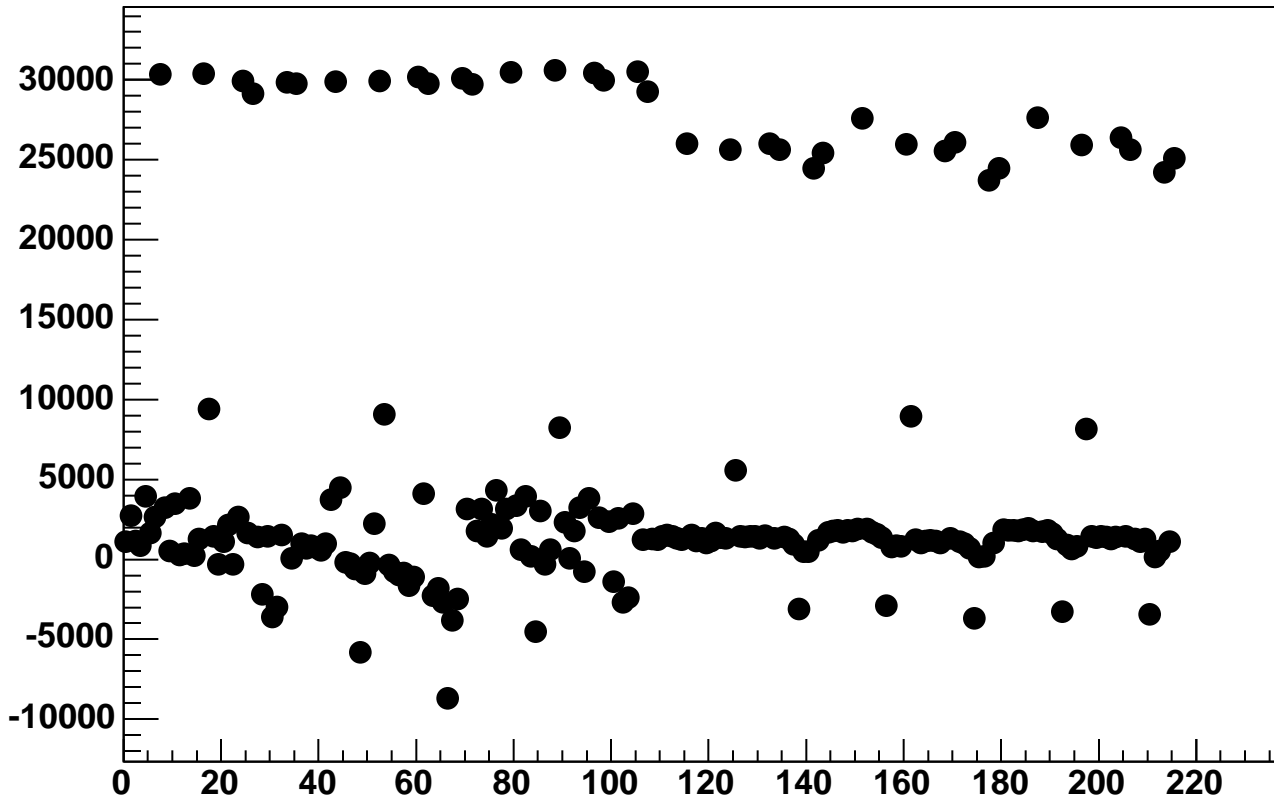
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18\*Chip+Chan



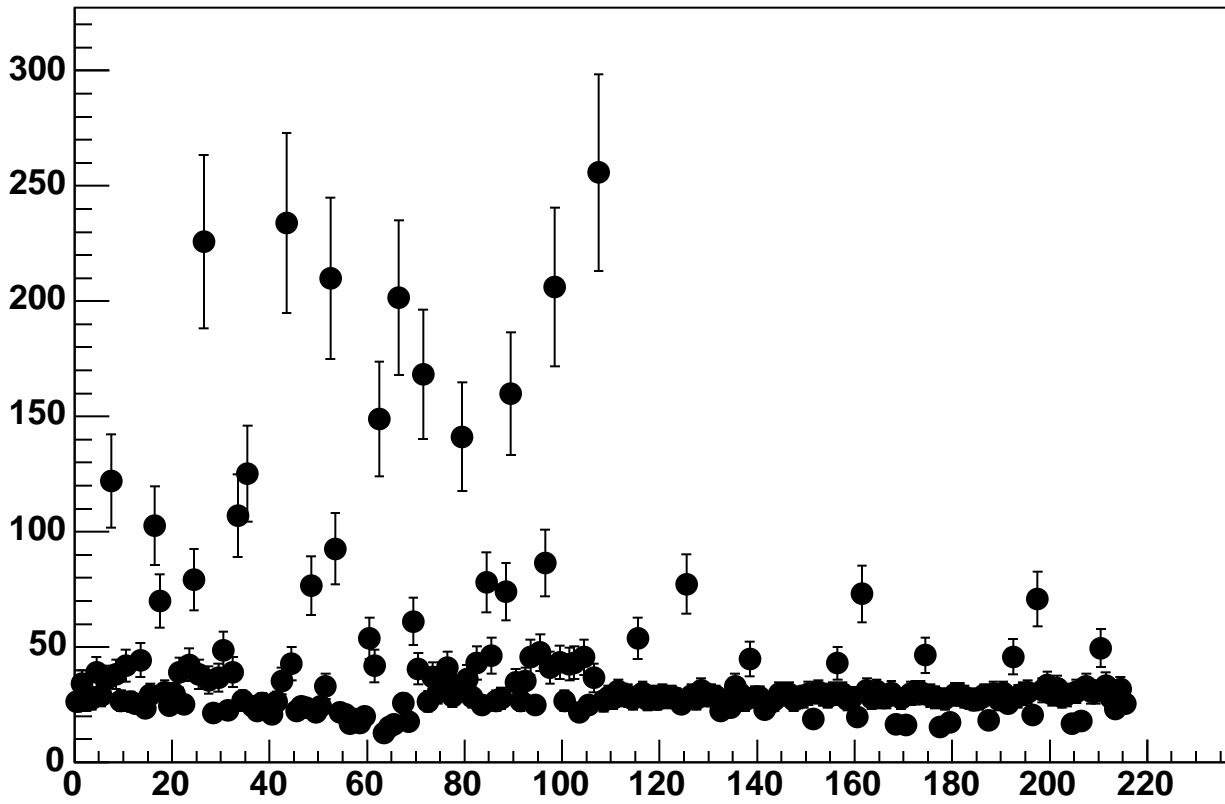
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18\*Chip+Chan



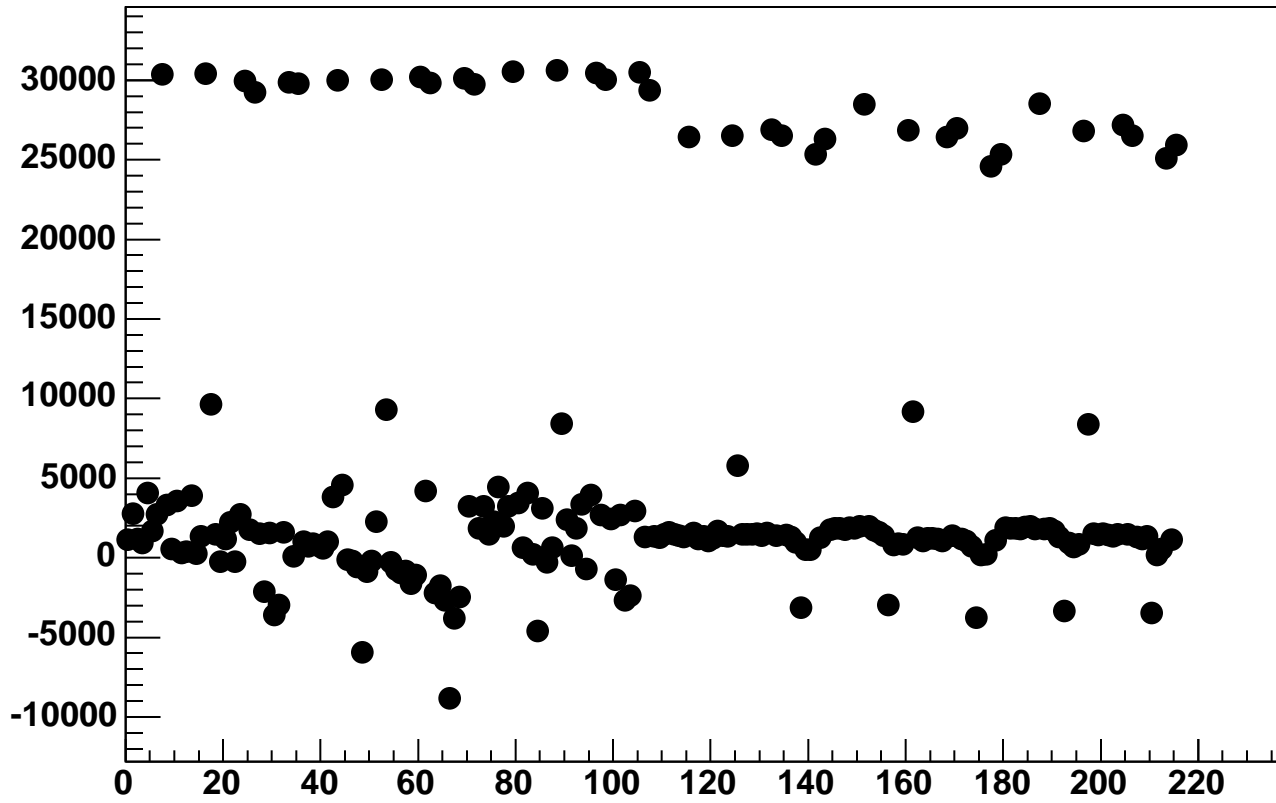
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18\*Chip+Chan



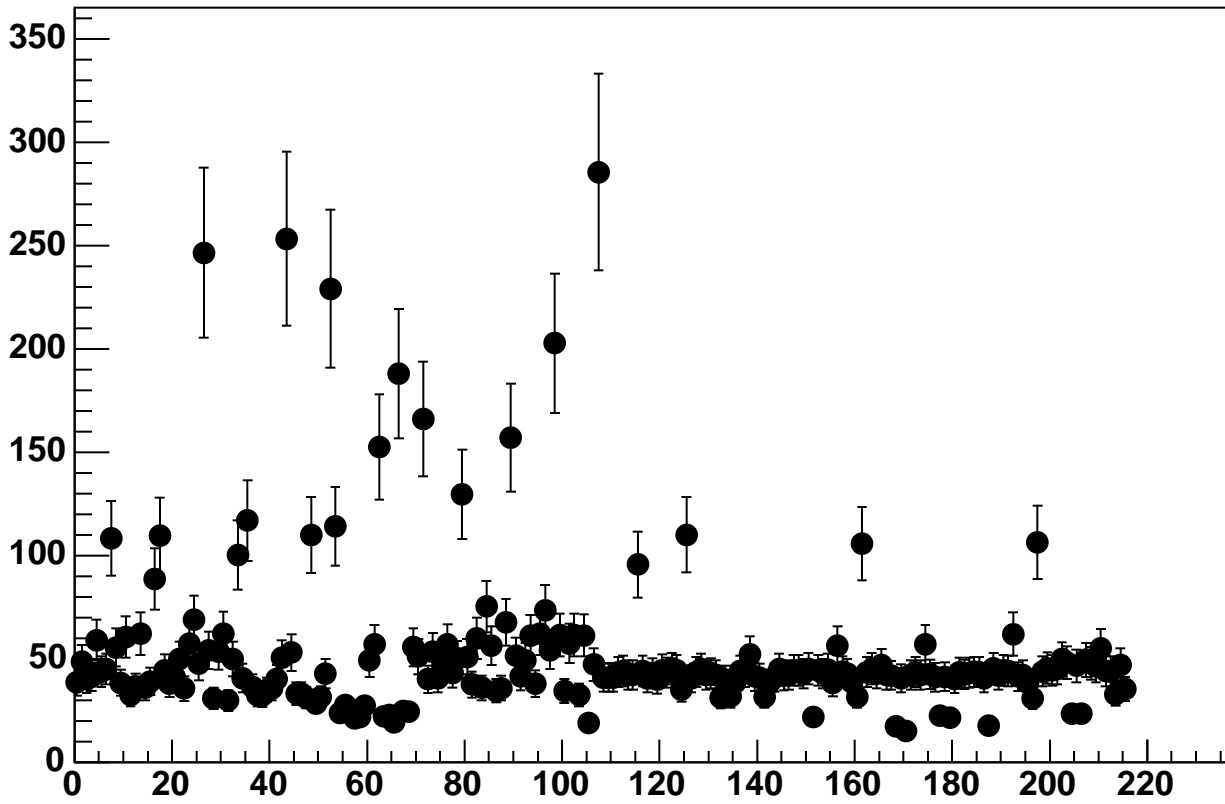
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18\*Chip+Chan



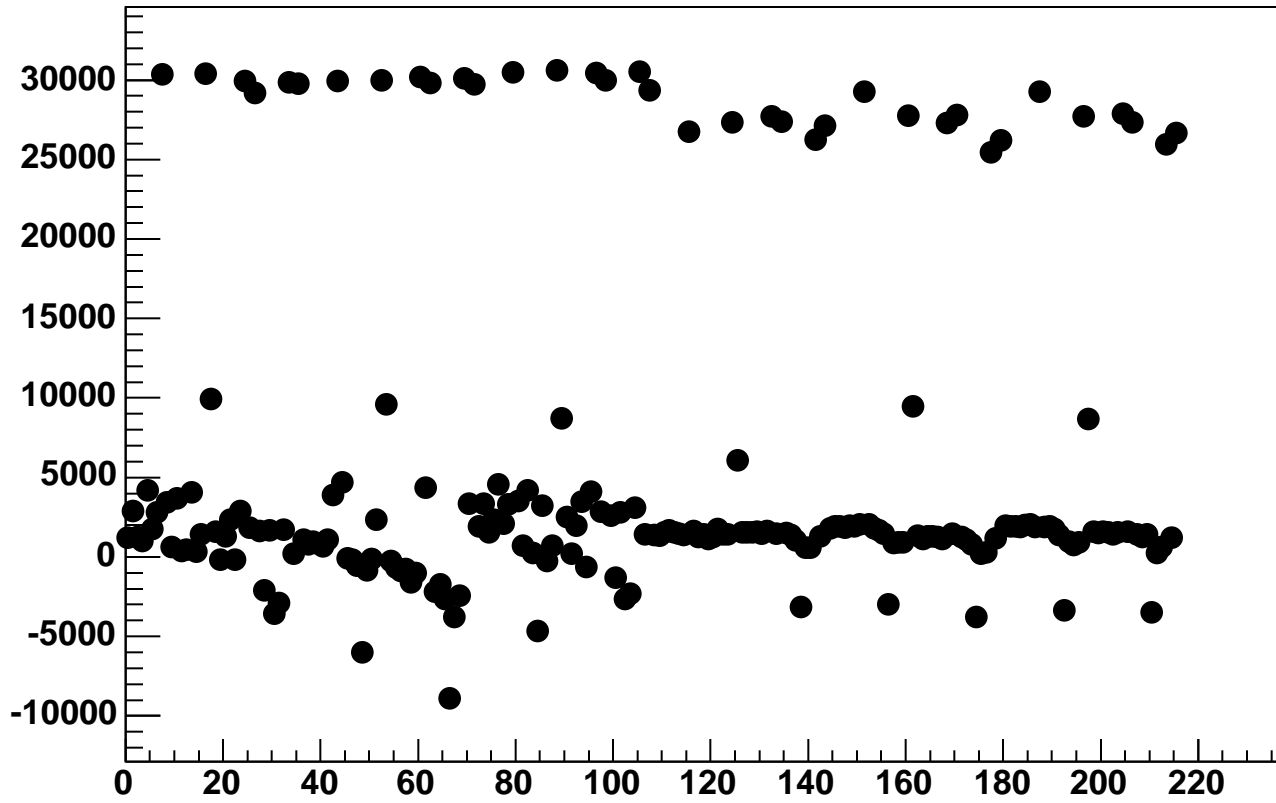
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18\*Chip+Chan



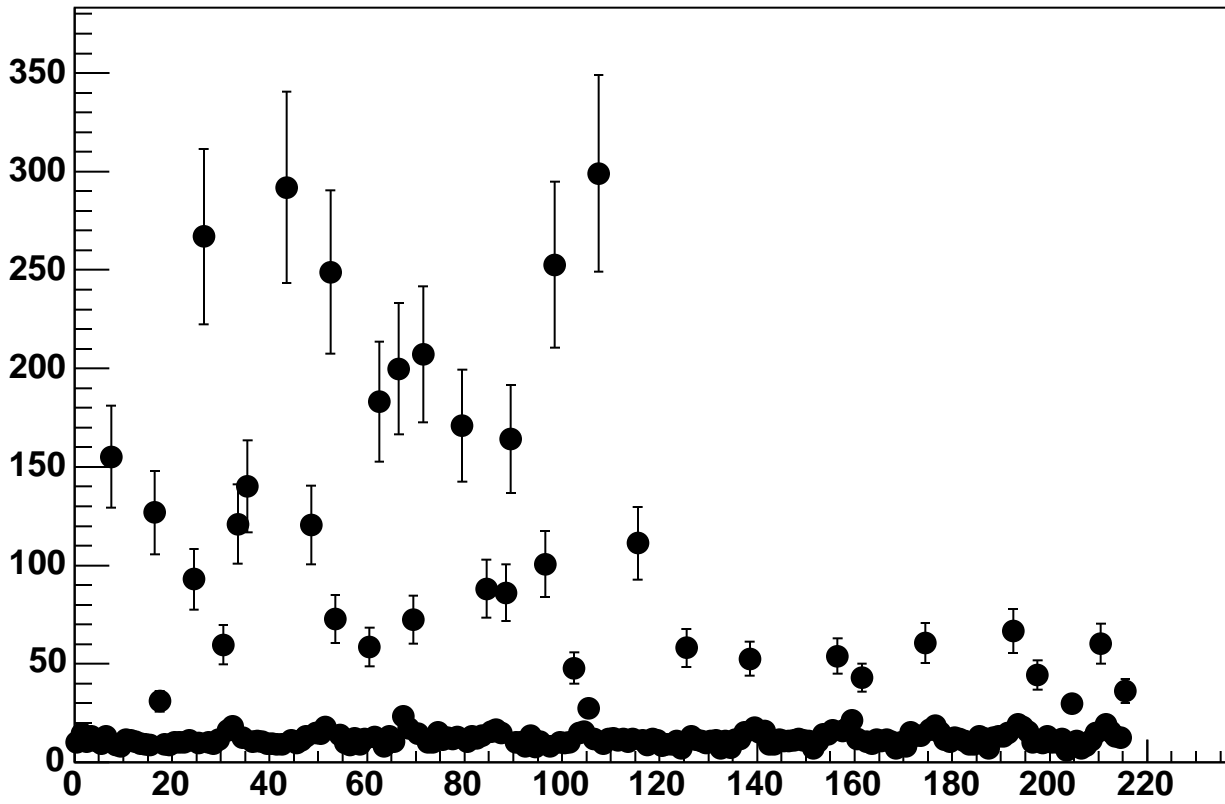
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18\*Chip+Chan



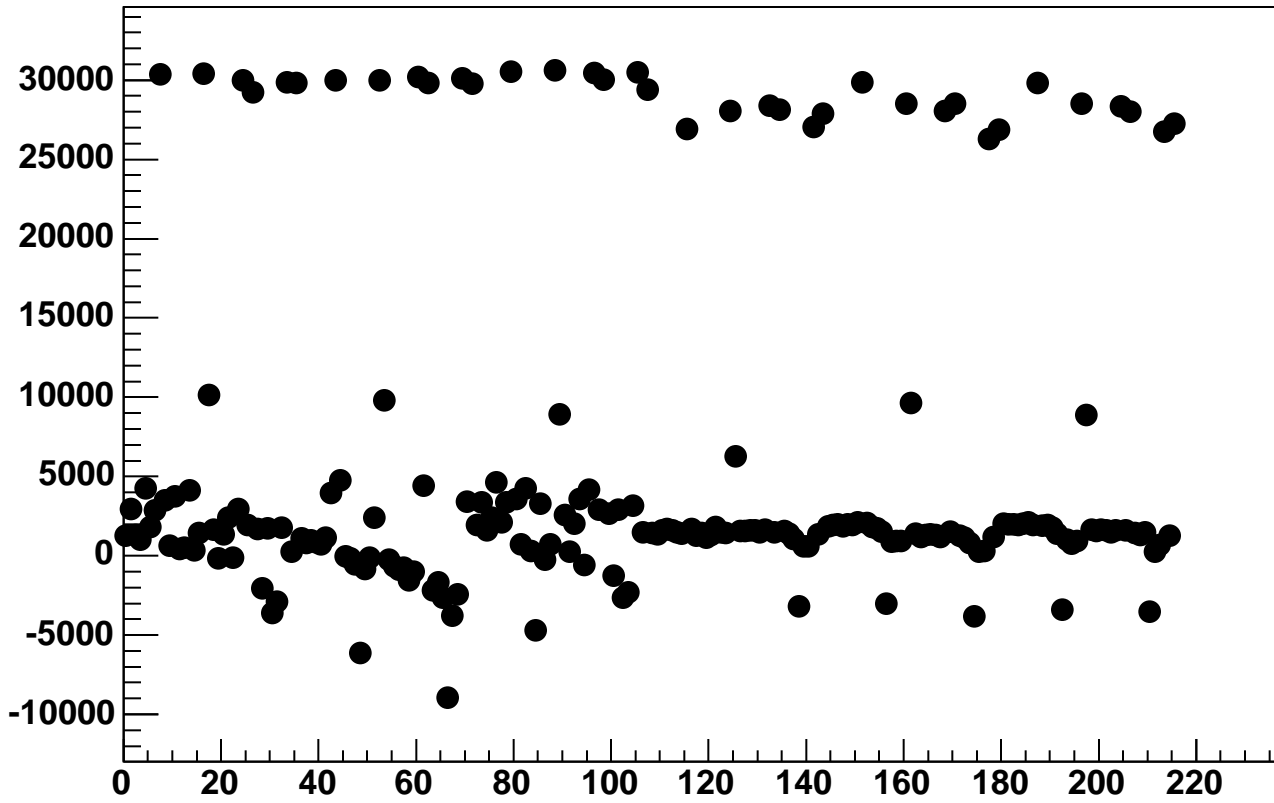
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18\*Chip+Chan



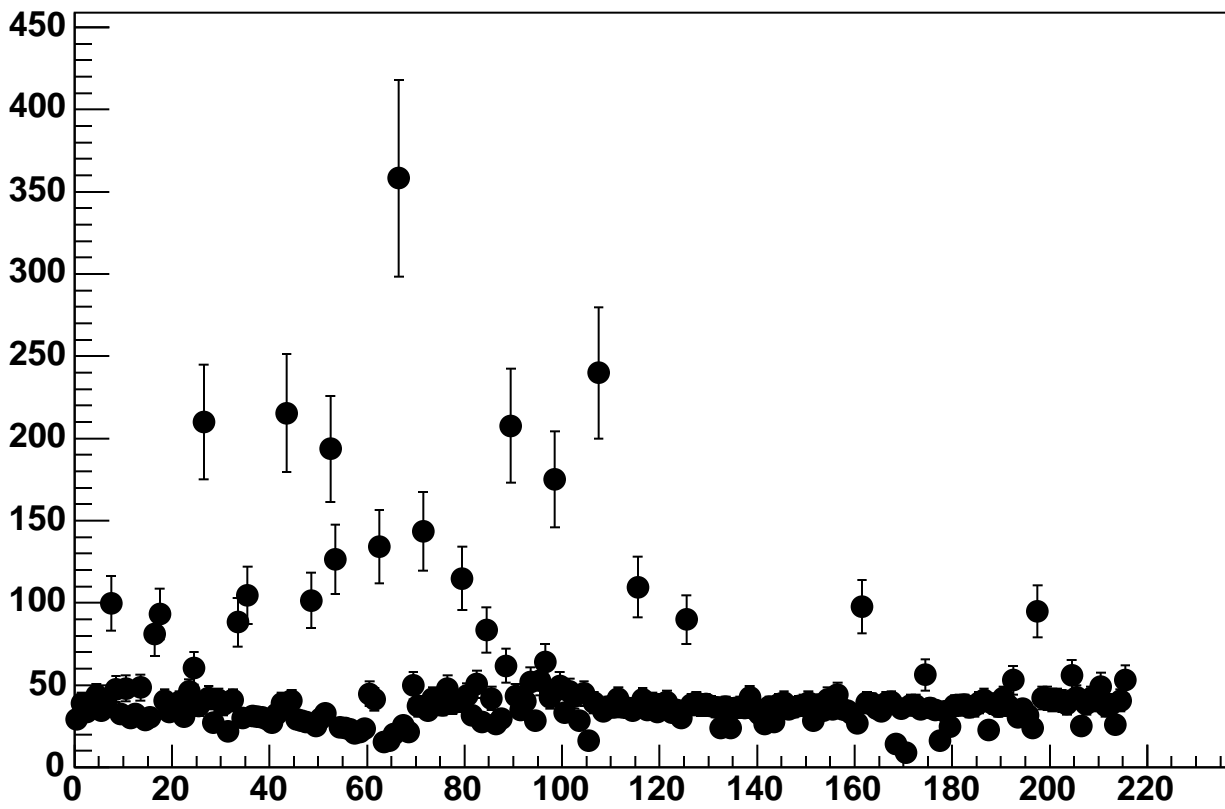
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18\*Chip+Chan



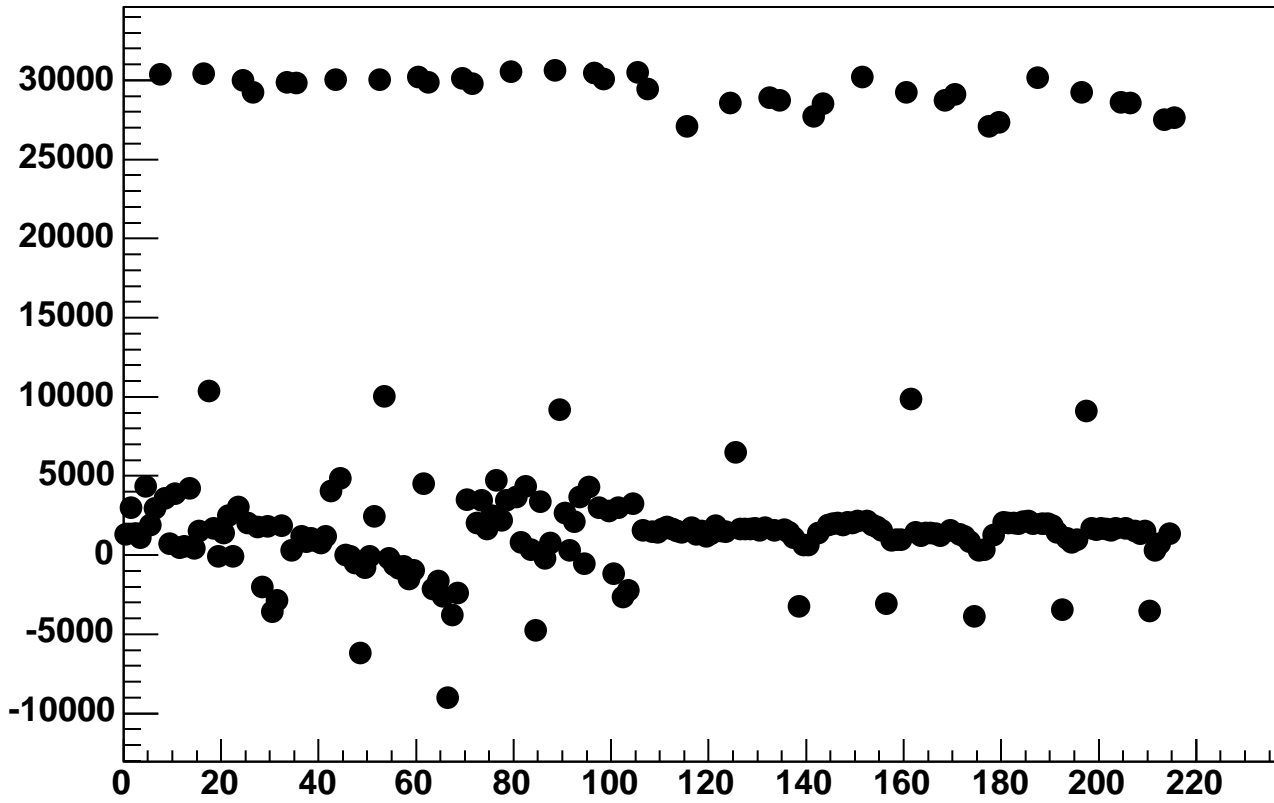
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18\*Chip+Chan



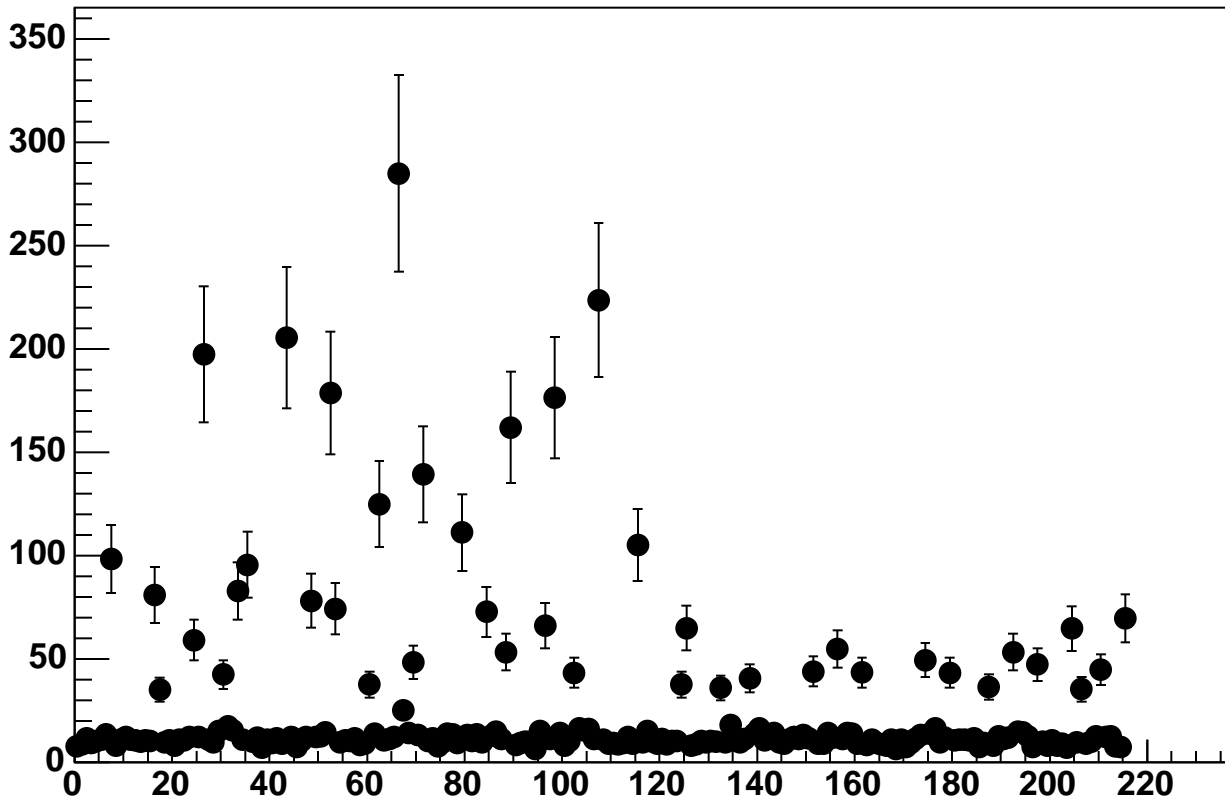
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18\*Chip+Chan



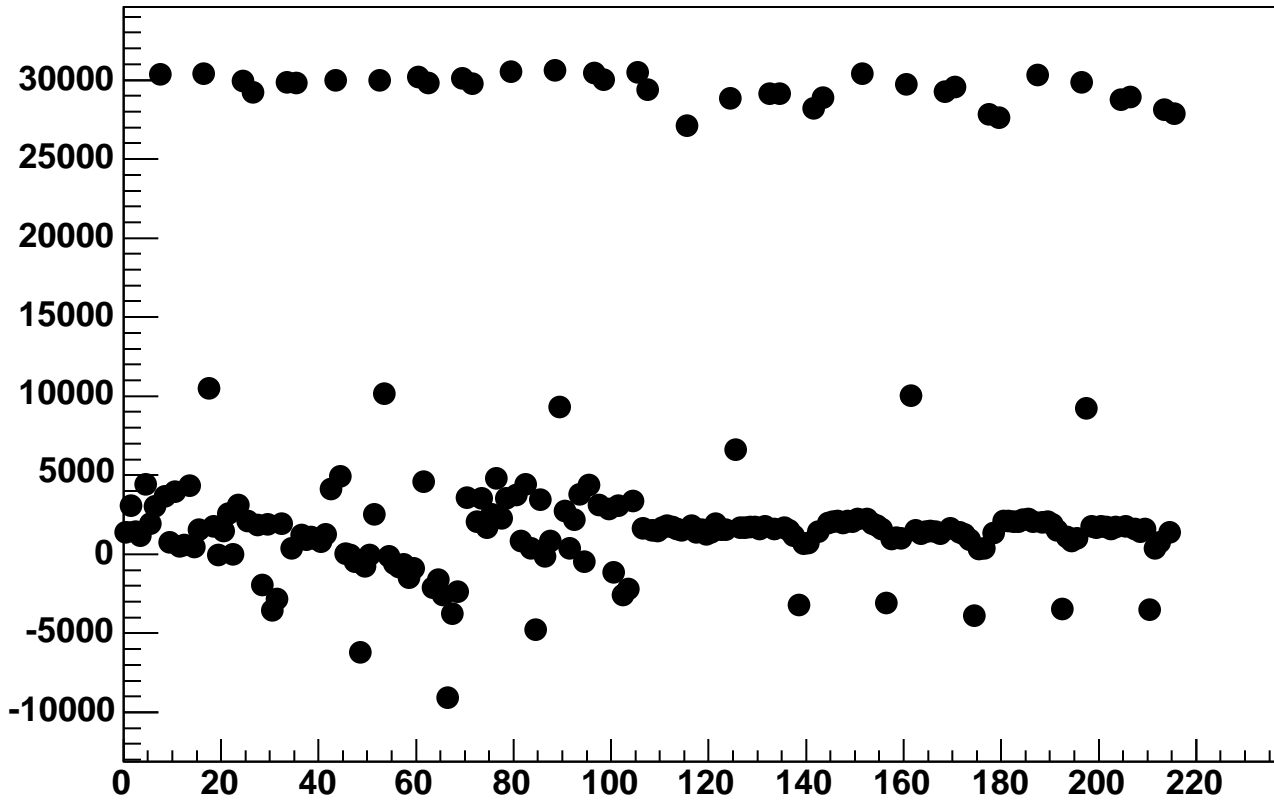
Enable 5, Hold=35, DAC=12400, ADC Mean vs 18\*Chip+Chan



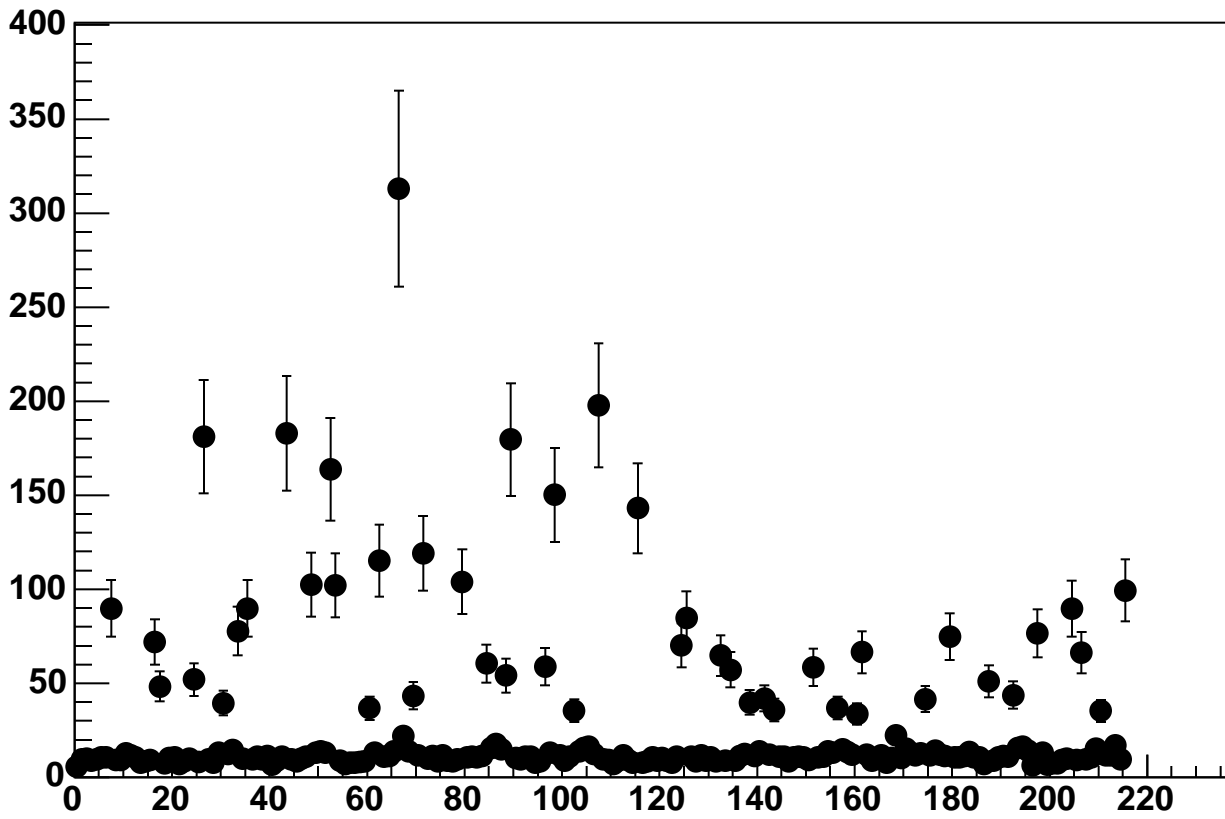
Enable 5, Hold=35, DAC=12400, ADC Noise vs 18\*Chip+Chan



Enable 5, Hold=35, DAC=12800, ADC Mean vs 18\*Chip+Chan

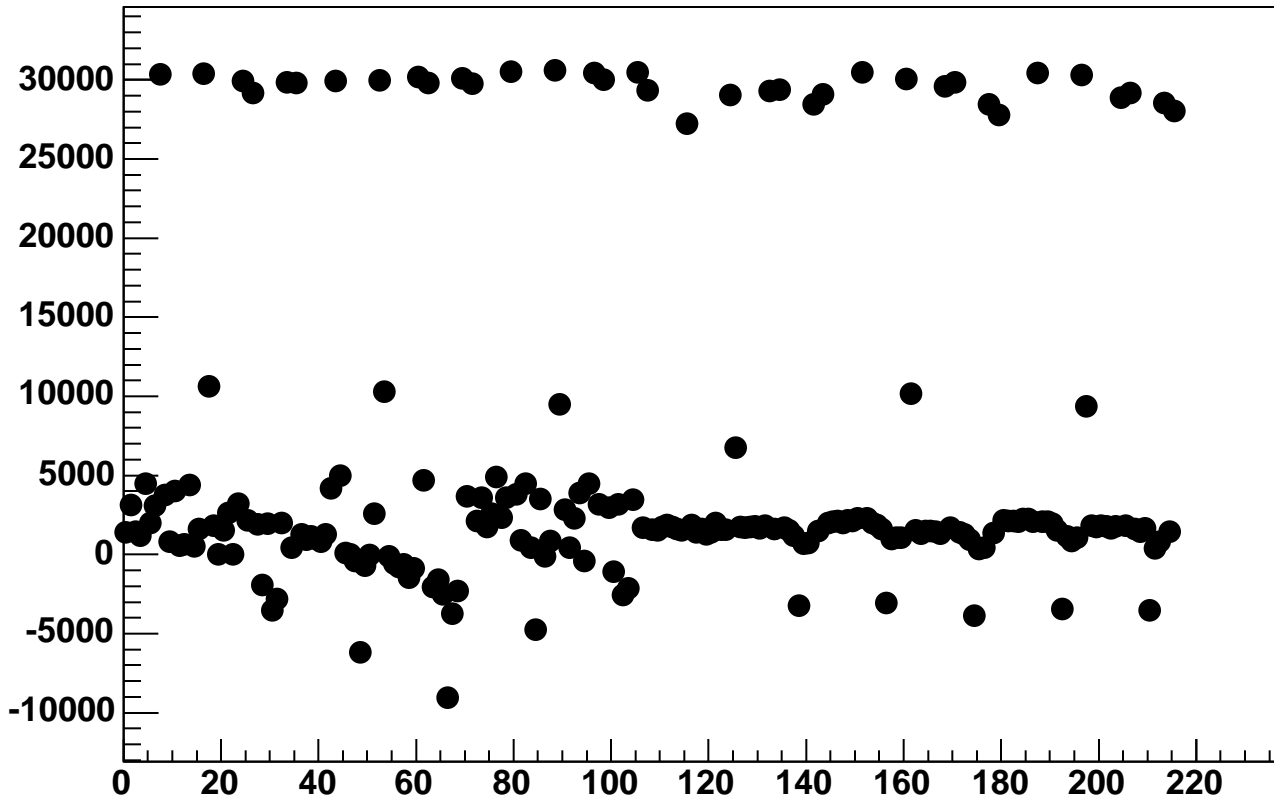


Enable 5, Hold=35, DAC=12800, ADC Noise vs 18\*Chip+Chan

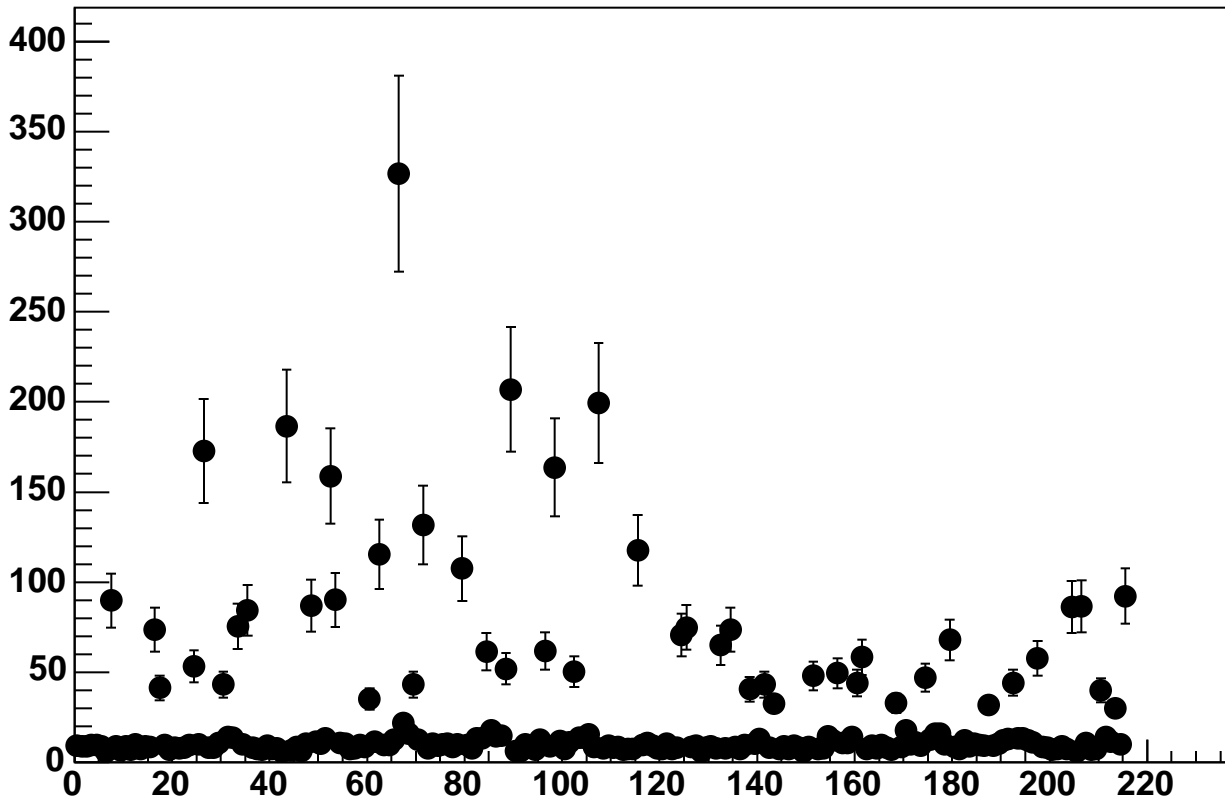




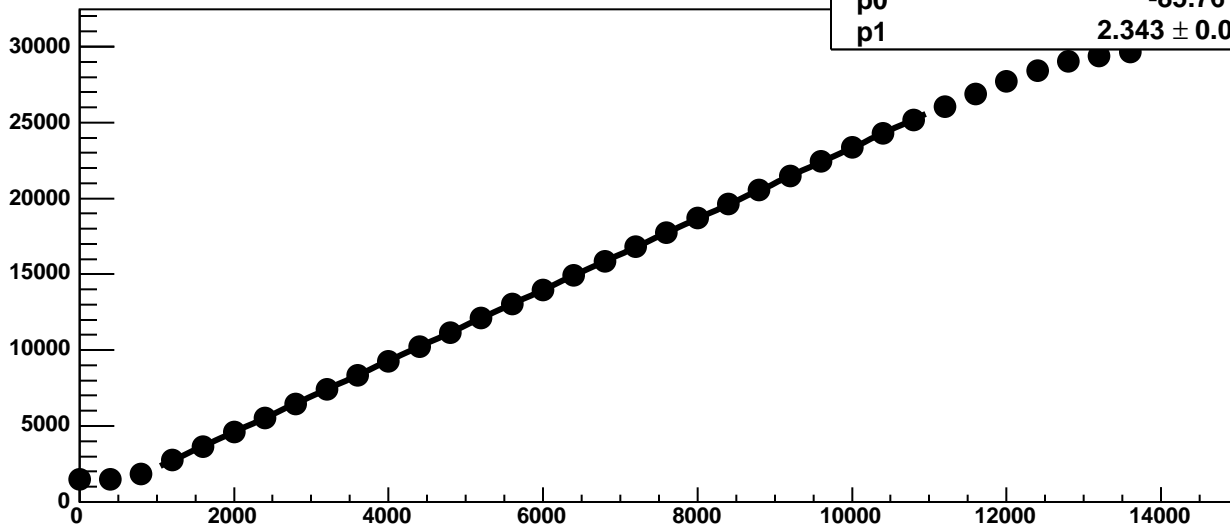
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18\*Chip+Chan



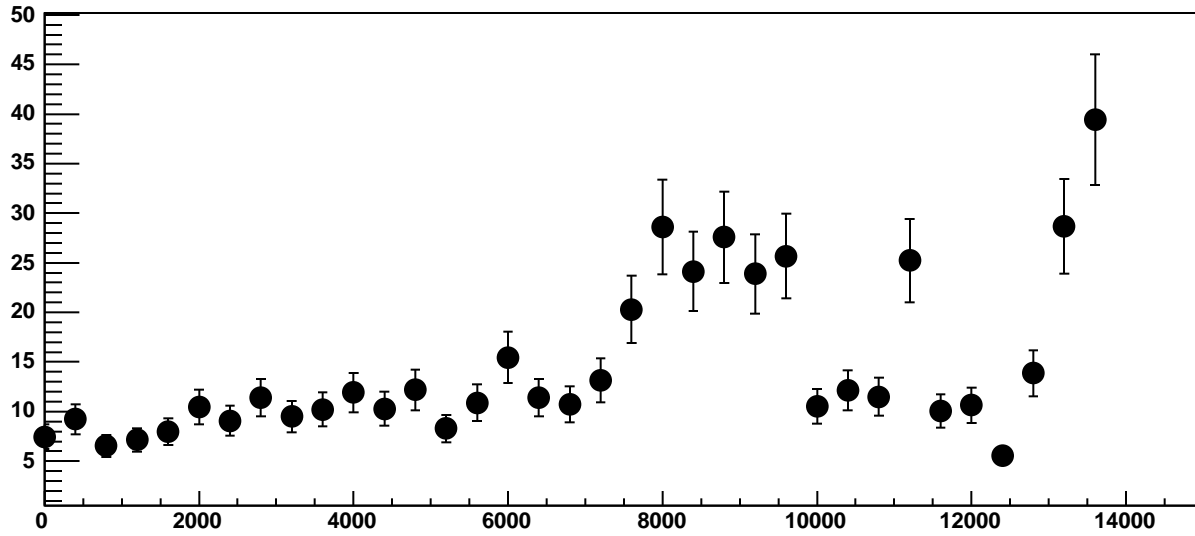
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18\*Chip+Chan



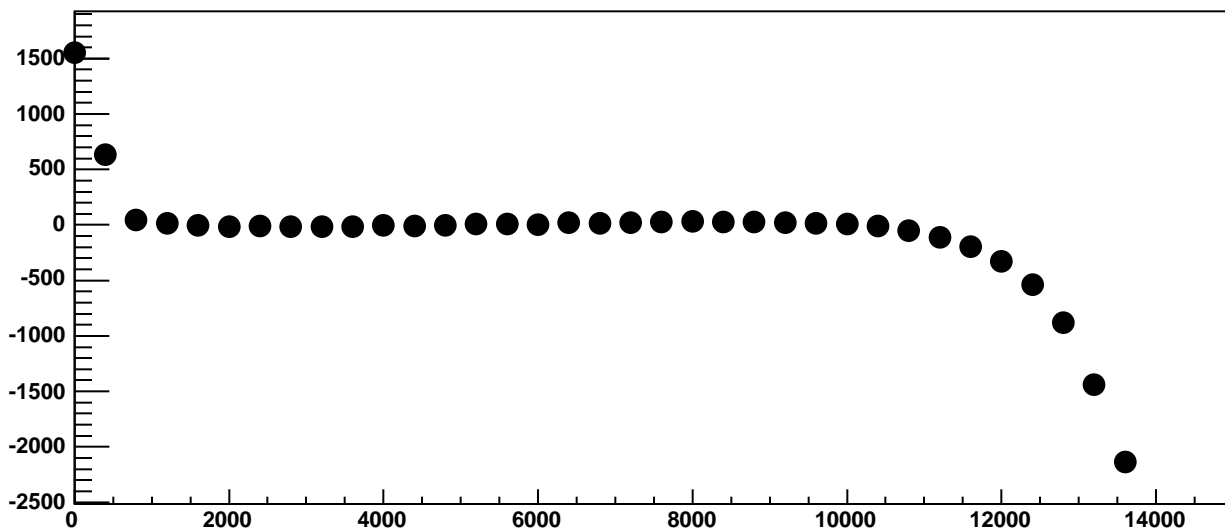
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



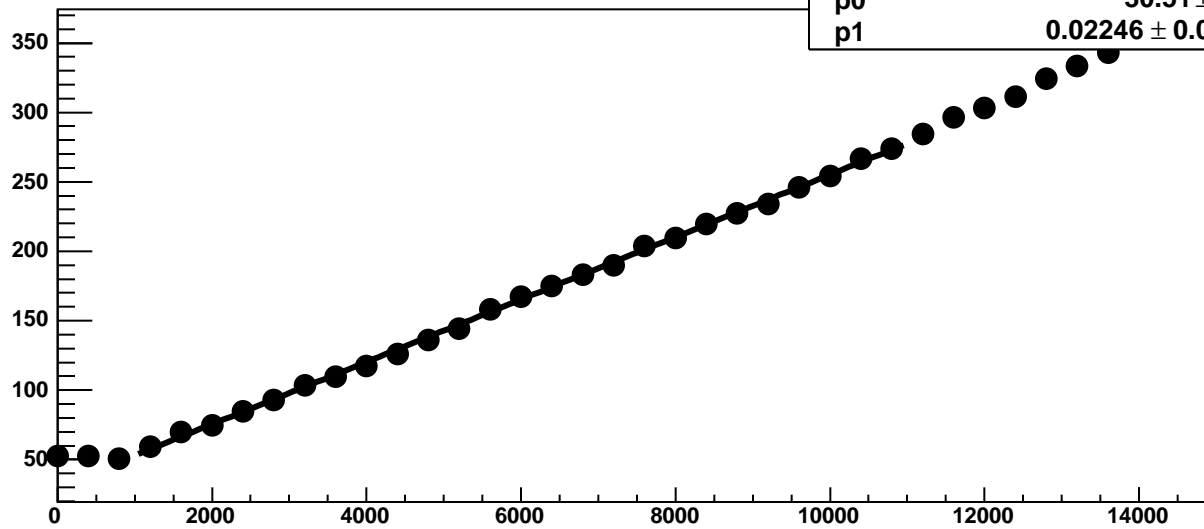
Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

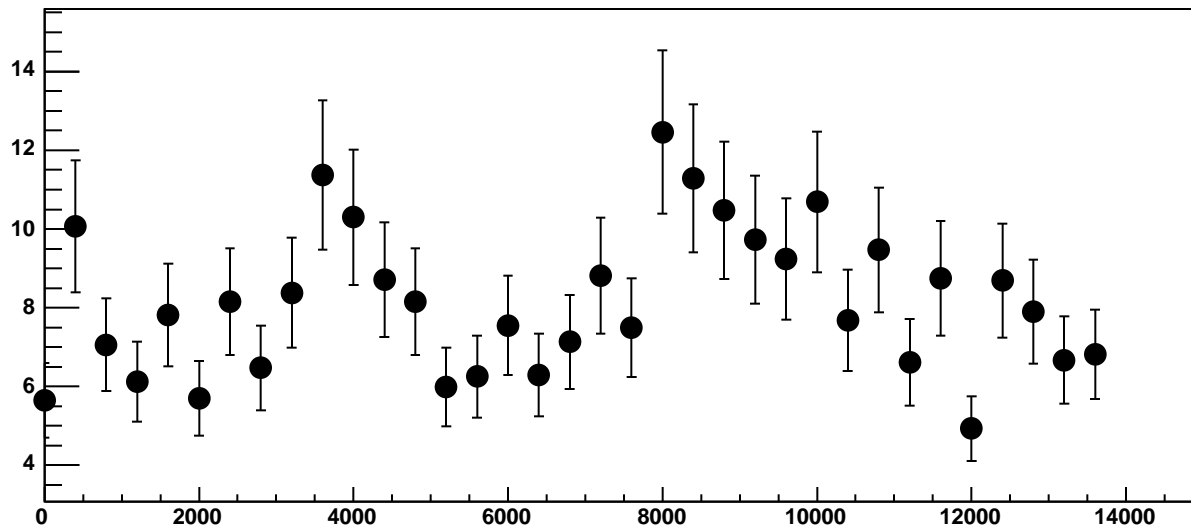


Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

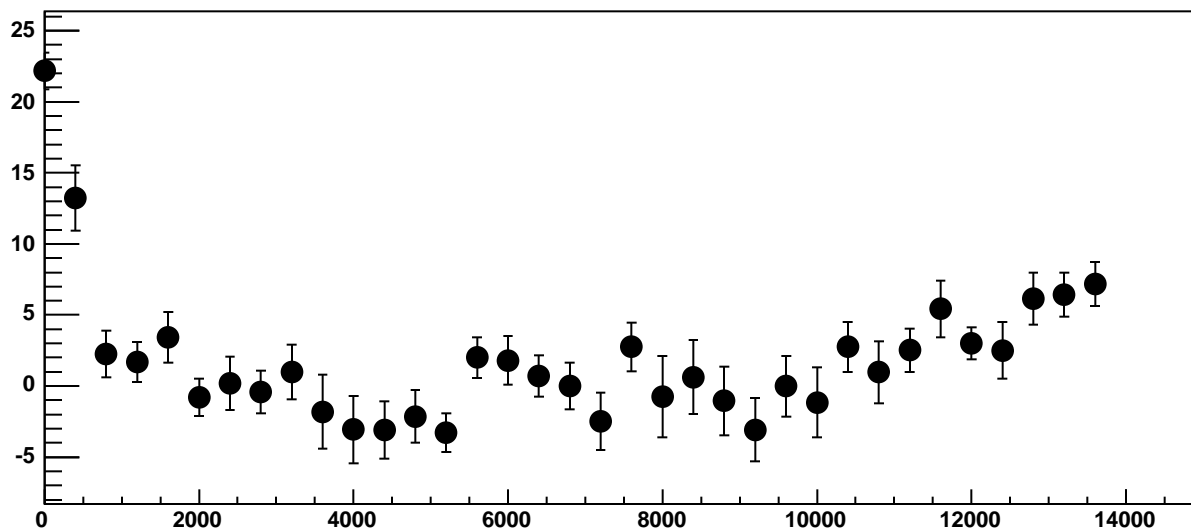


$\chi^2 / \text{ndf}$  29.8 / 23  
p0  $30.51 \pm 0.7823$   
p1  $0.02246 \pm 0.0001282$

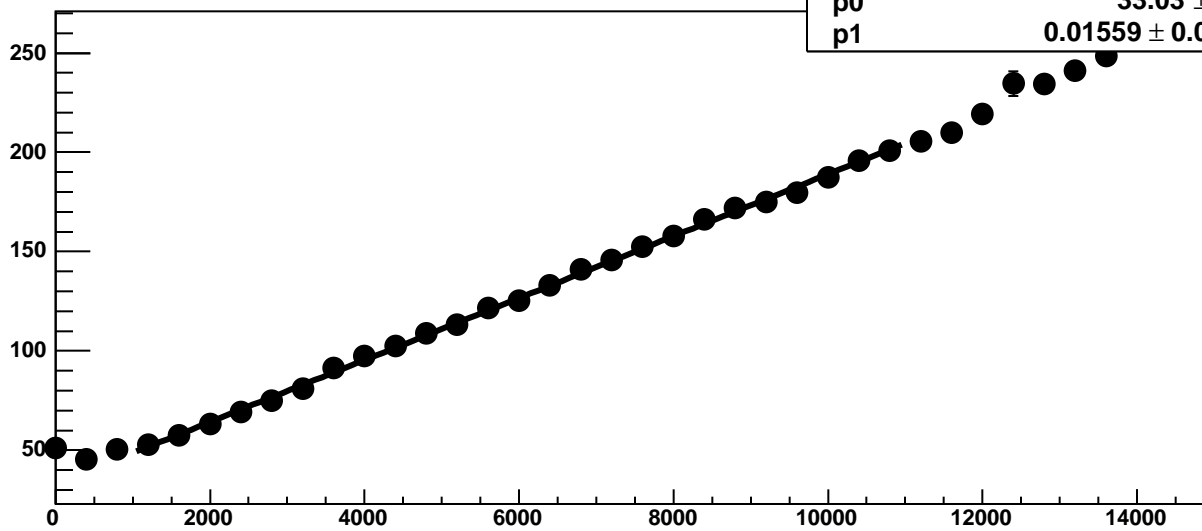
Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



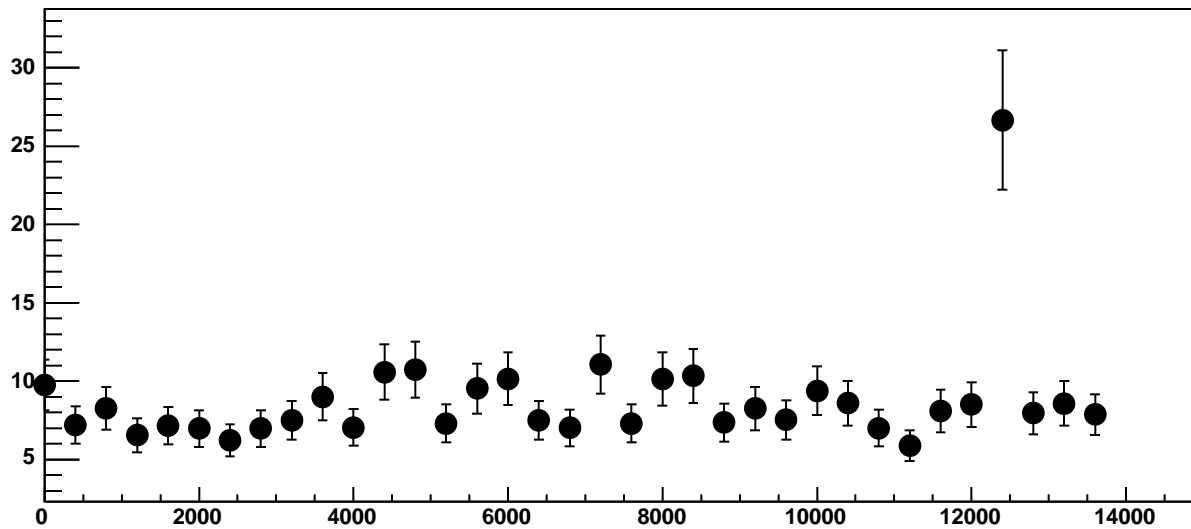
Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



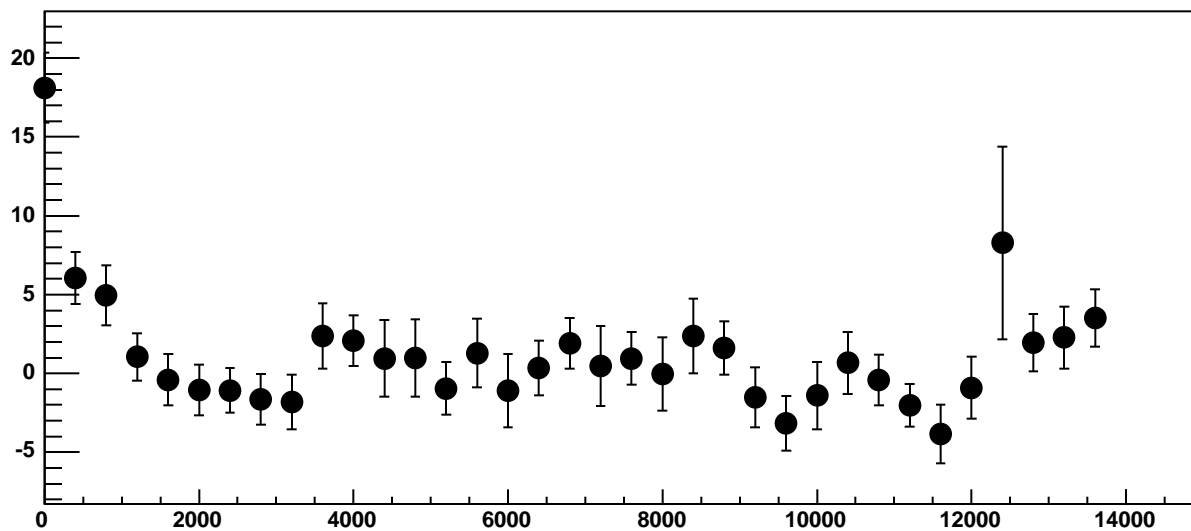
Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



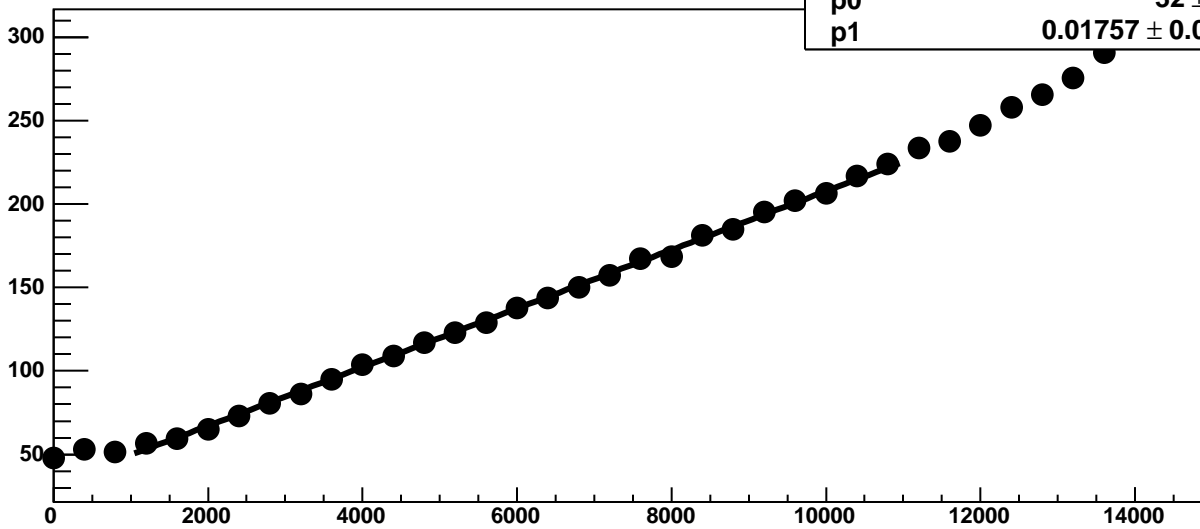
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

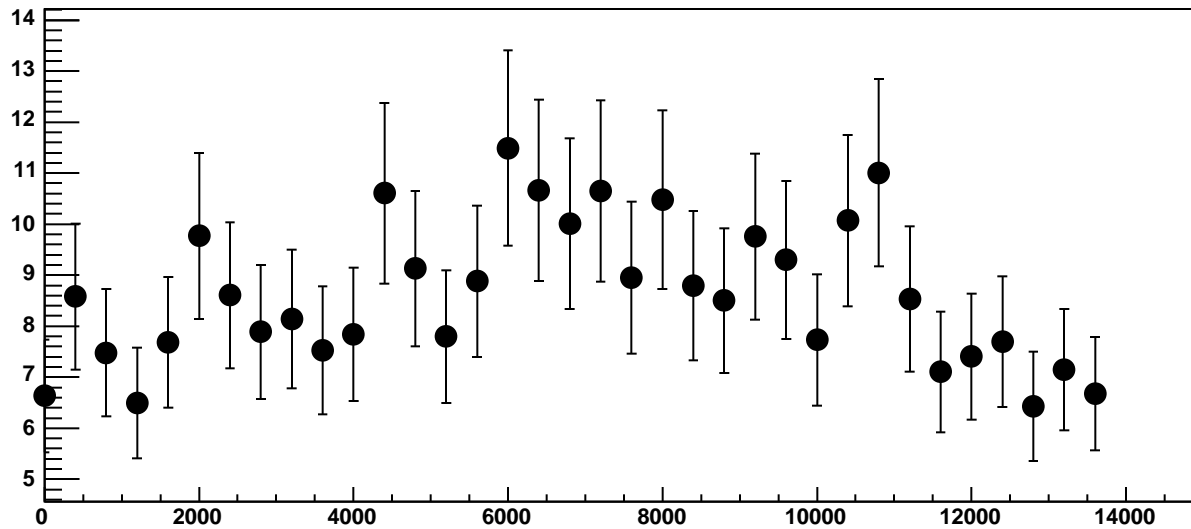


Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

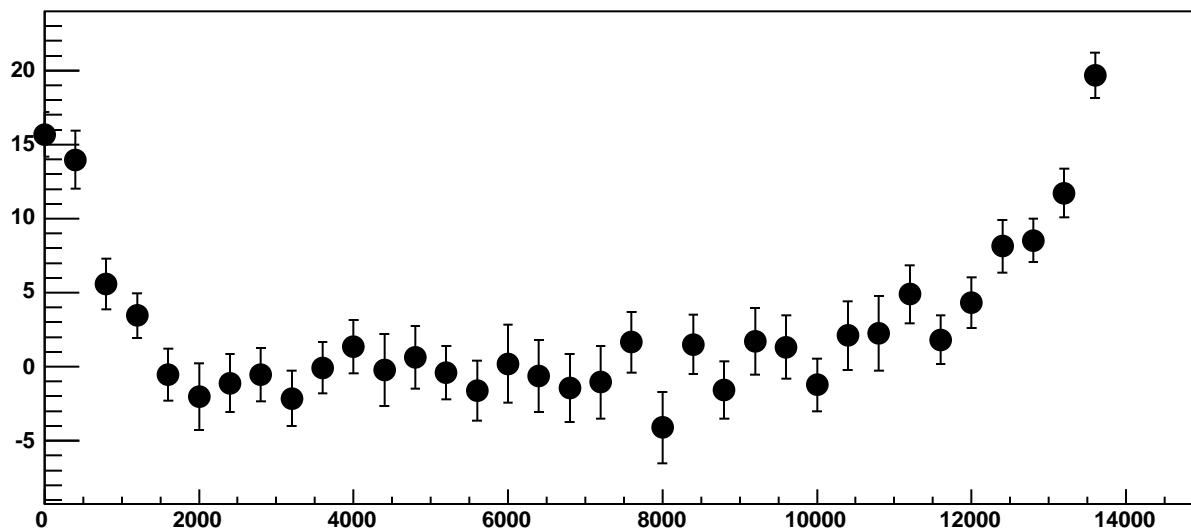


$\chi^2 / \text{ndf}$  17.87 / 23  
p0  $32 \pm 0.8597$   
p1  $0.01757 \pm 0.0001362$

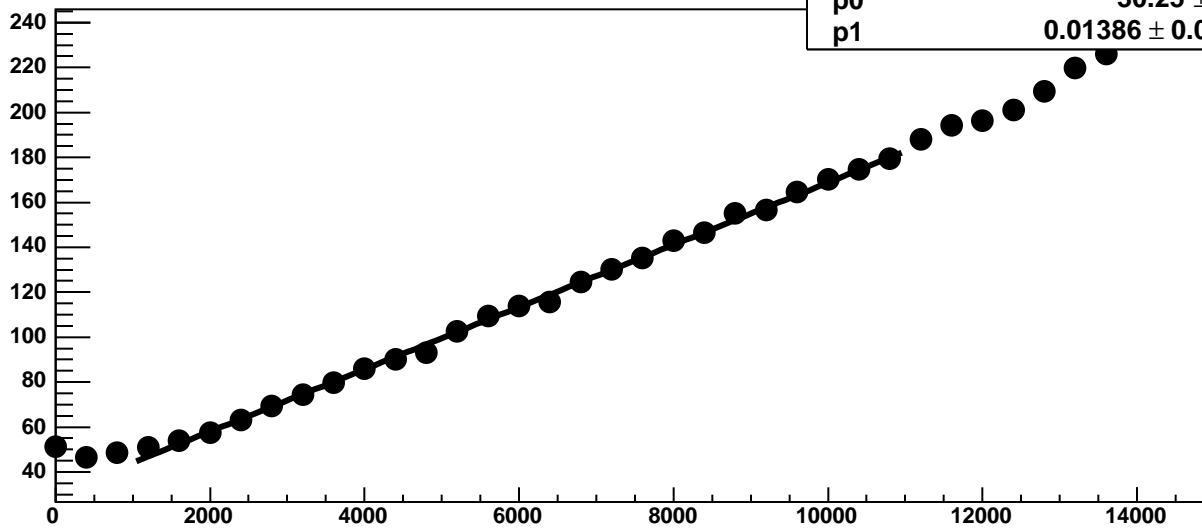
Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18.14 / 23

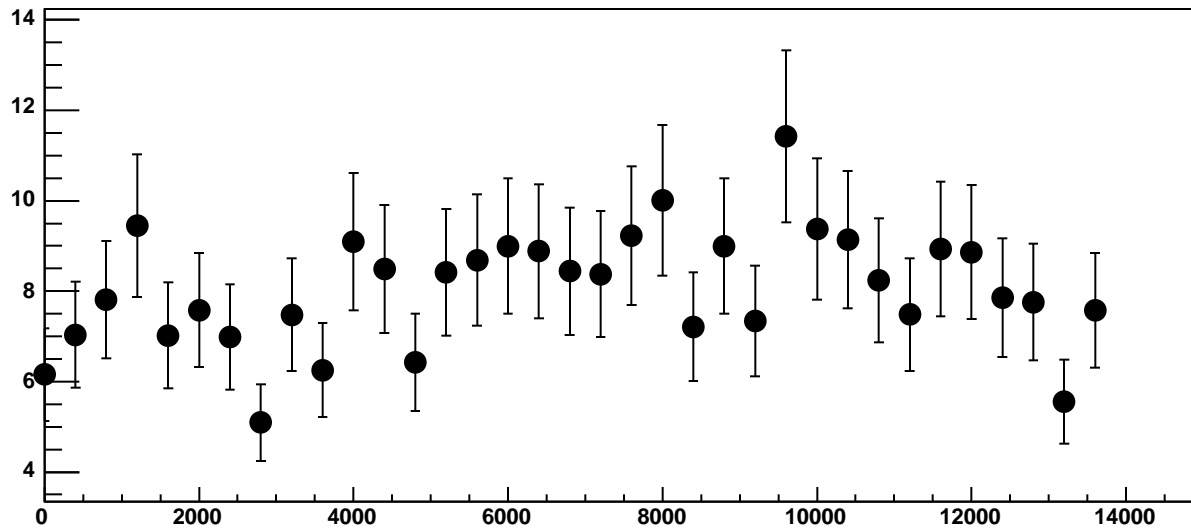
p0

$30.25 \pm 0.7868$

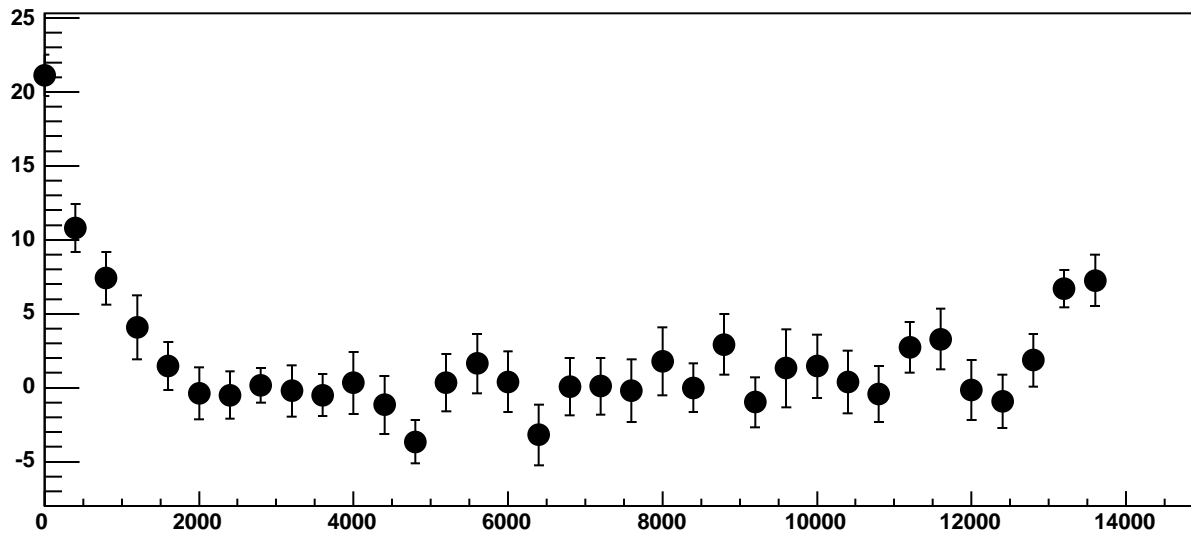
p1

$0.01386 \pm 0.0001272$

Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

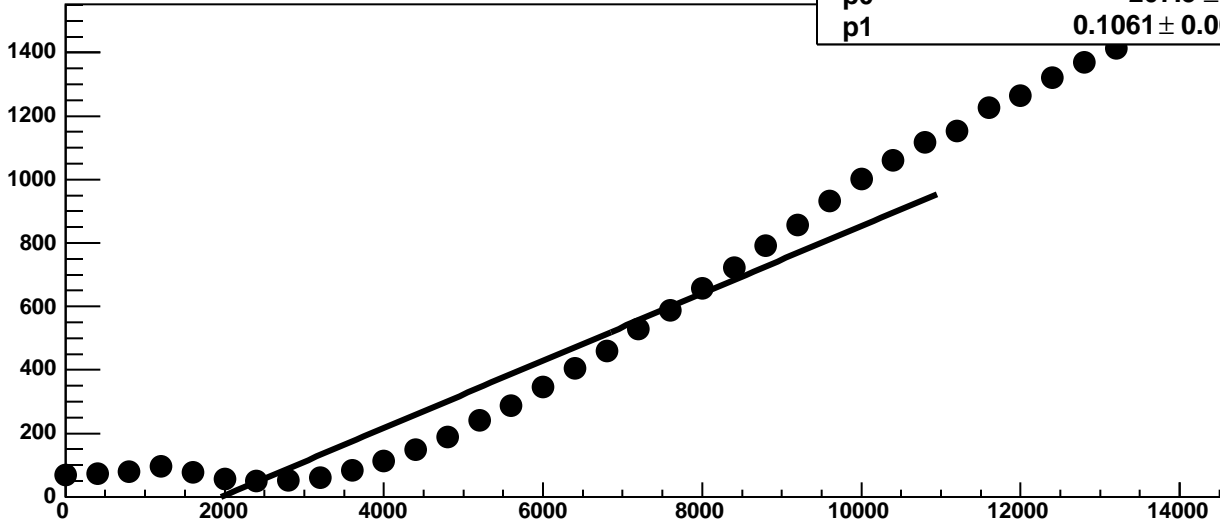


Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

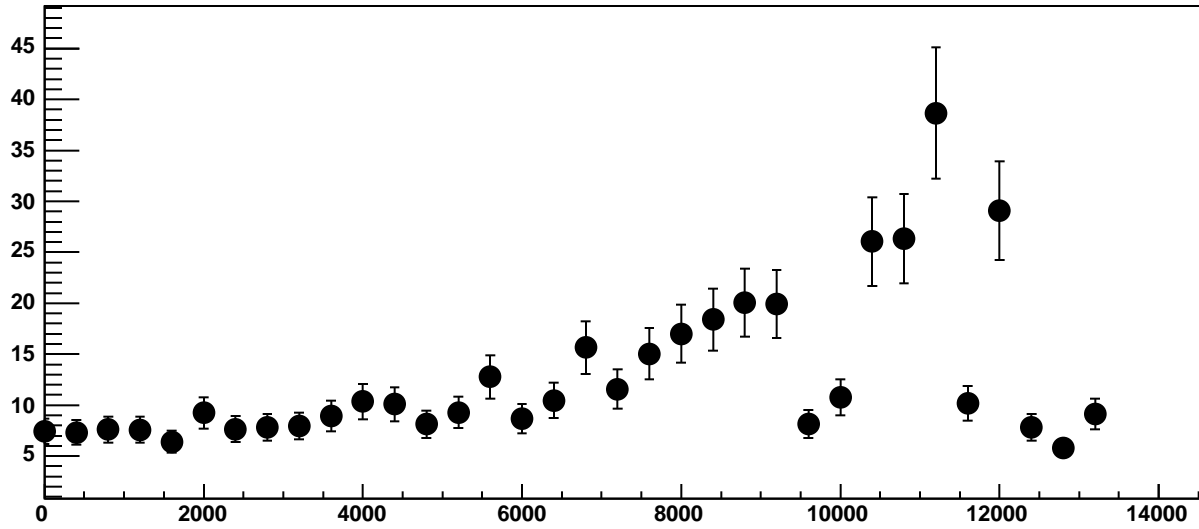


Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

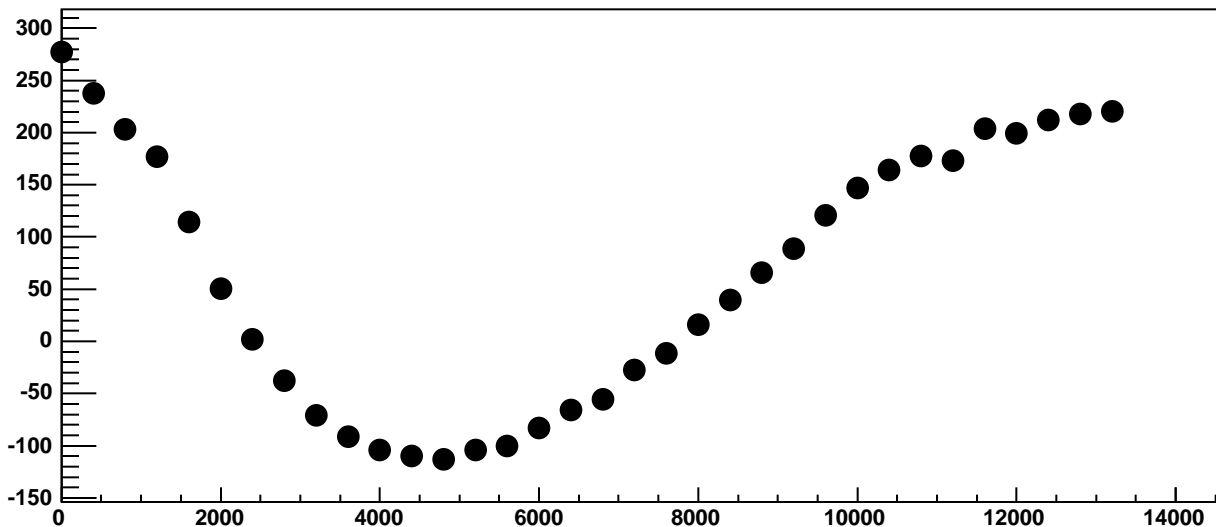
$\chi^2 / \text{ndf}$  4.518e+04 / 23  
p0 -207.5 ± 0.9218  
p1 0.1061 ± 0.0001723



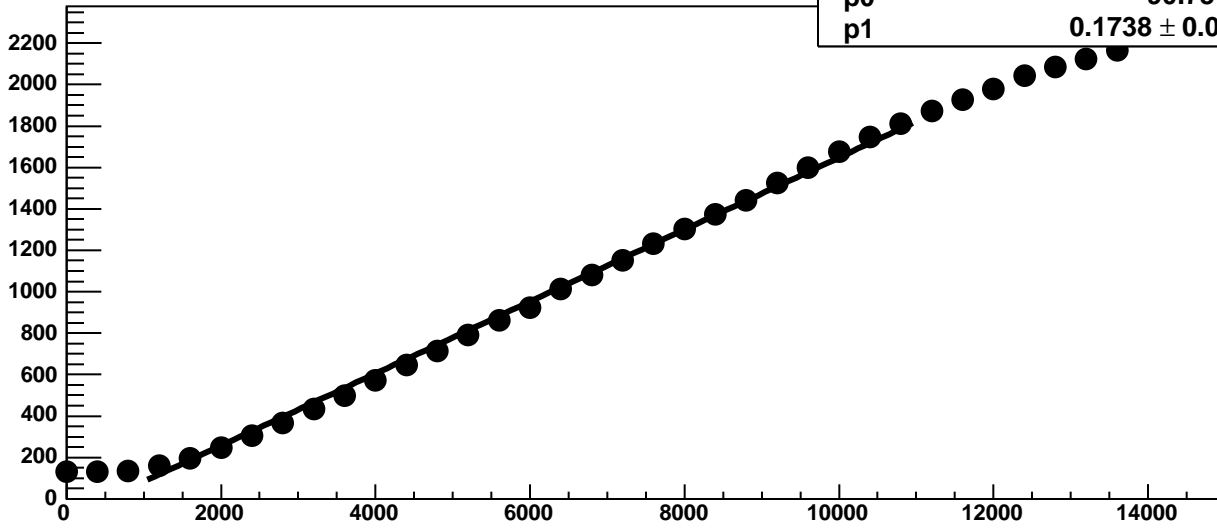
Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1727 / 23

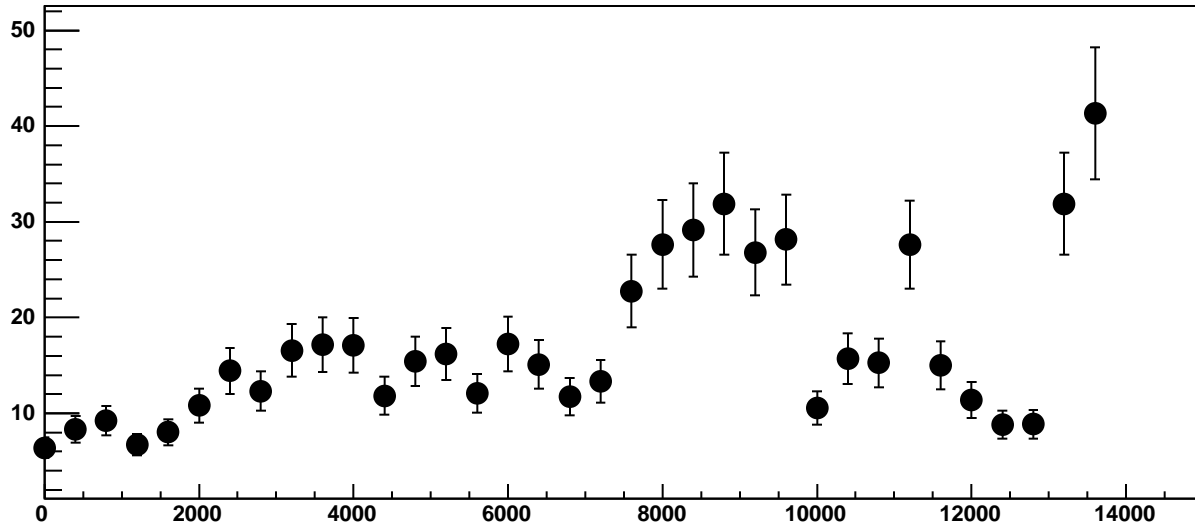
p0

$-90.73 \pm 1.119$

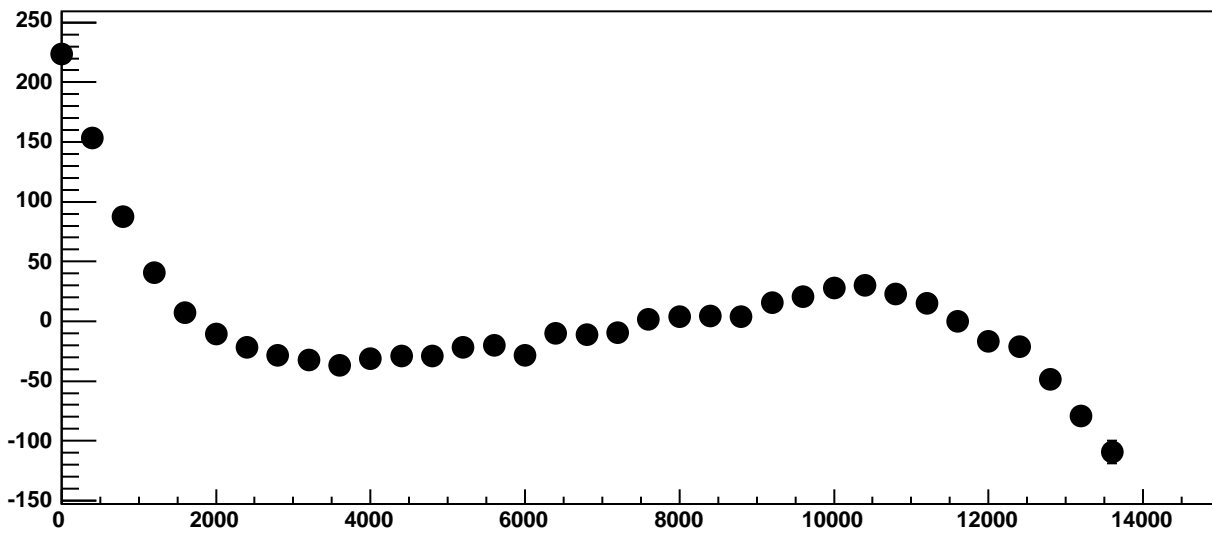
p1

$0.1738 \pm 0.0002009$

Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

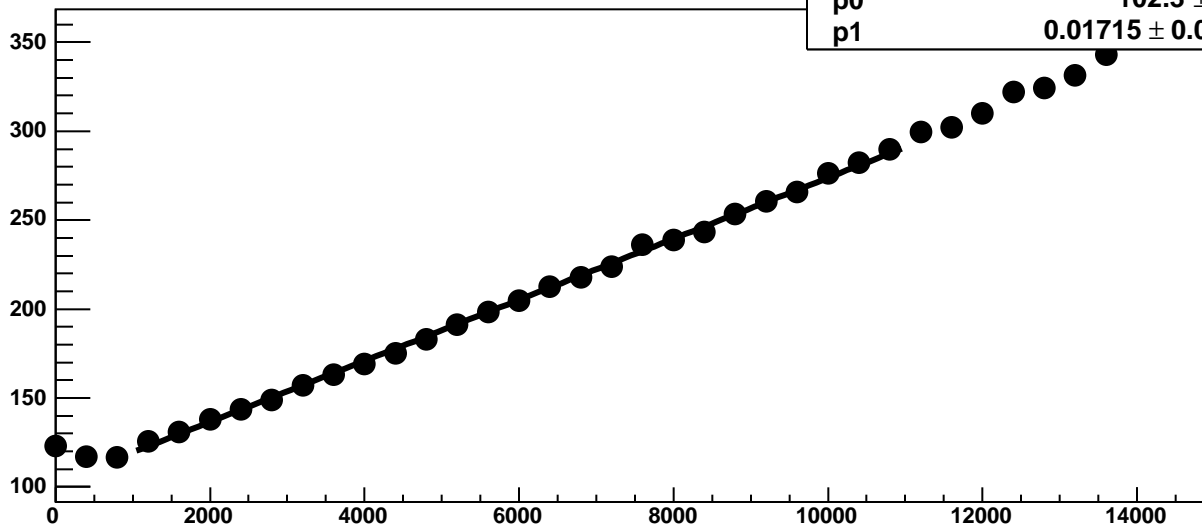


Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



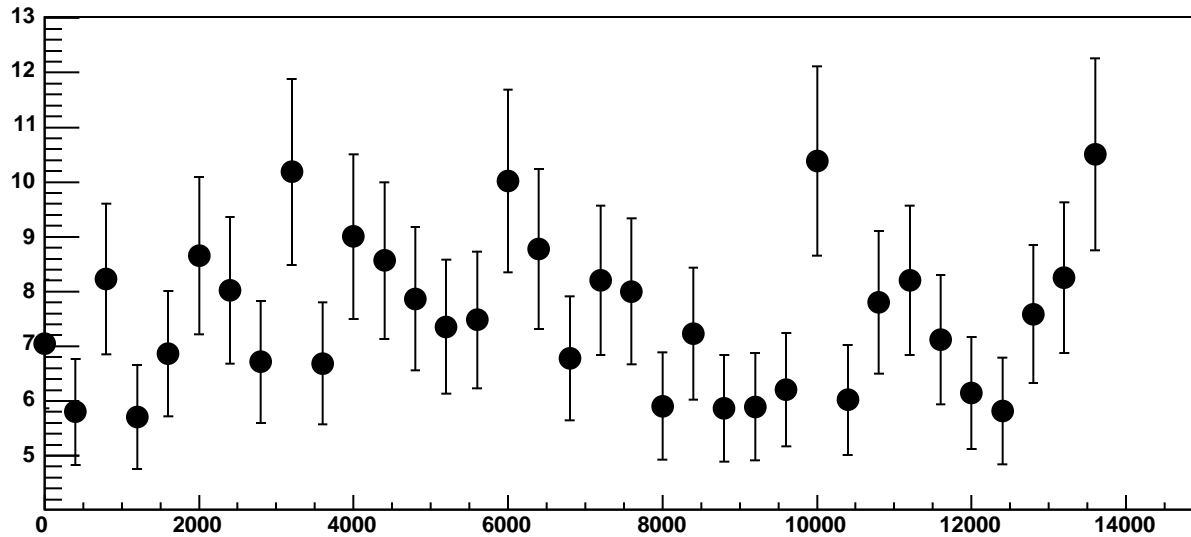


Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

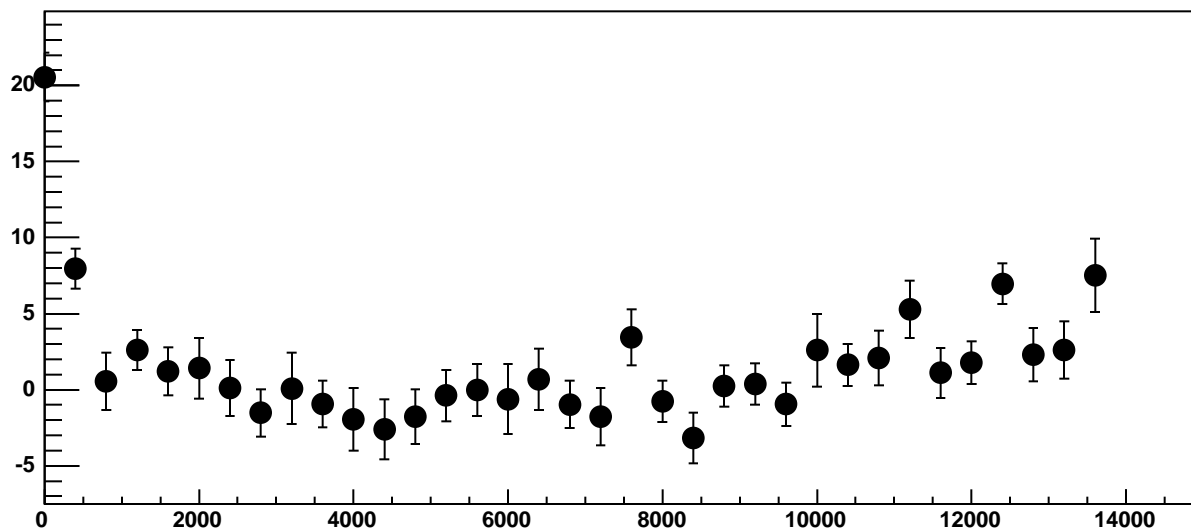


$\chi^2 / \text{ndf}$  23.59 / 23  
p0  $102.3 \pm 0.7608$   
p1  $0.01715 \pm 0.0001107$

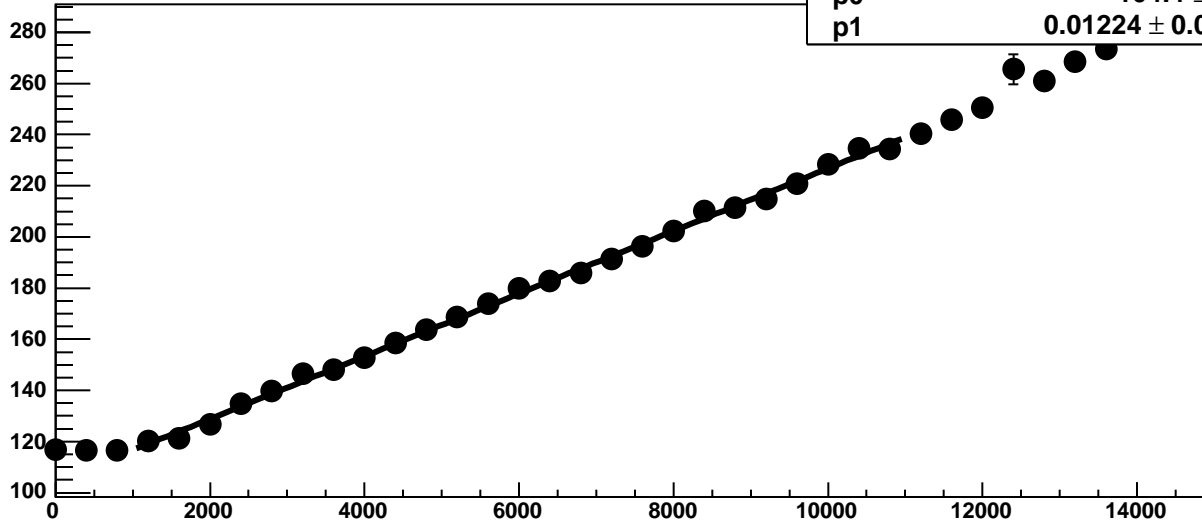
Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

24.98 / 23

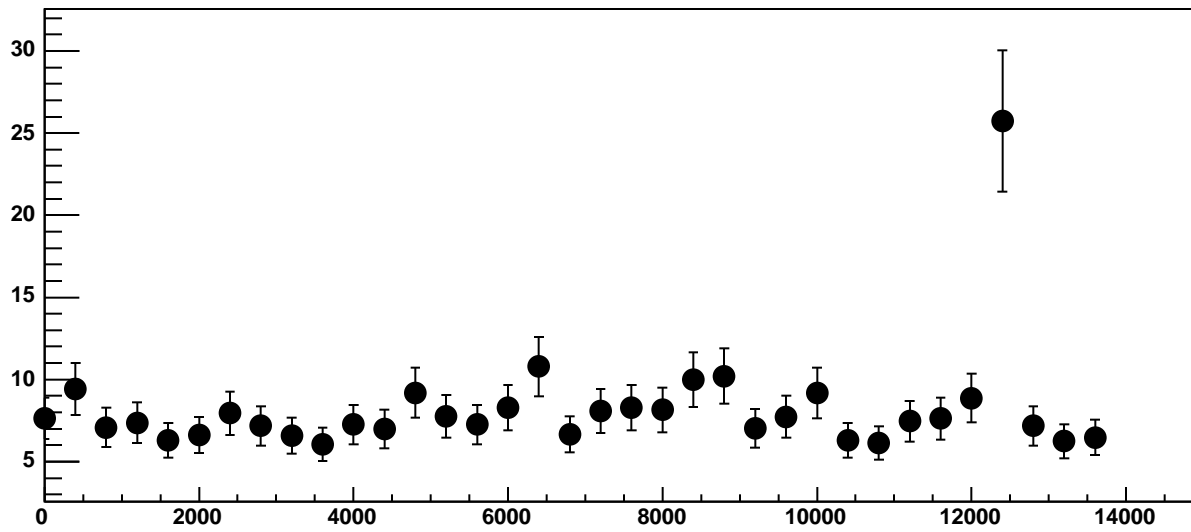
p0

$104.4 \pm 0.7372$

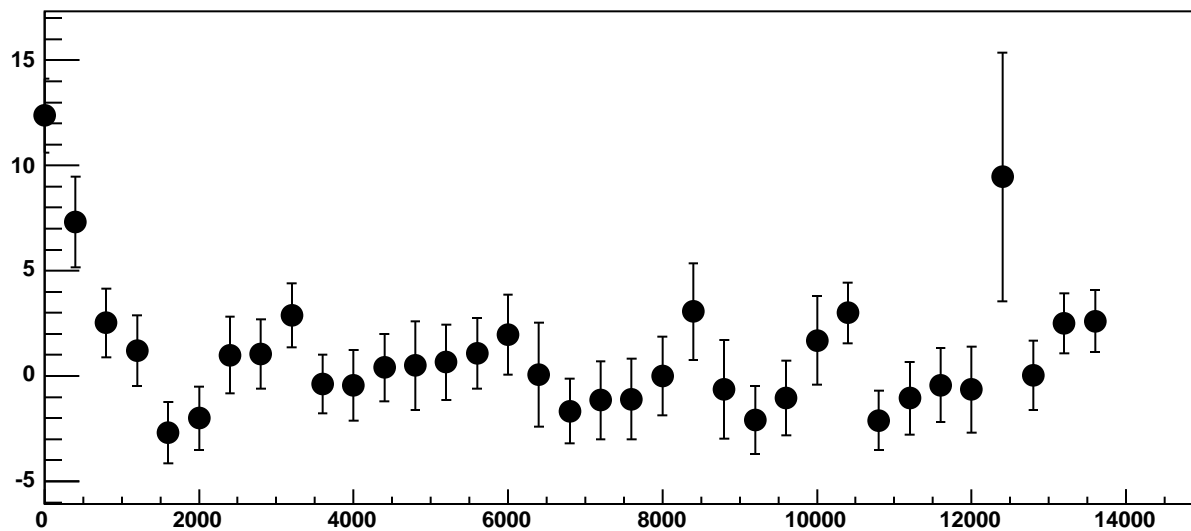
p1

$0.01224 \pm 0.0001126$

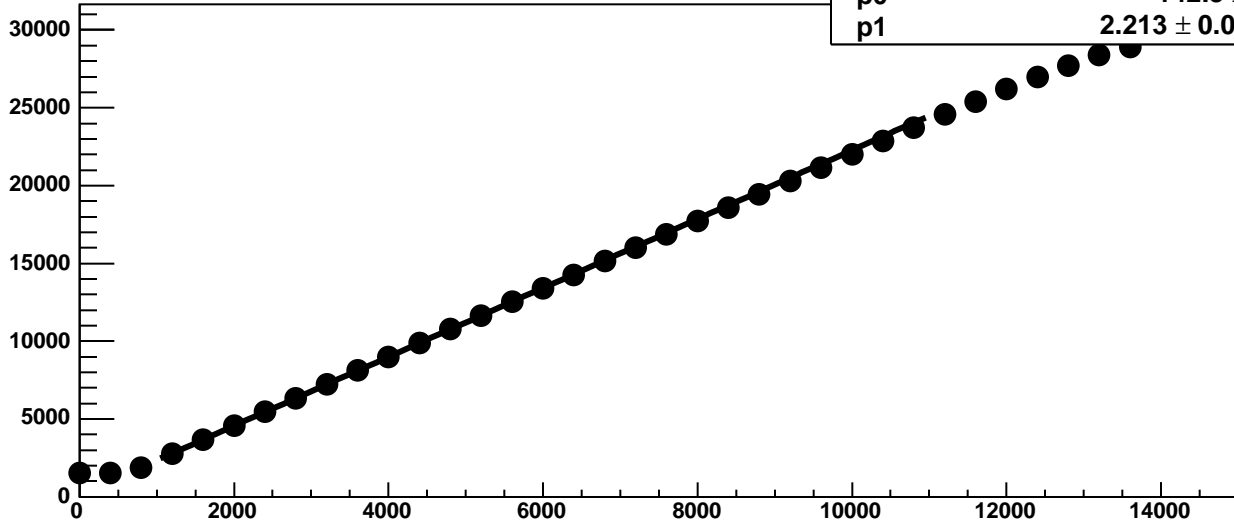
Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

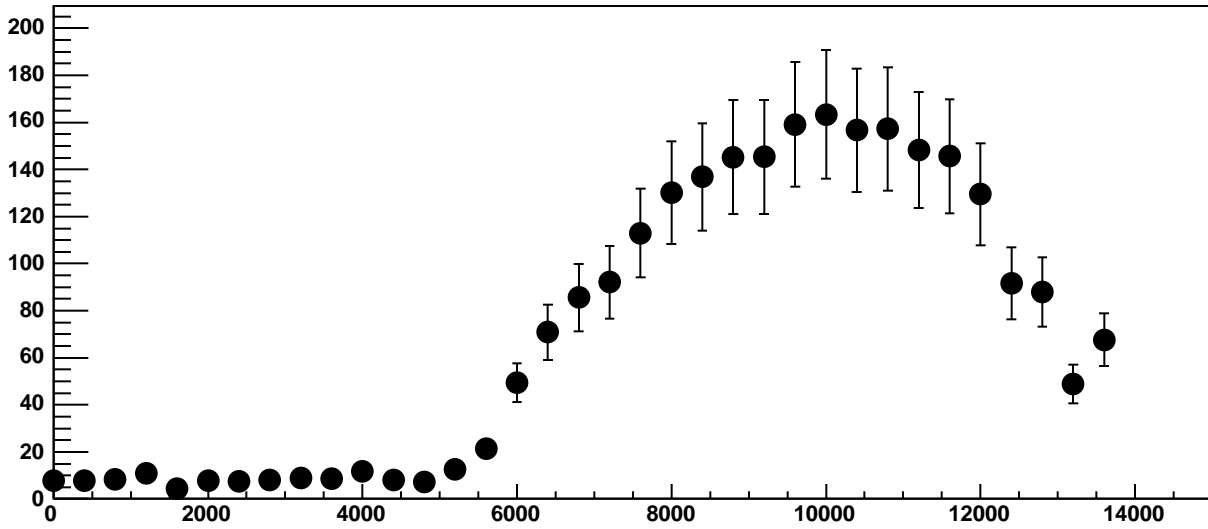


Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

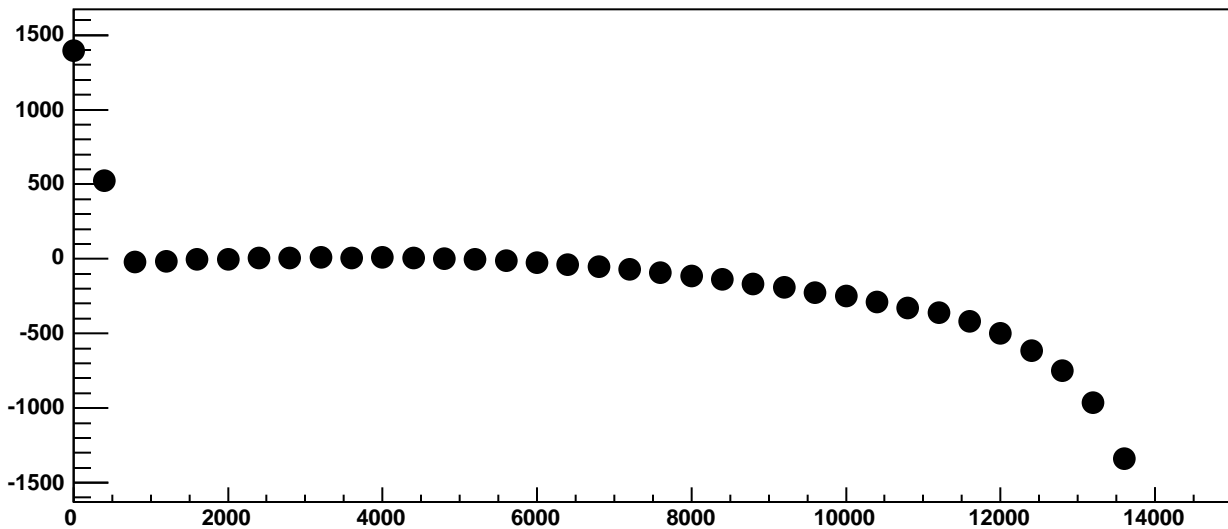


$\chi^2 / \text{ndf}$  509.5 / 23  
p0  $142.3 \pm 1.277$   
p1  $2.213 \pm 0.0004001$

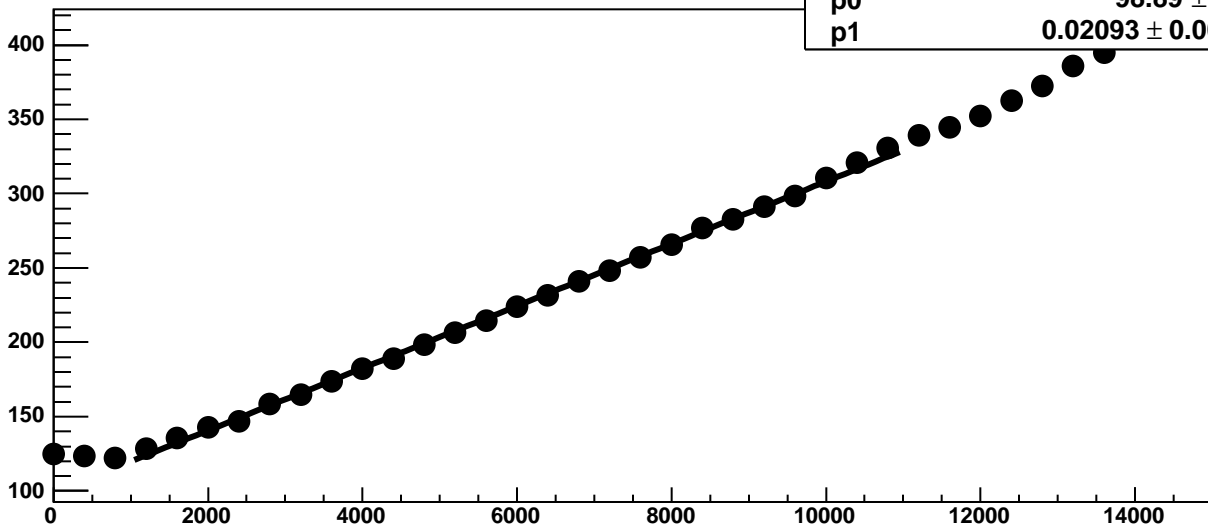
Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

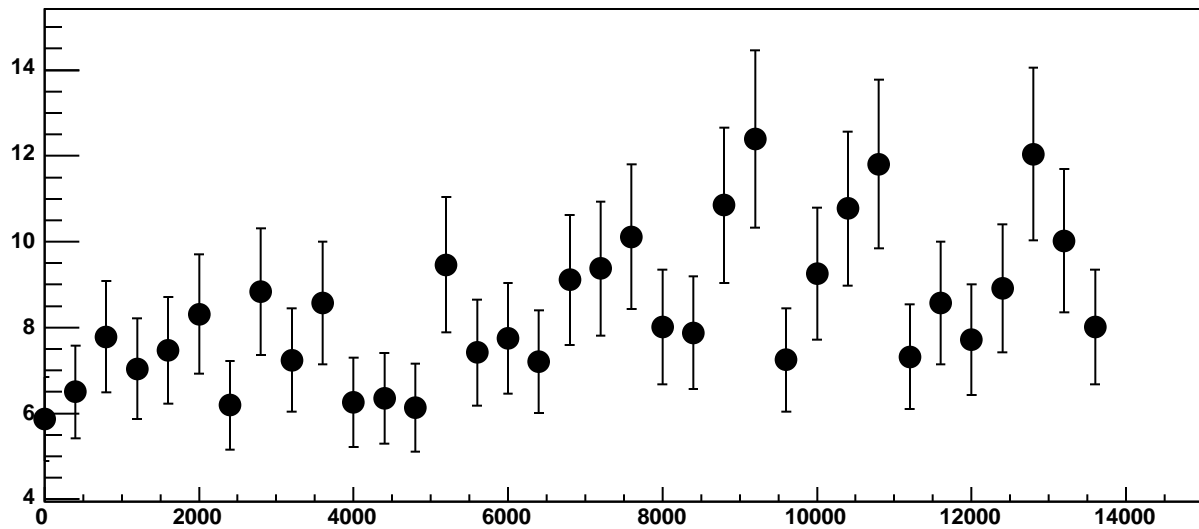


Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

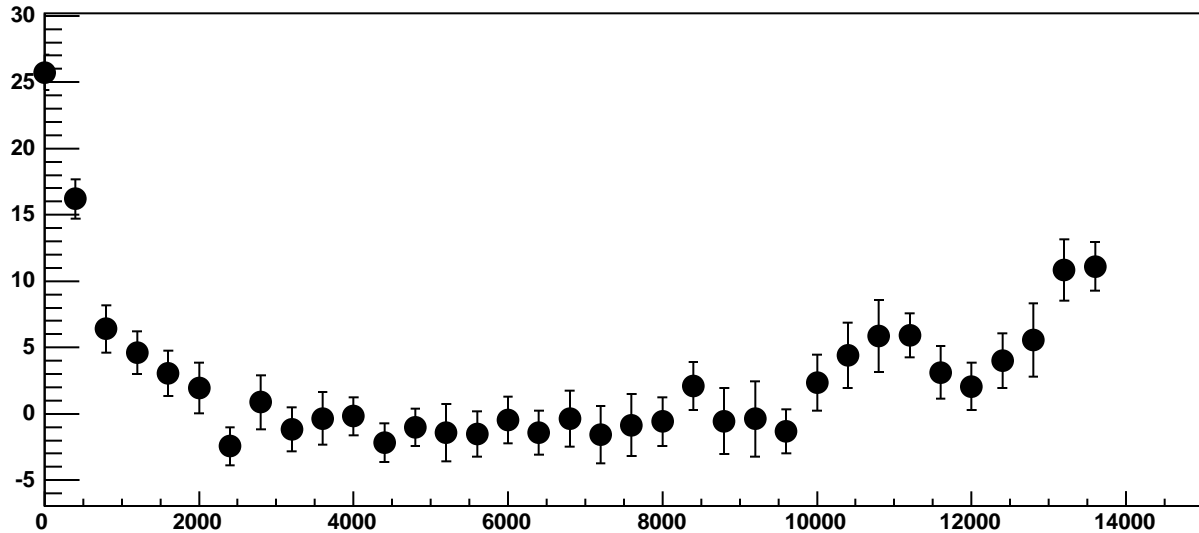


$\chi^2 / \text{ndf}$  32.81 / 23  
p0 98.89 ± 0.8106  
p1 0.02093 ± 0.0001345

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

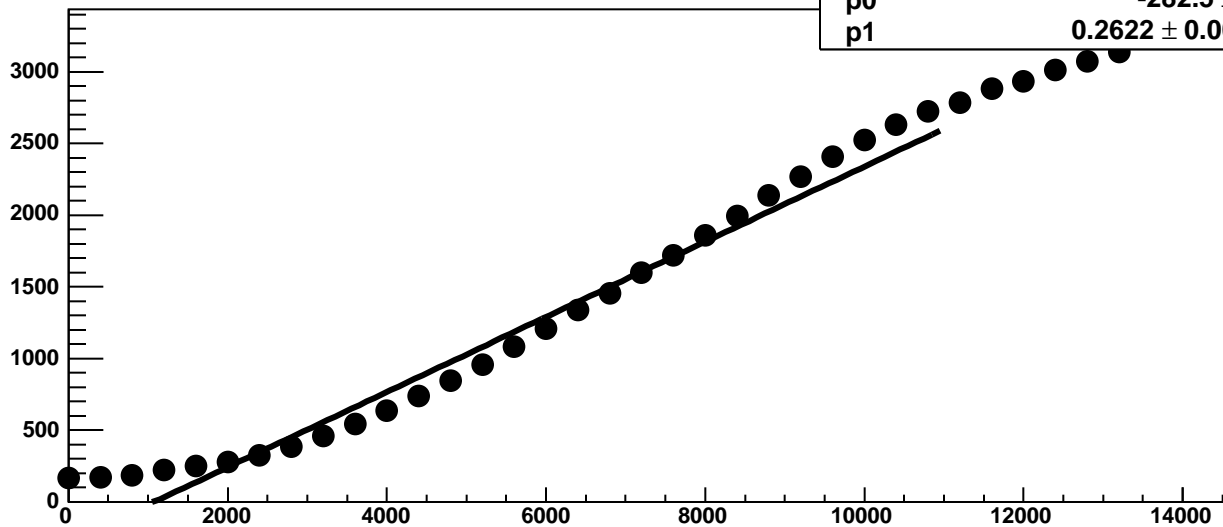


Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

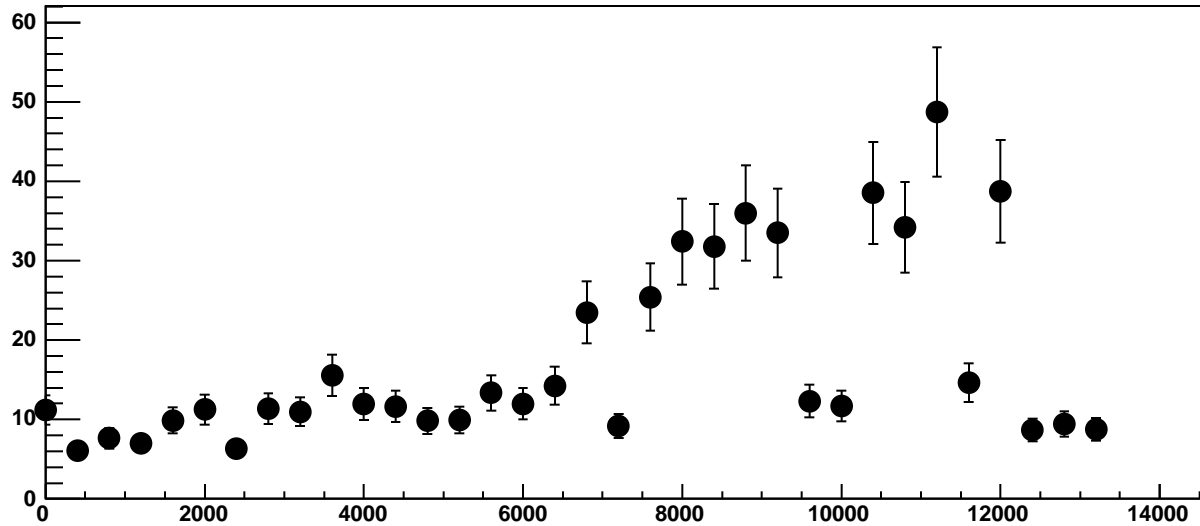


Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

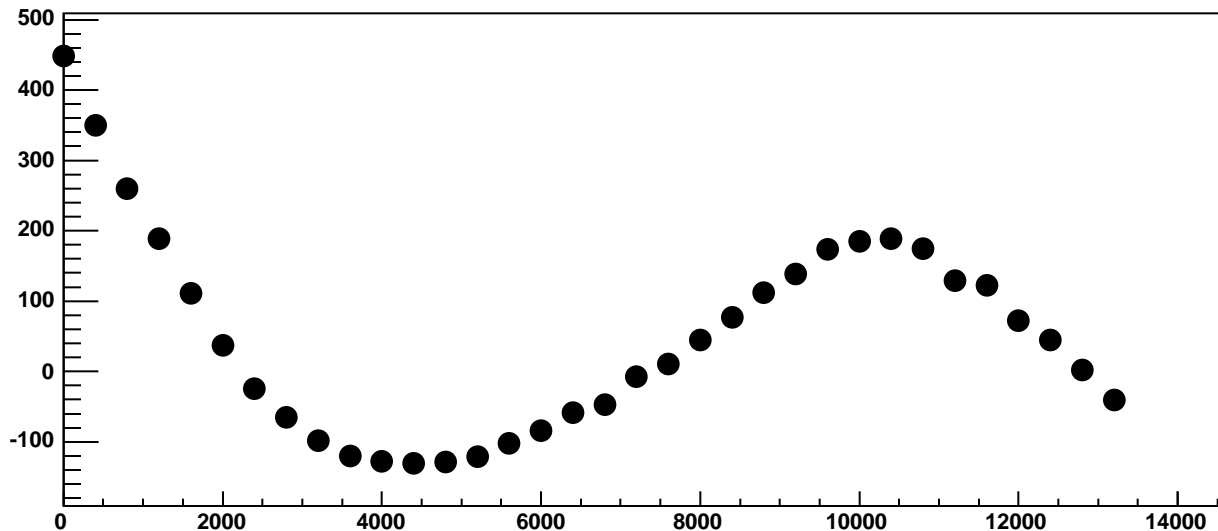
$\chi^2 / \text{ndf}$  4.341e+04 / 23  
p0 -282.5 ± 1.074  
p1 0.2622 ± 0.0002116



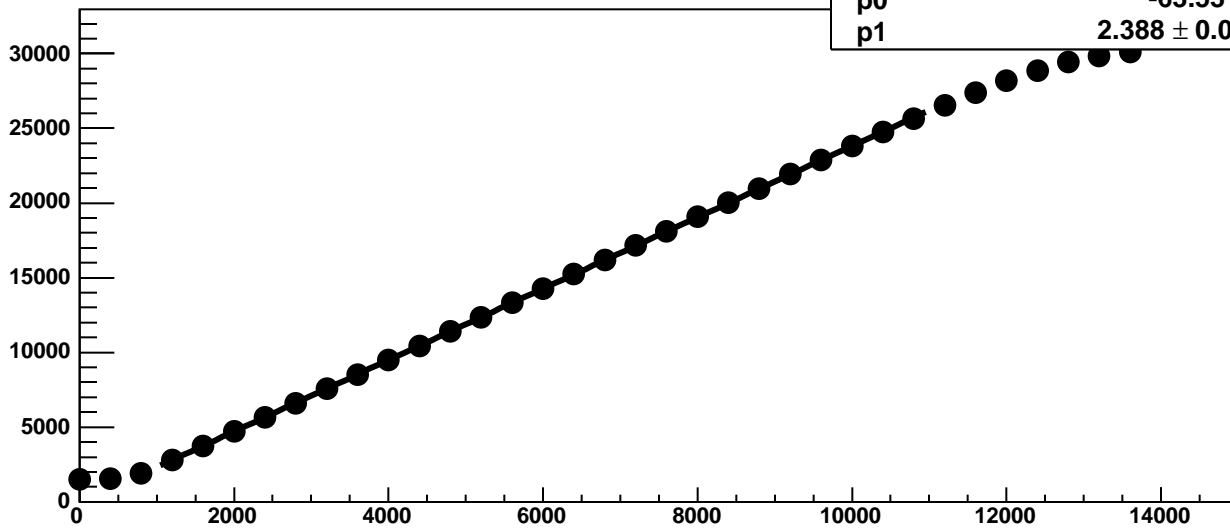
Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

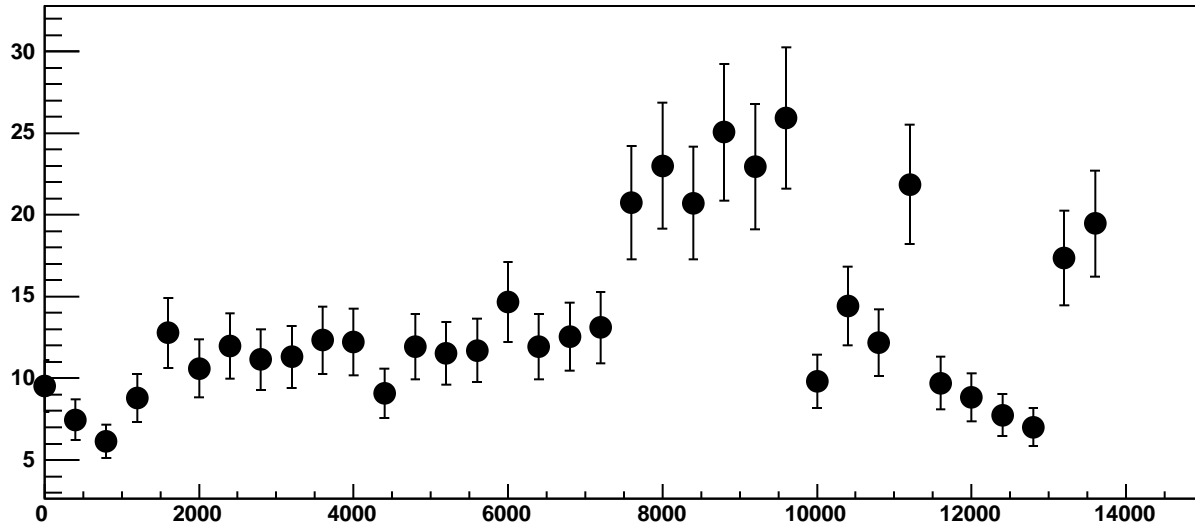


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

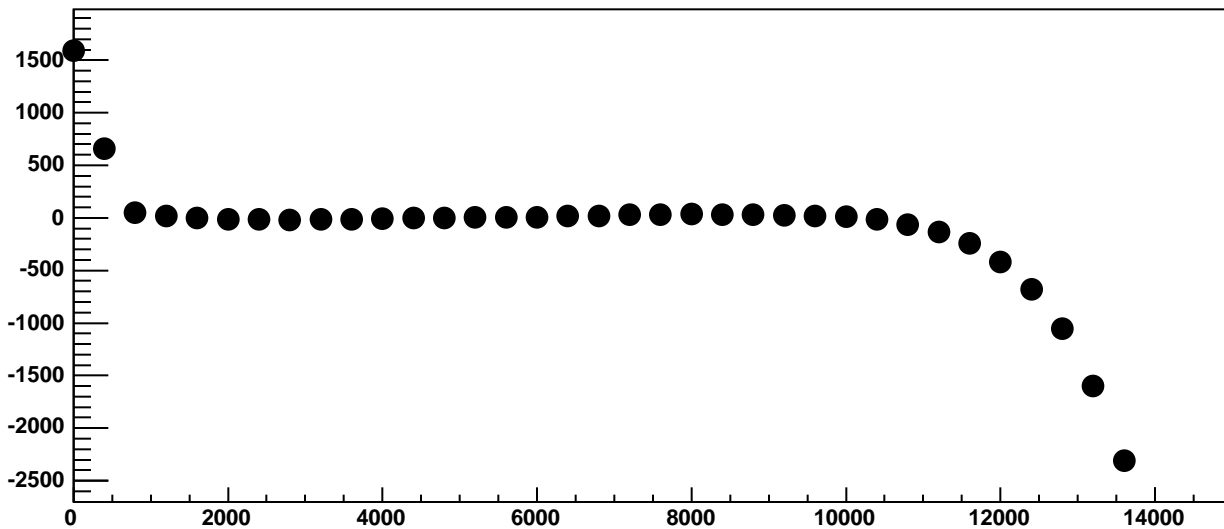


$\chi^2 / \text{ndf}$  1162 / 23  
p0  $-63.53 \pm 1.193$   
p1  $2.388 \pm 0.0002004$

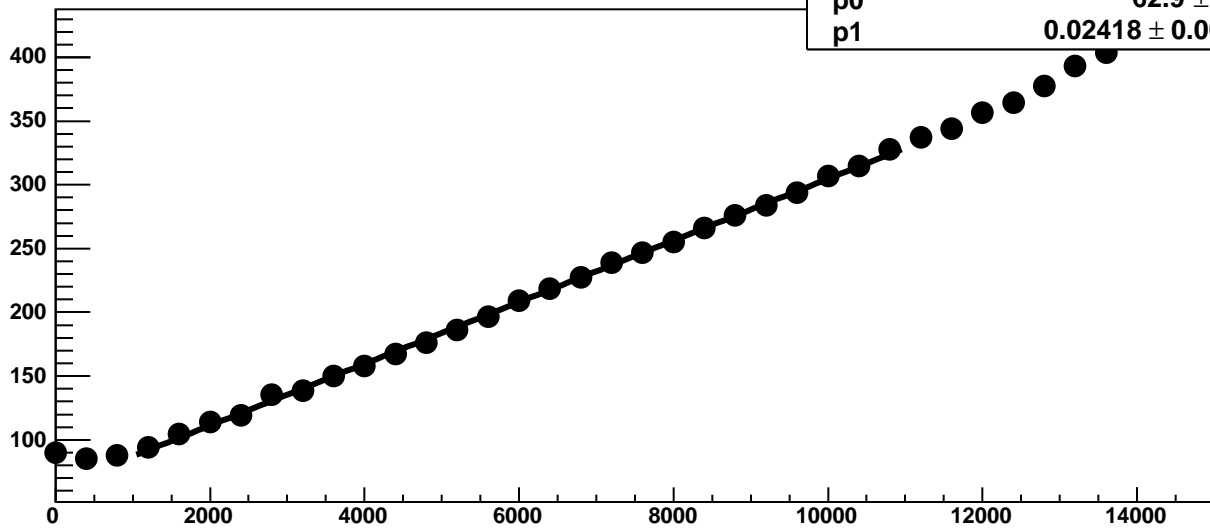
Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

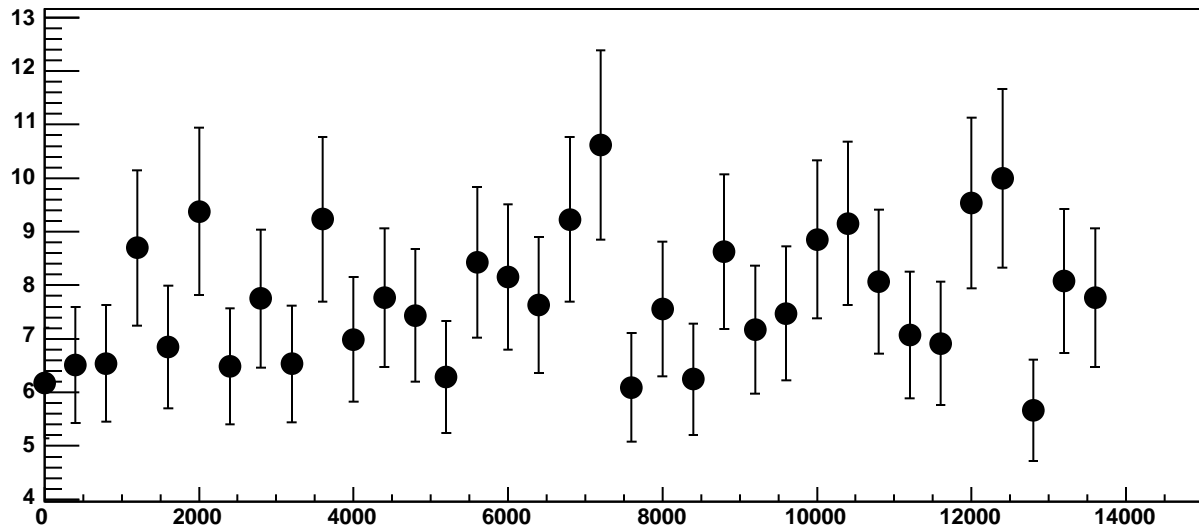


Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

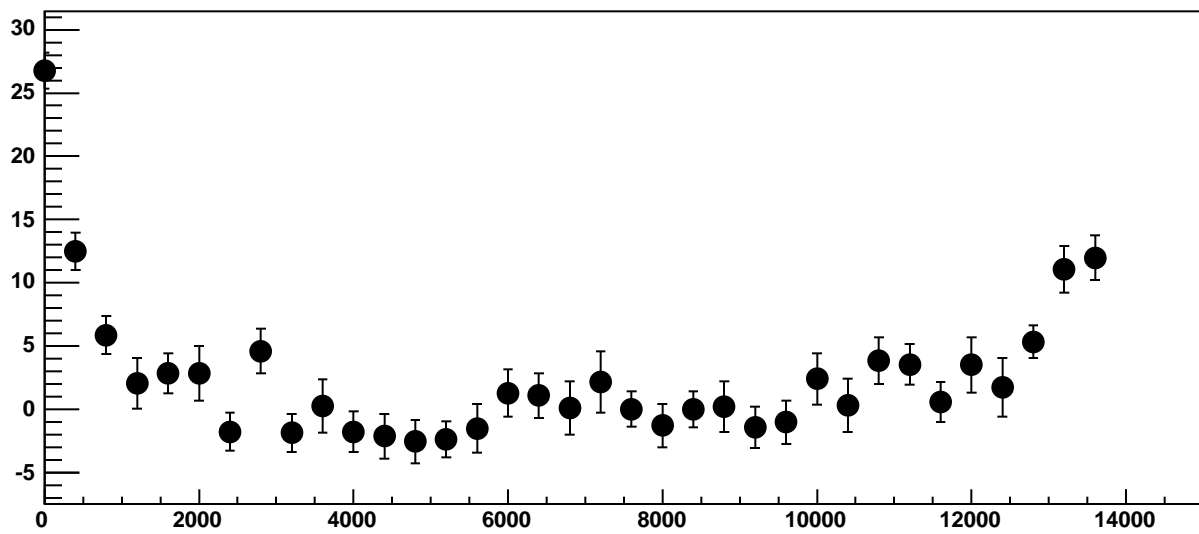


$\chi^2 / \text{ndf}$  32.98 / 23  
p0 62.9 ± 0.8107  
p1 0.02418 ± 0.0001239

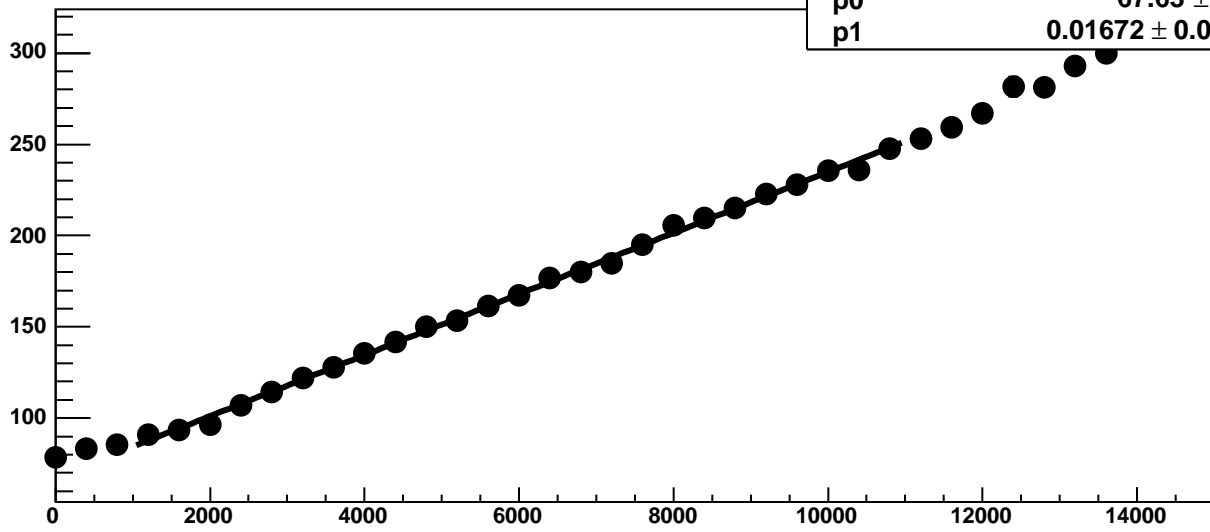
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

39.1 / 23

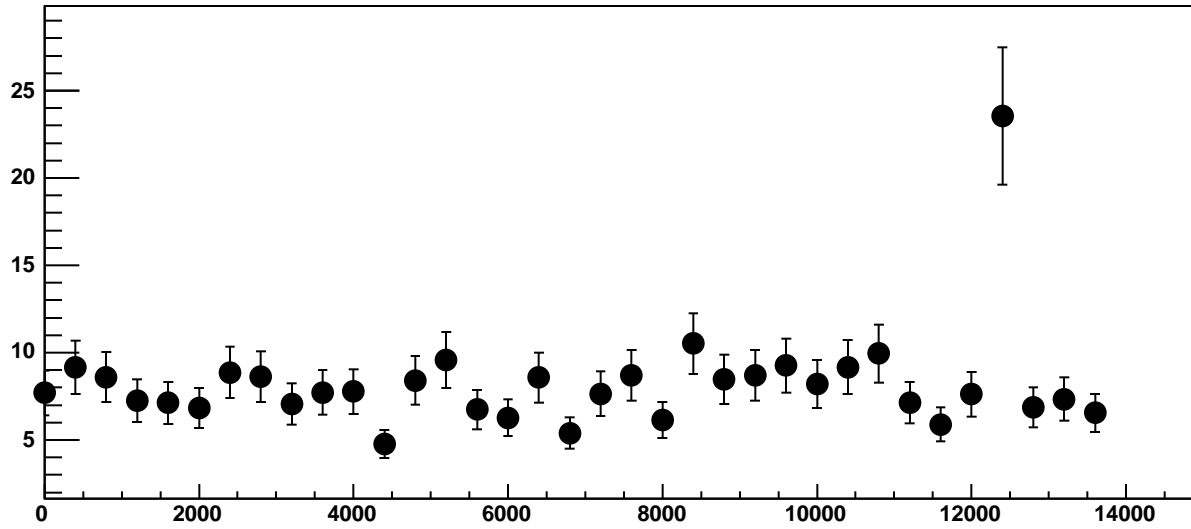
p0

$67.63 \pm 0.8015$

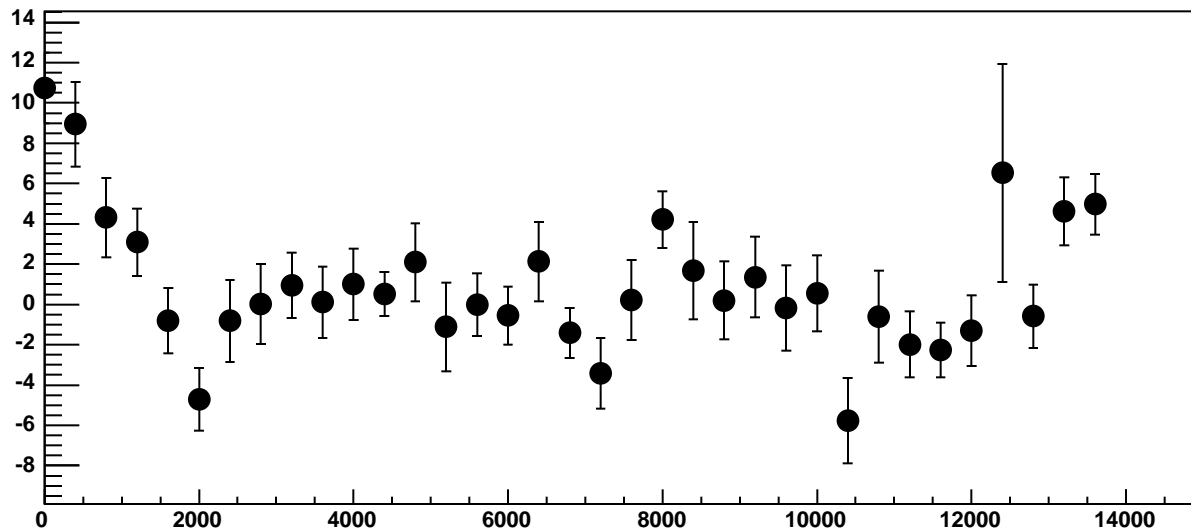
p1

$0.01672 \pm 0.0001281$

Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

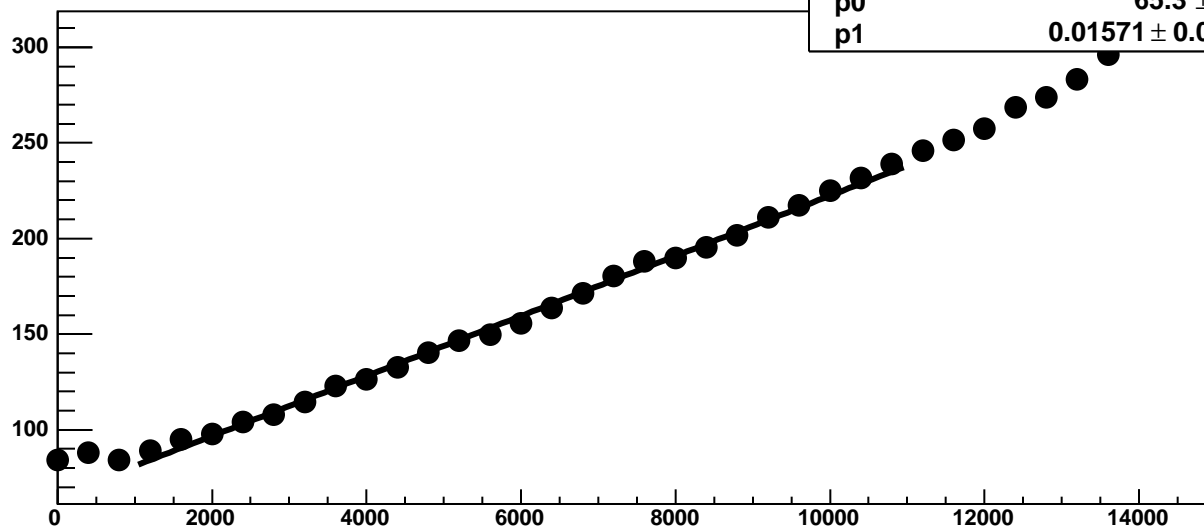


Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



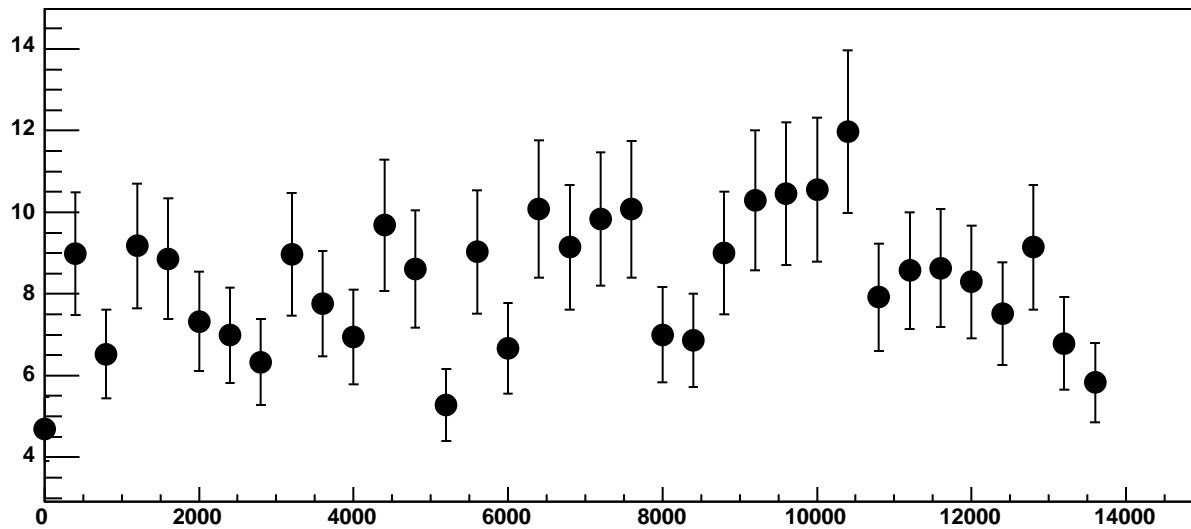


Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

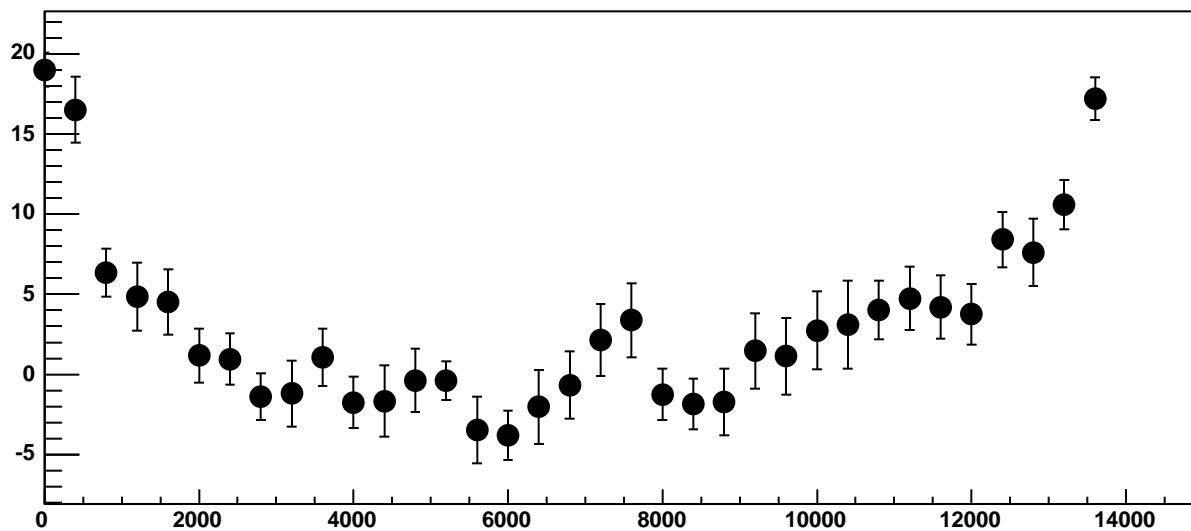


$\chi^2 / \text{ndf}$  38.27 / 23  
p0 65.3 ± 0.8492  
p1 0.01571 ± 0.0001362

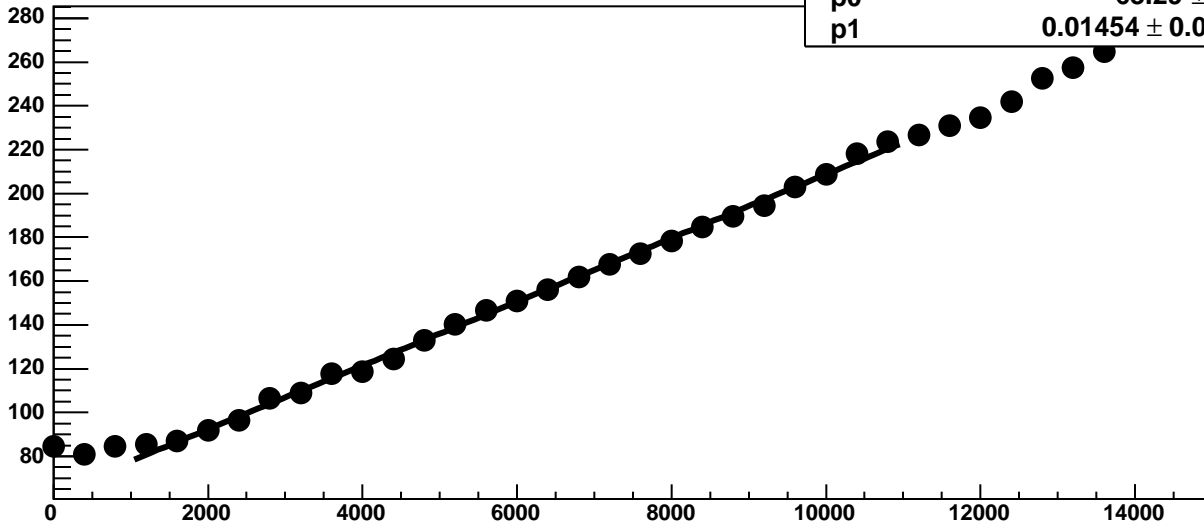
Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

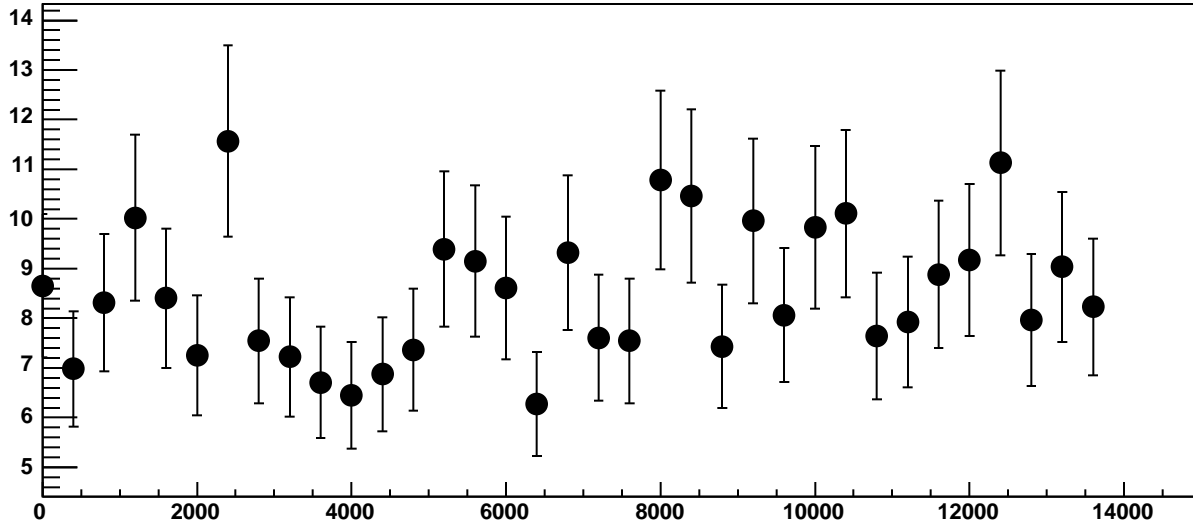


Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

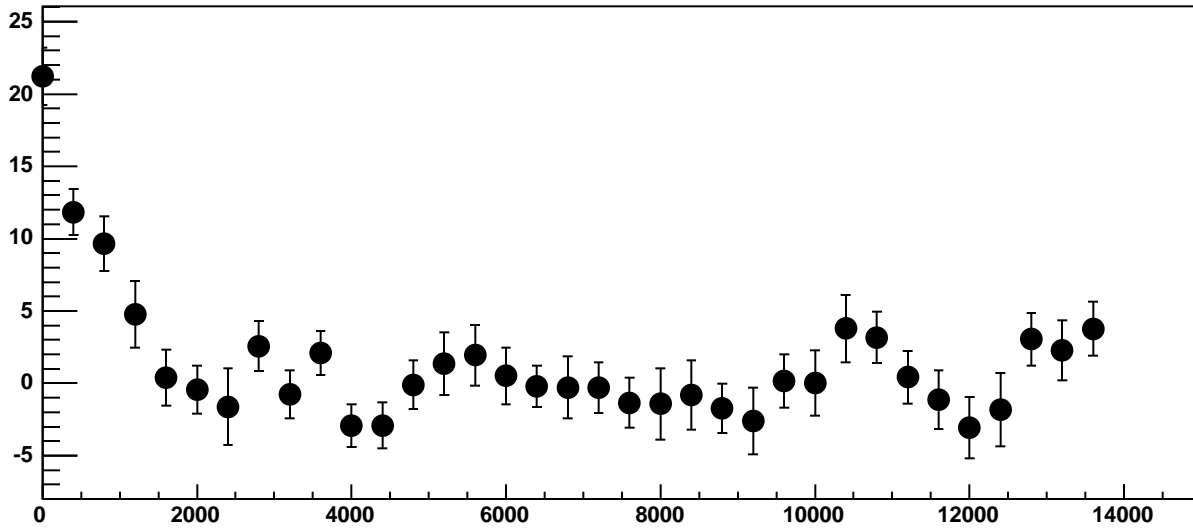


$\chi^2 / \text{ndf}$  27.14 / 23  
p0  $63.29 \pm 0.8644$   
p1  $0.01454 \pm 0.0001349$

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

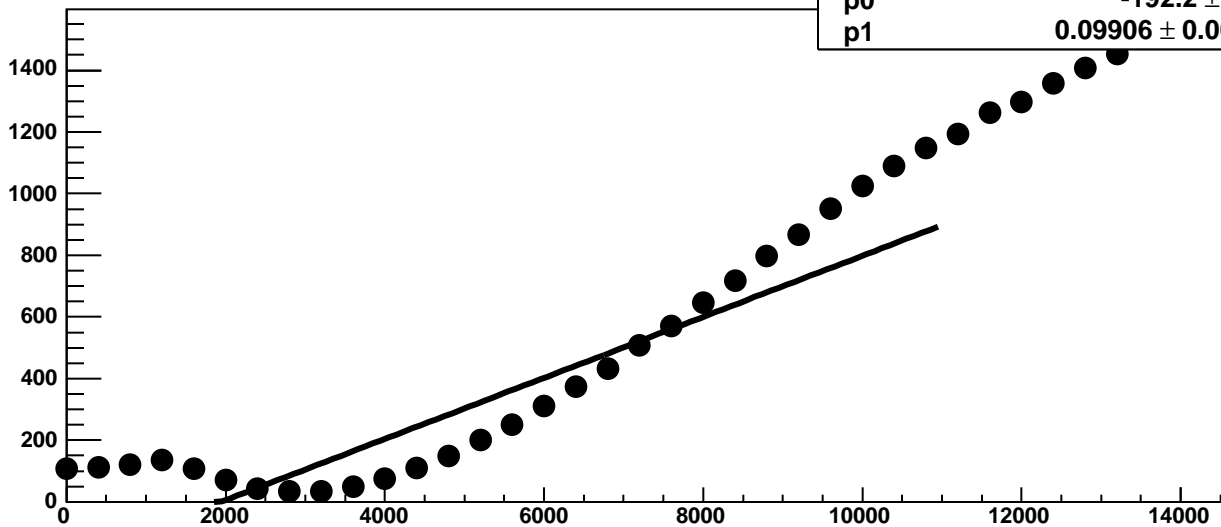


Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

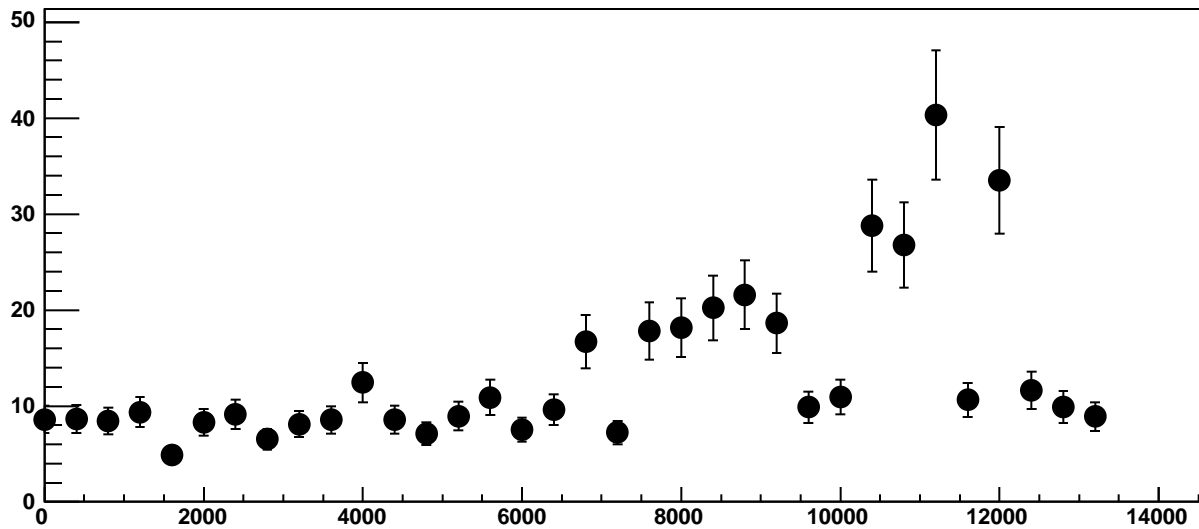


Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

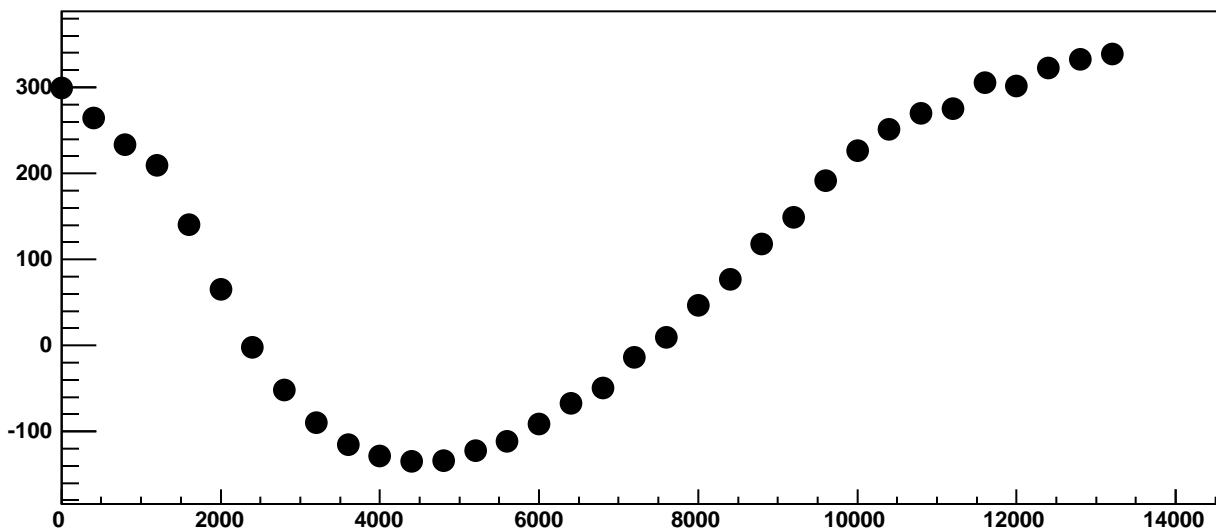
$\chi^2 / \text{ndf}$  7.699e+04 / 23  
p0 -192.2 ± 0.8838  
p1 0.09906 ± 0.0001708



Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

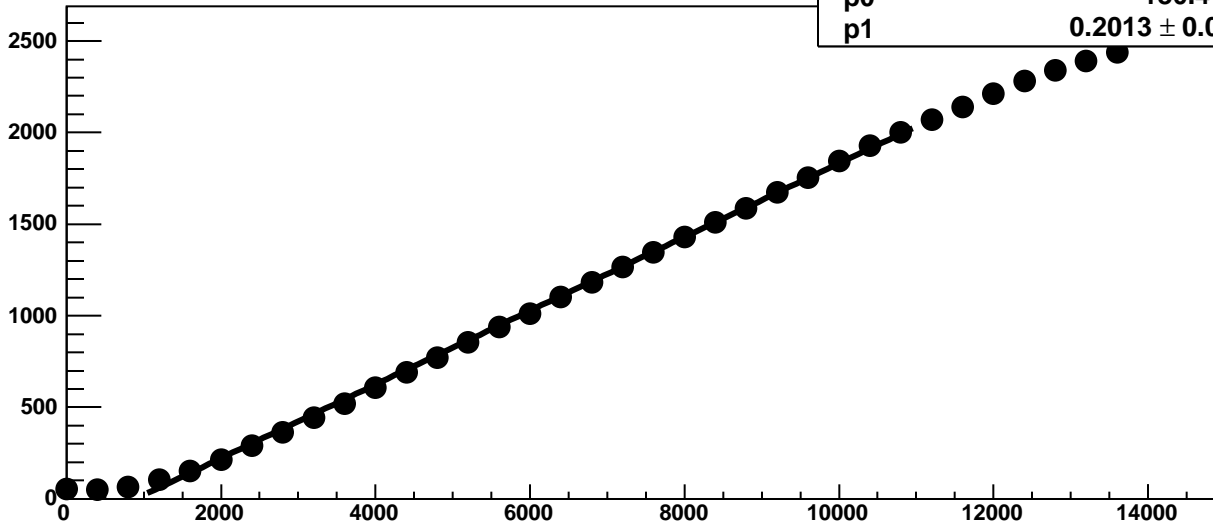


Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

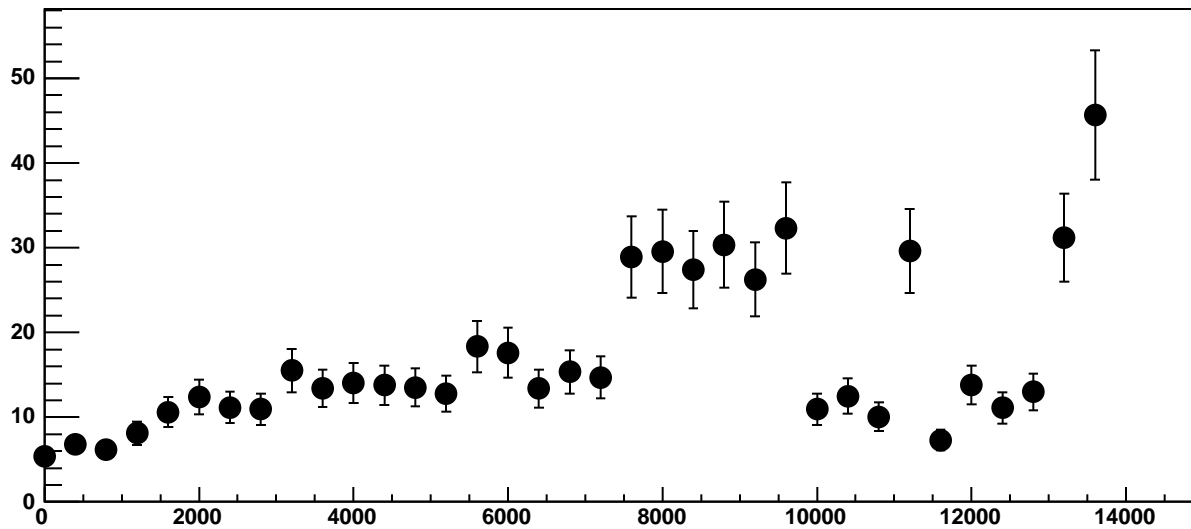


Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

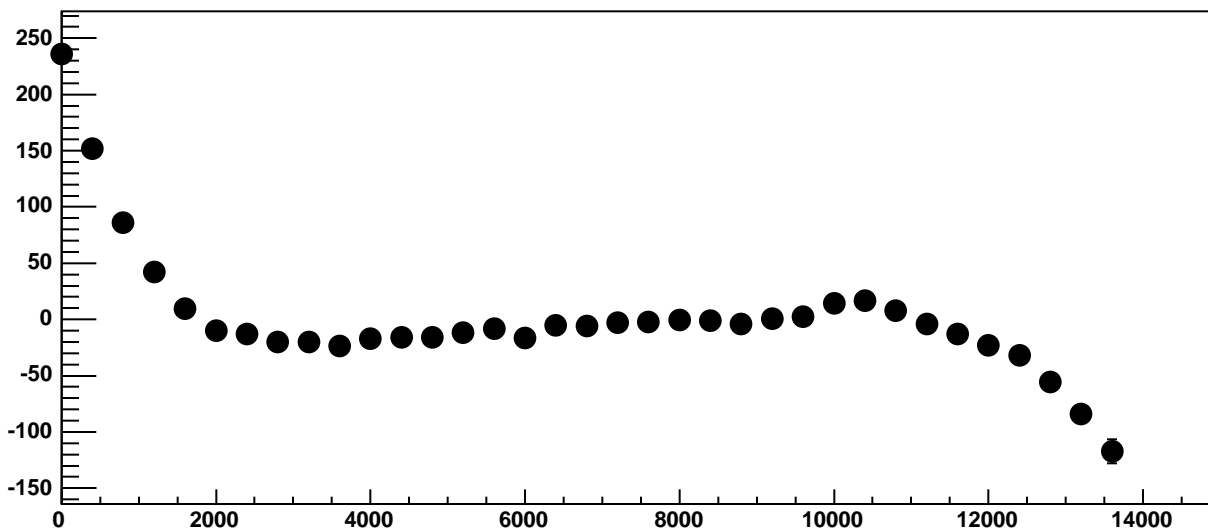
$\chi^2 / \text{ndf}$  924.8 / 23  
p0  $-180.4 \pm 1.189$   
p1  $0.2013 \pm 0.0001954$



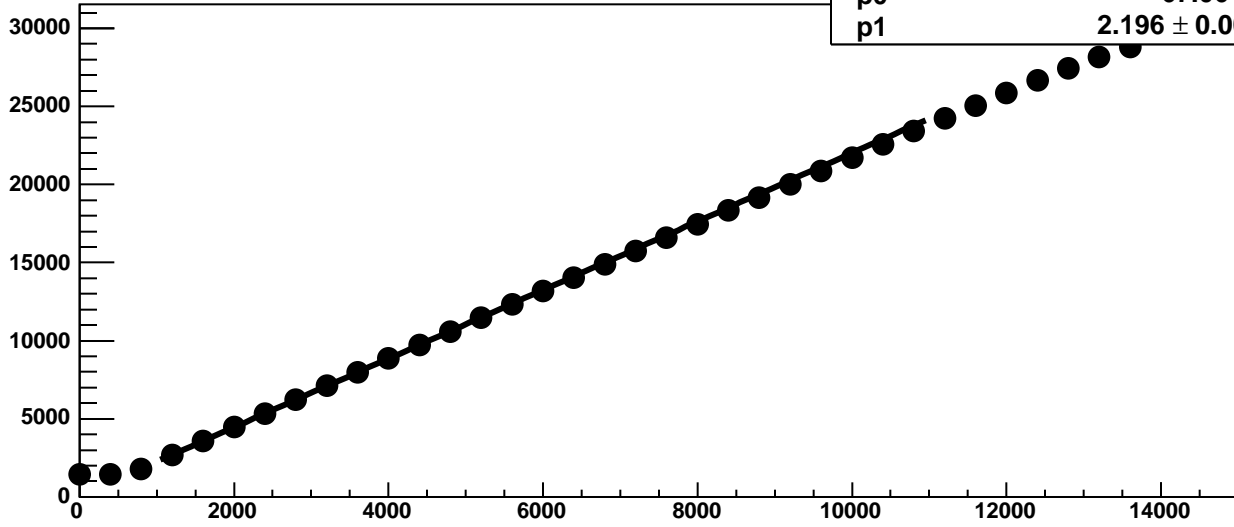
Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

724.5 / 23

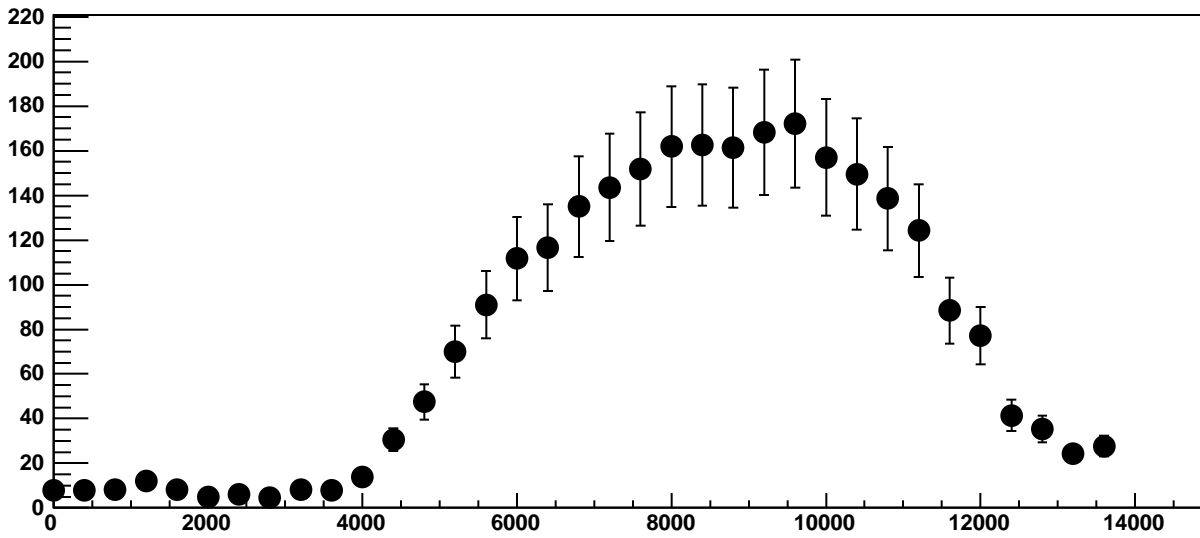
p0

$67.06 \pm 1.901$

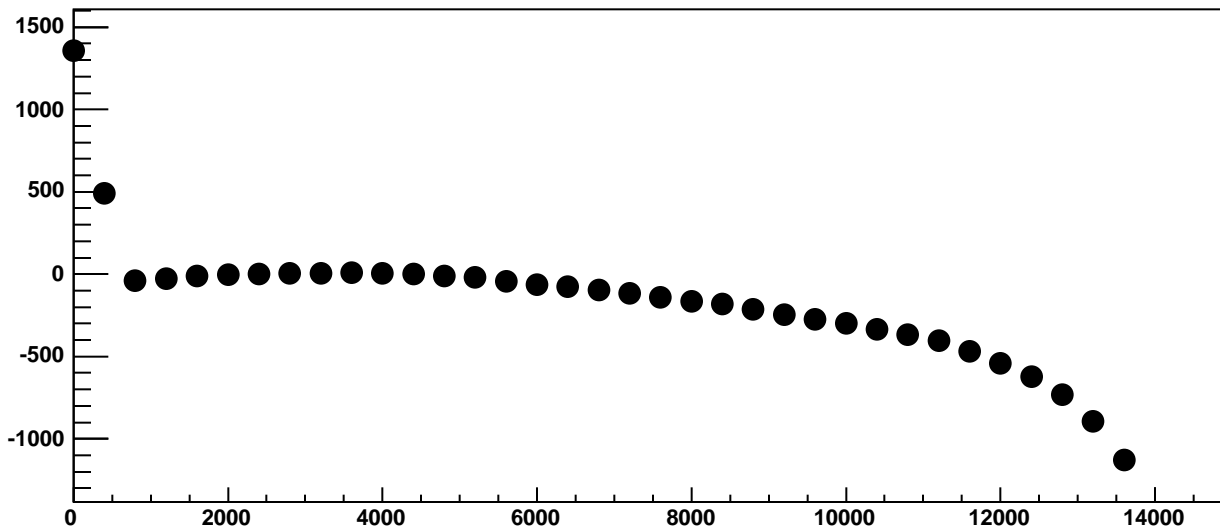
p1

$2.196 \pm 0.0007069$

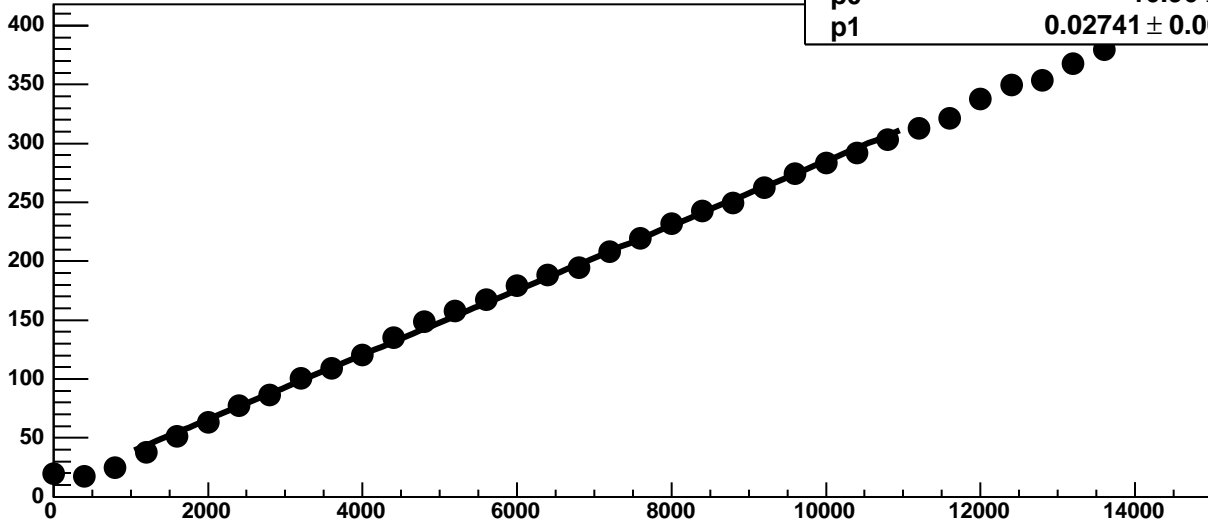
Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

67.96 / 23

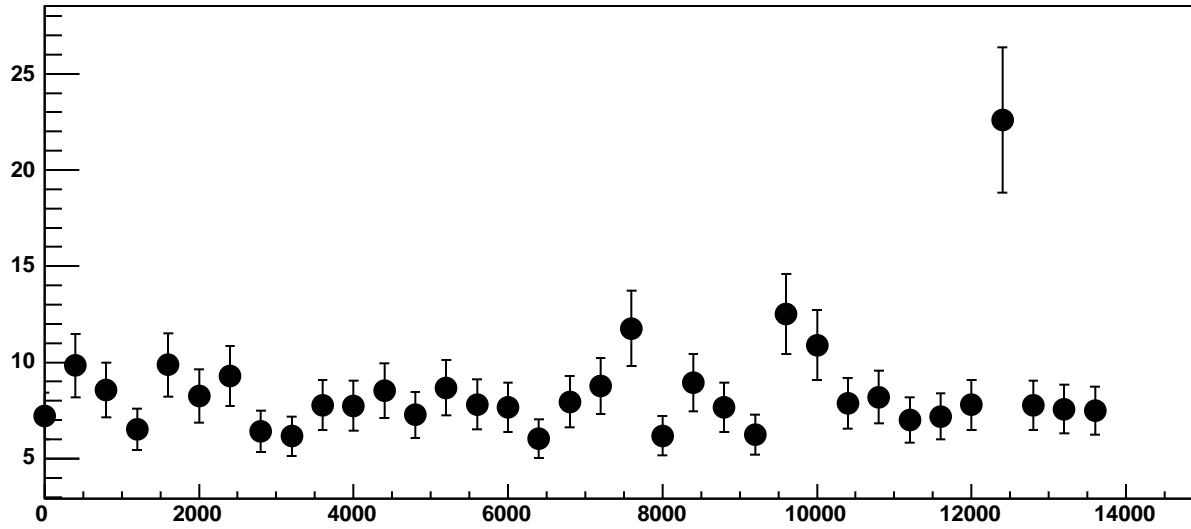
p0

$10.96 \pm 0.815$

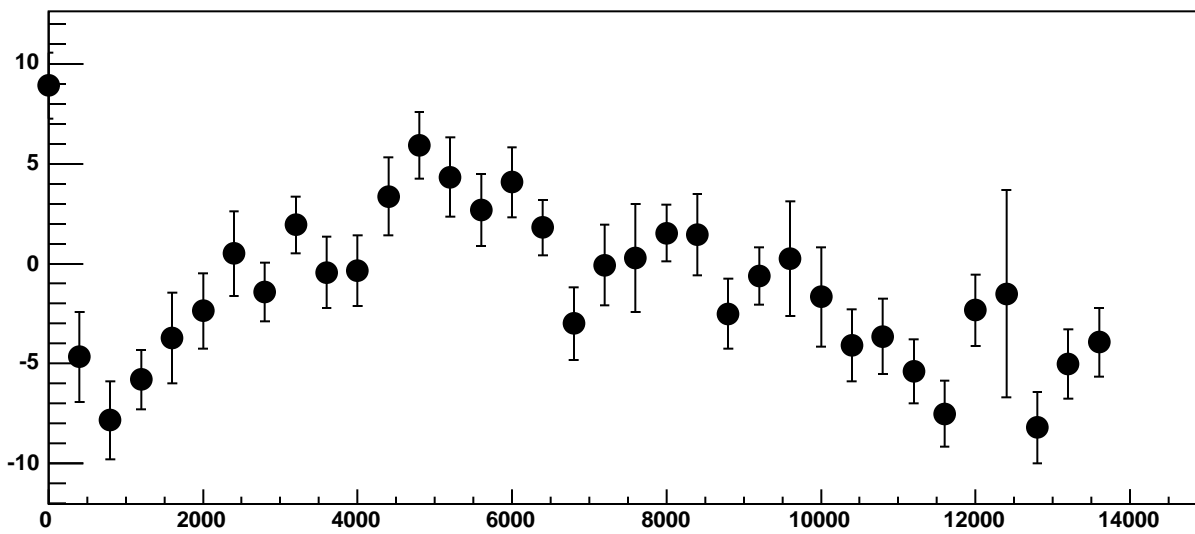
p1

$0.02741 \pm 0.0001264$

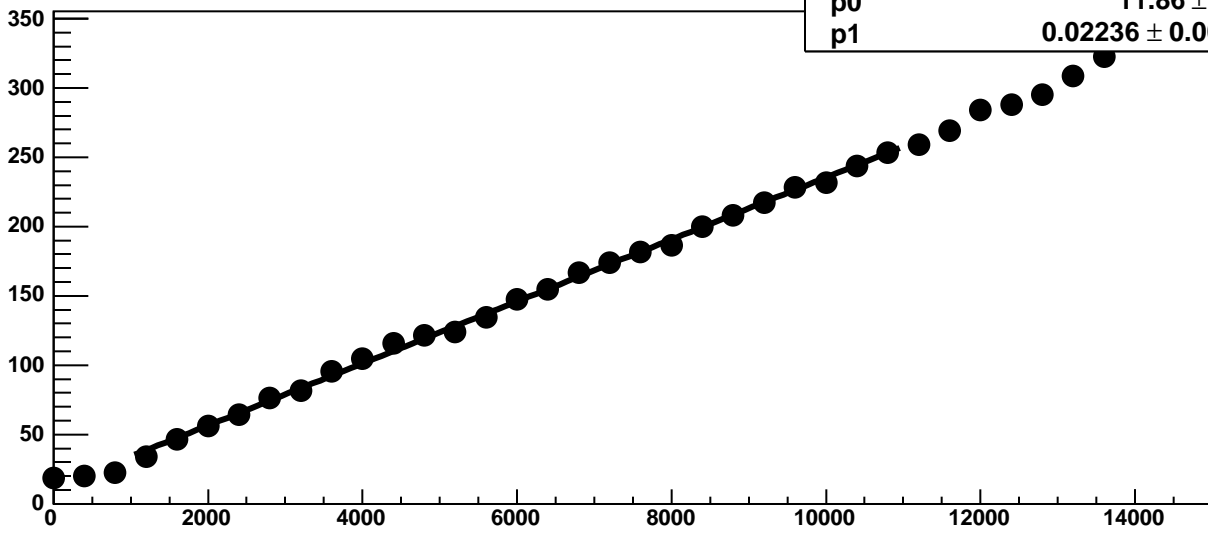
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

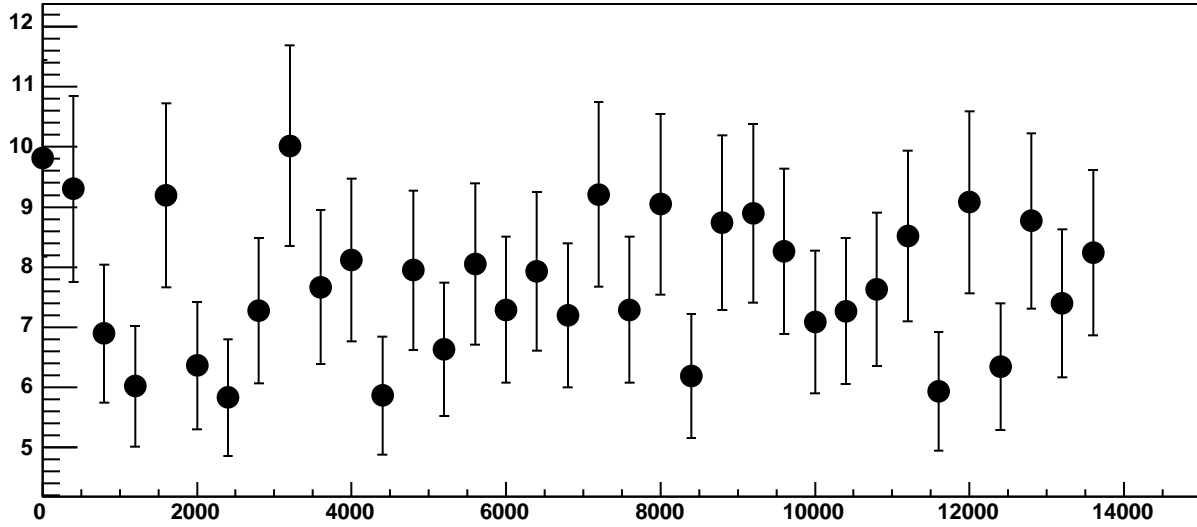


Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

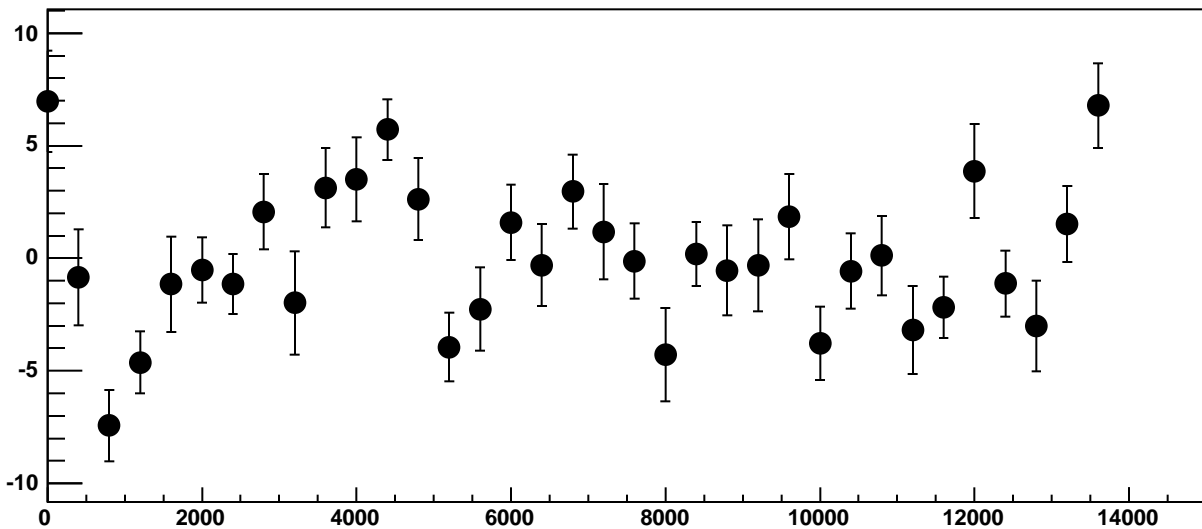


$\chi^2 / \text{ndf}$  65.13 / 23  
p0 11.86 ± 0.7472  
p1 0.02236 ± 0.0001156

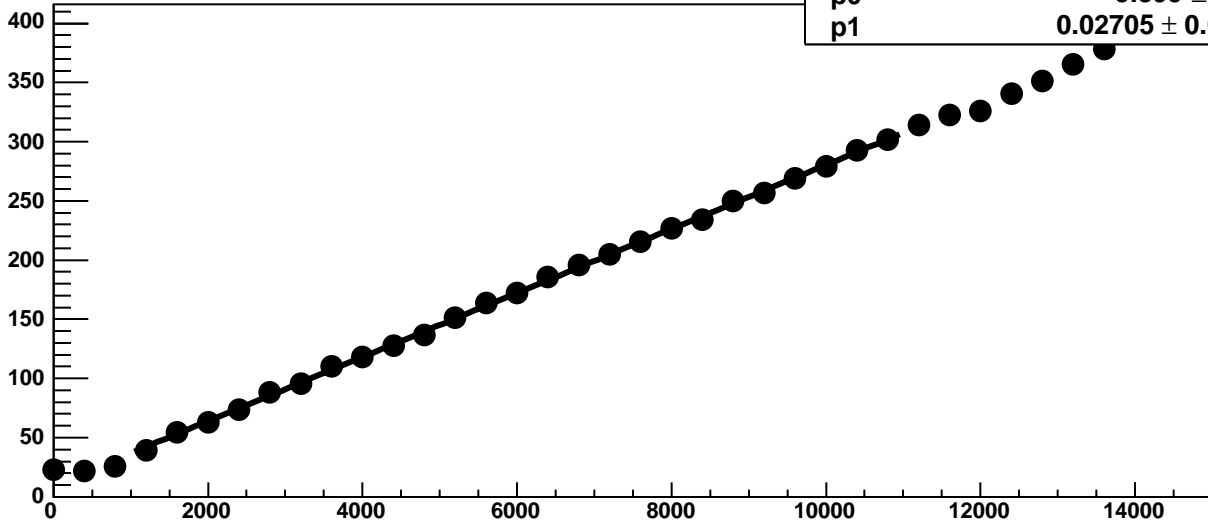
Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

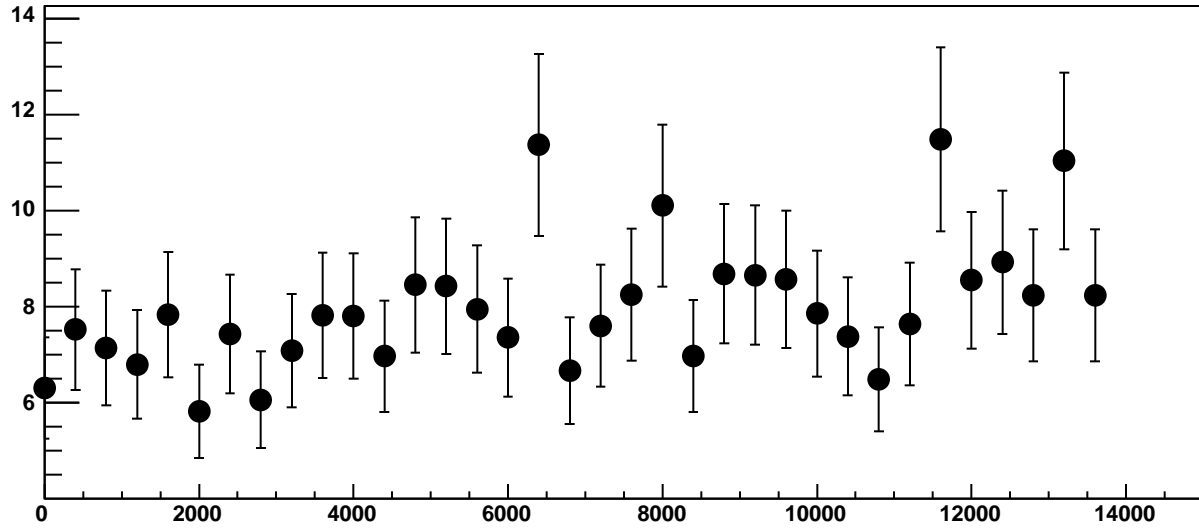


Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

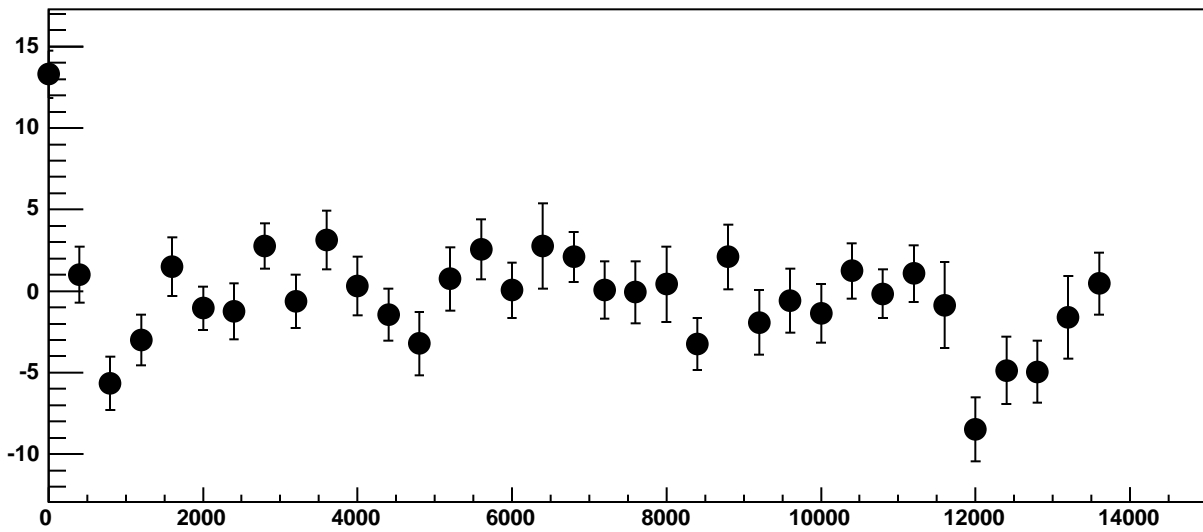


$\chi^2 / \text{ndf}$  28.83 / 23  
p0  $9.899 \pm 0.7445$   
p1  $0.02705 \pm 0.000115$

Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

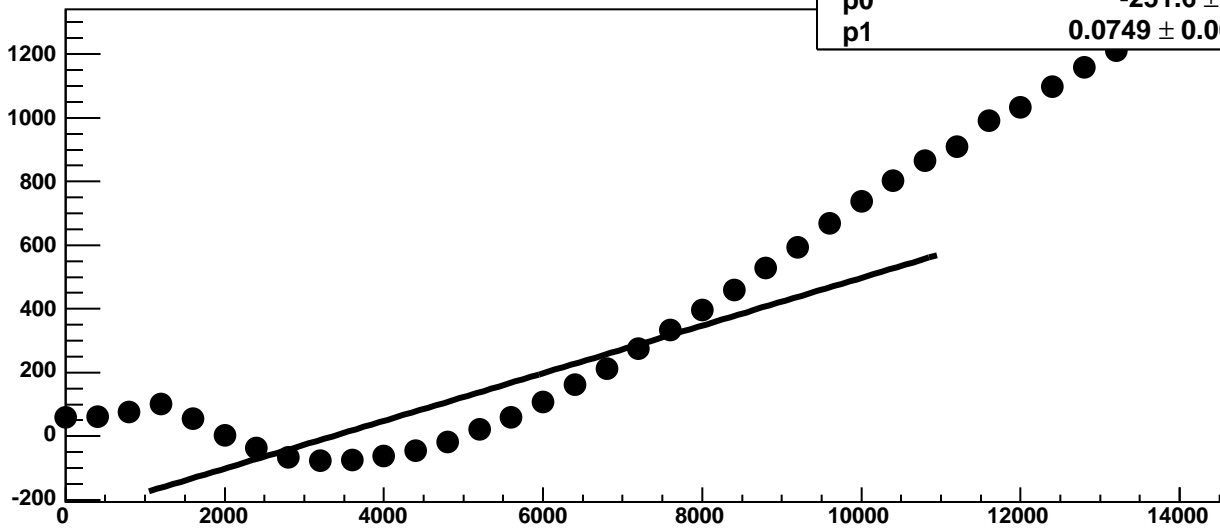


Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



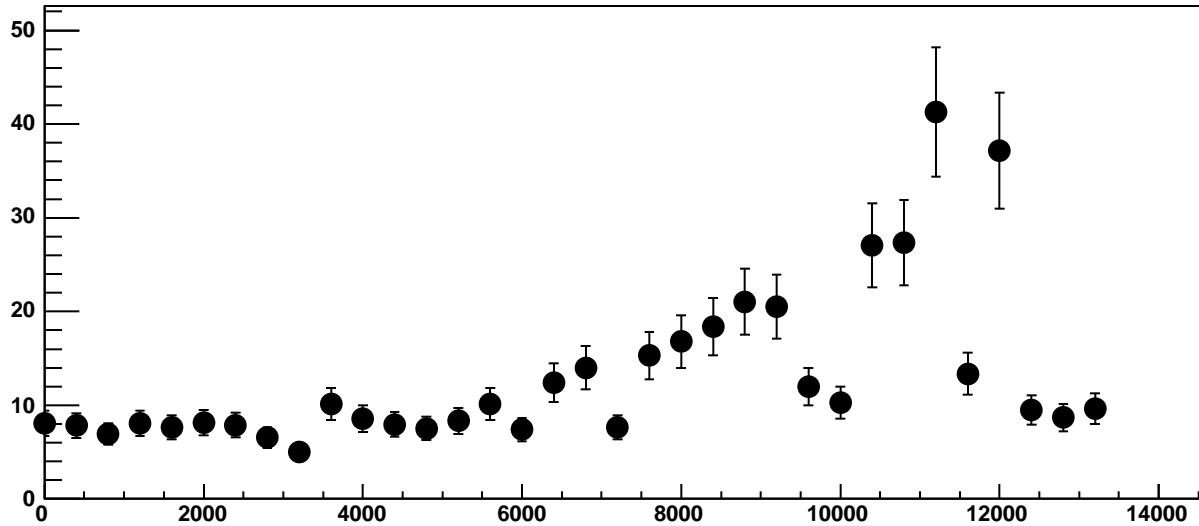


Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

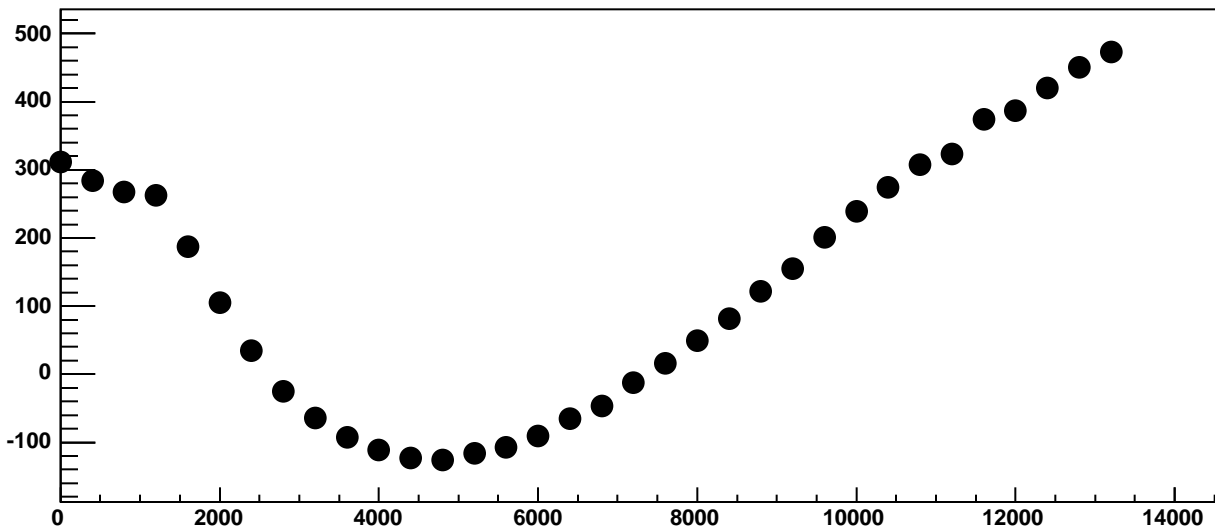


$\chi^2 / \text{ndf}$  8.471e+04 / 23  
p0 -251.6 ± 0.9135  
p1 0.0749 ± 0.0001786

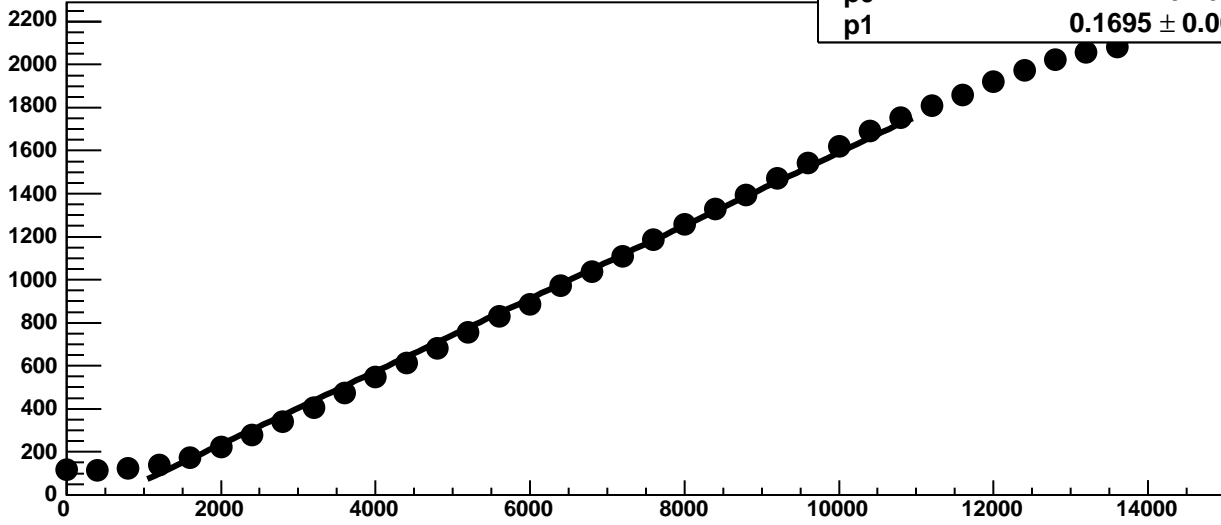
Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1939 / 23

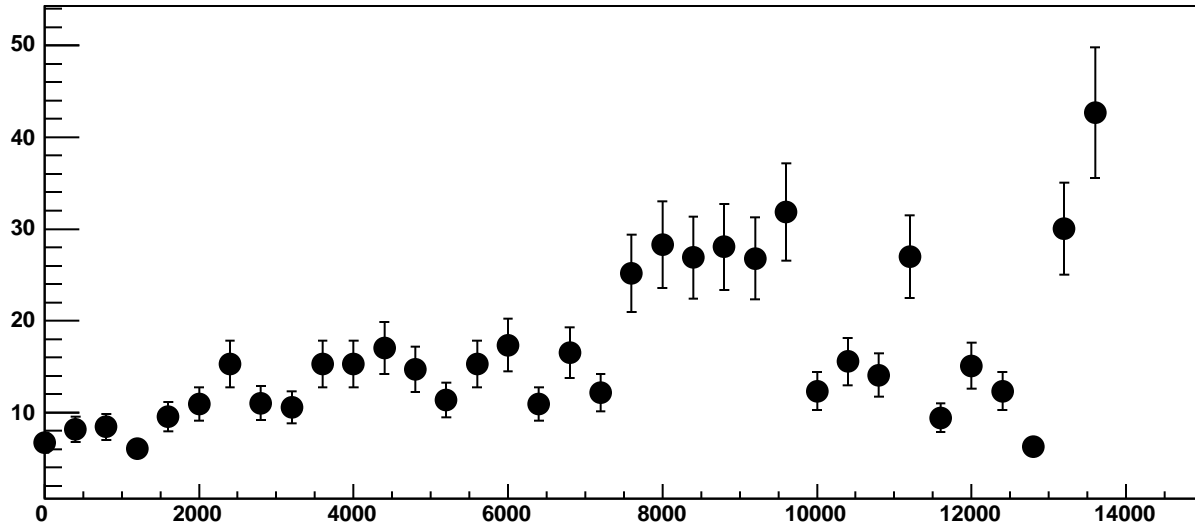
p0

$-104.6 \pm 1.09$

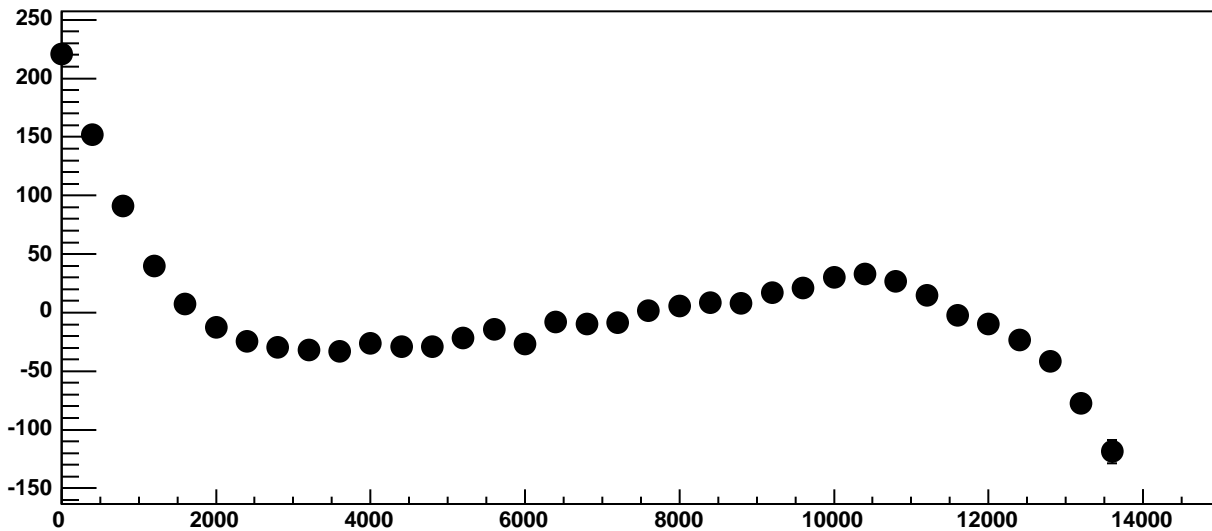
p1

$0.1695 \pm 0.0002015$

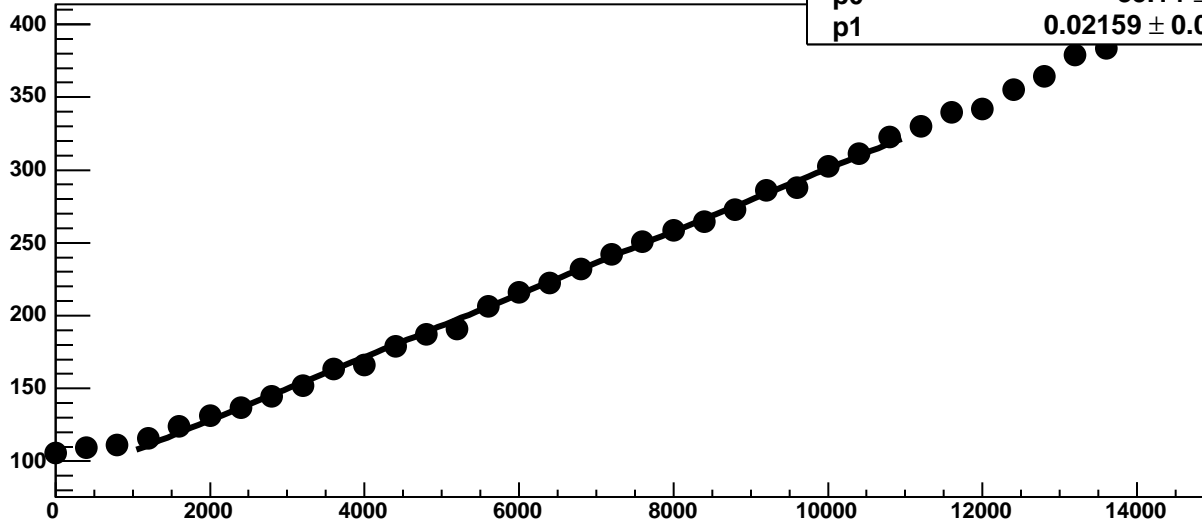
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

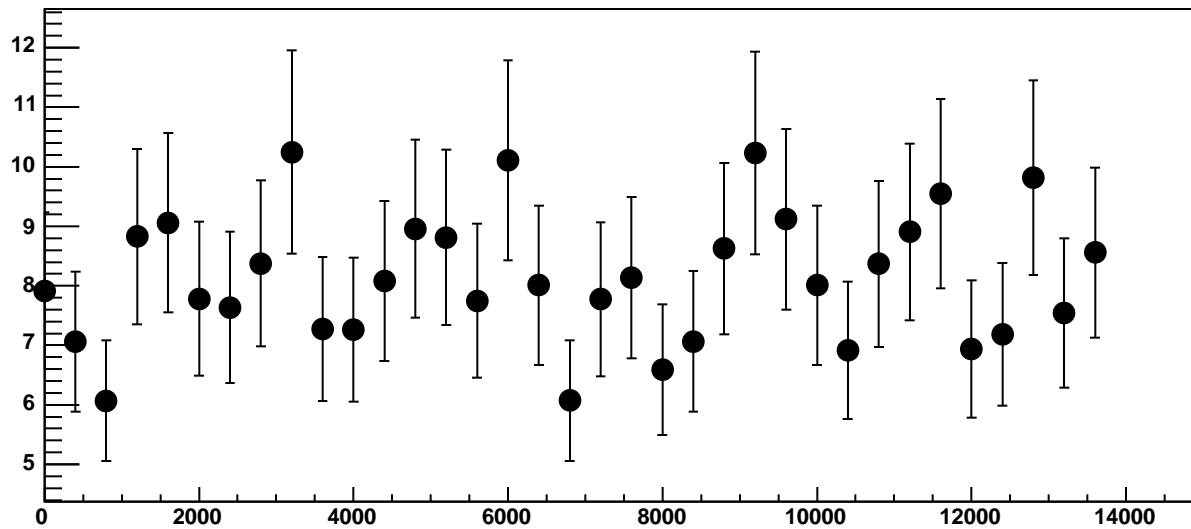


Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

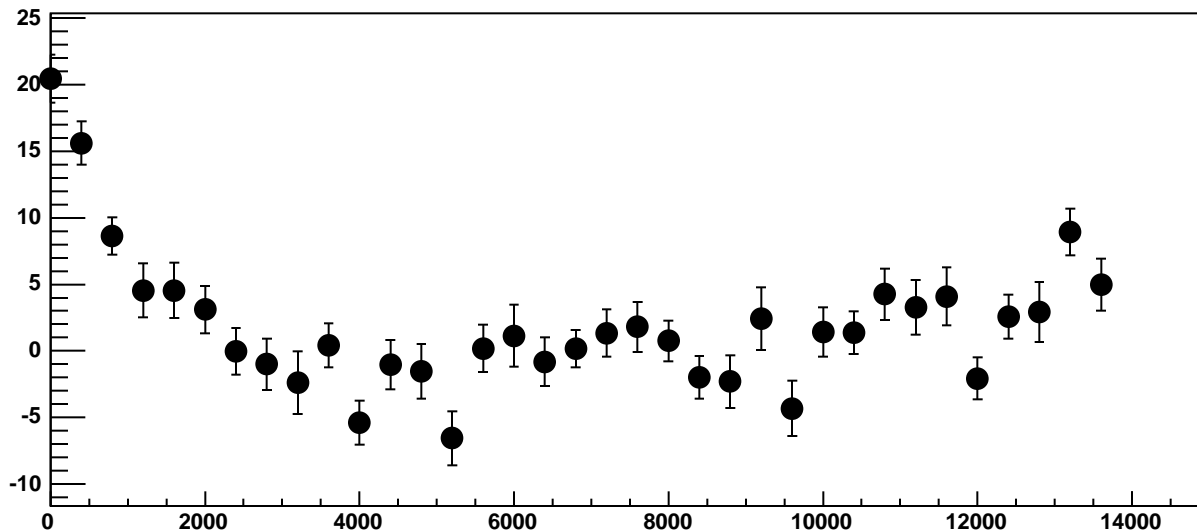


$\chi^2 / \text{ndf}$  52.56 / 23  
p0 85.14 ± 0.8754  
p1 0.02159 ± 0.0001299

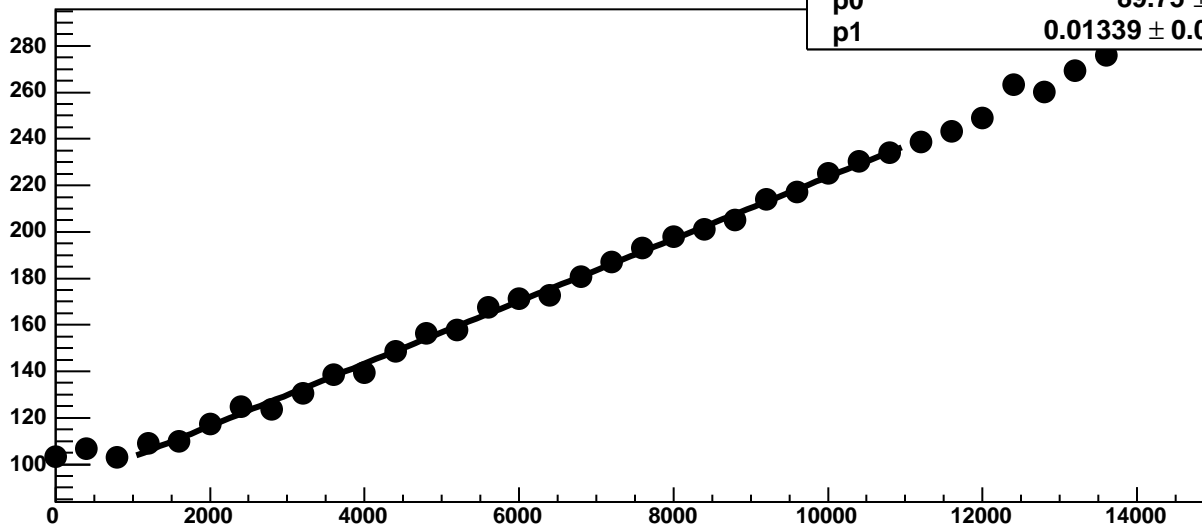
Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

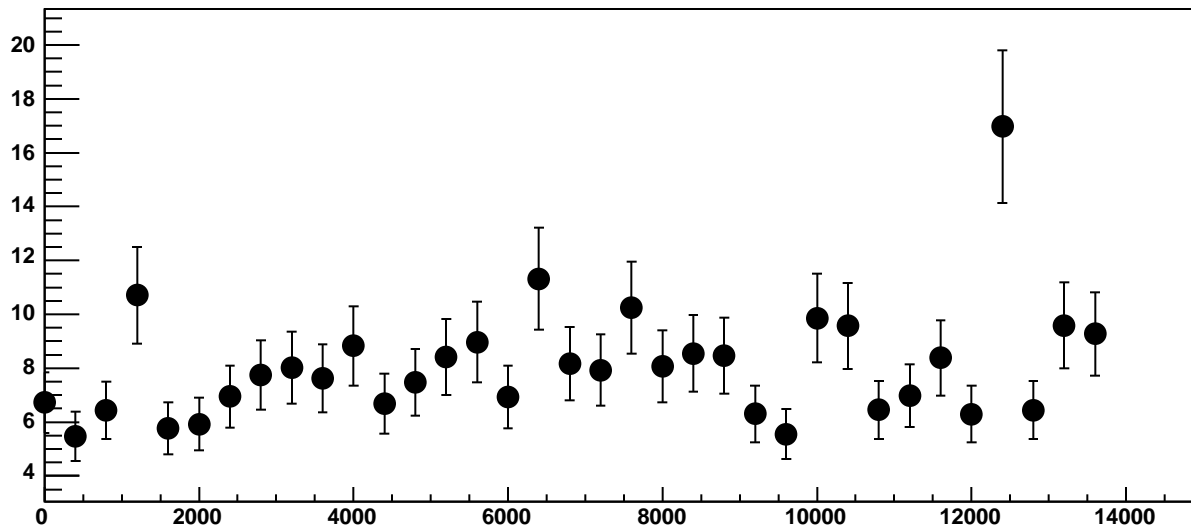


Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

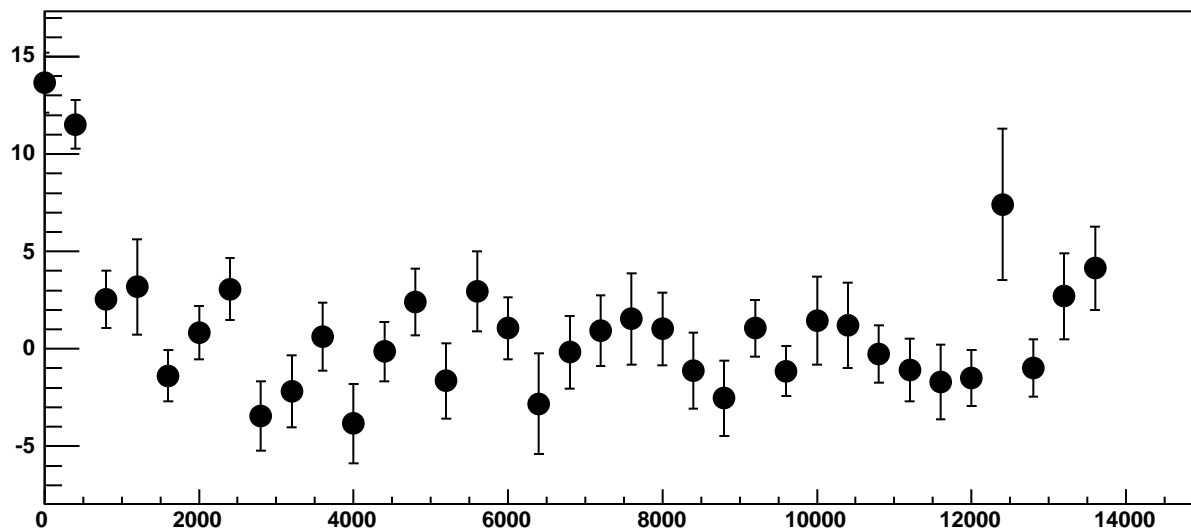


$\chi^2 / \text{ndf}$  27.21 / 23  
p0 89.75 ± 0.7644  
p1 0.01339 ± 0.0001154

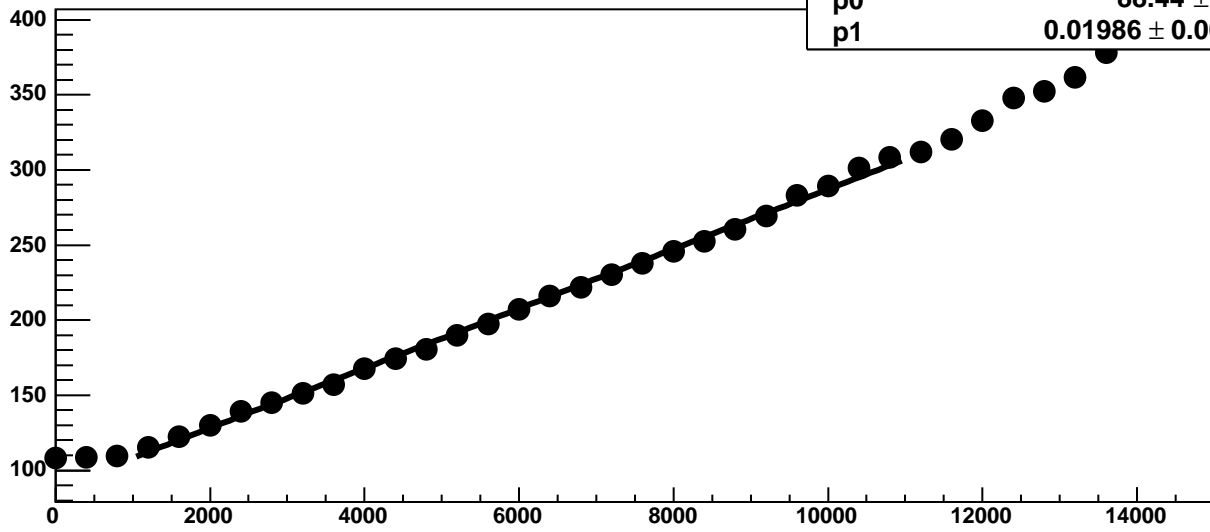
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

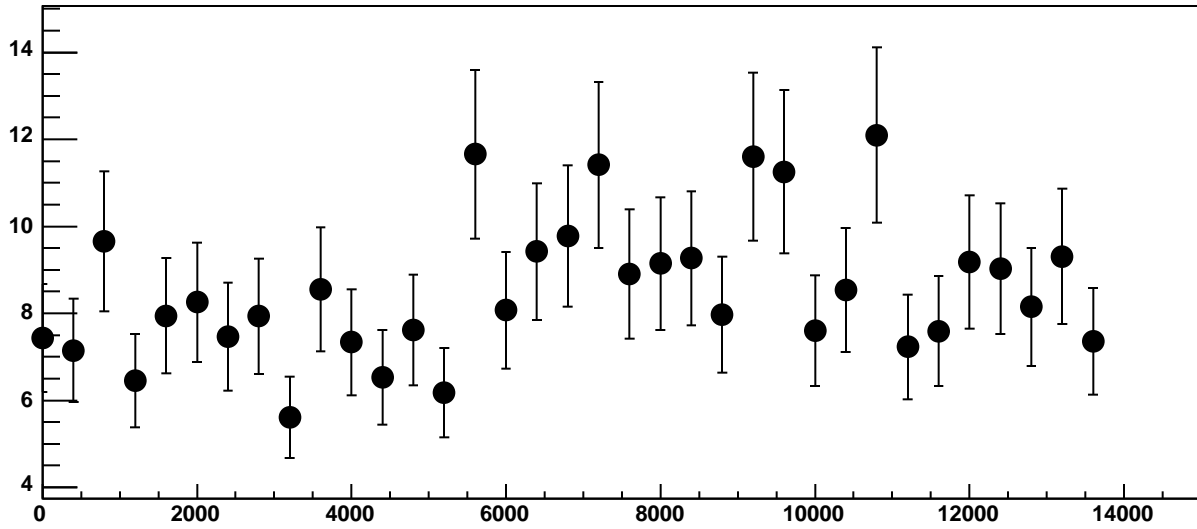


Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

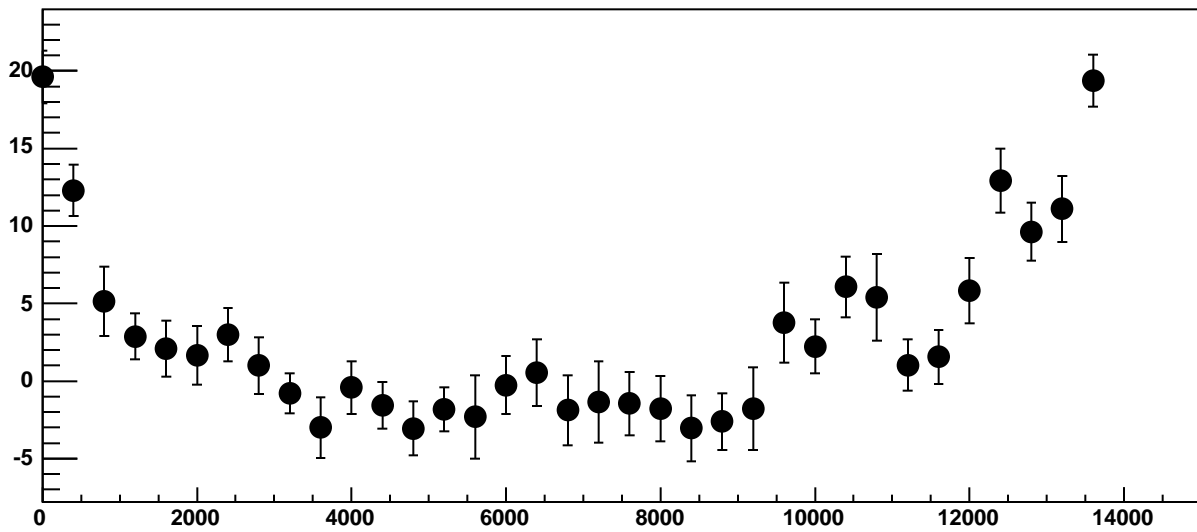


$\chi^2 / \text{ndf}$  42.59 / 23  
p0 88.44 ± 0.8004  
p1 0.01986 ± 0.0001324

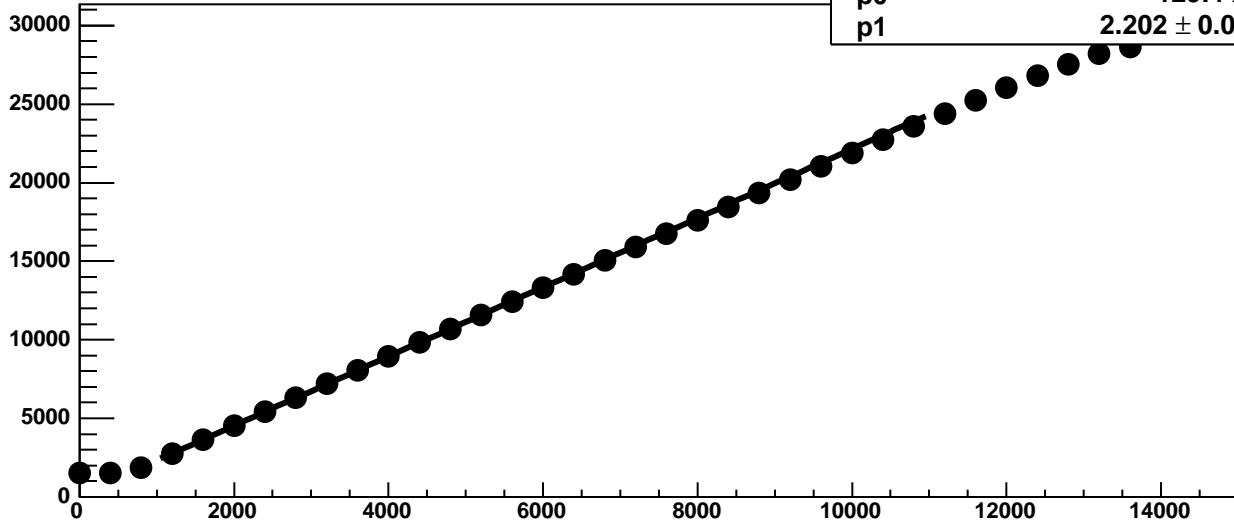
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

601.6 / 23

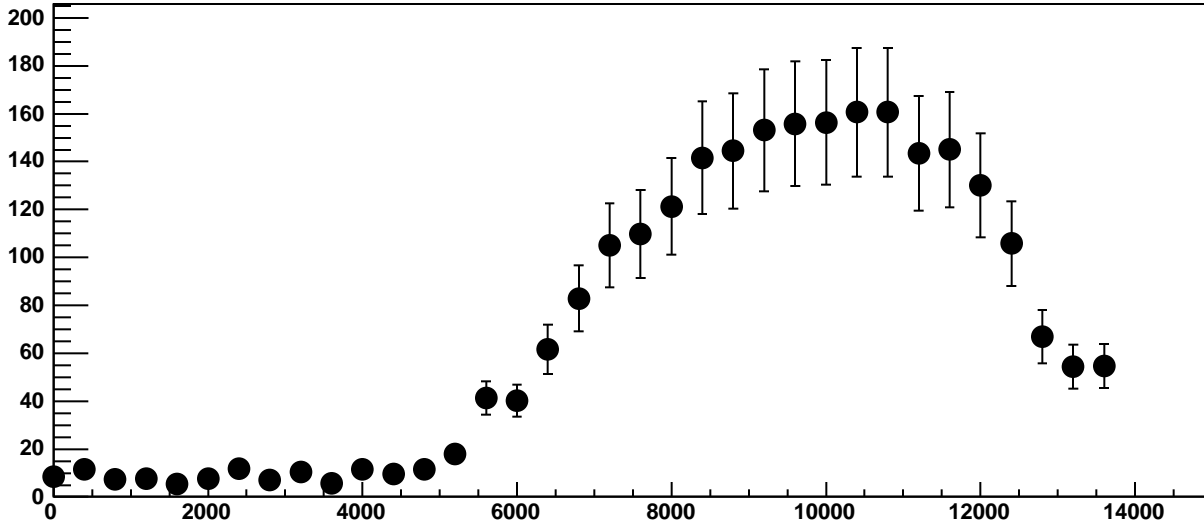
p0

$129.4 \pm 1.402$

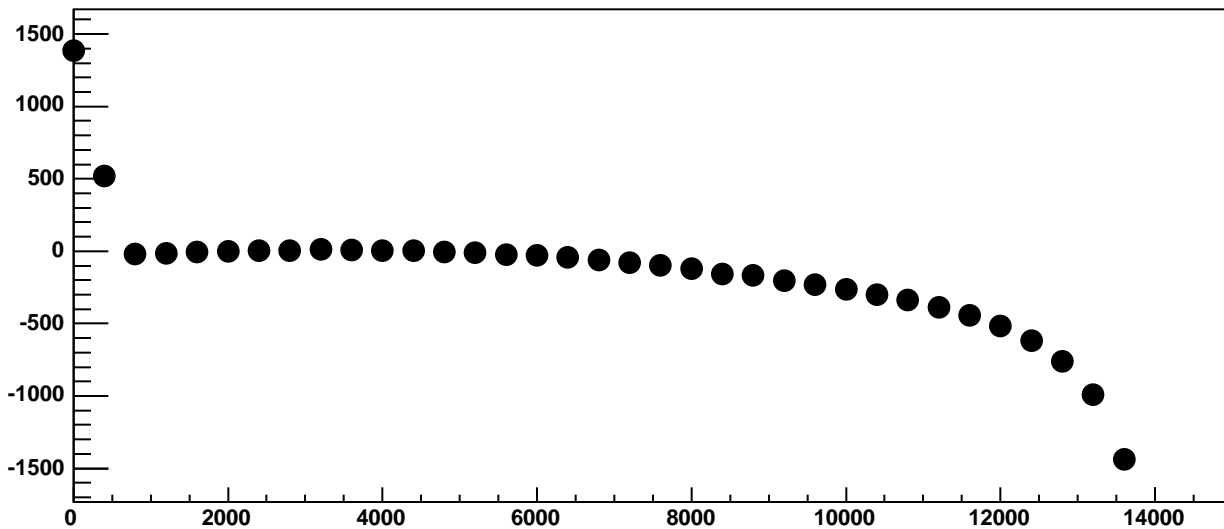
p1

$2.202 \pm 0.0004591$

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC

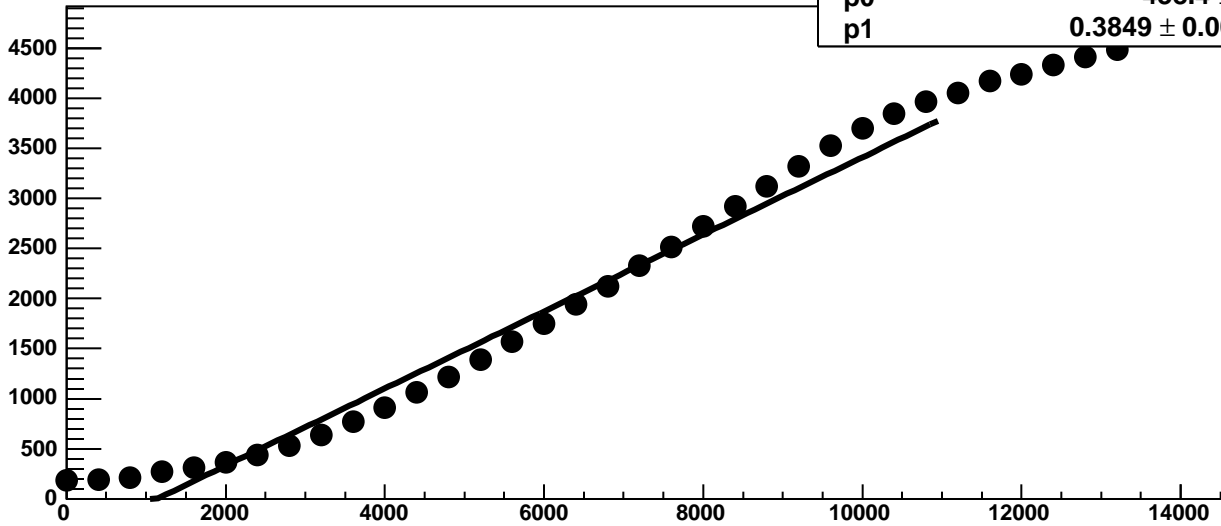


Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

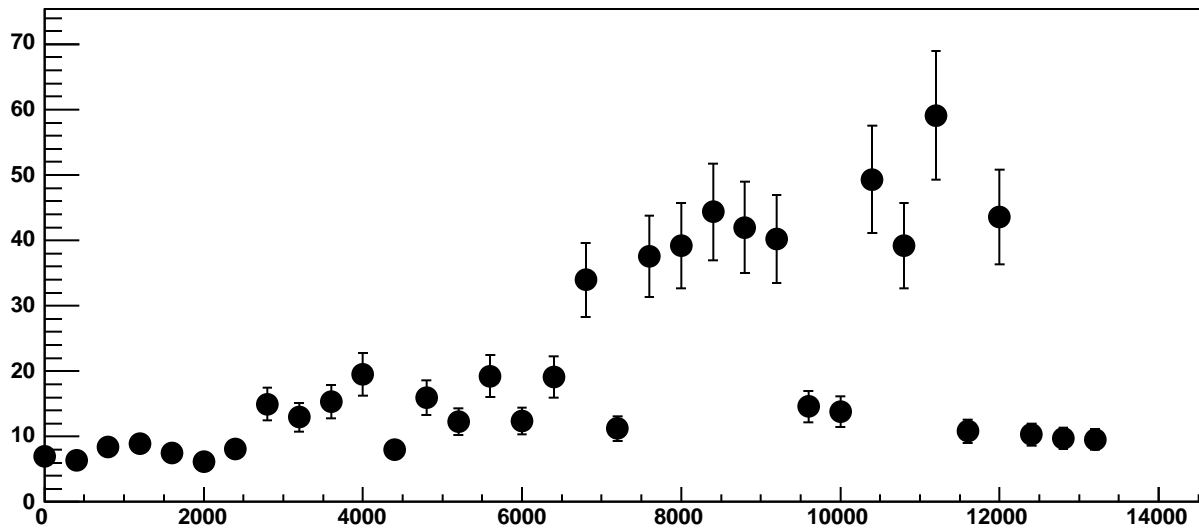


Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

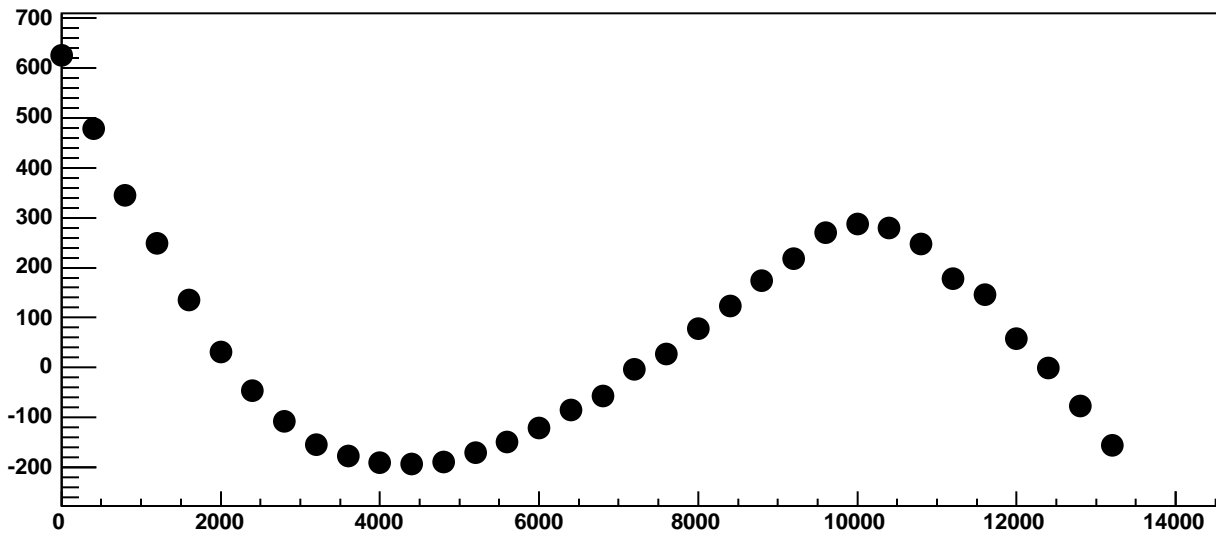
$\chi^2 / \text{ndf}$  6.851e+04 / 23  
p0 -438.4 ± 1.087  
p1 0.3849 ± 0.0002356



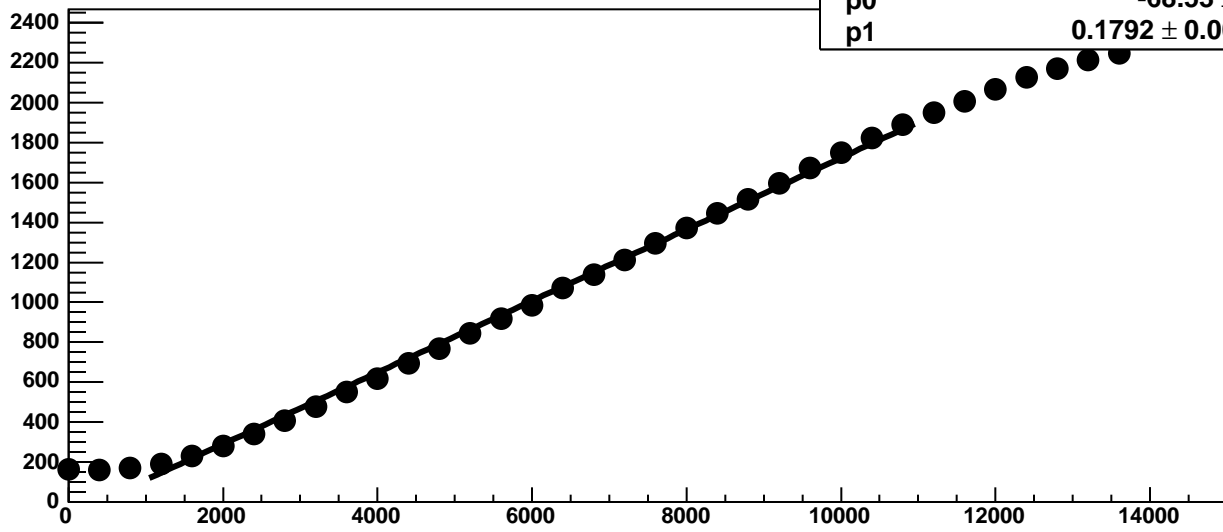
Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1568 / 23

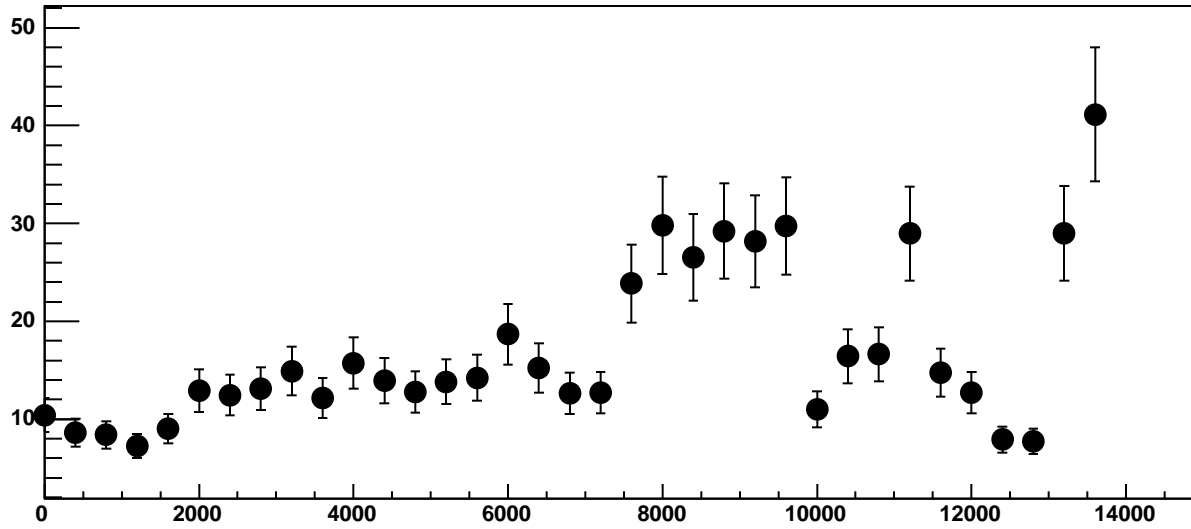
p0

$-68.53 \pm 1.184$

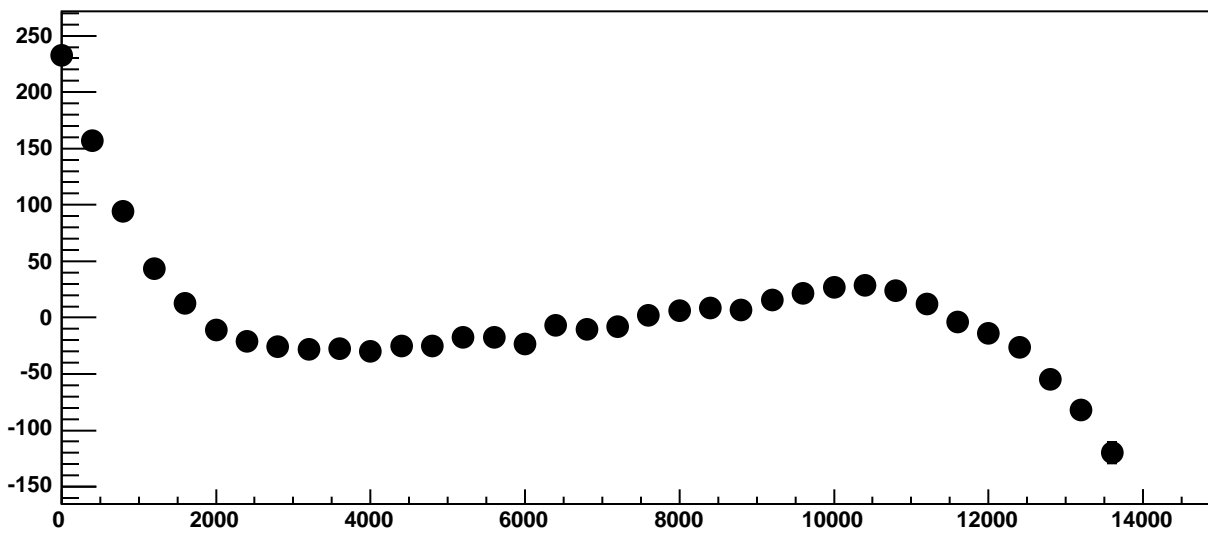
p1

$0.1792 \pm 0.0002126$

Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

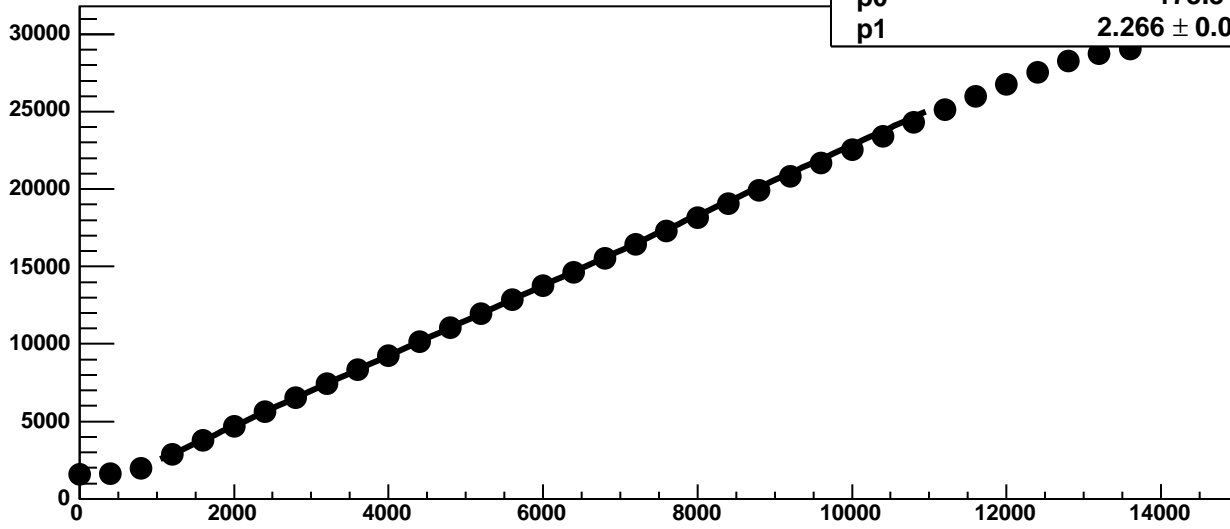


Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



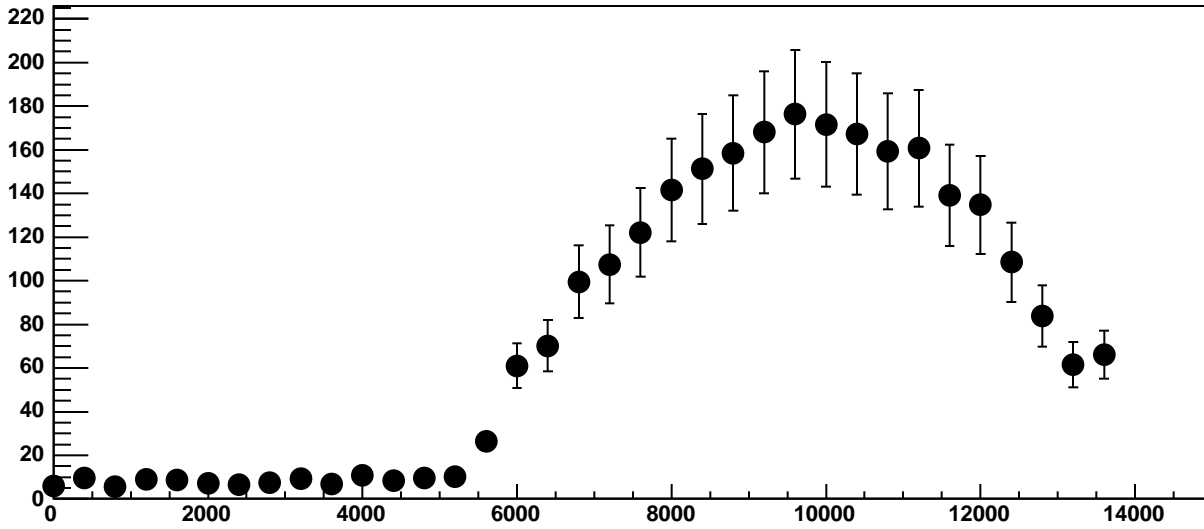


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

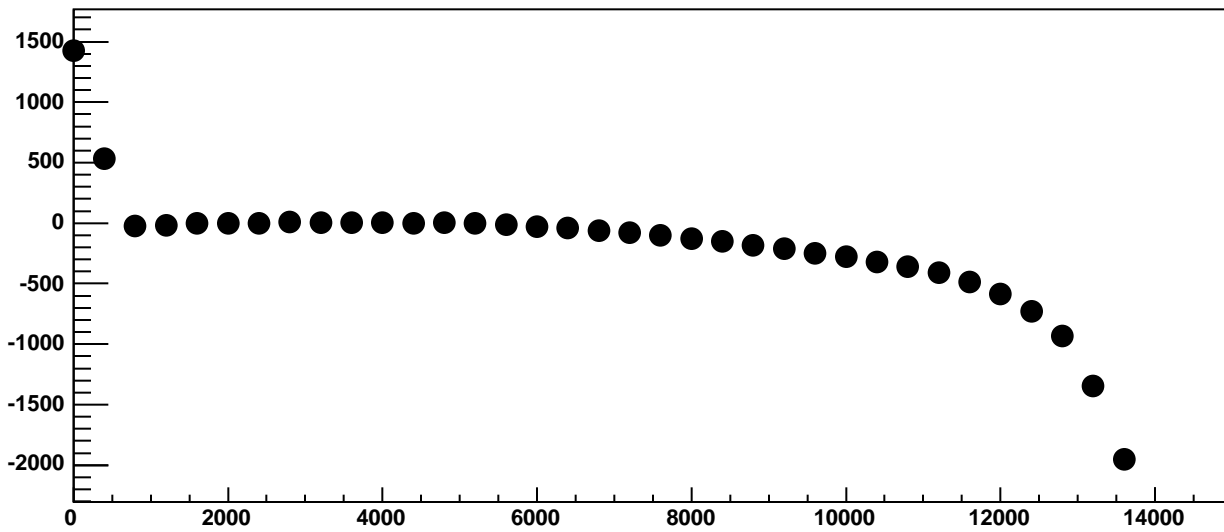


$\chi^2 / \text{ndf}$  503.1 / 23  
p0  $173.3 \pm 1.537$   
p1  $2.266 \pm 0.0004616$

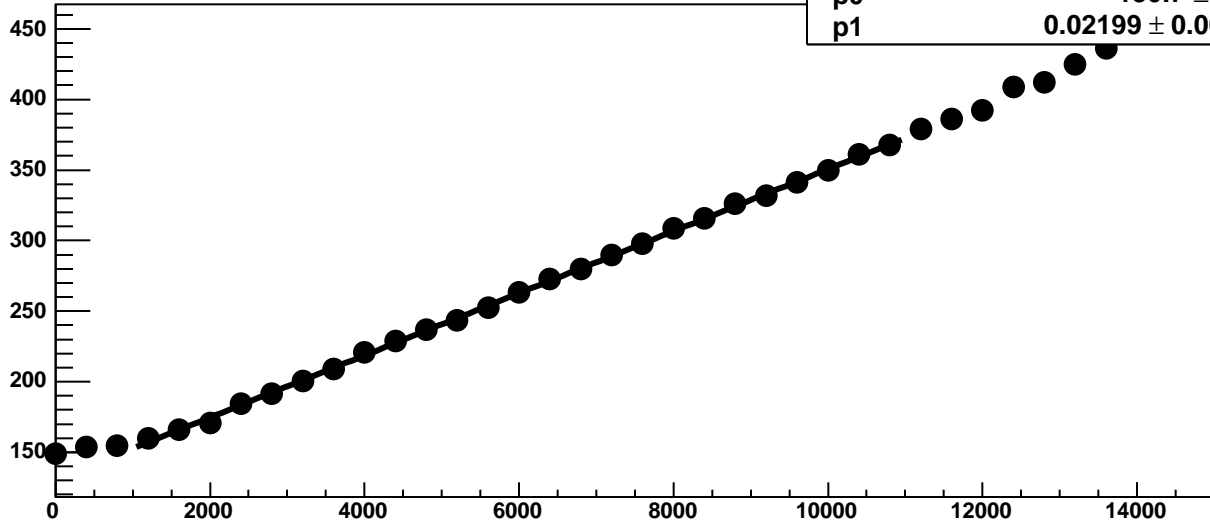
Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

16.04 / 23

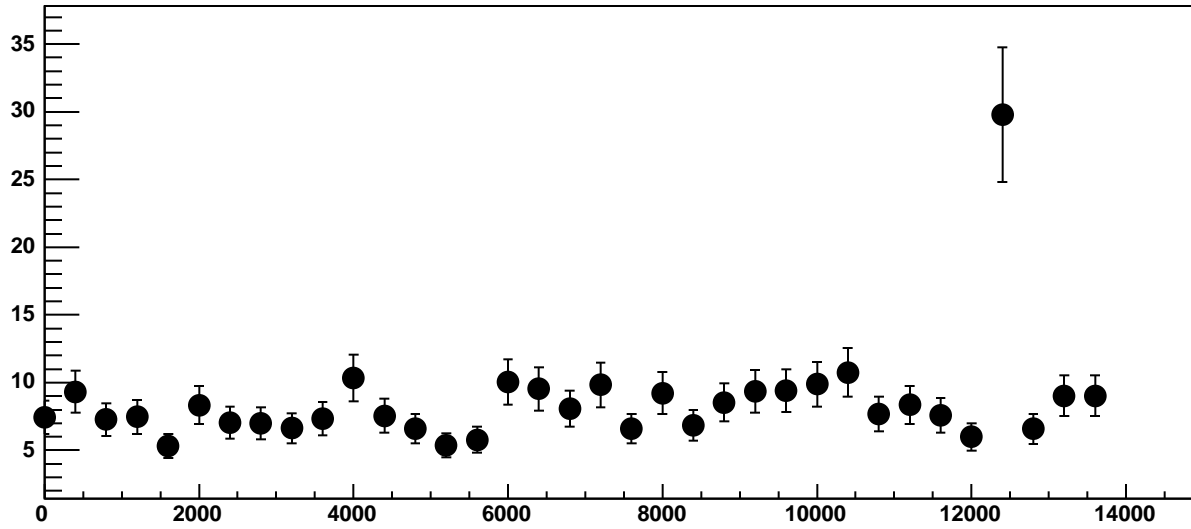
p0

$130.7 \pm 0.7578$

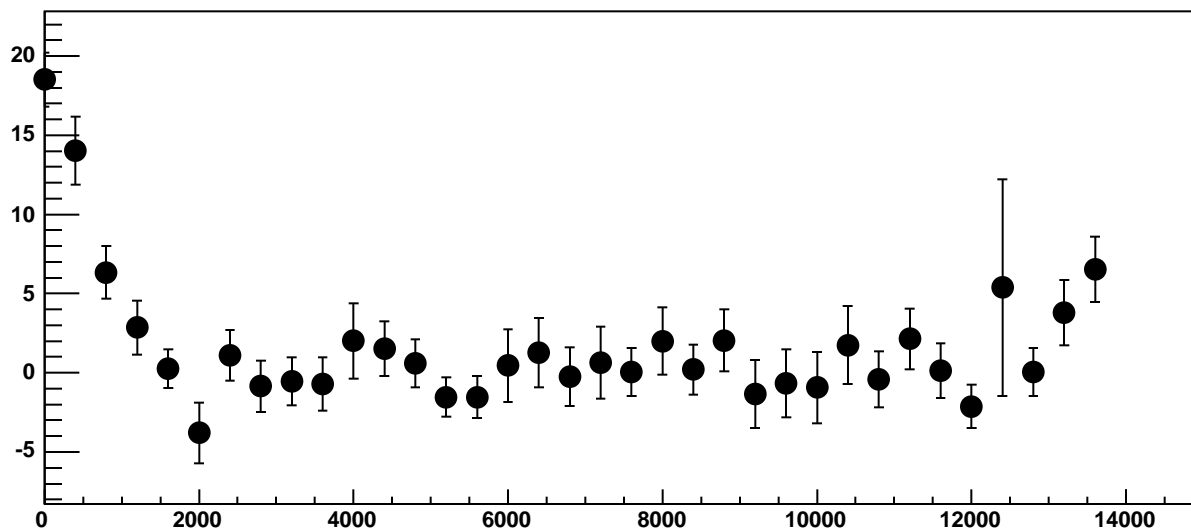
p1

$0.02199 \pm 0.0001238$

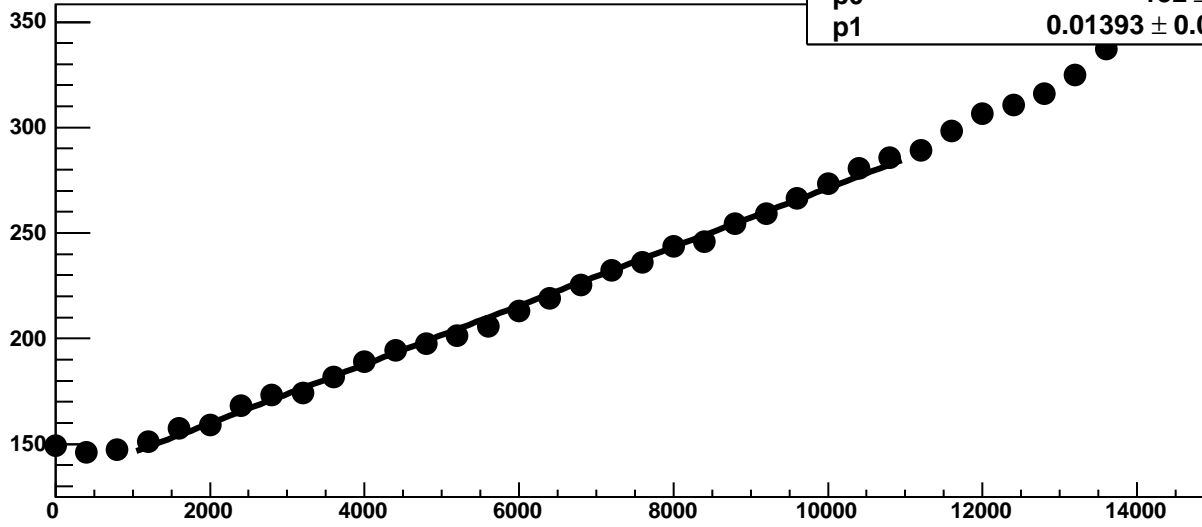
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

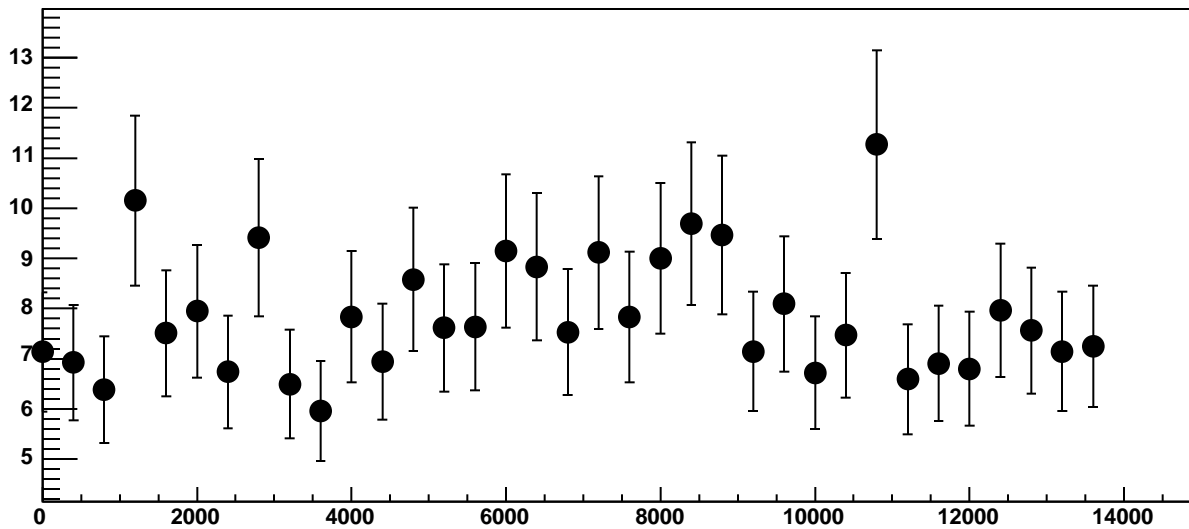


Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

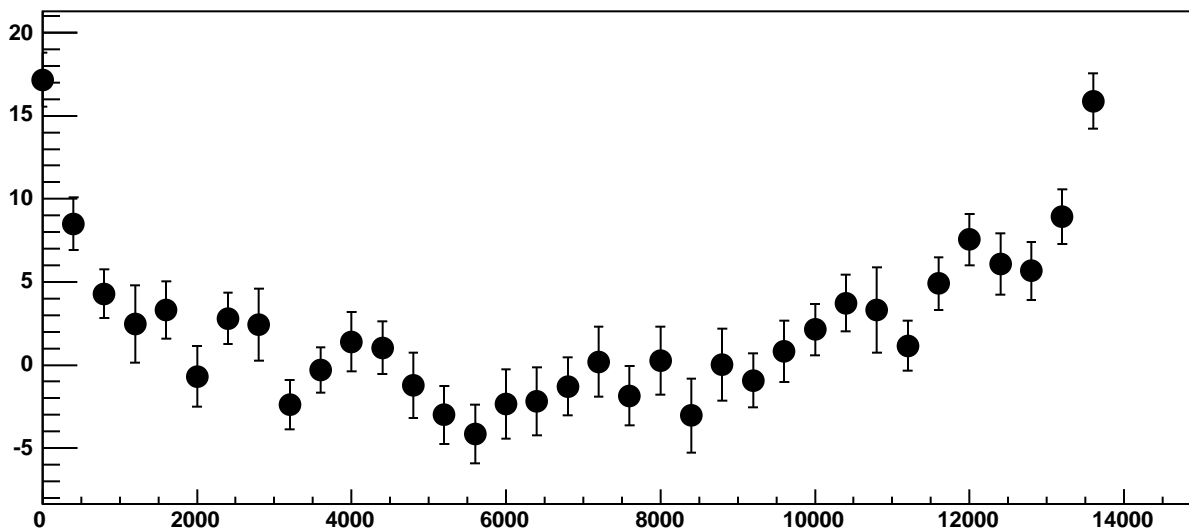


$\chi^2 / \text{ndf}$  36.83 / 23  
p0  $132 \pm 0.8225$   
p1  $0.01393 \pm 0.0001271$

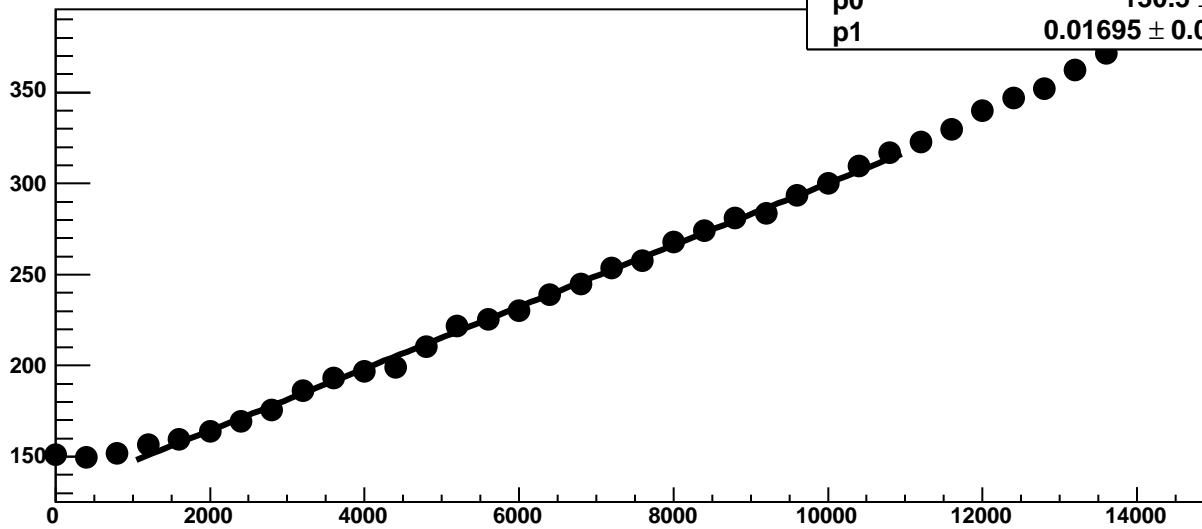
Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

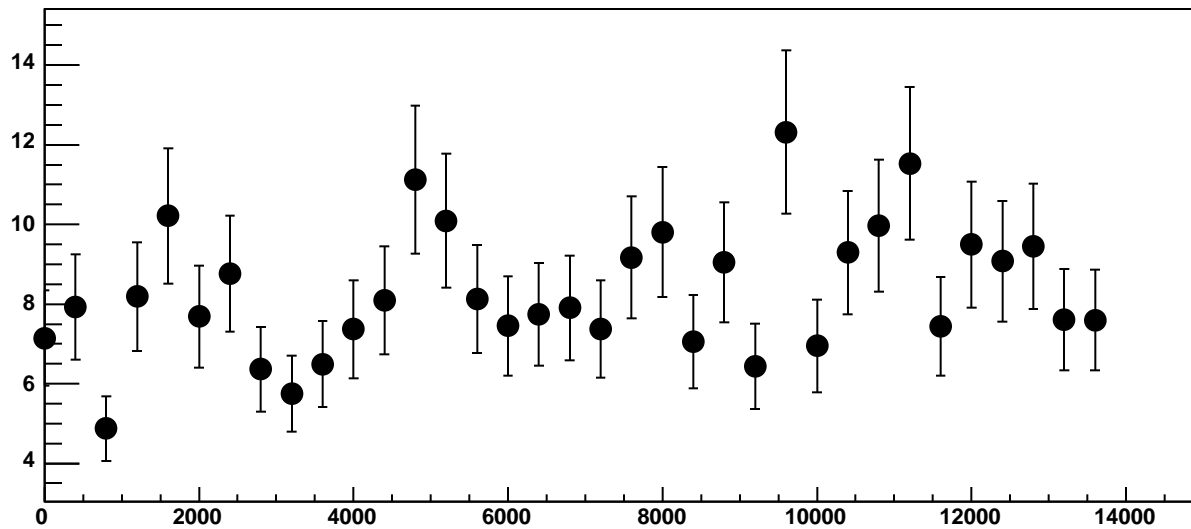


Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

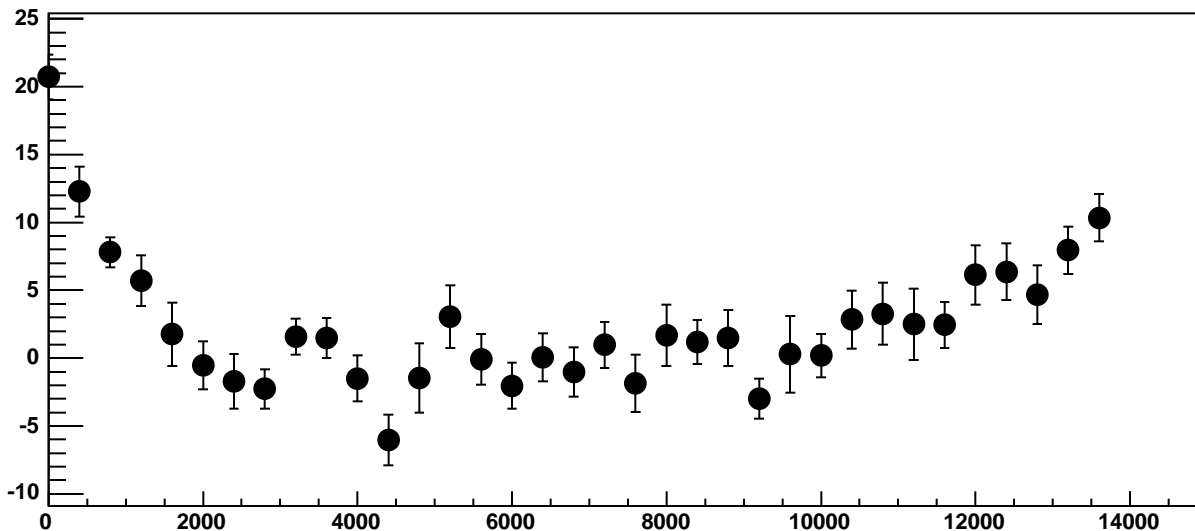


$\chi^2 / \text{ndf}$  41.17 / 23  
p0 130.5 ± 0.8311  
p1 0.01695 ± 0.0001292

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC

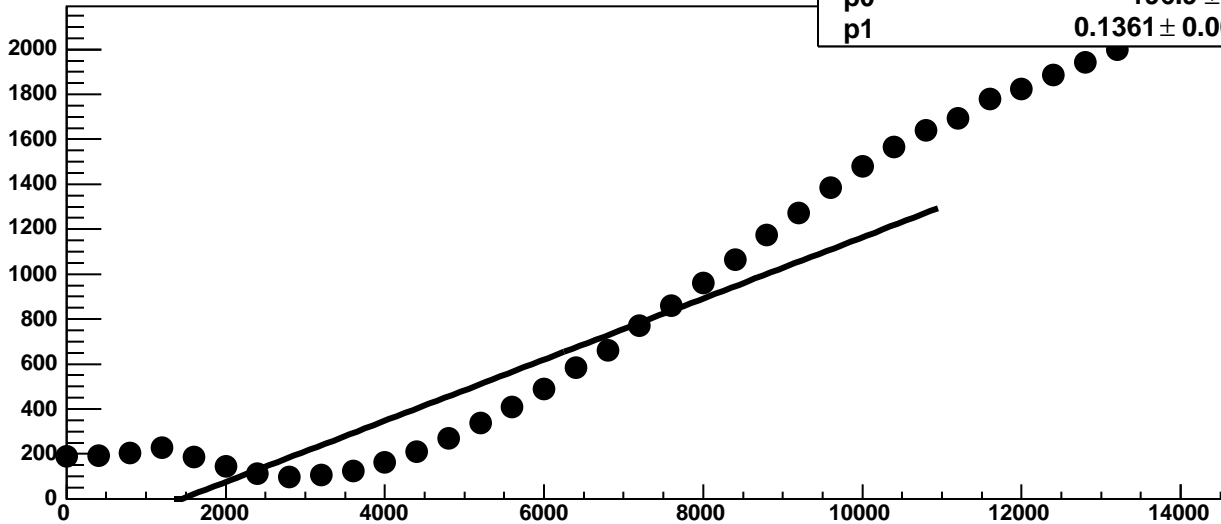


Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

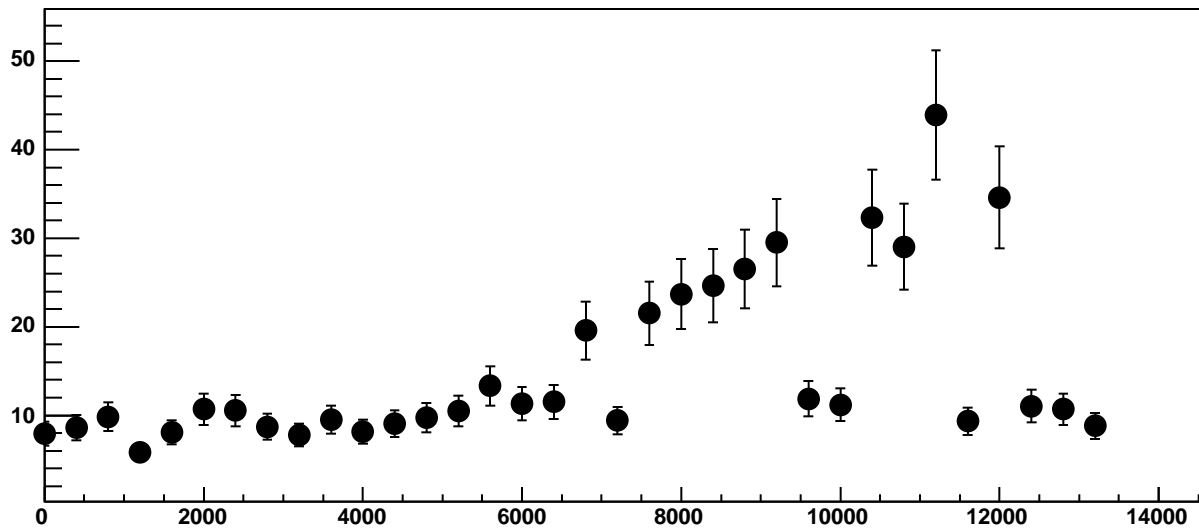


Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

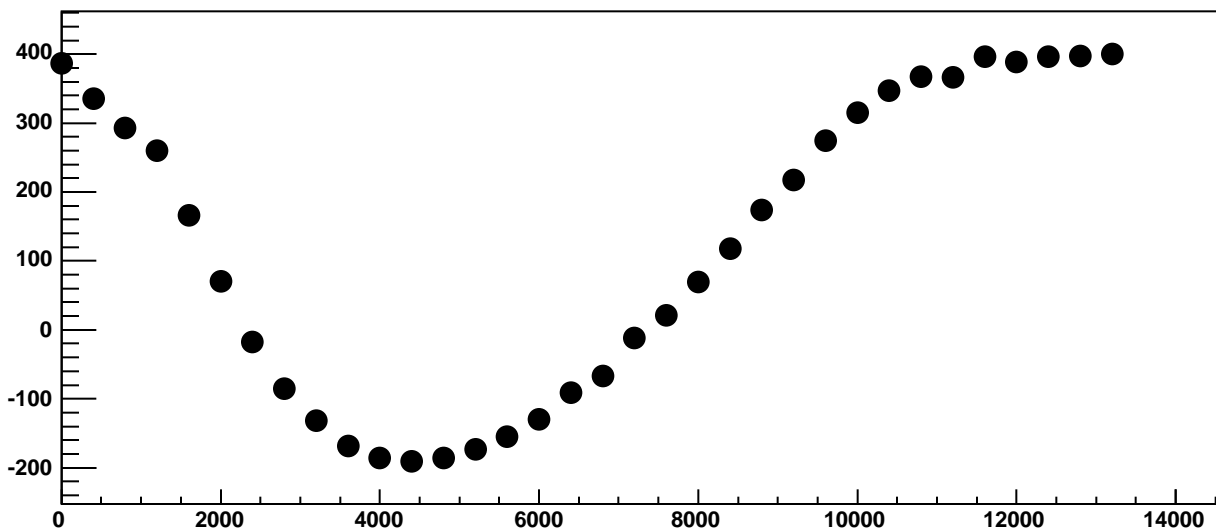
$\chi^2 / \text{ndf}$  1.297e+05 / 23  
p0 -196.9 ± 0.9746  
p1 0.1361 ± 0.0001946



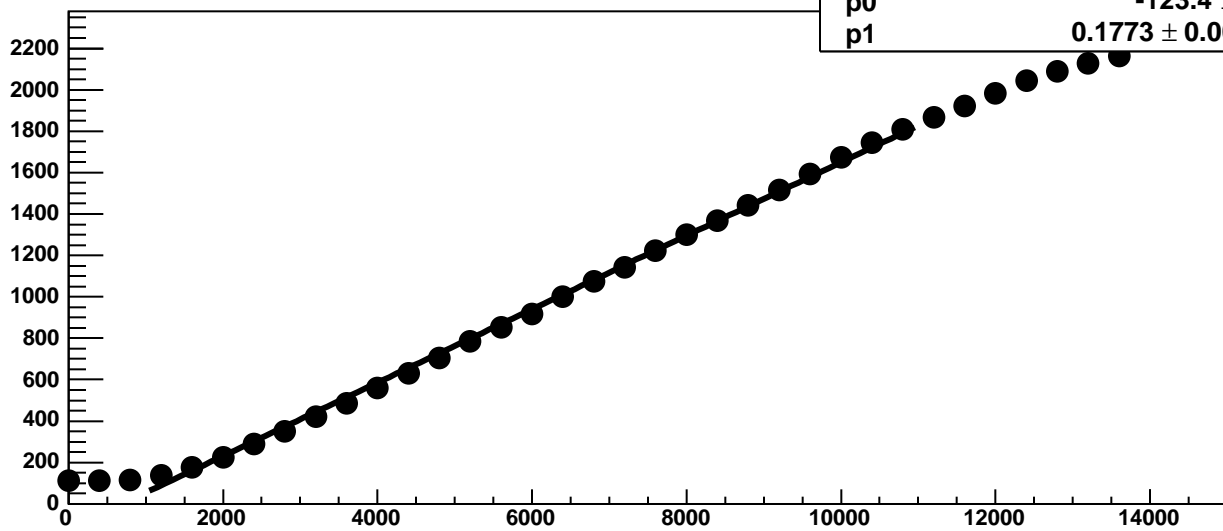
Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1593 / 23

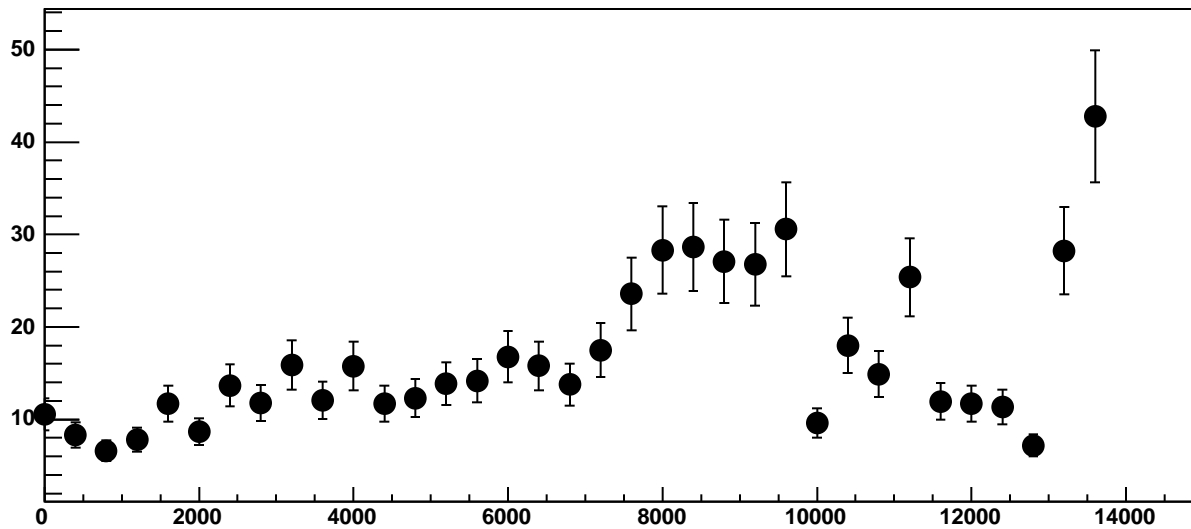
p0

$-123.4 \pm 1.184$

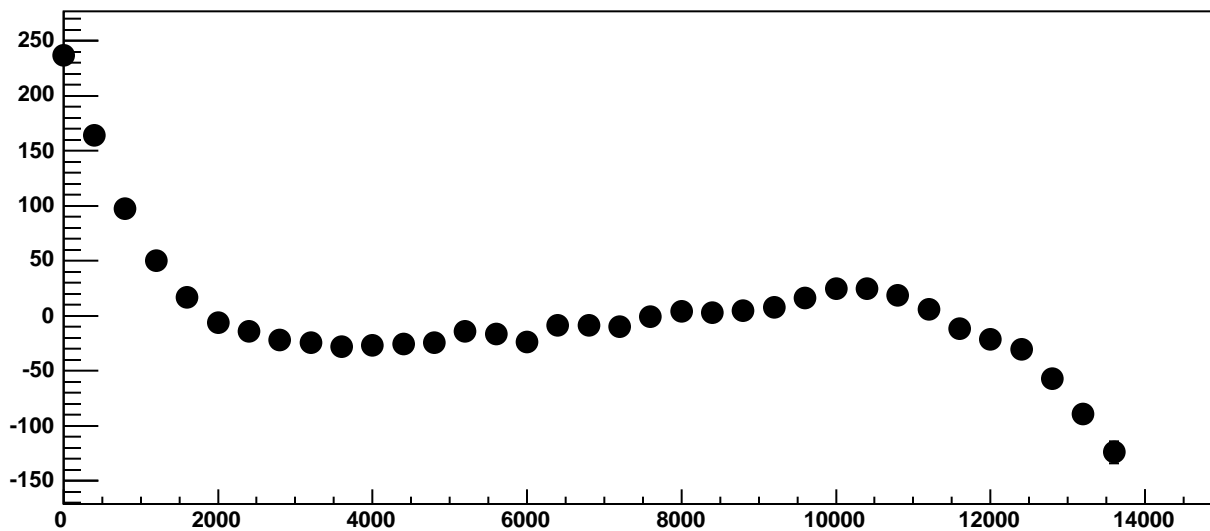
p1

$0.1773 \pm 0.0002092$

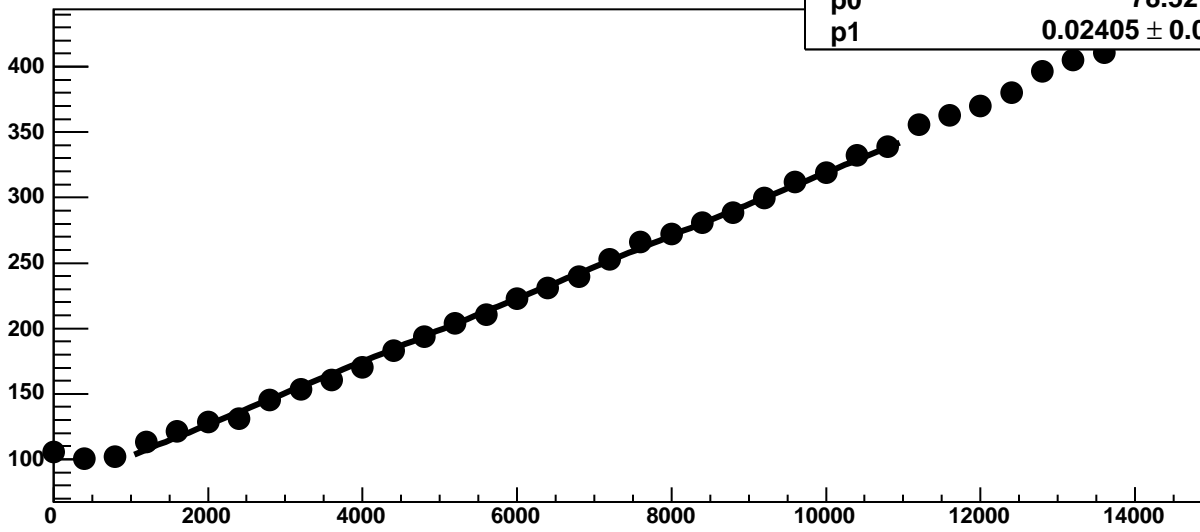
Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

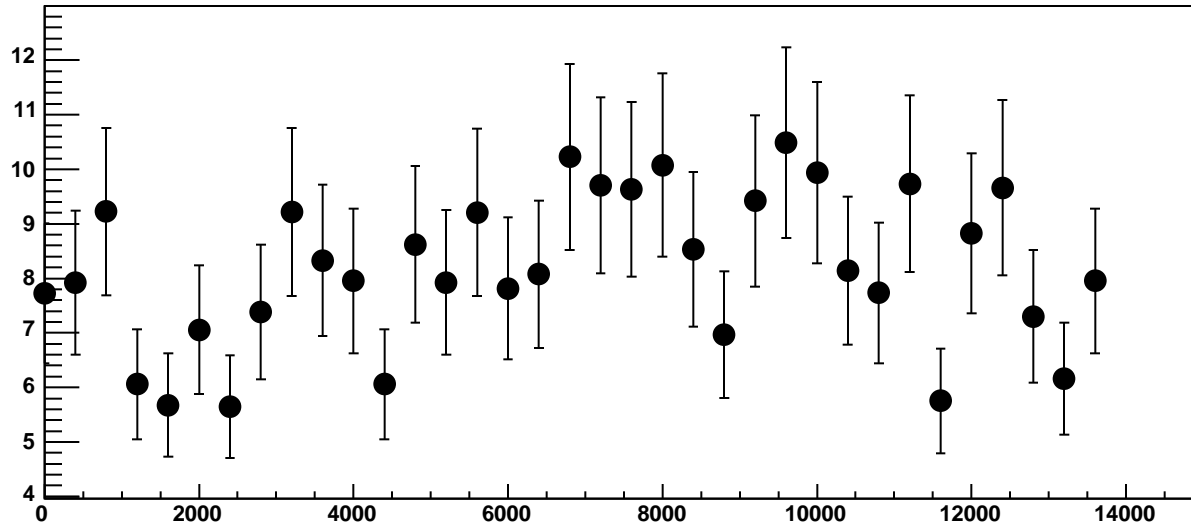


Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

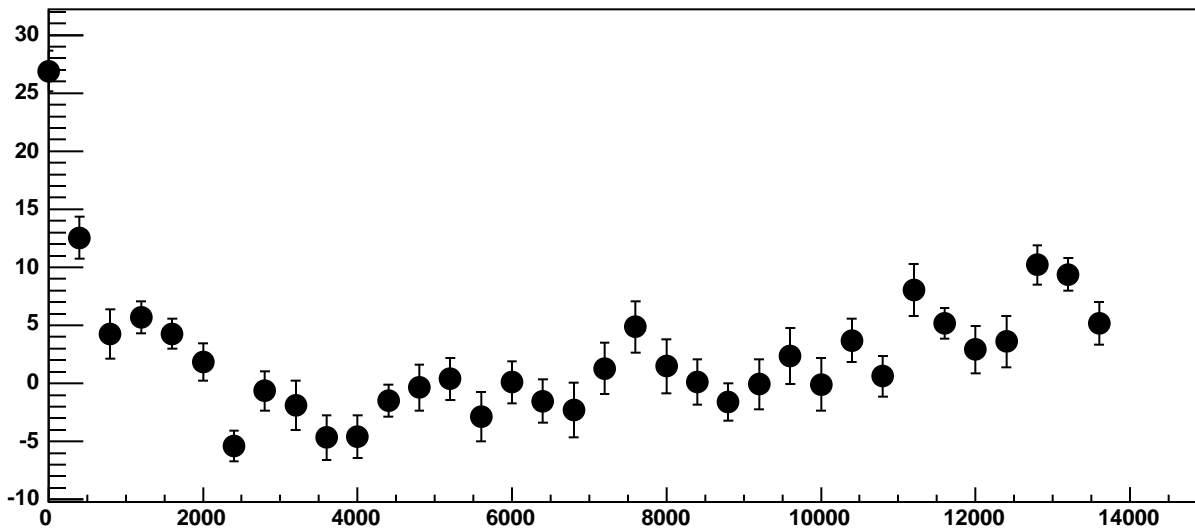


$\chi^2 / \text{ndf}$  75.67 / 23  
p0 78.52 ± 0.728  
p1 0.02405 ± 0.0001195

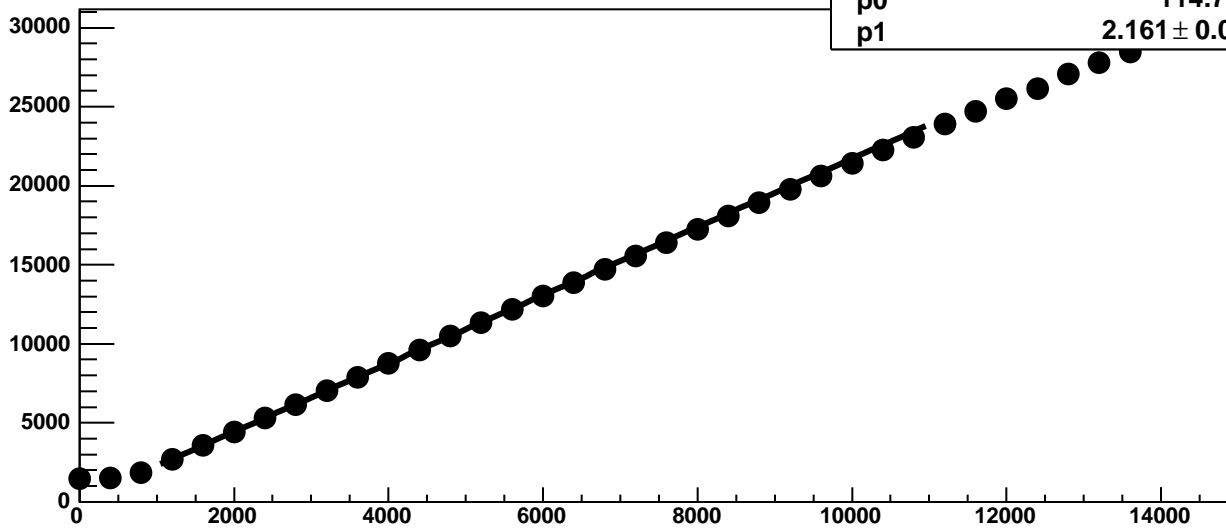
Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



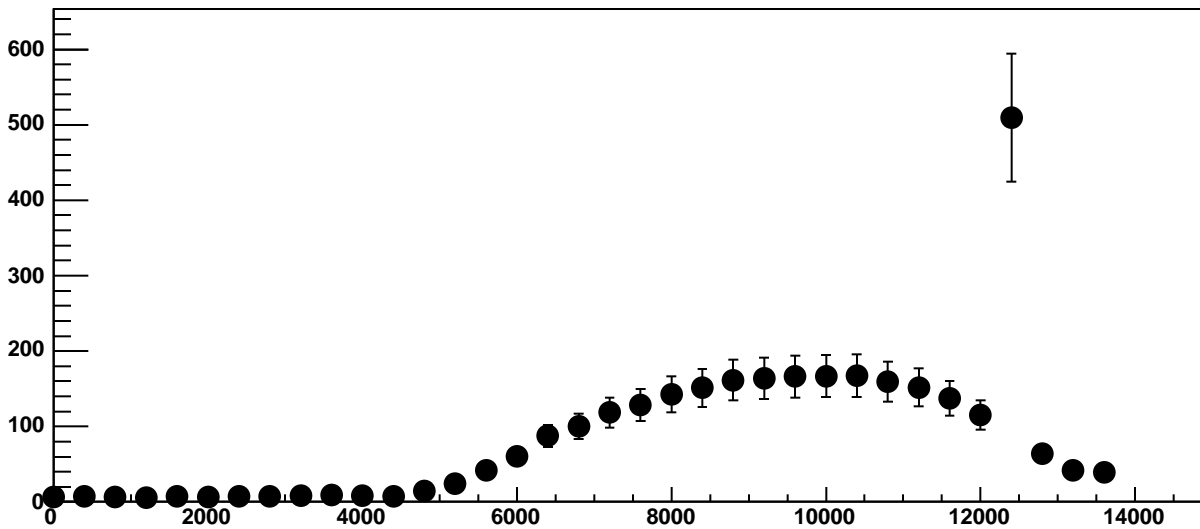
Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



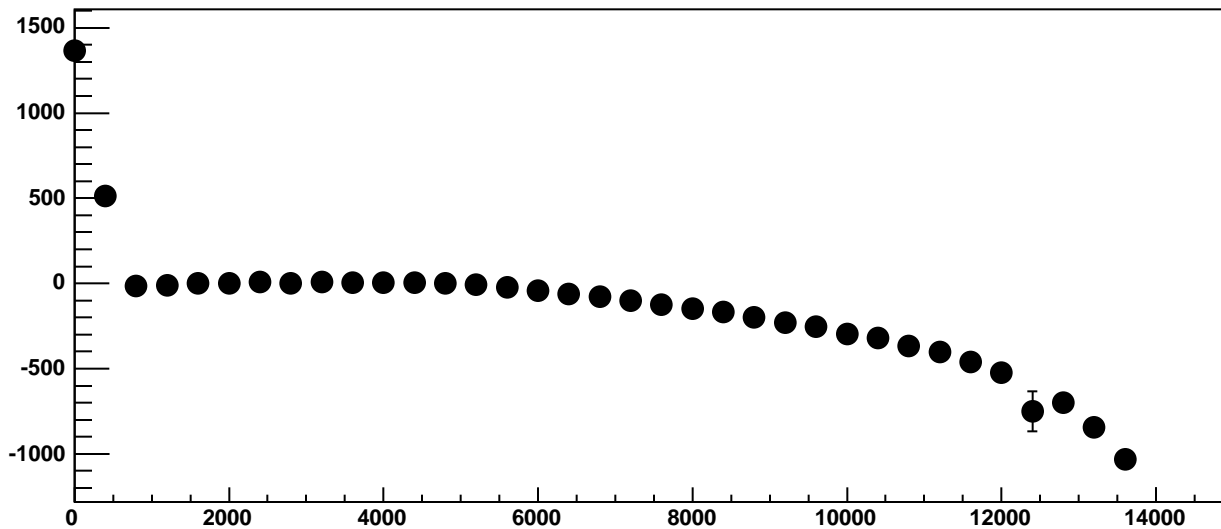
Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

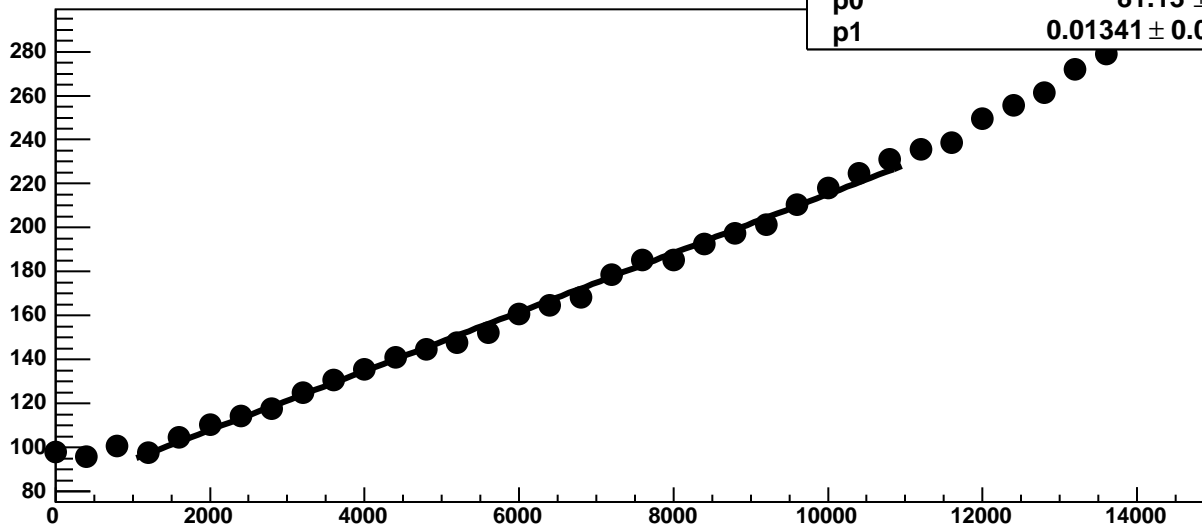


Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC





Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

51.23 / 23

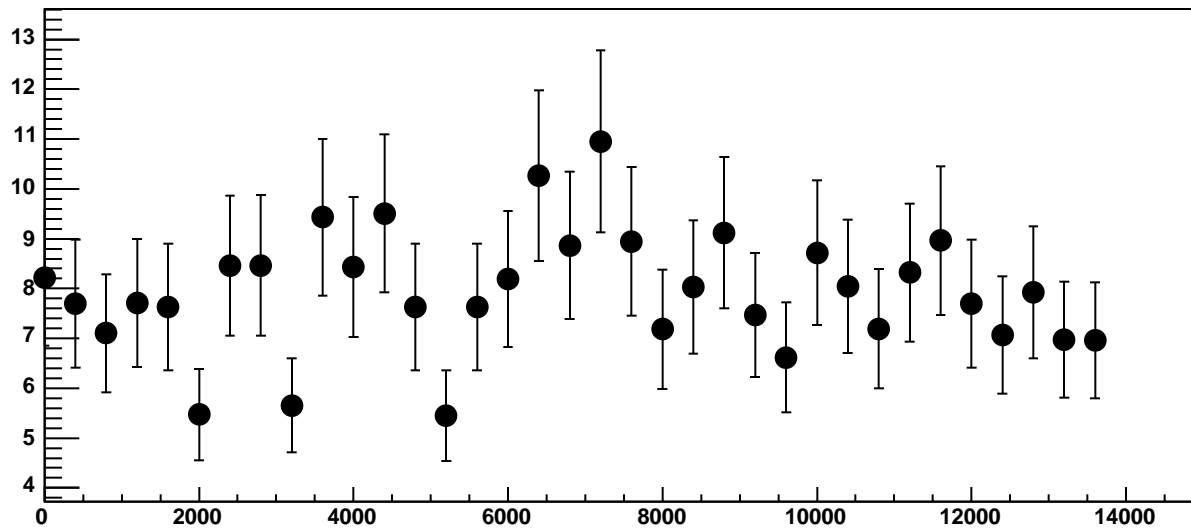
p0

$81.13 \pm 0.7698$

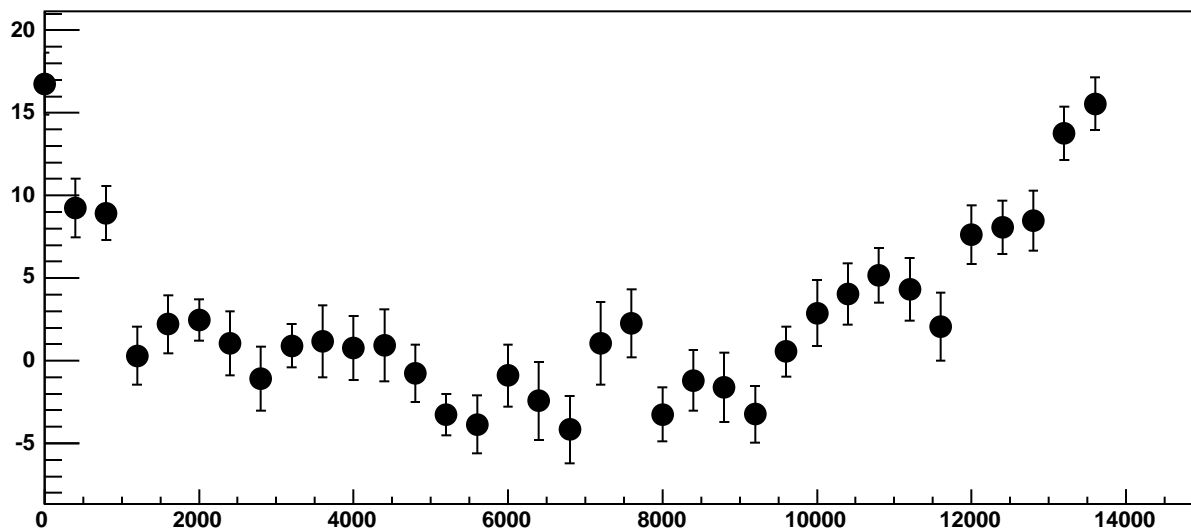
p1

$0.01341 \pm 0.0001184$

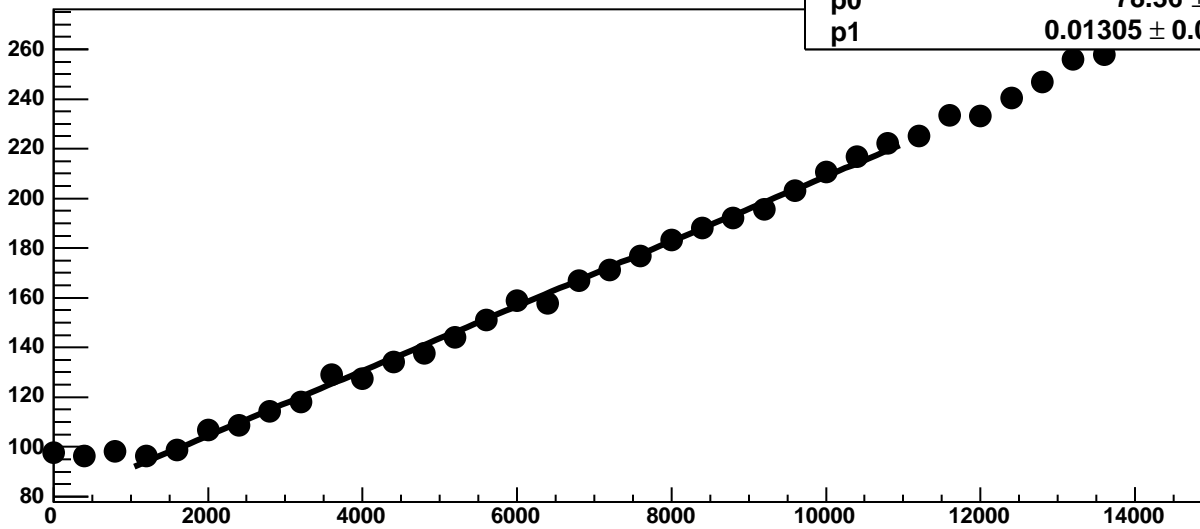
Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

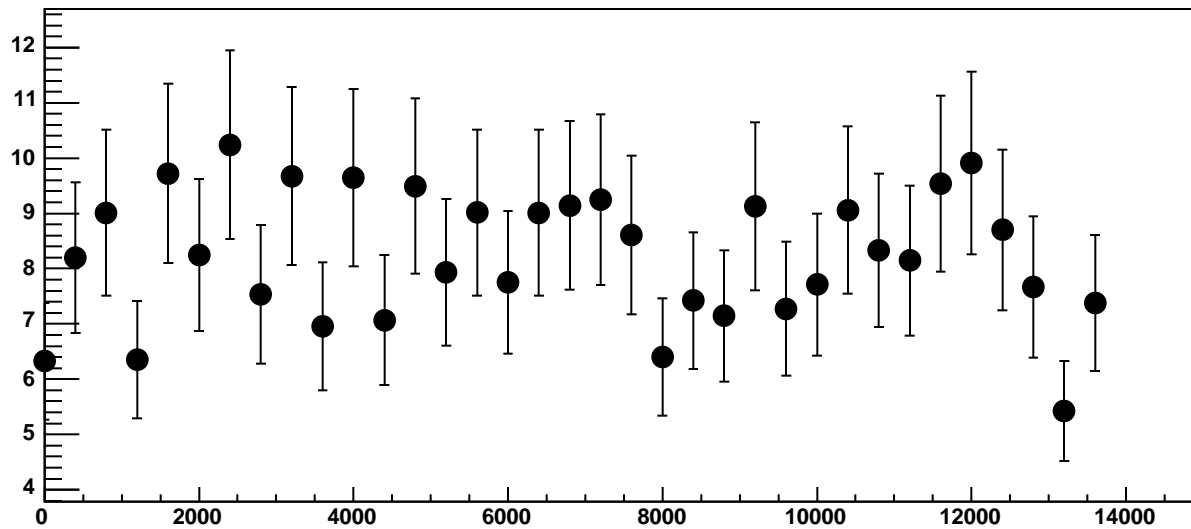


Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

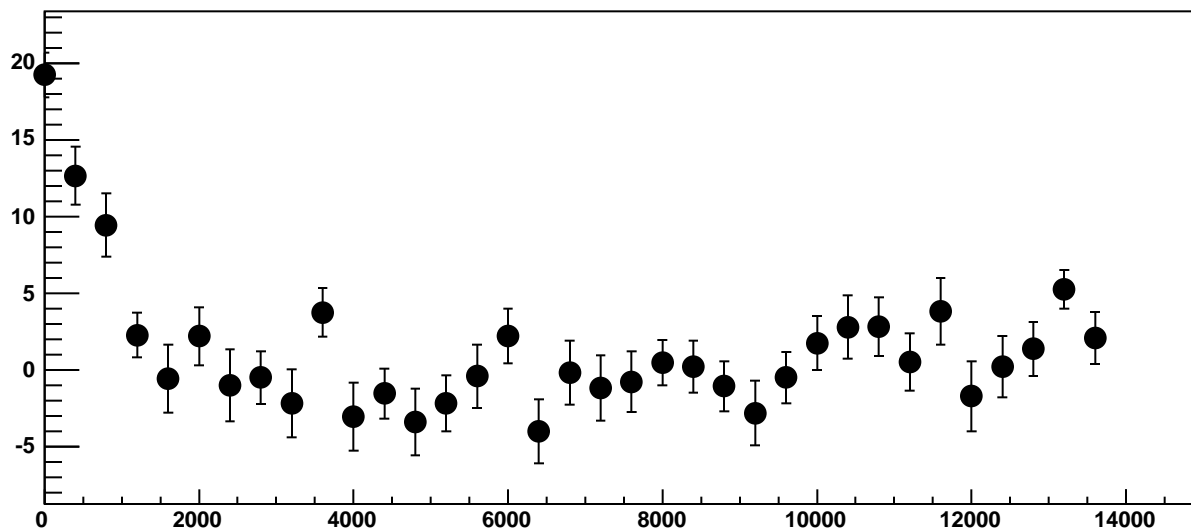


$\chi^2 / \text{ndf}$  30.51 / 23  
p0 78.36 ± 0.8525  
p1 0.01305 ± 0.0001271

Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

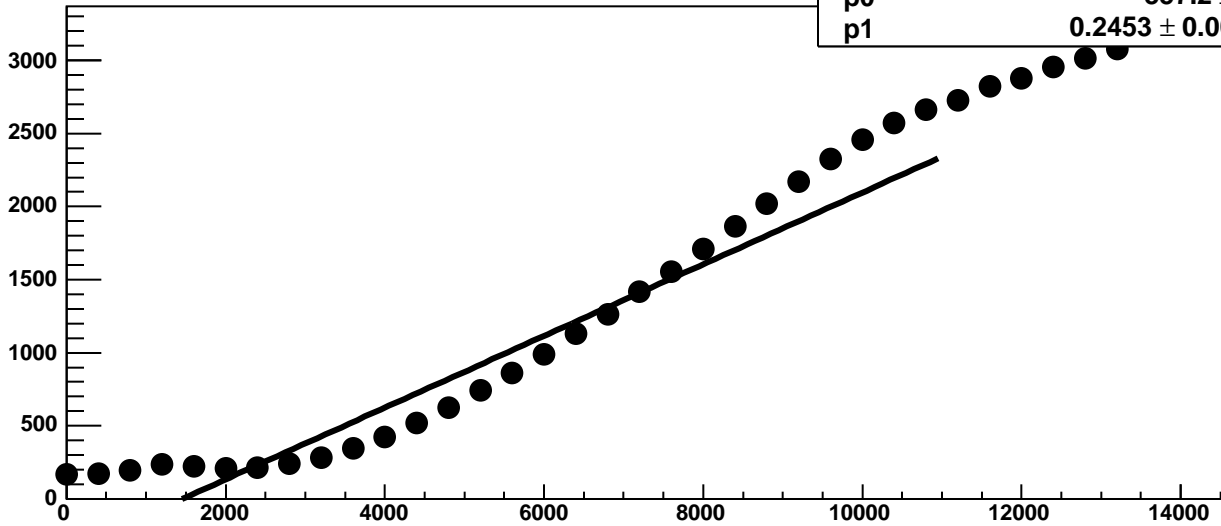


Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

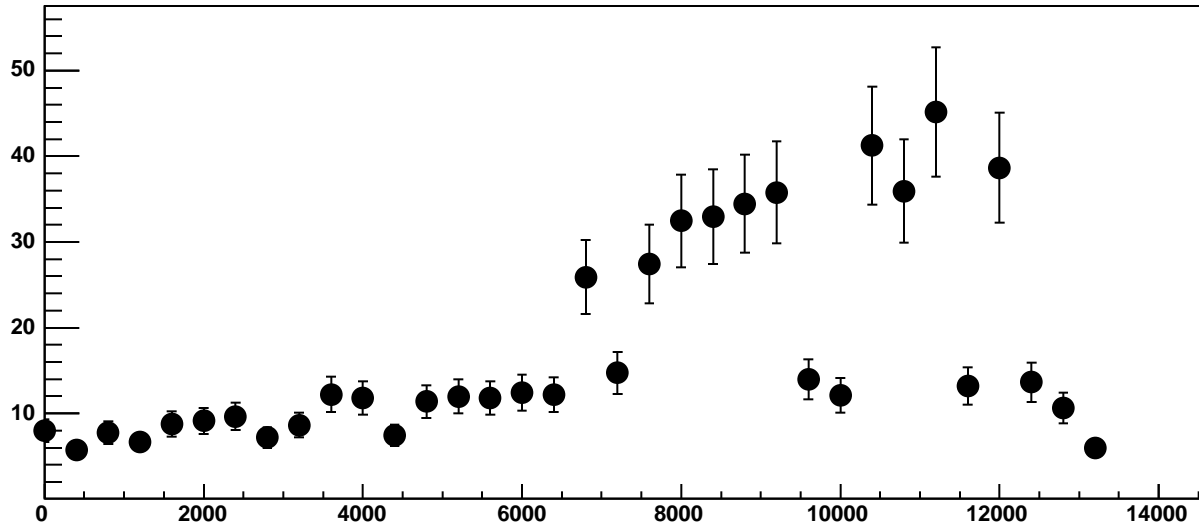


Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

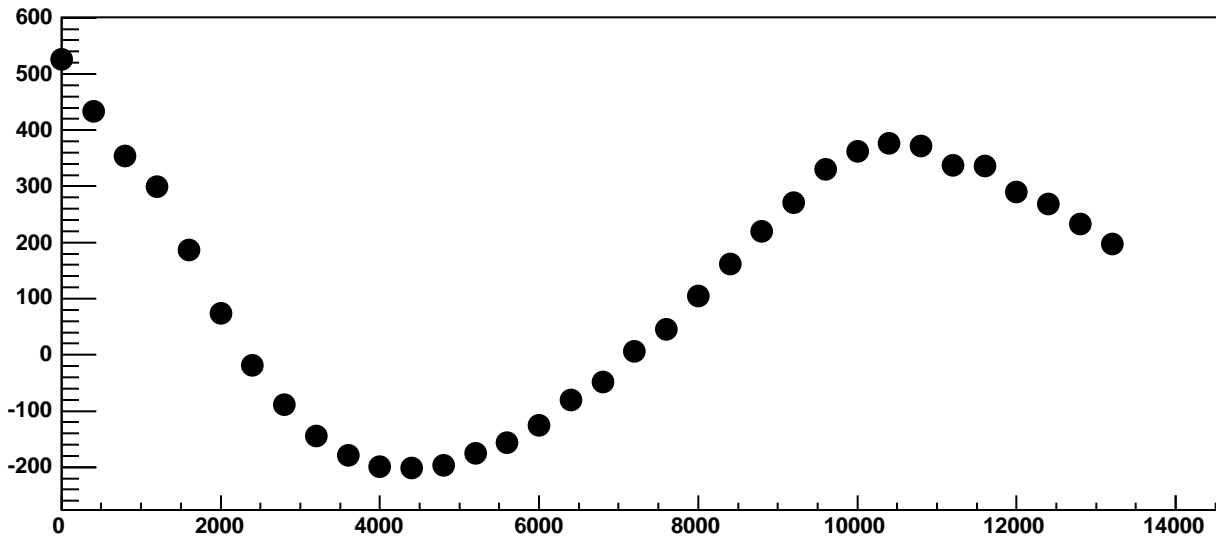
$\chi^2 / \text{ndf}$  1.293e+05 / 23  
p0 -357.2 ± 1.043  
p1 0.2453 ± 0.0002209



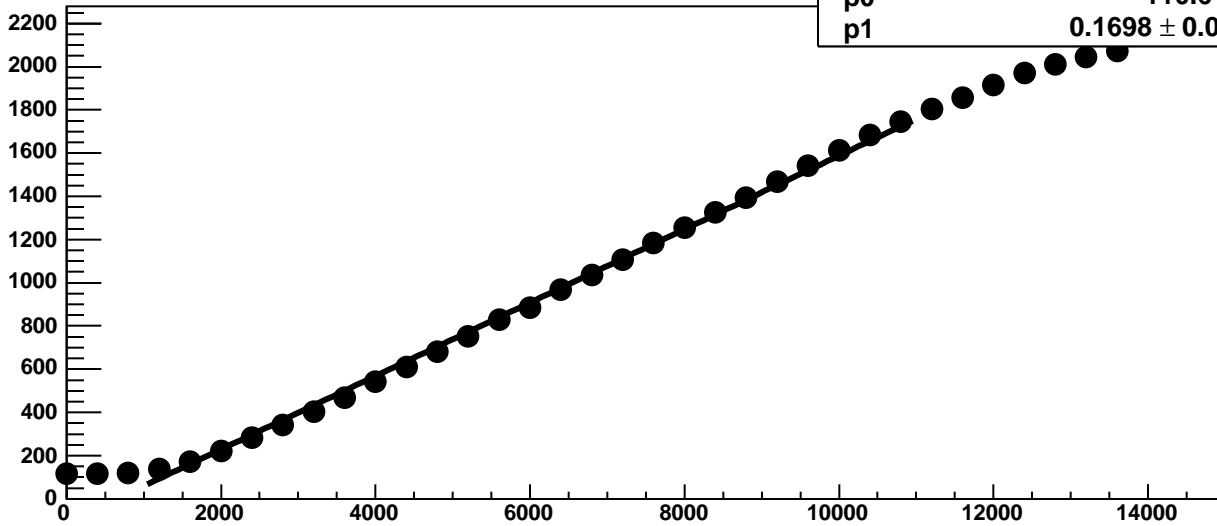
Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



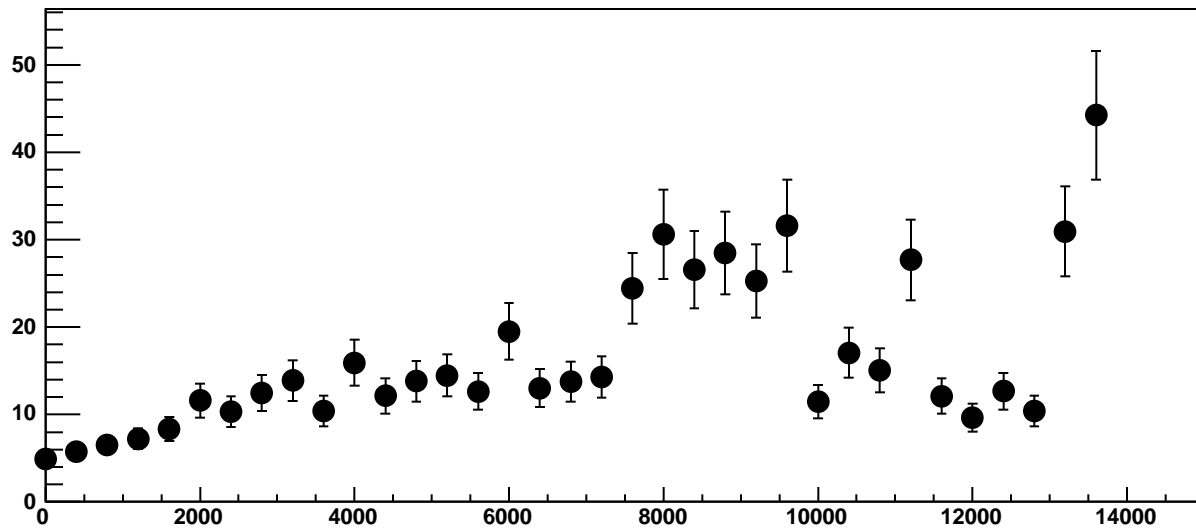
Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



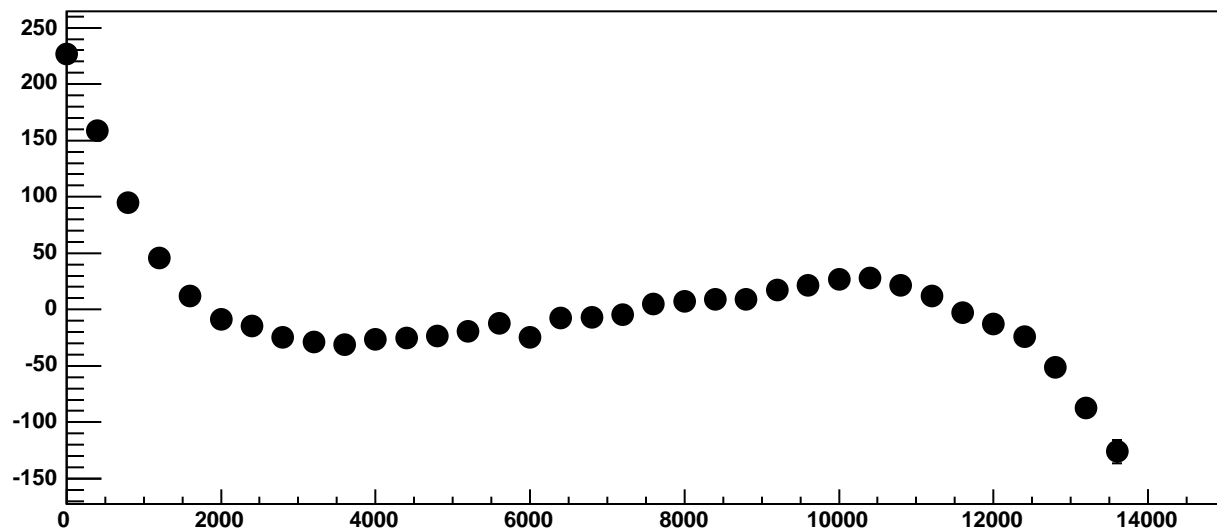
Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



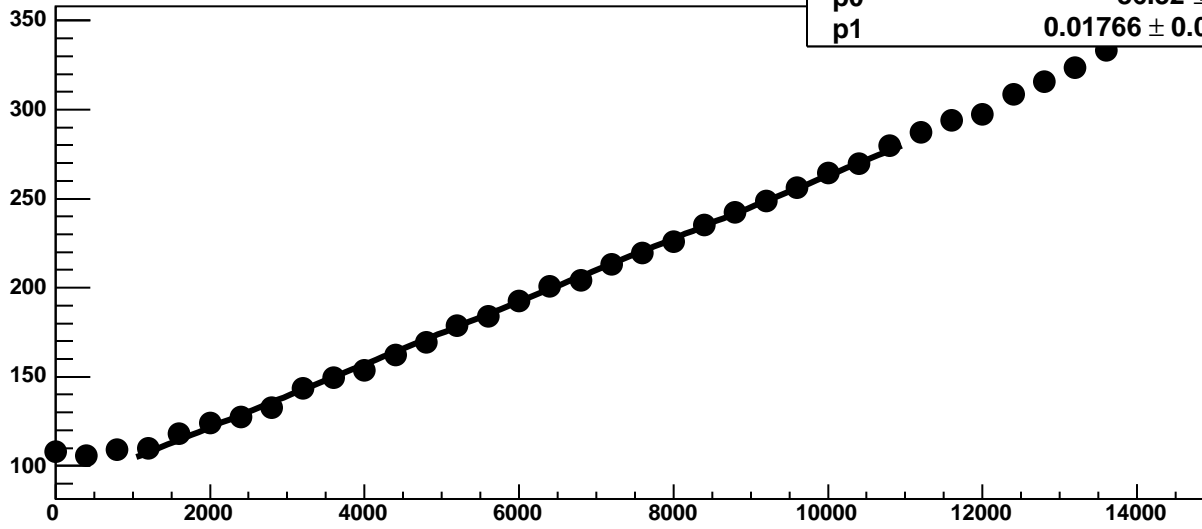
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



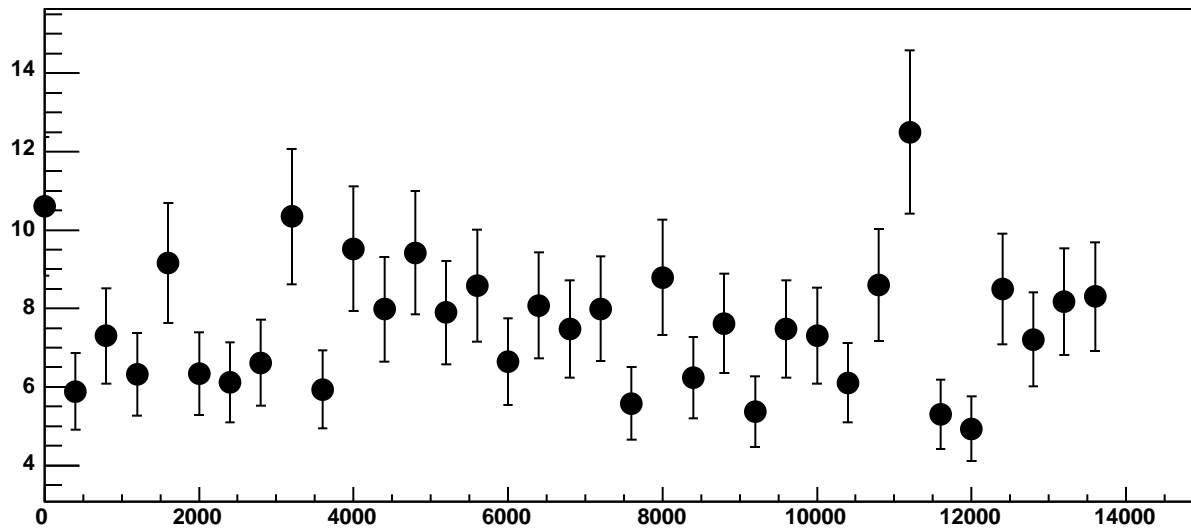
Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



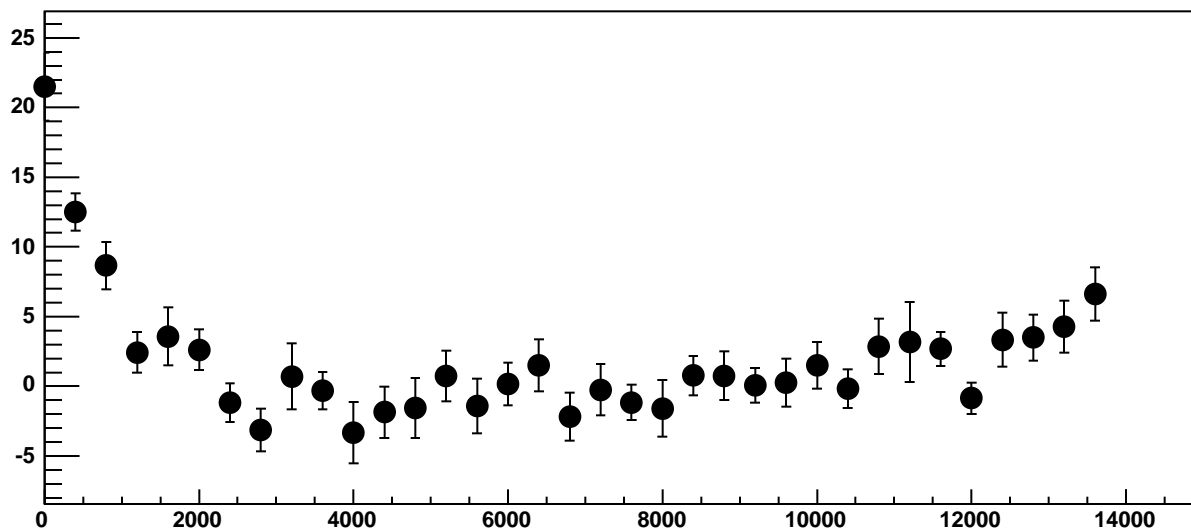
Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



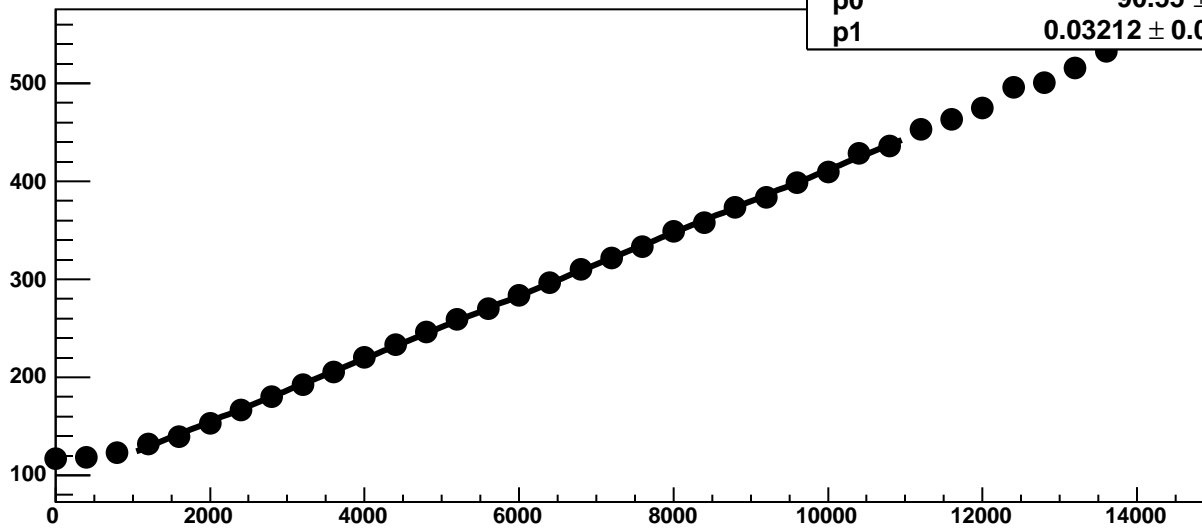
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



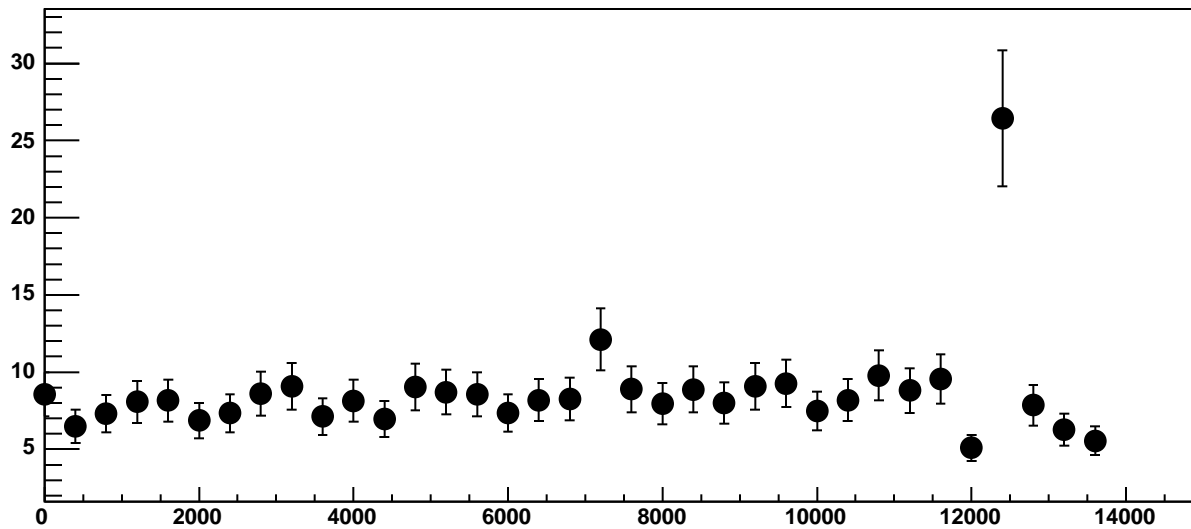
Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



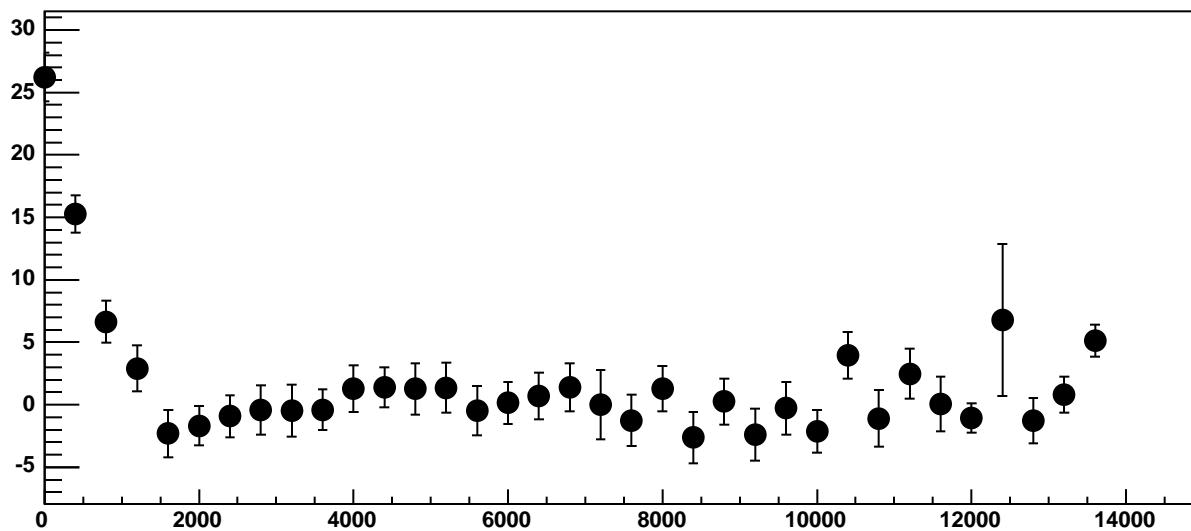
Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



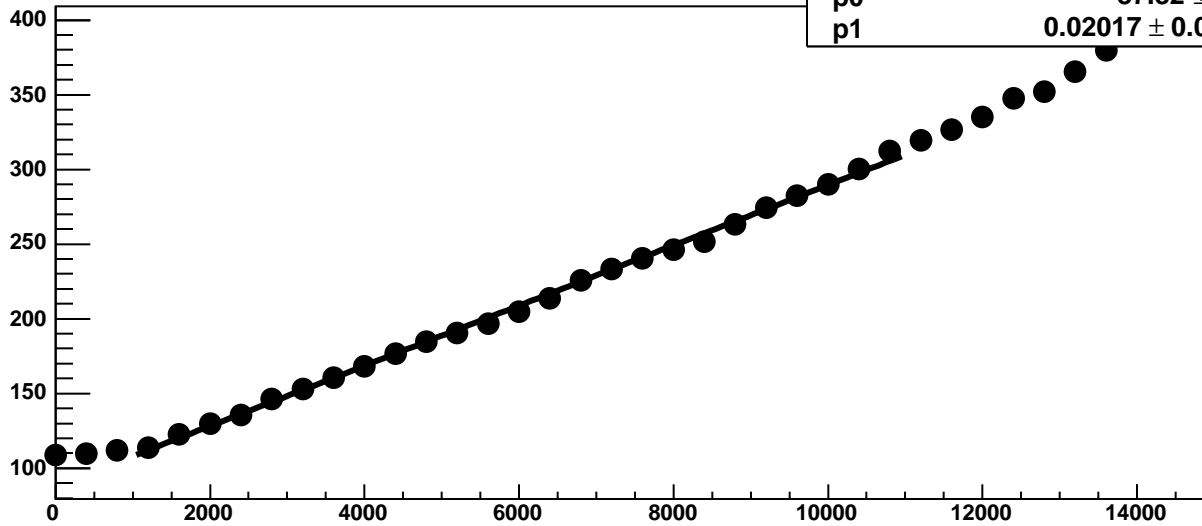
Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

54.2 / 23

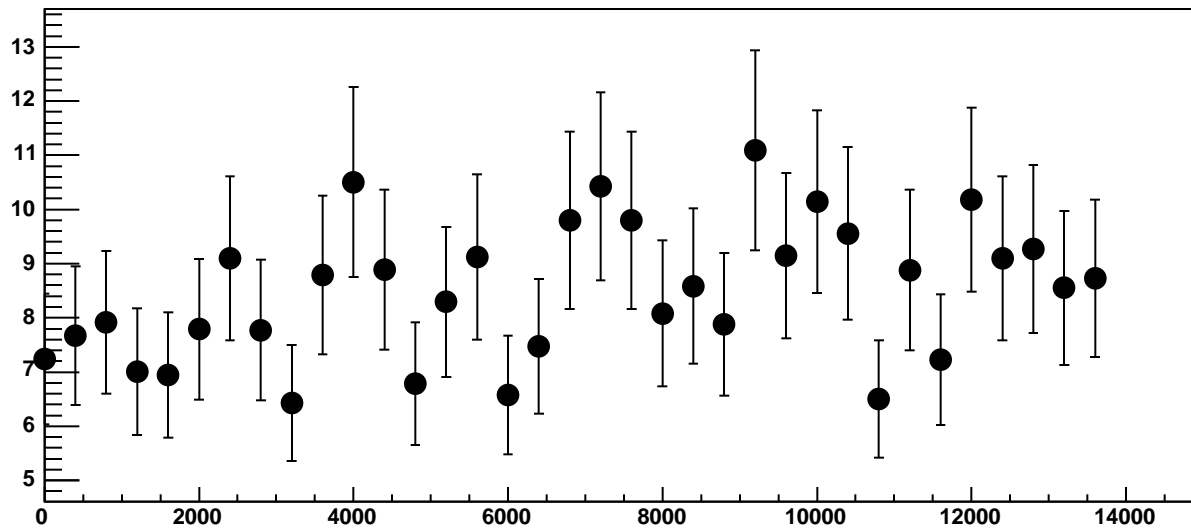
p0

$87.82 \pm 0.8154$

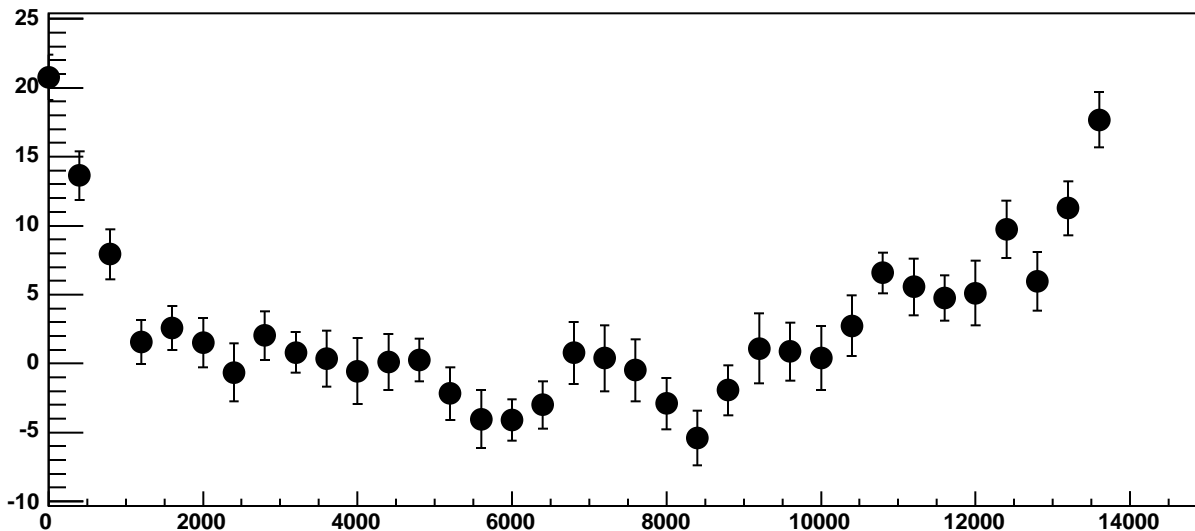
p1

$0.02017 \pm 0.0001269$

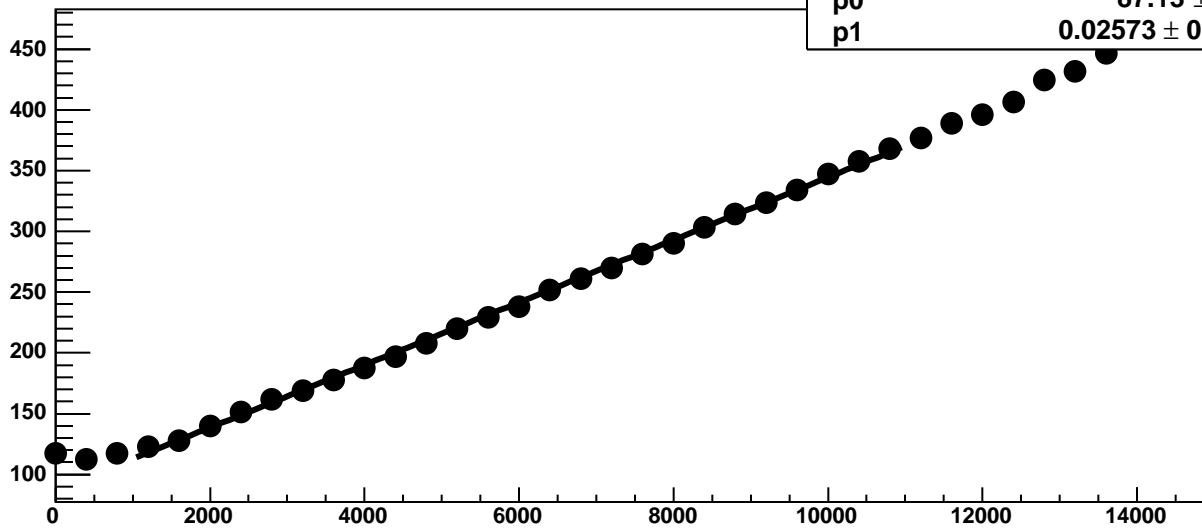
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

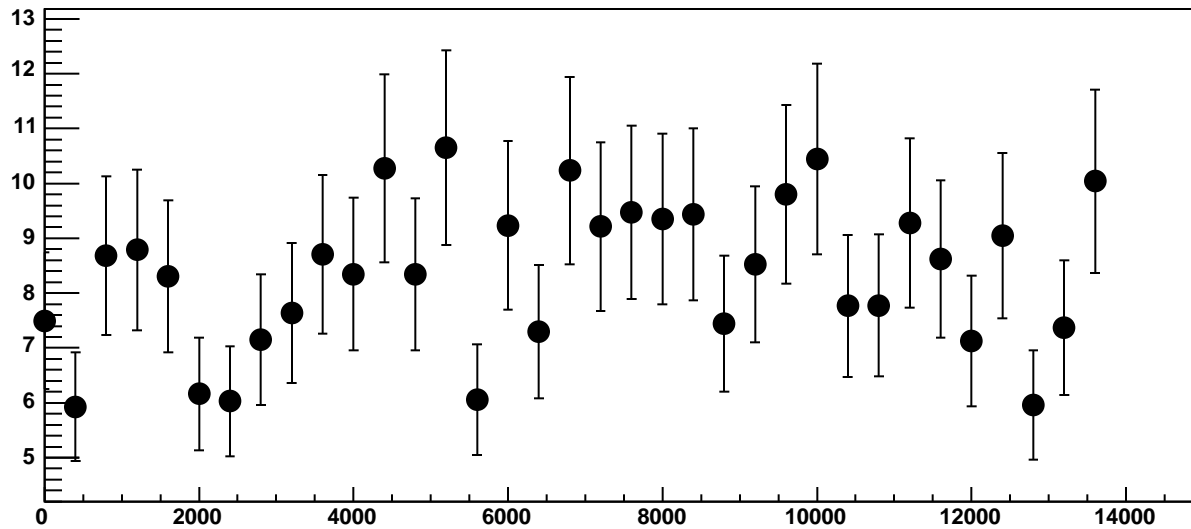


Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

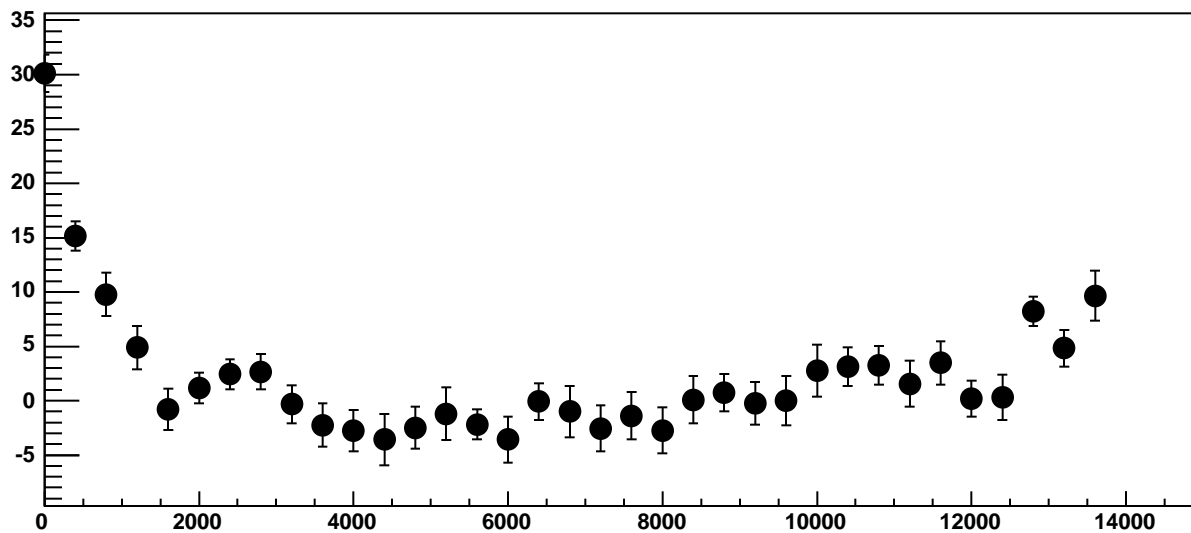


$\chi^2 / \text{ndf}$  36.97 / 23  
p0  $87.13 \pm 0.8086$   
p1  $0.02573 \pm 0.000127$

Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

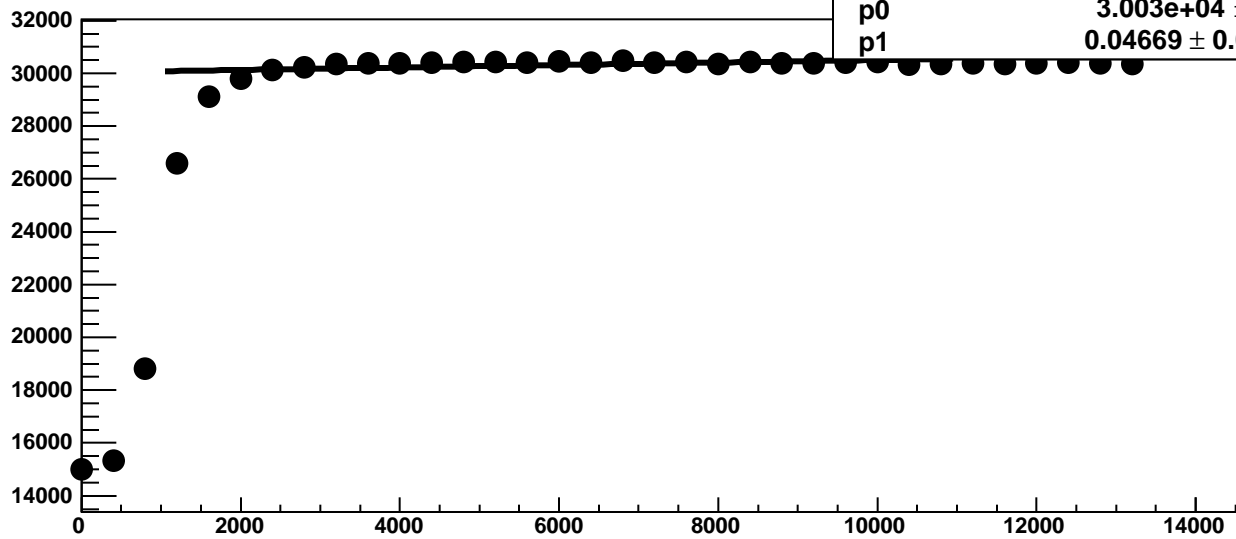


Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

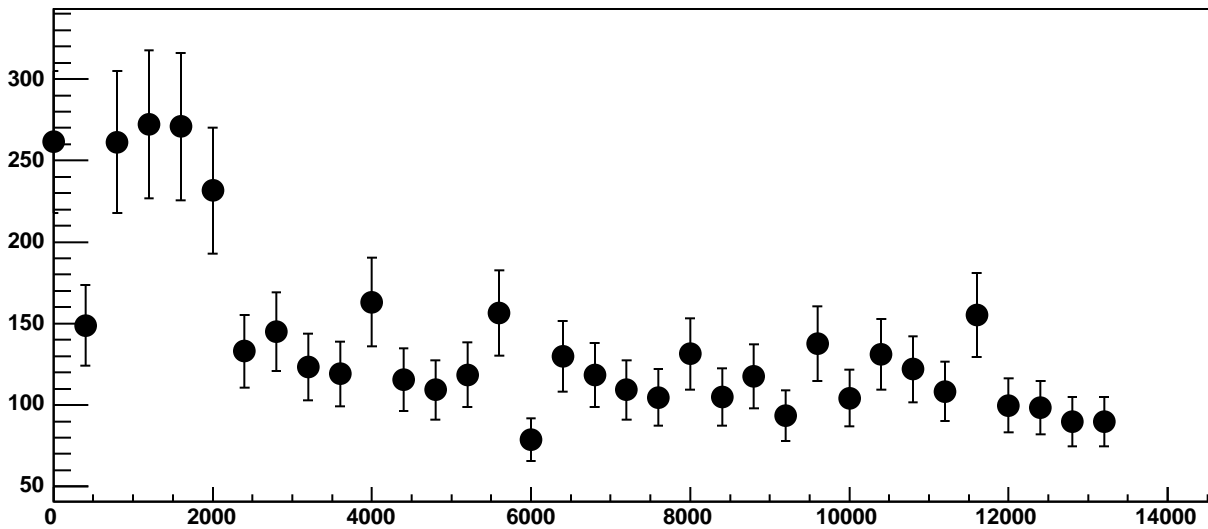




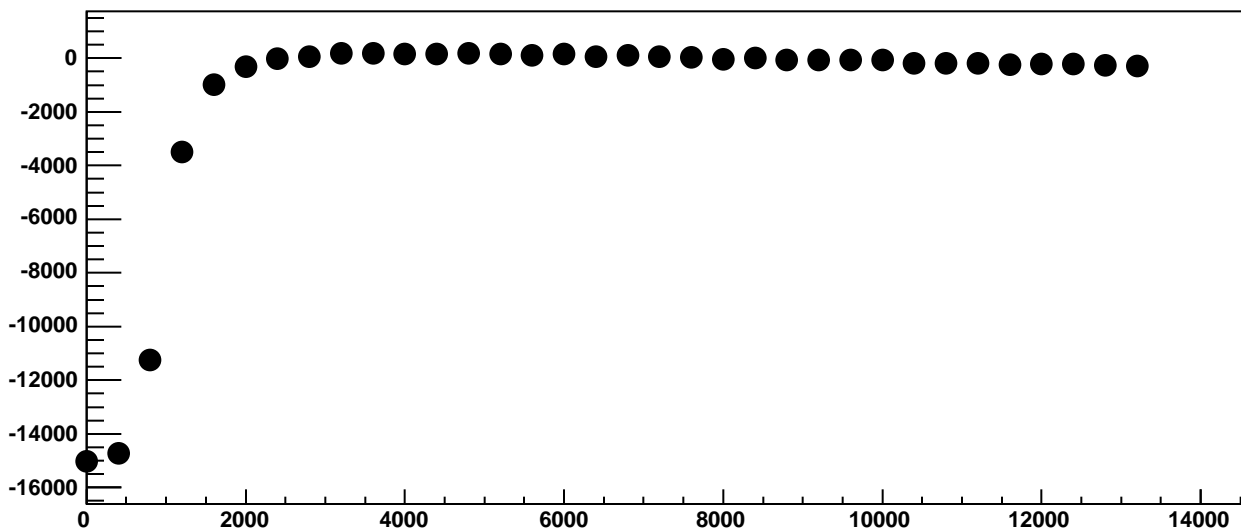
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



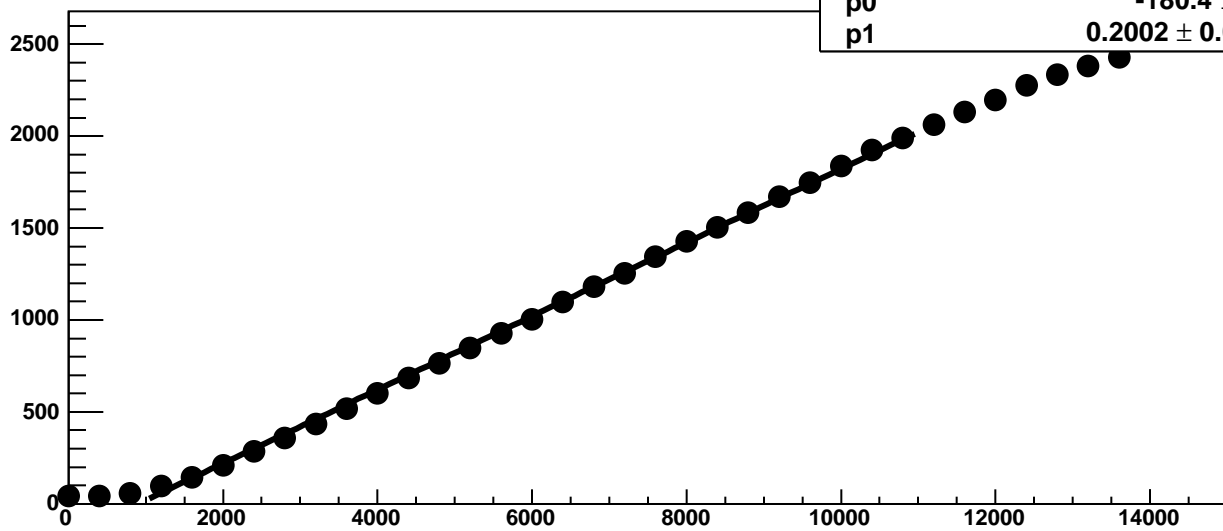
Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

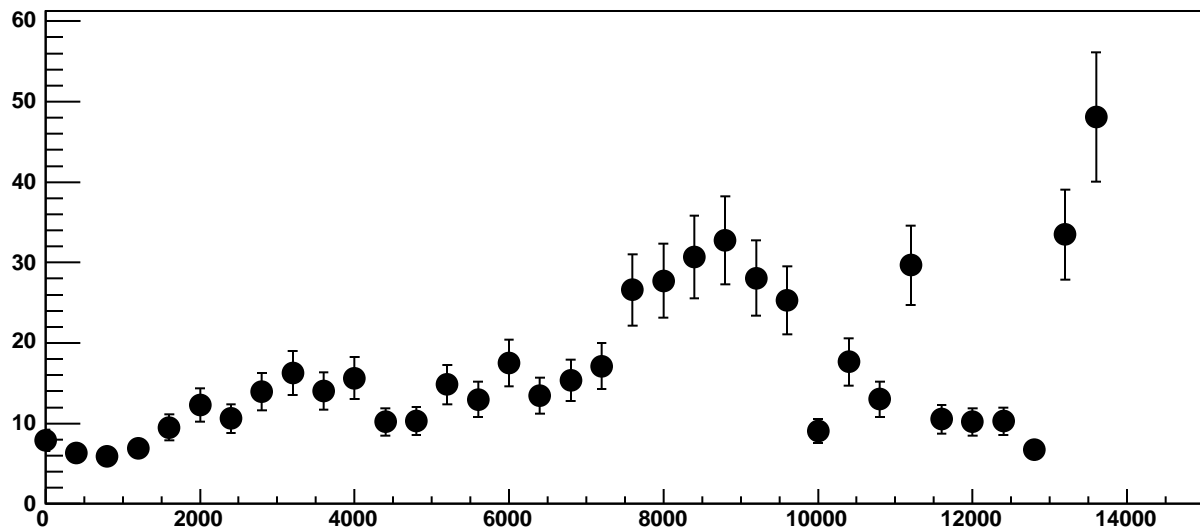


Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

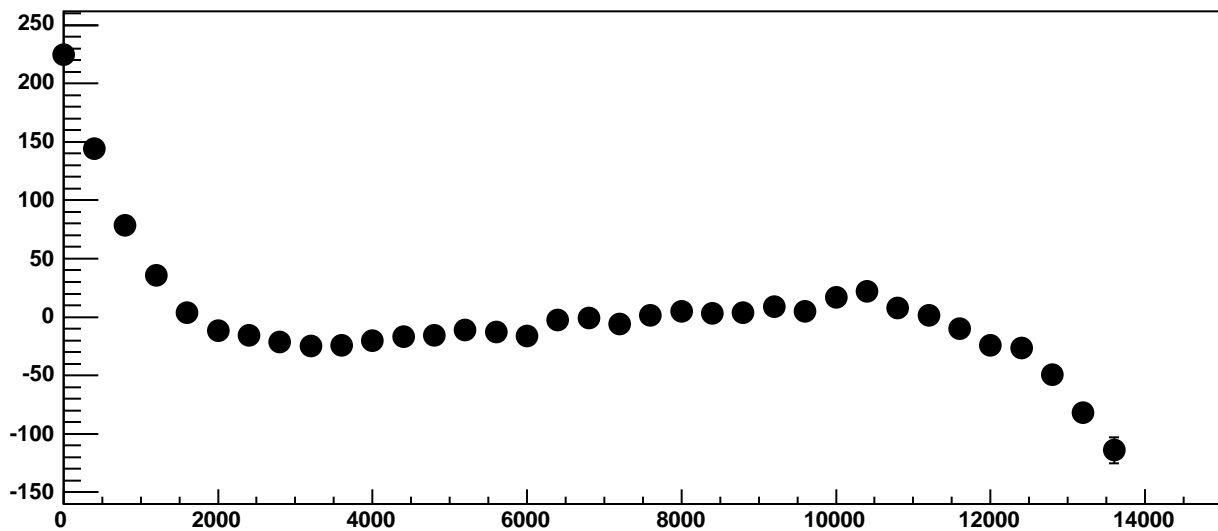


$\chi^2 / \text{ndf}$  998 / 23  
p0  $-180.4 \pm 1.132$   
p1  $0.2002 \pm 0.000198$

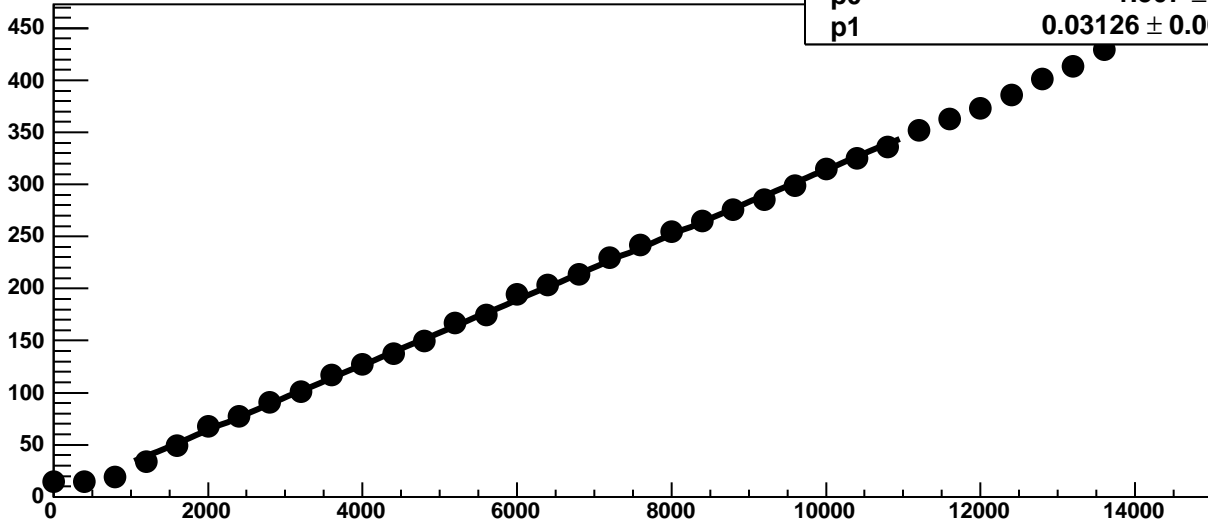
Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

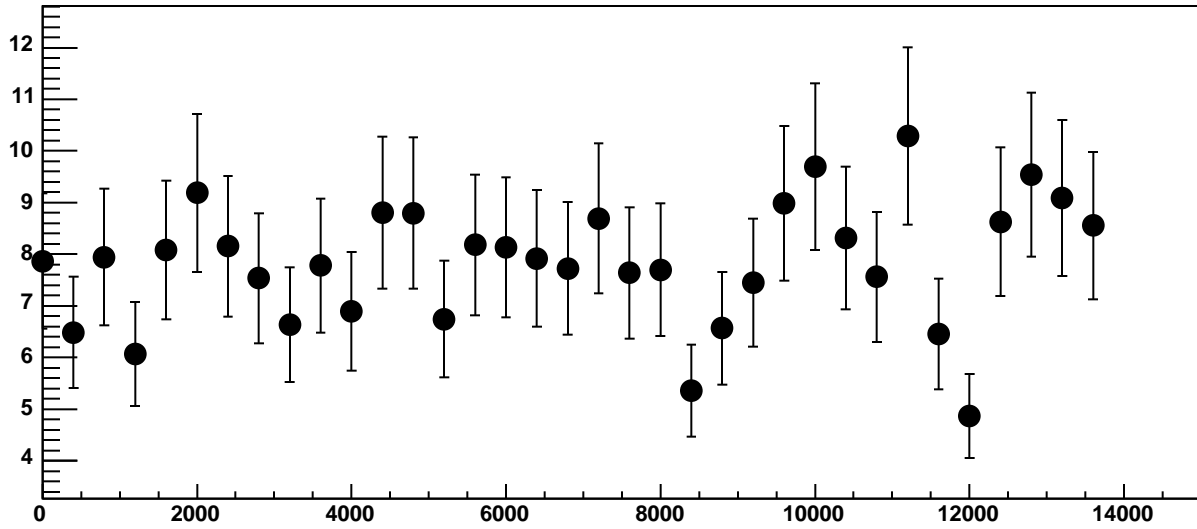


Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

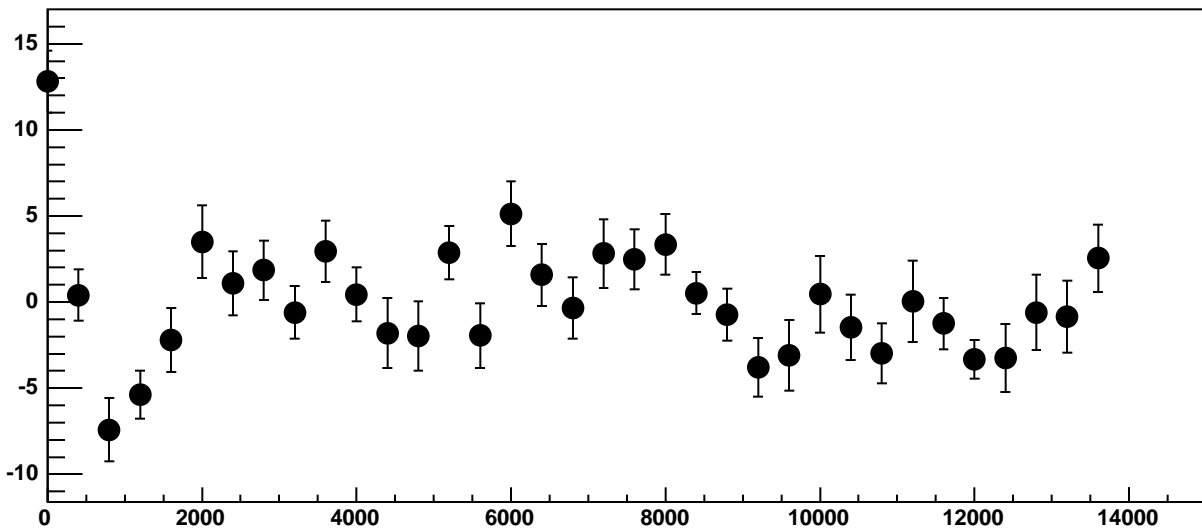


$\chi^2 / \text{ndf}$  57.08 / 23  
p0  $1.507 \pm 0.7954$   
p1  $0.03126 \pm 0.0001203$

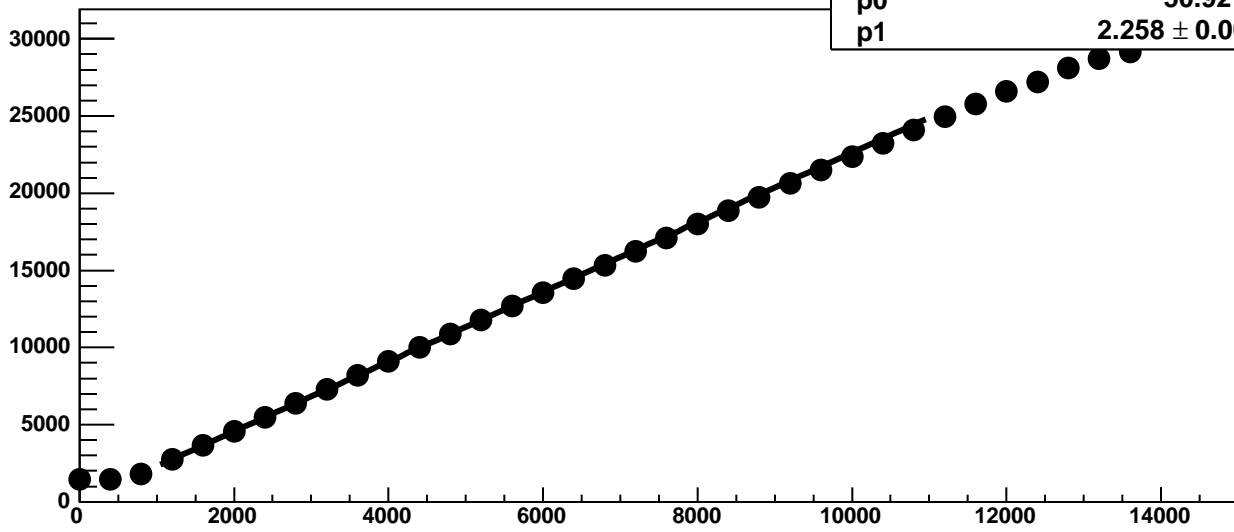
Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



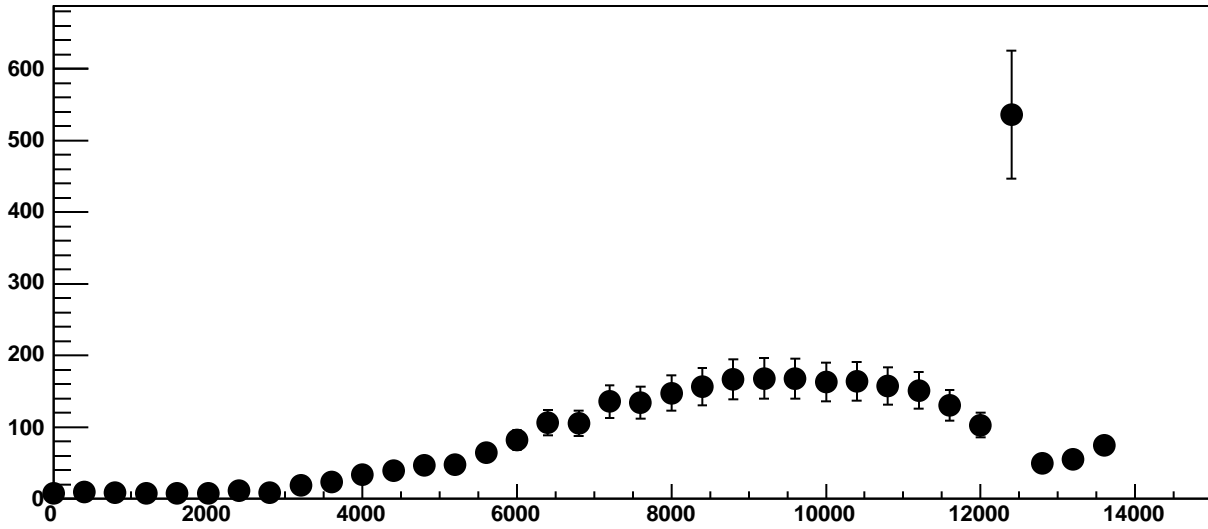
Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



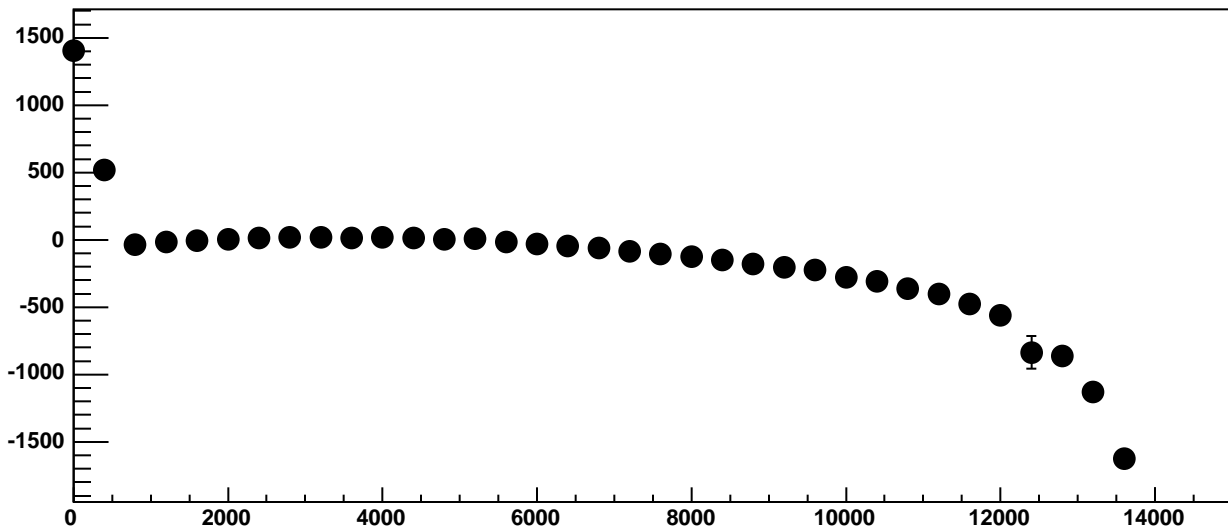
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



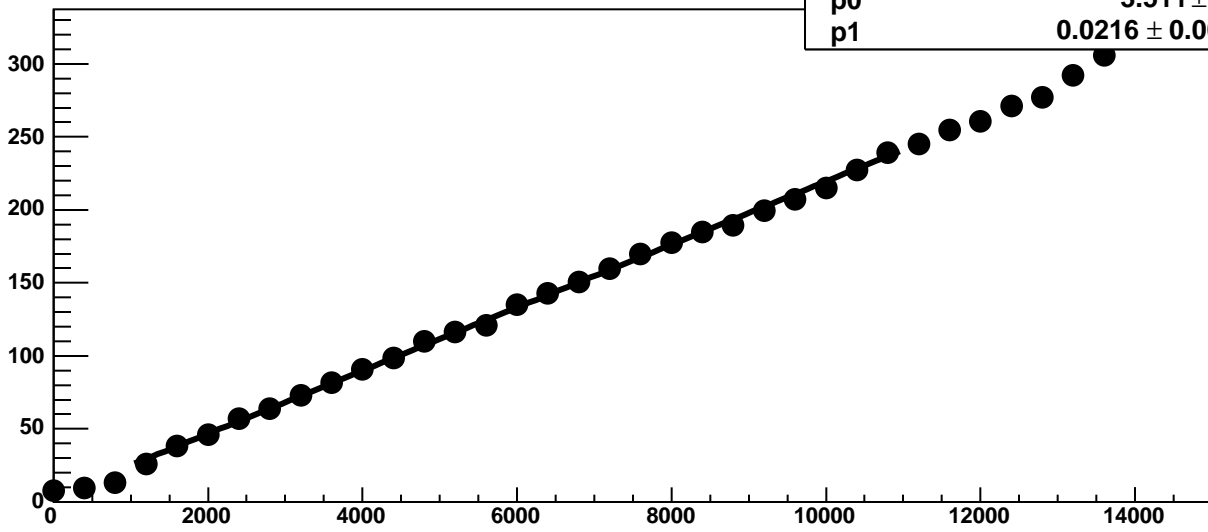
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

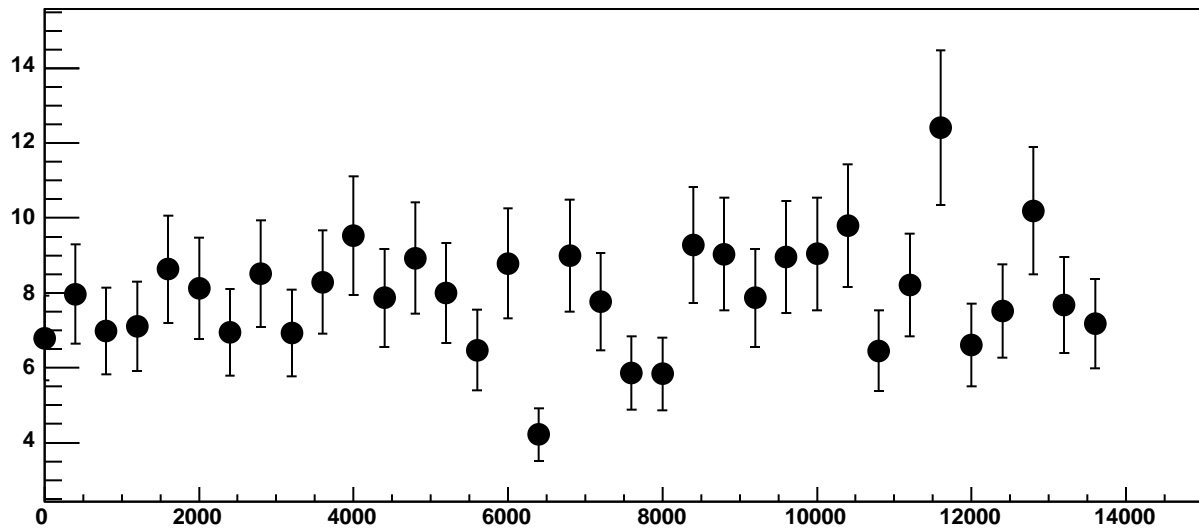


Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

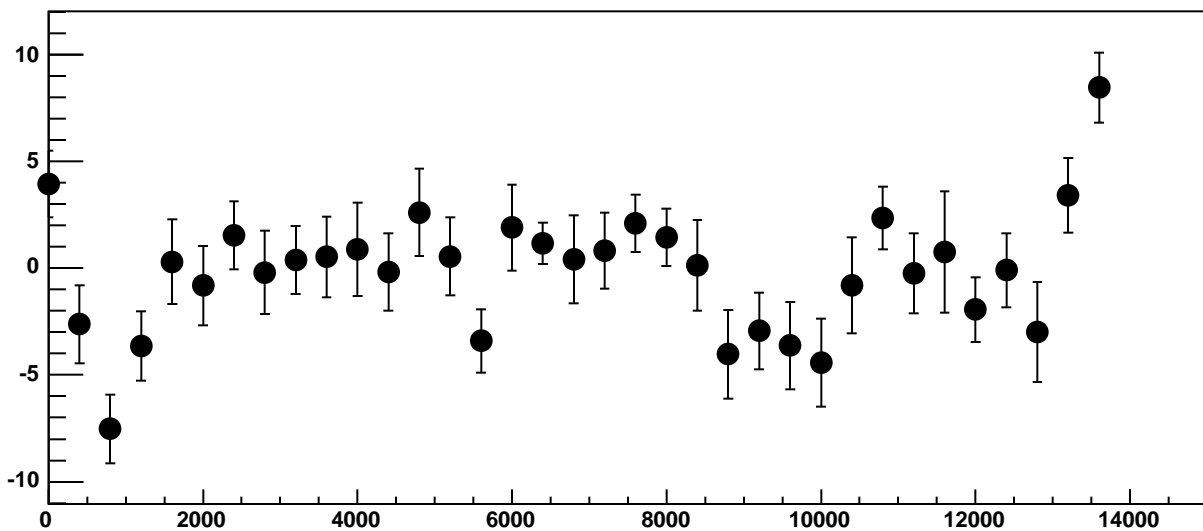


$\chi^2 / \text{ndf}$  36.4 / 23  
p0  $3.511 \pm 0.8237$   
p1  $0.0216 \pm 0.0001243$

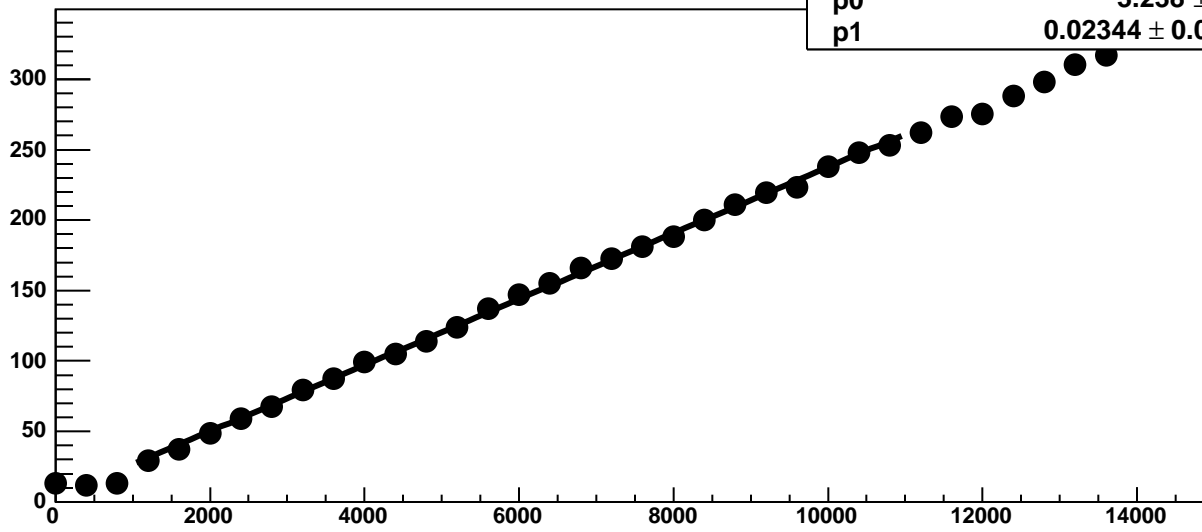
Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

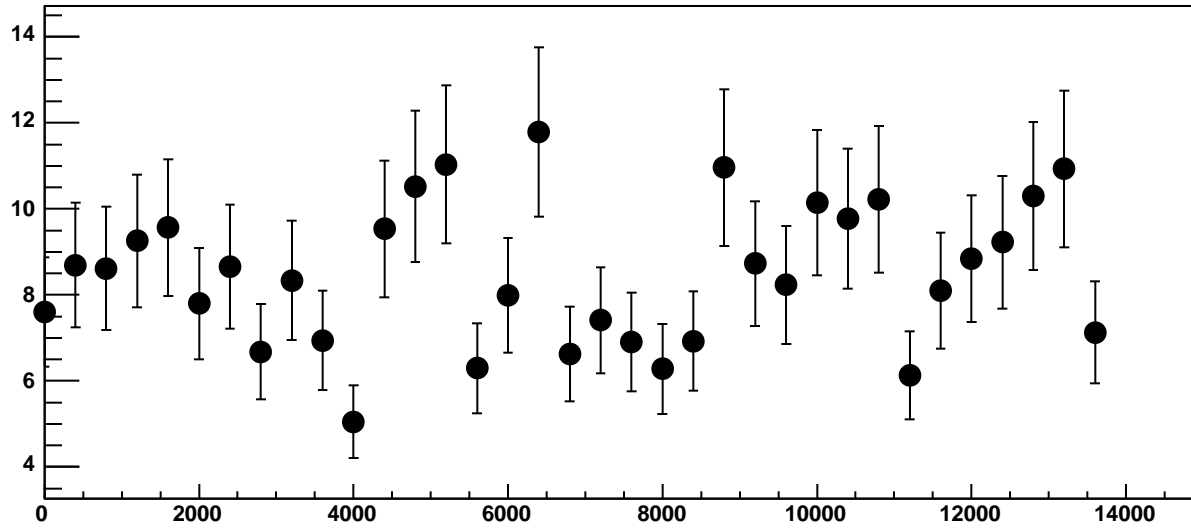


Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

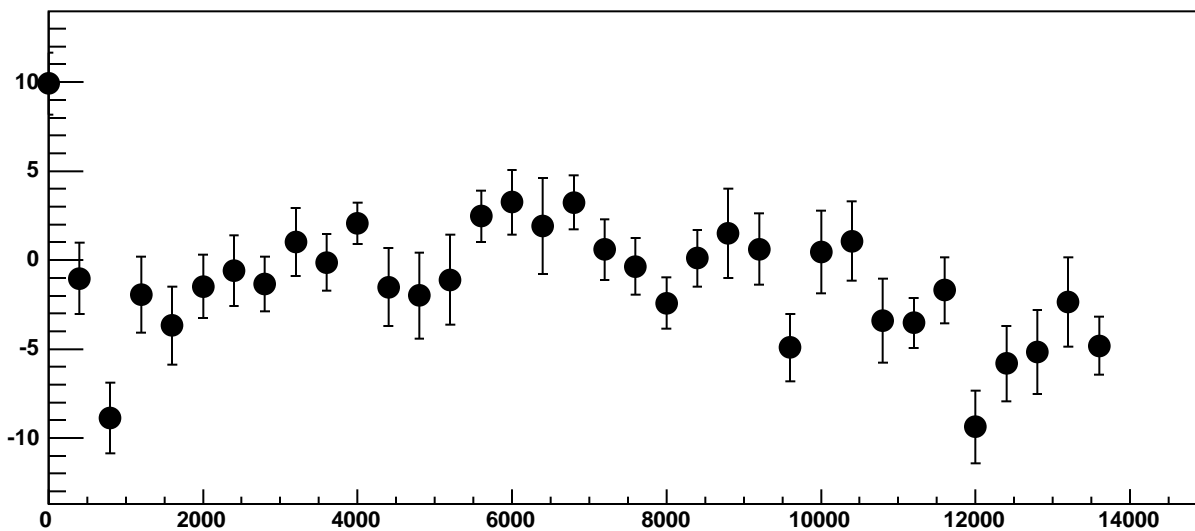


$\chi^2 / \text{ndf}$  33.71 / 23  
p0  $3.238 \pm 0.8677$   
p1  $0.02344 \pm 0.0001357$

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

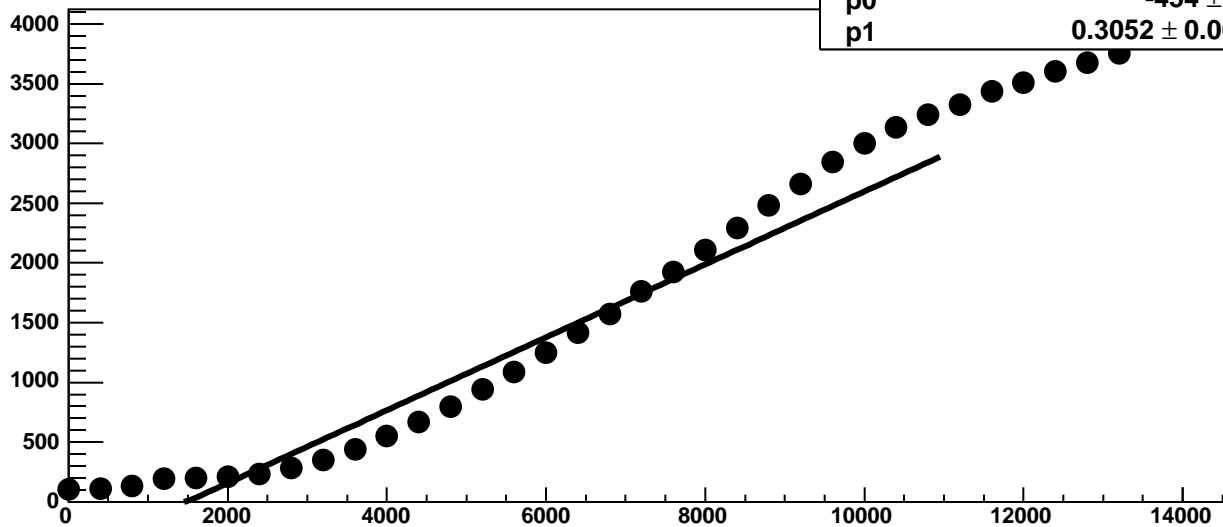


Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

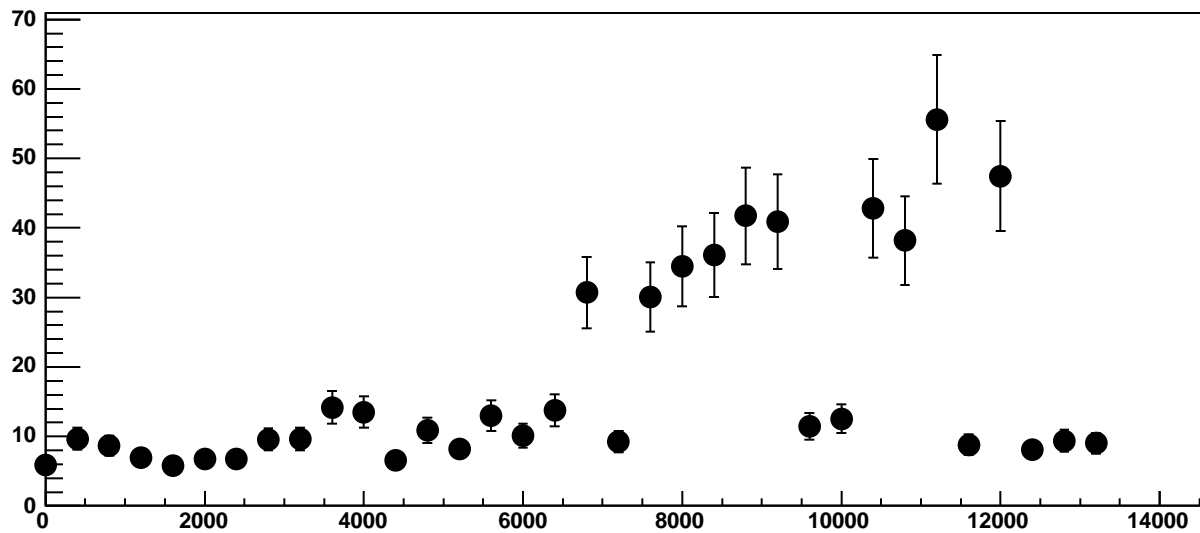


Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

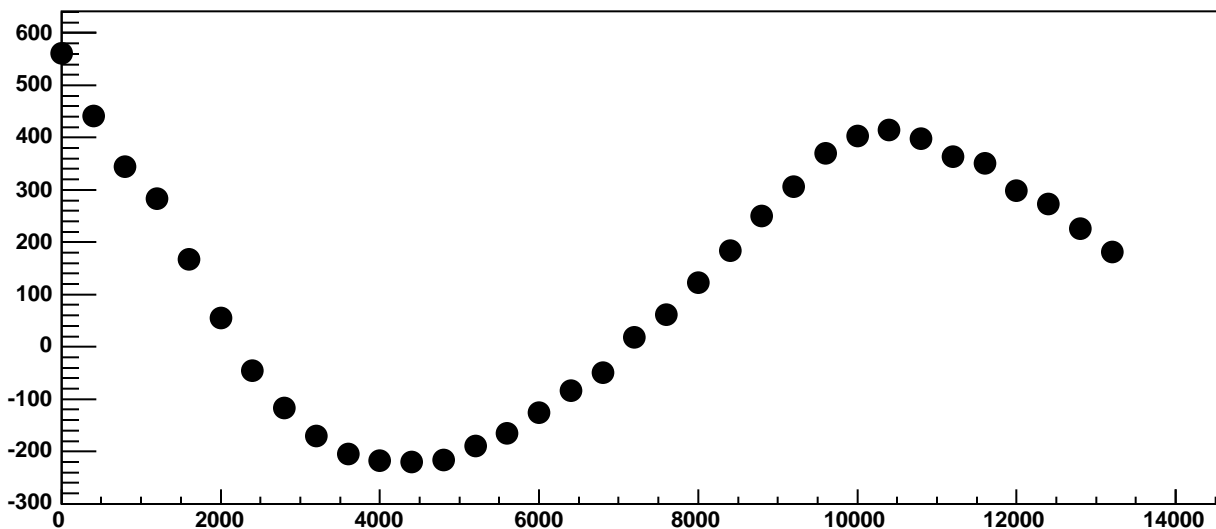
$\chi^2 / \text{ndf}$  1.591e+05 / 23  
p0 -454 ± 0.9199  
p1 0.3052 ± 0.0001983



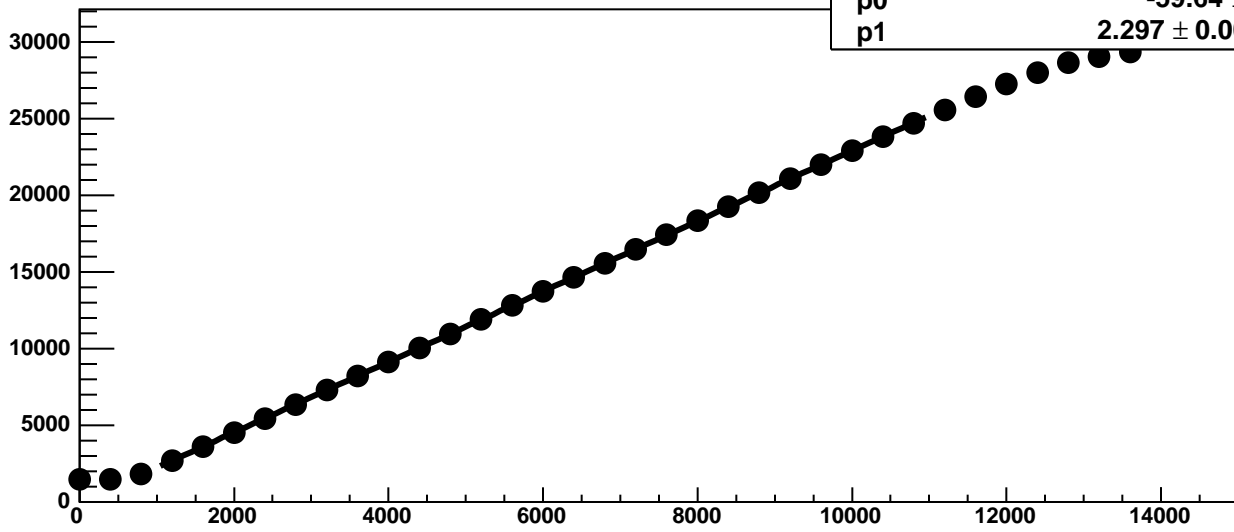
Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



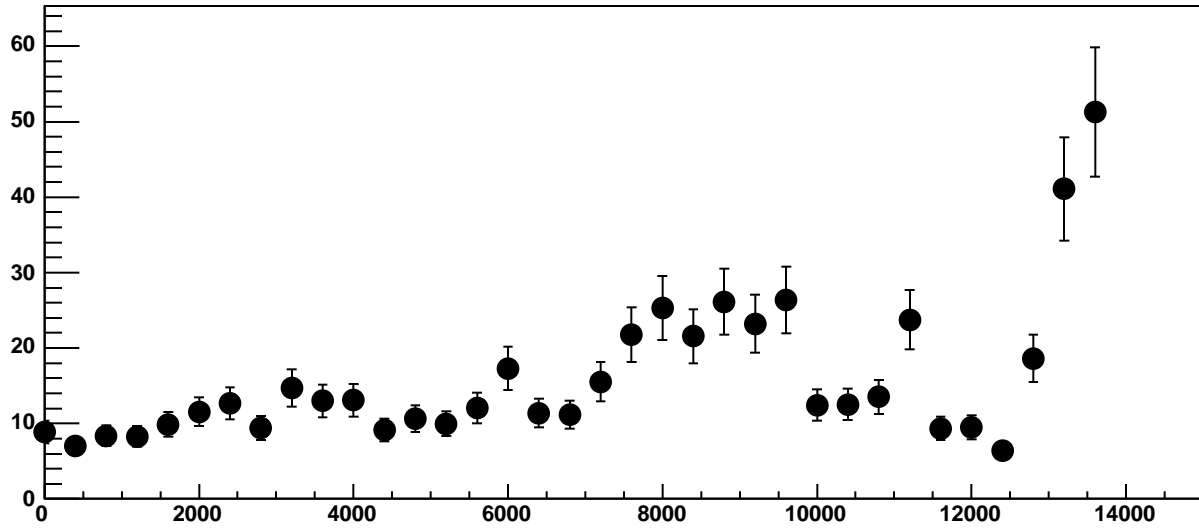
Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



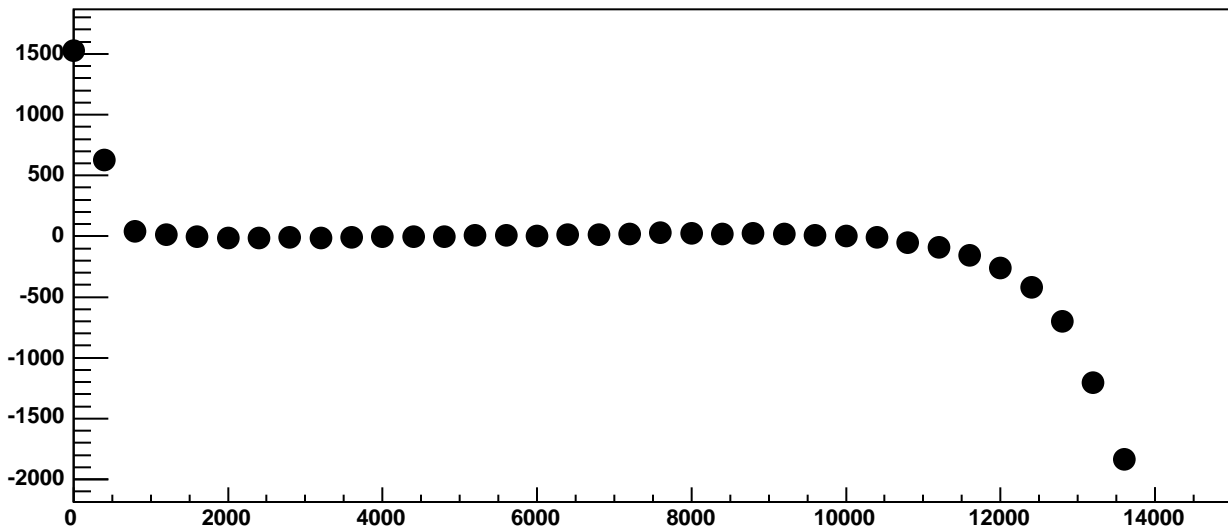
Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

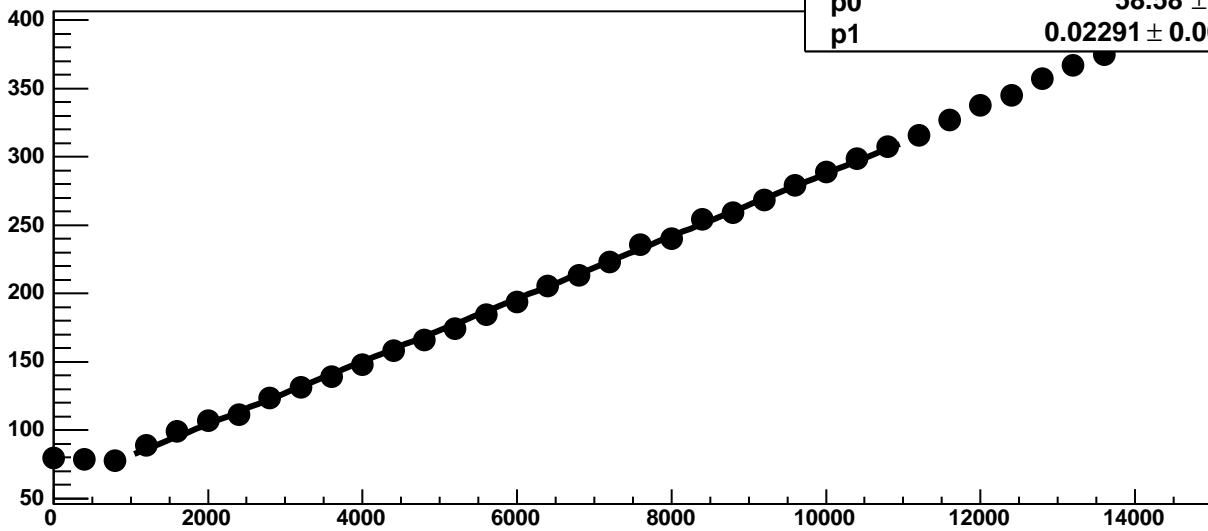


Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



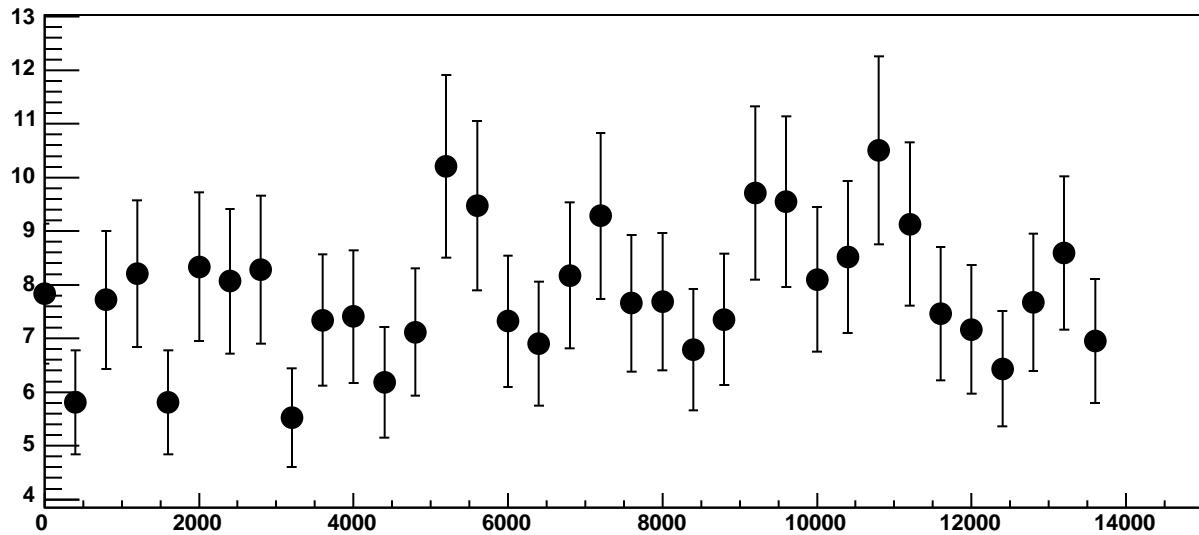


Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

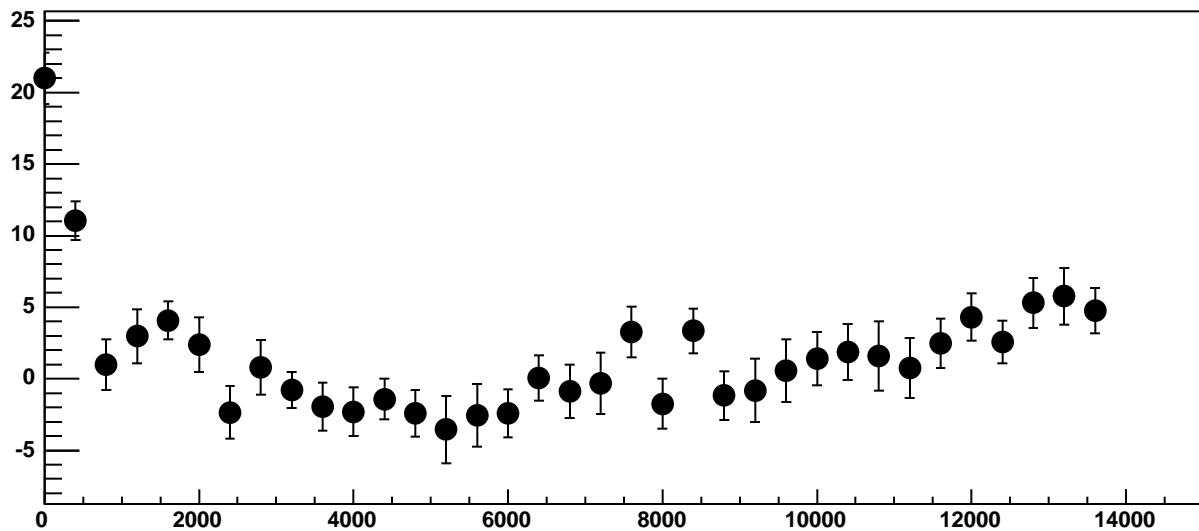


$\chi^2 / \text{ndf}$  39.65 / 23  
p0 58.58 ± 0.7849  
p1 0.02291 ± 0.0001255

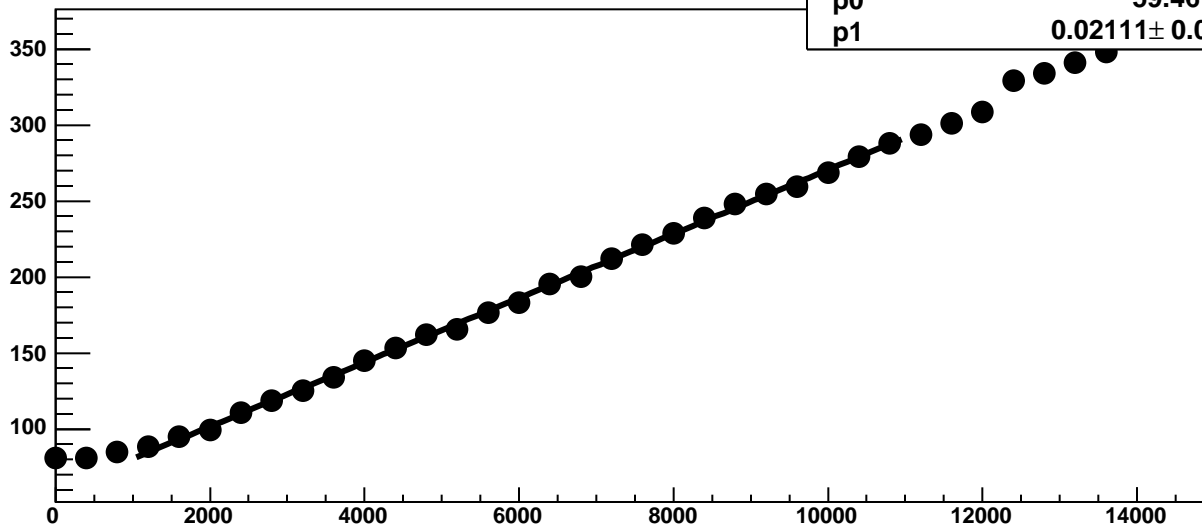
Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



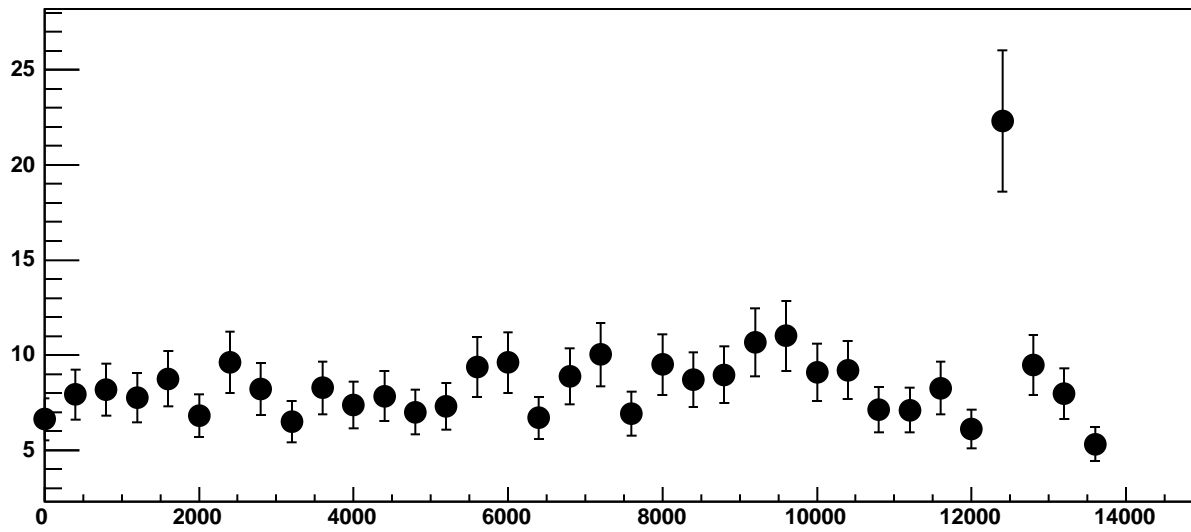
Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



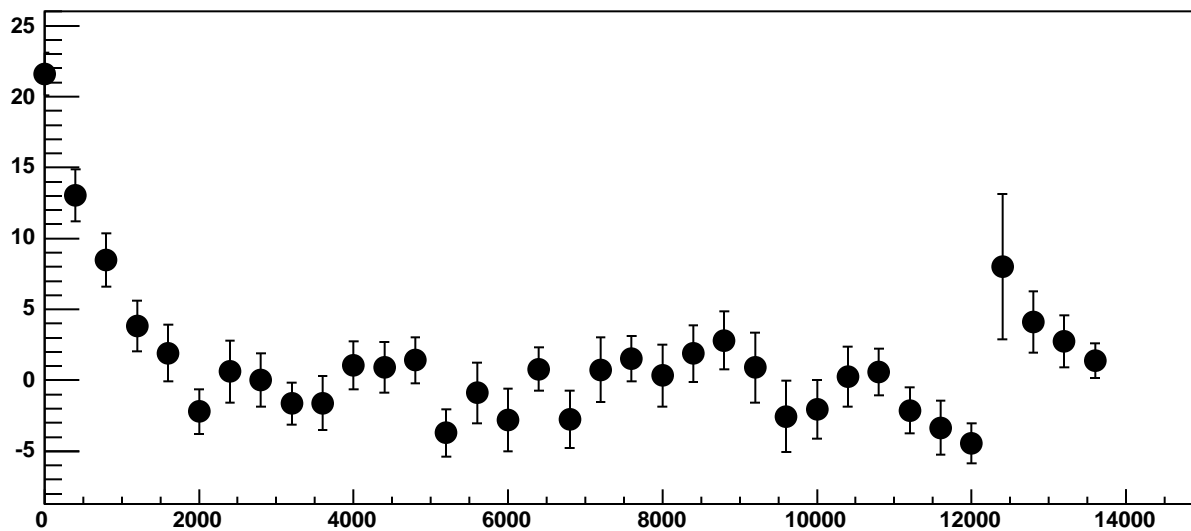
Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



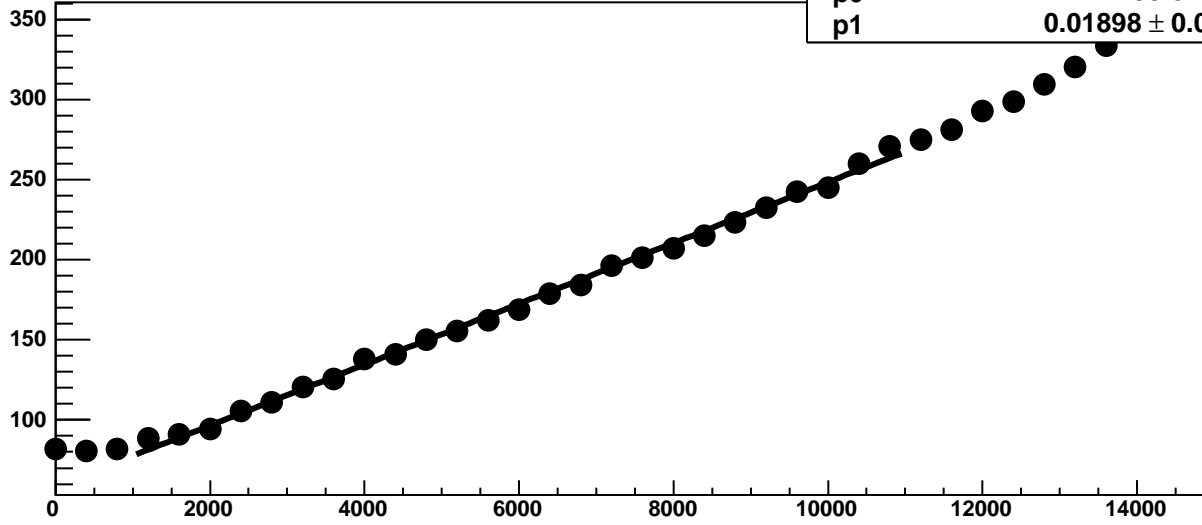
Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



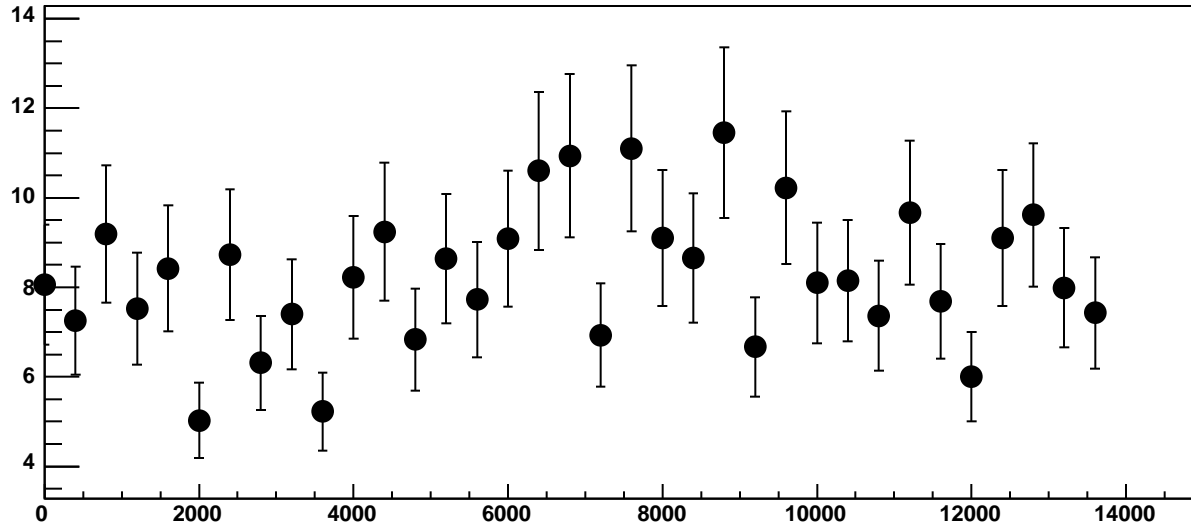
Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



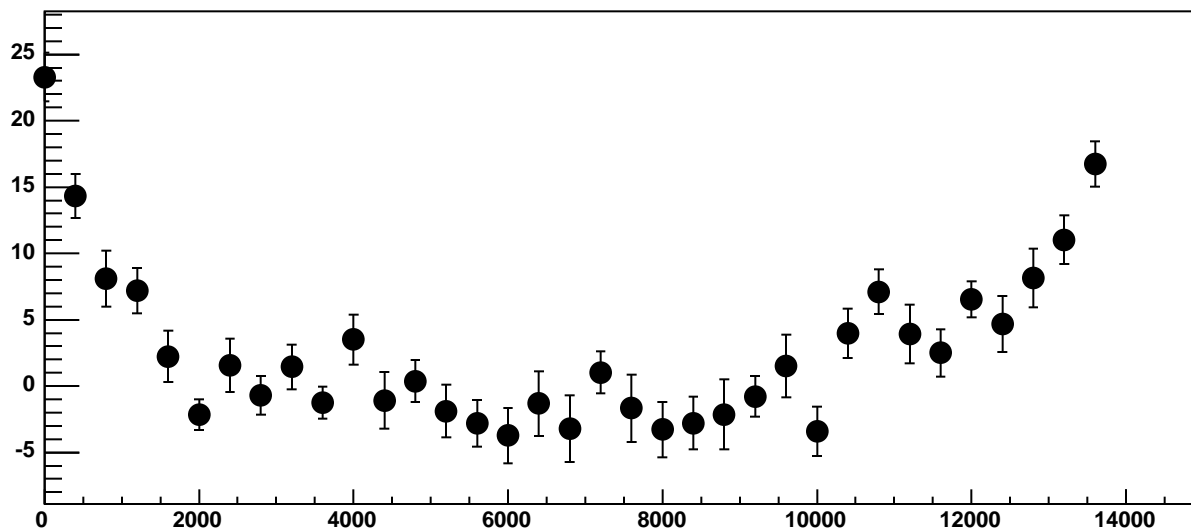
Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC



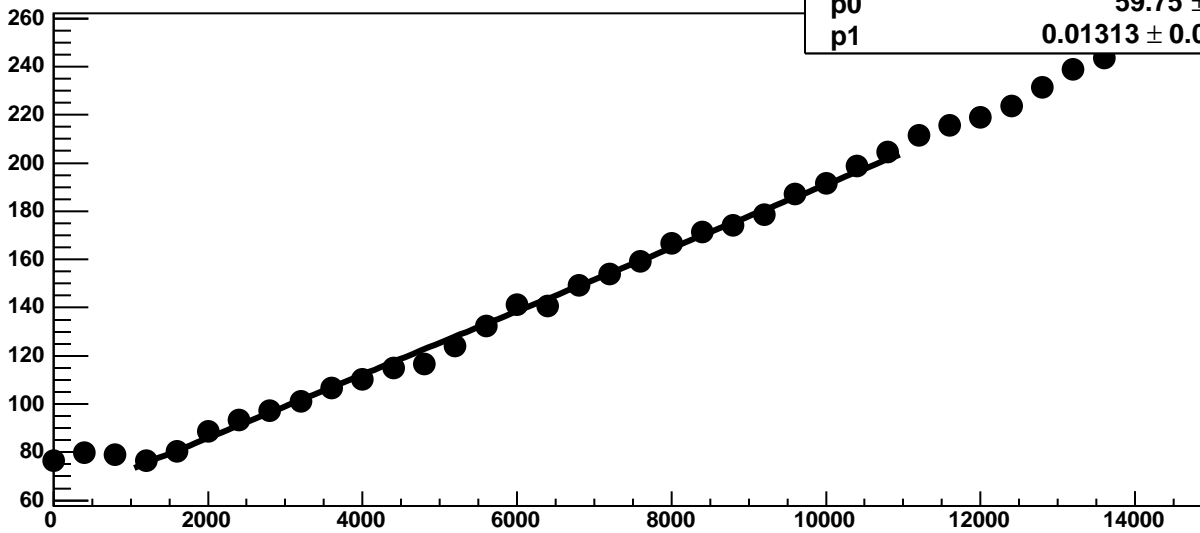
Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

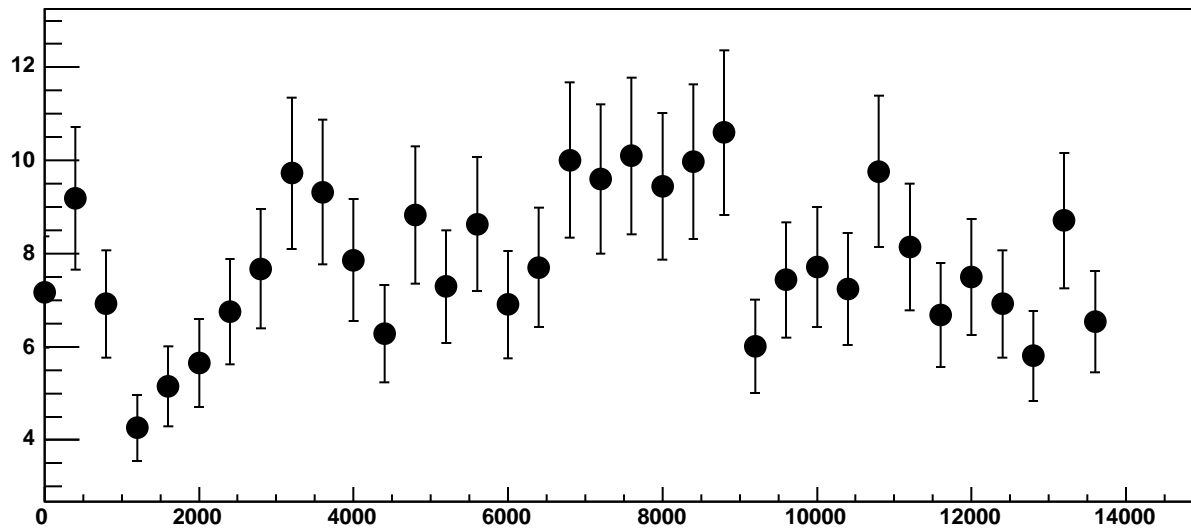


Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

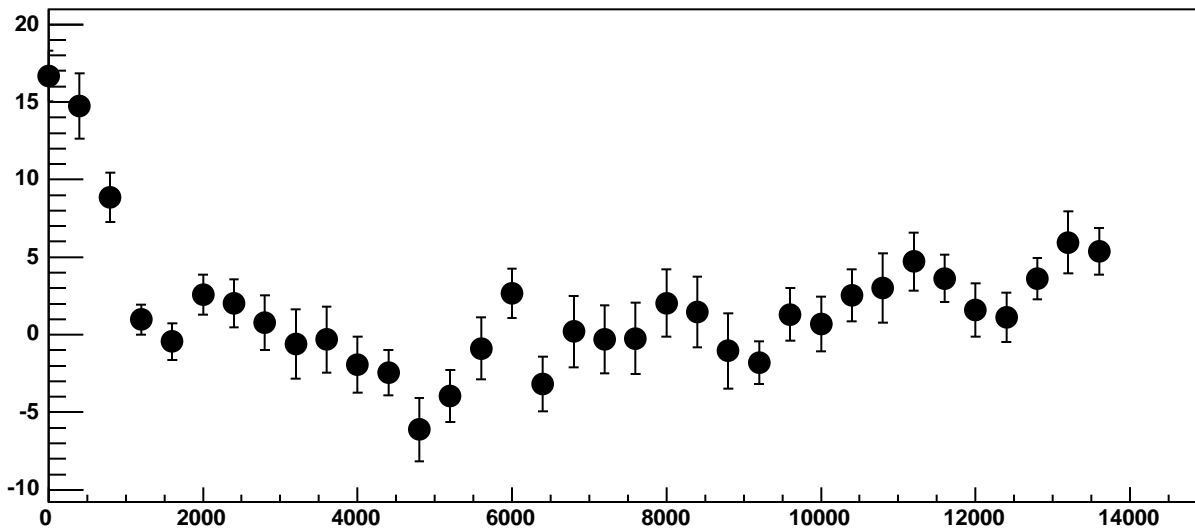


$\chi^2 / \text{ndf}$  40.16 / 23  
p0 59.75 ± 0.6493  
p1 0.01313 ± 0.0001073

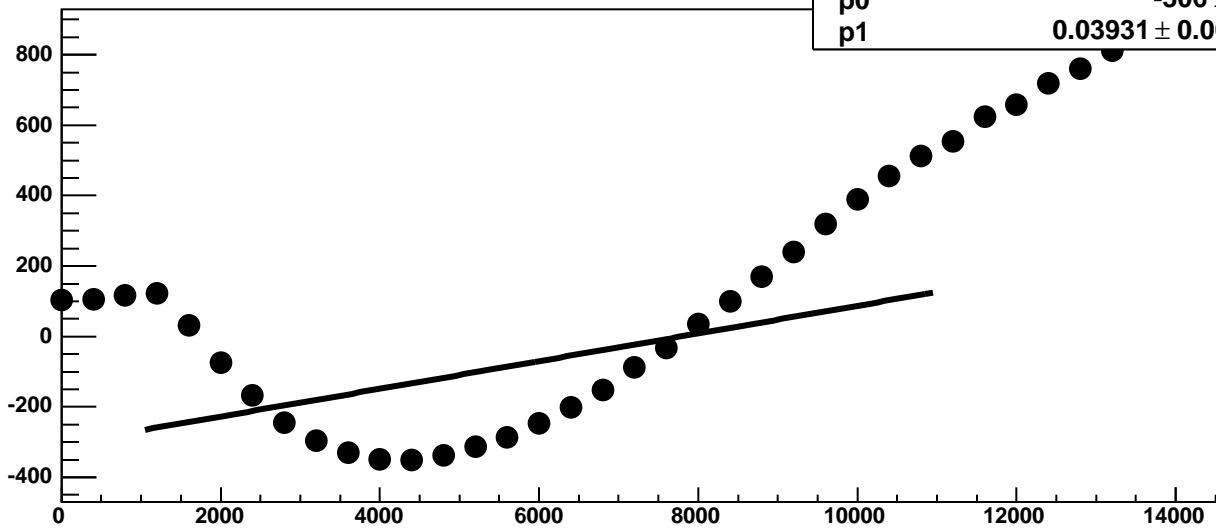
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



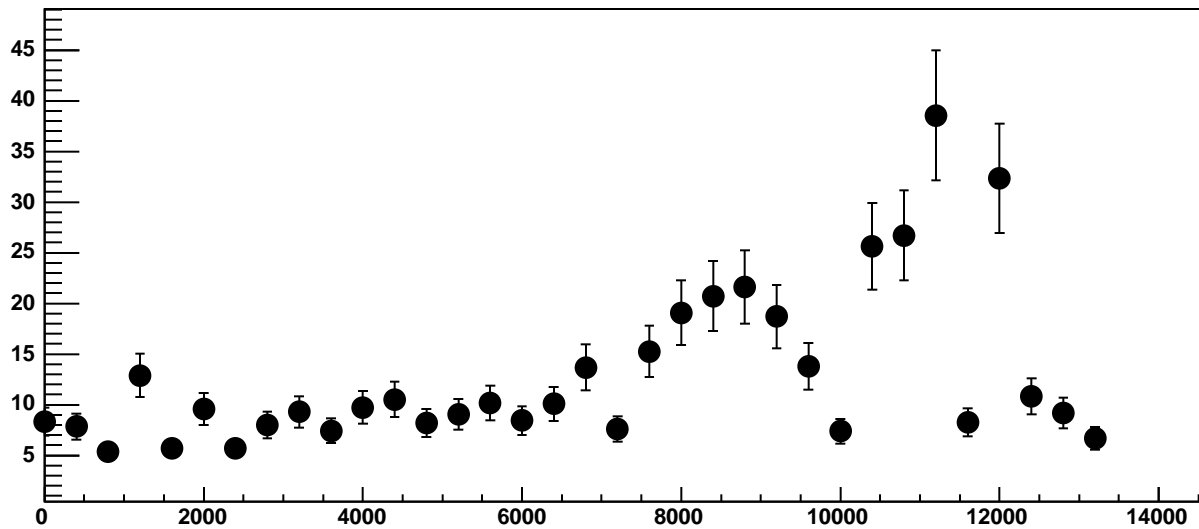
Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



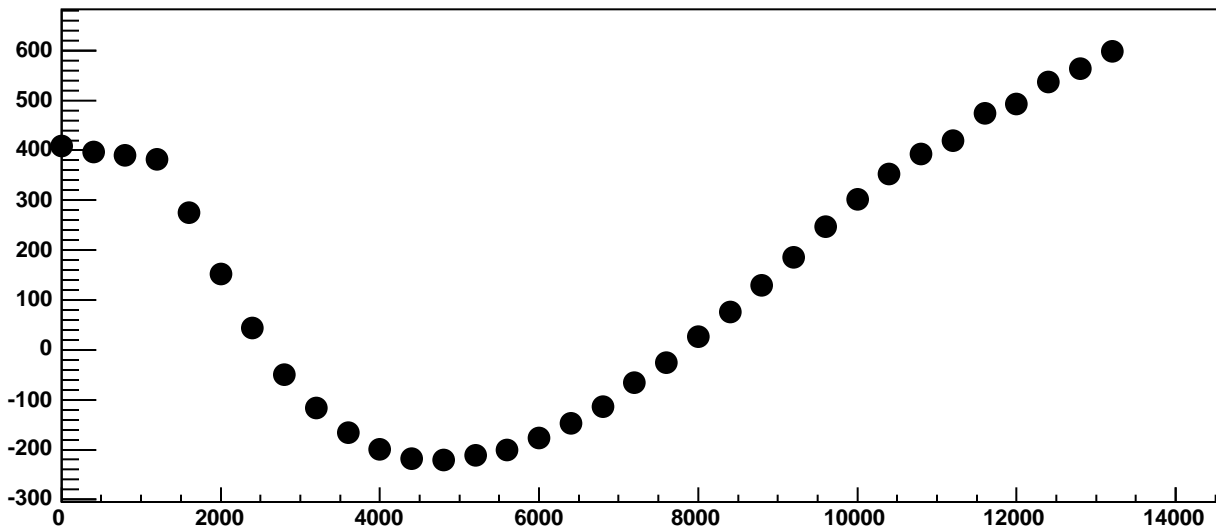
Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

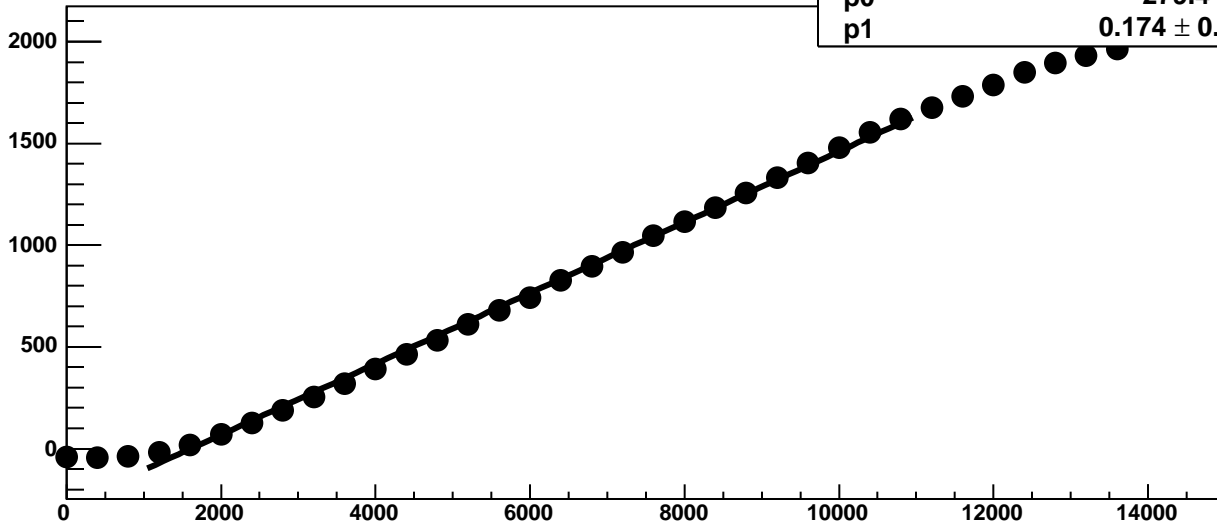


Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

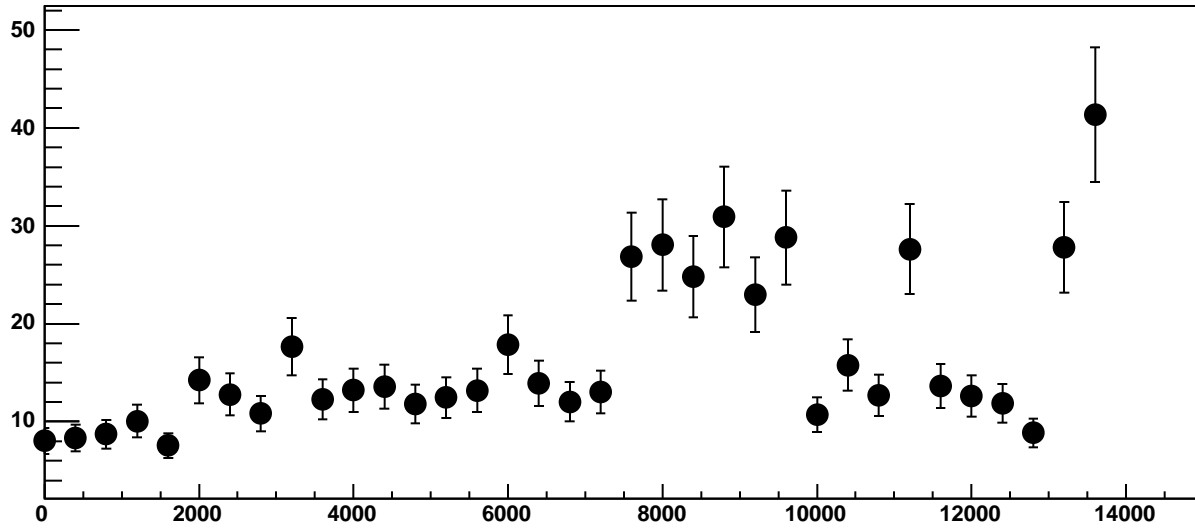


Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

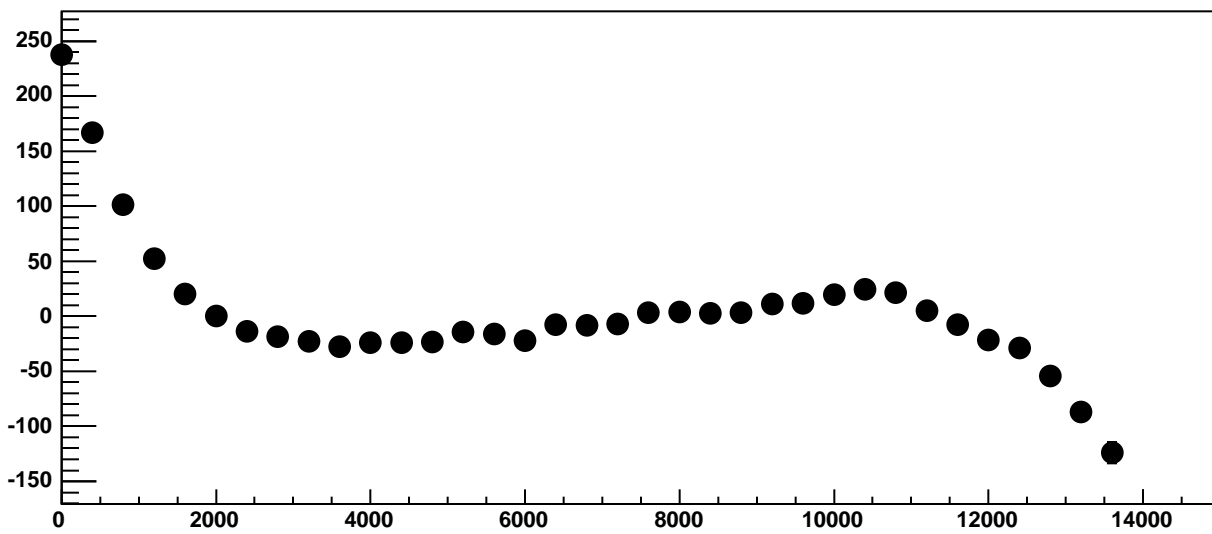
$\chi^2 / \text{ndf}$  1335 / 23  
p0  $-279.4 \pm 1.208$   
p1  $0.174 \pm 0.000207$



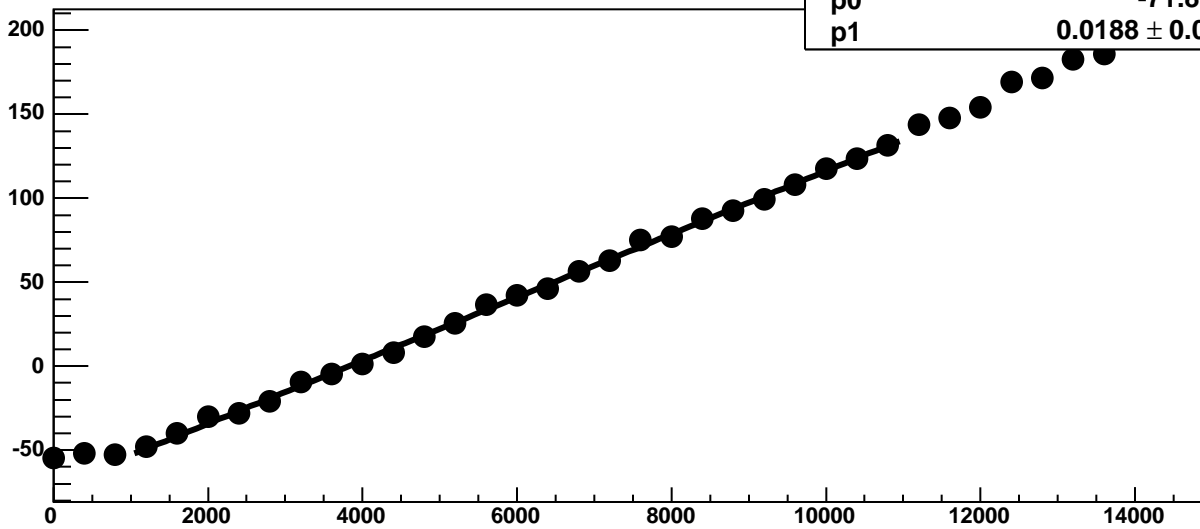
Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

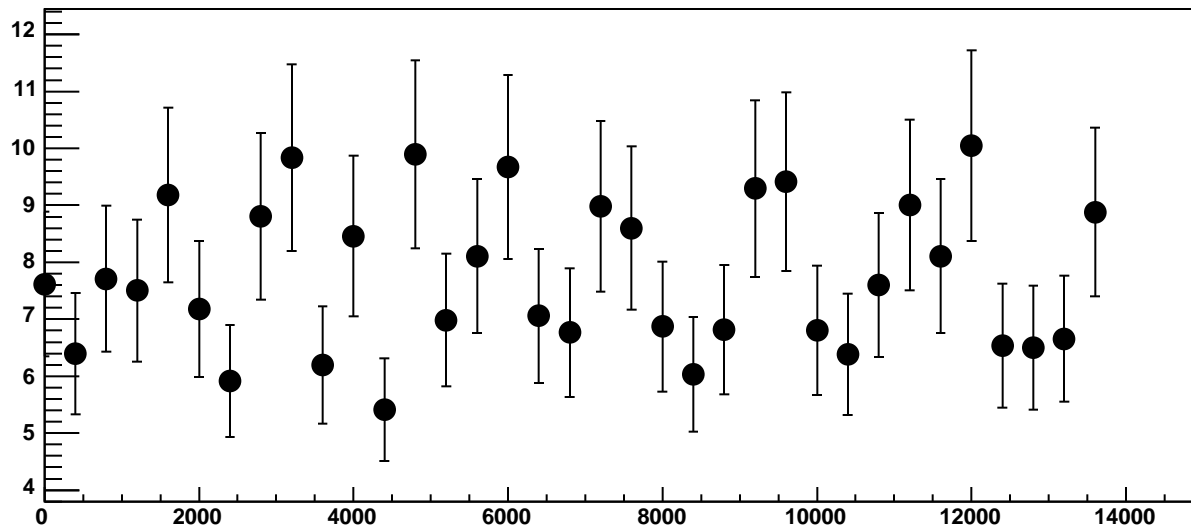


Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

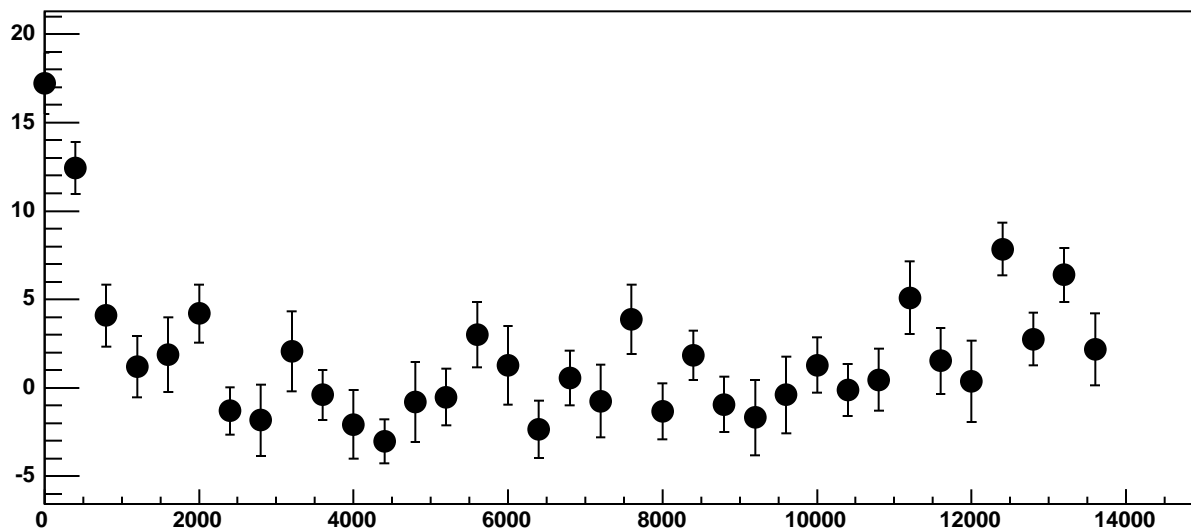


$\chi^2 / \text{ndf}$  31.17 / 23  
p0  $-71.8 \pm 0.786$   
p1  $0.0188 \pm 0.0001174$

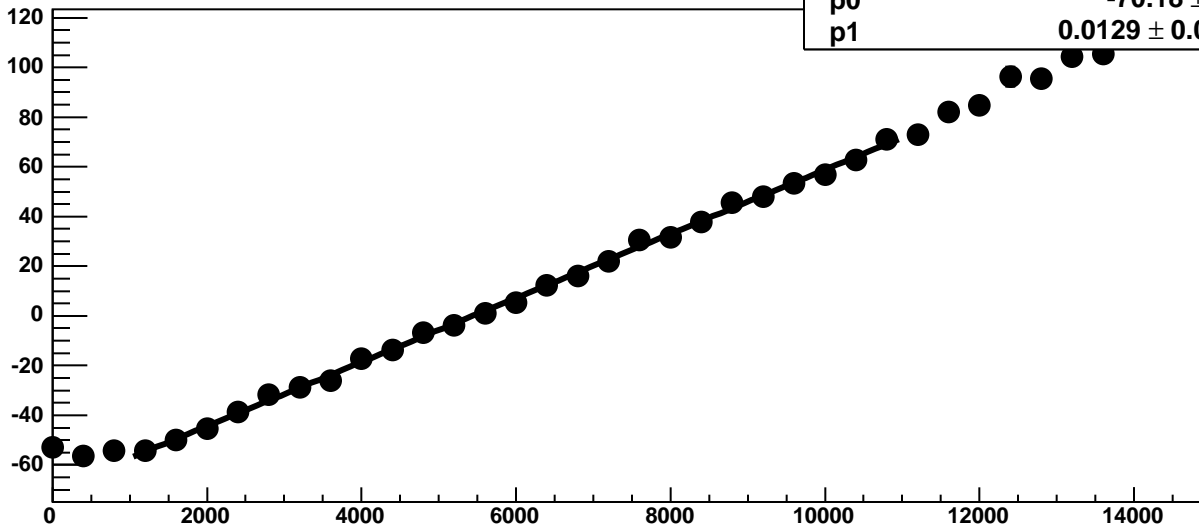
Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



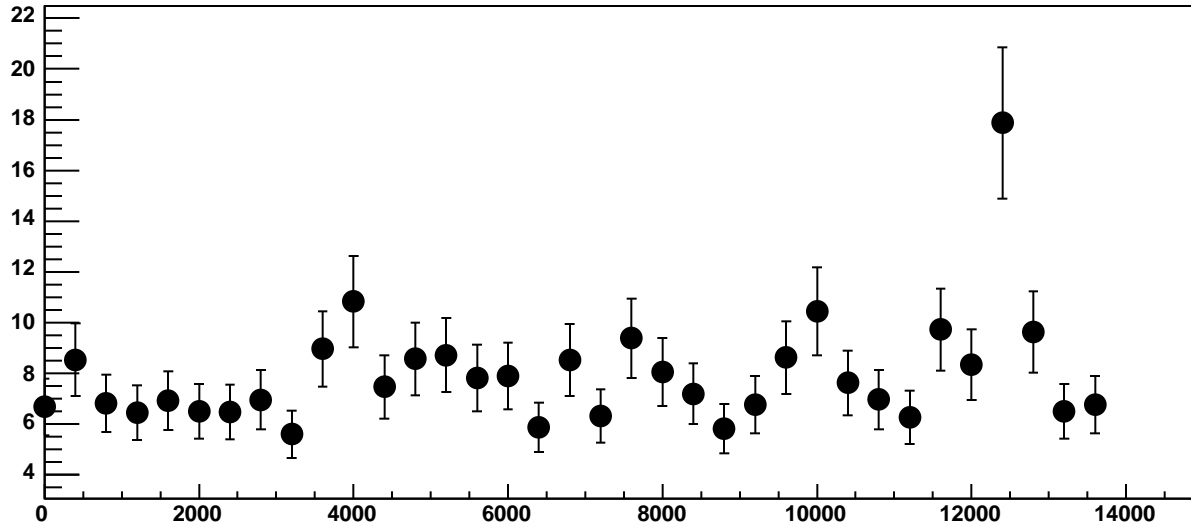
Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



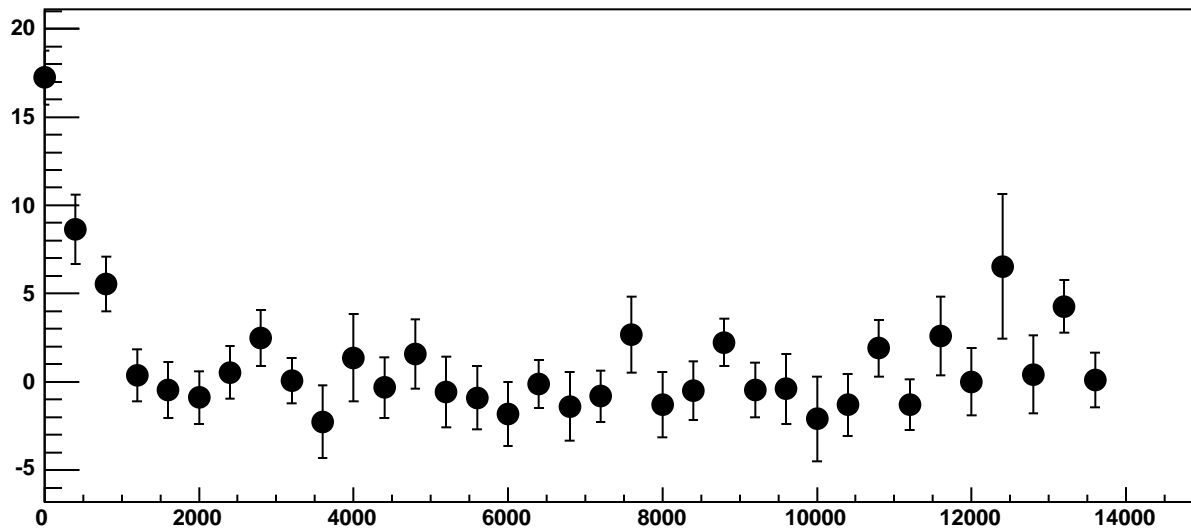
Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

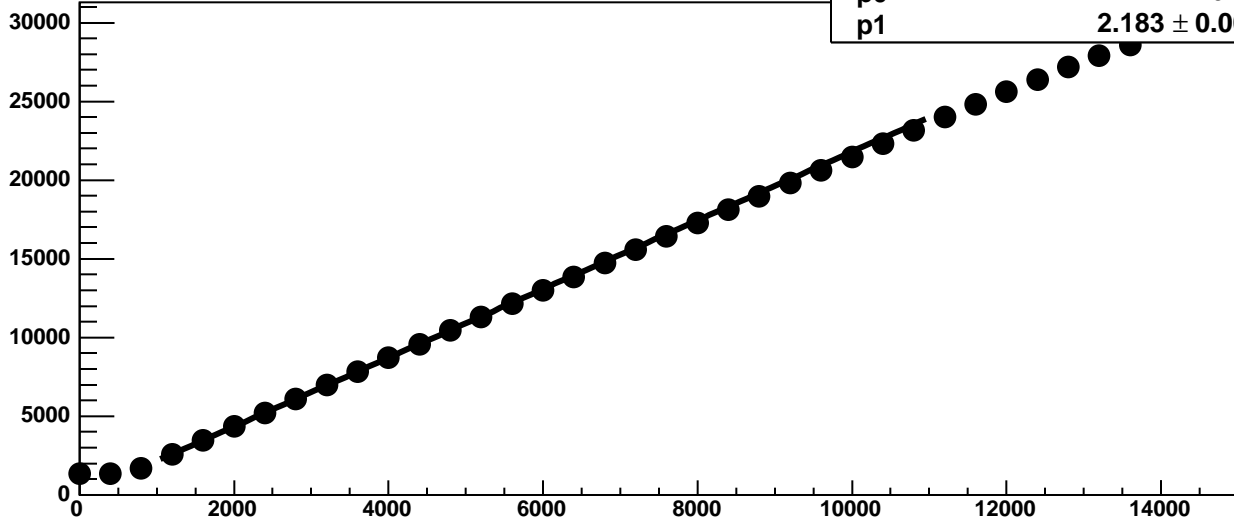


Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

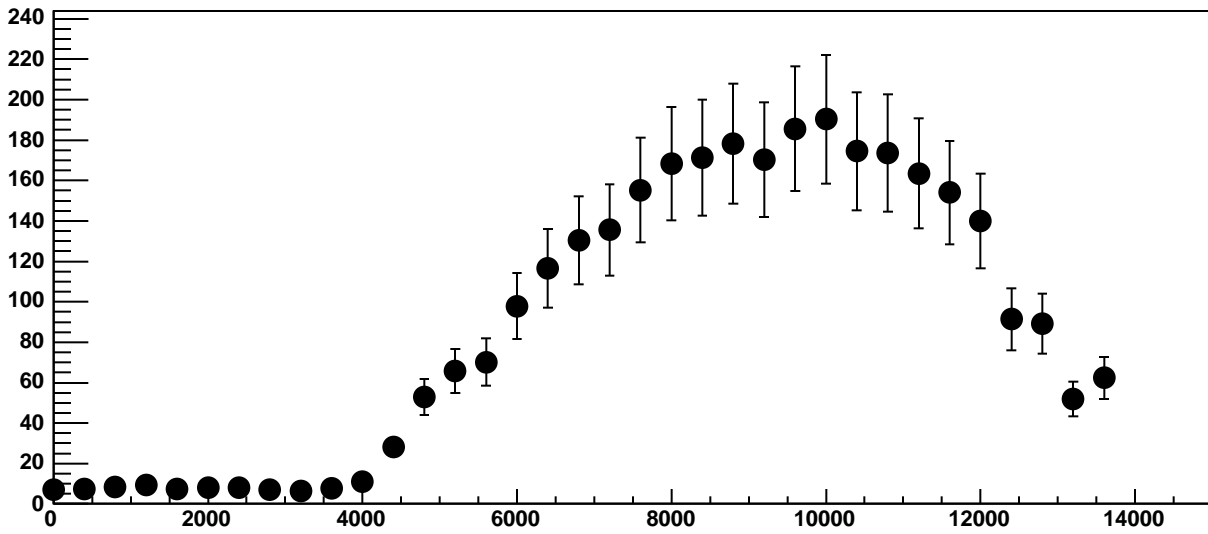




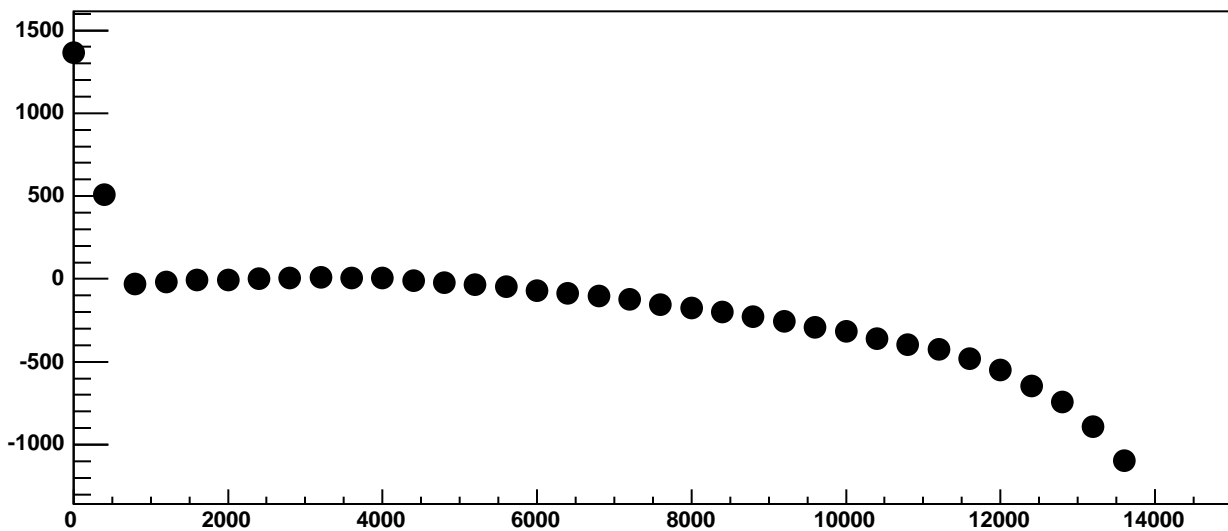
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



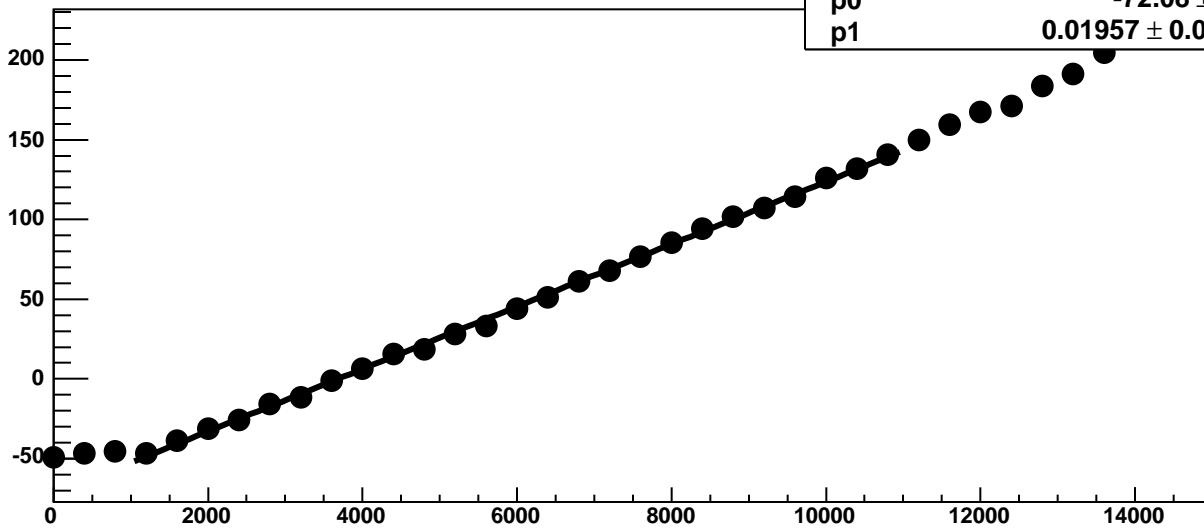
Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

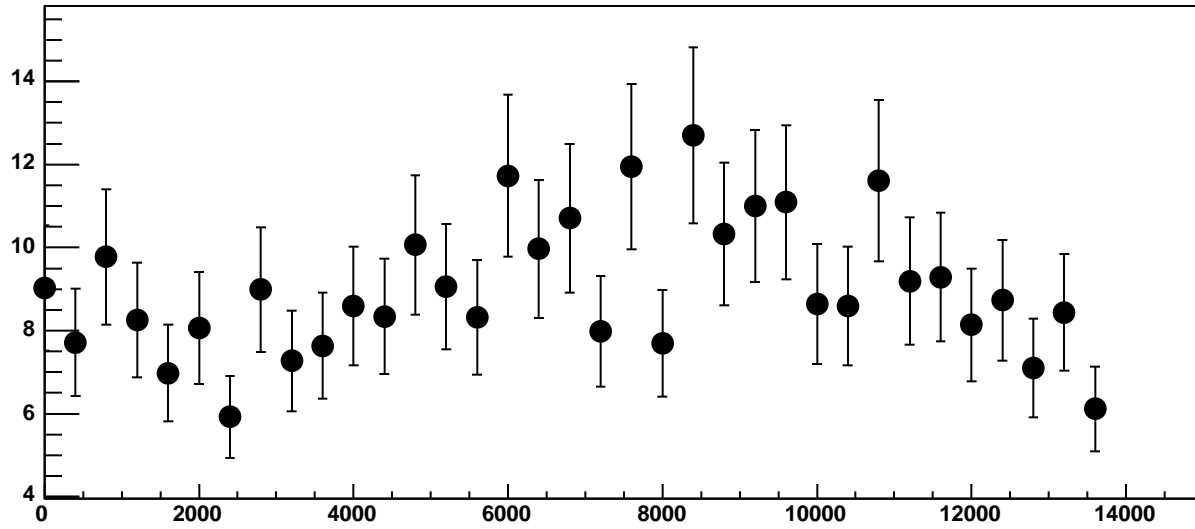


Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

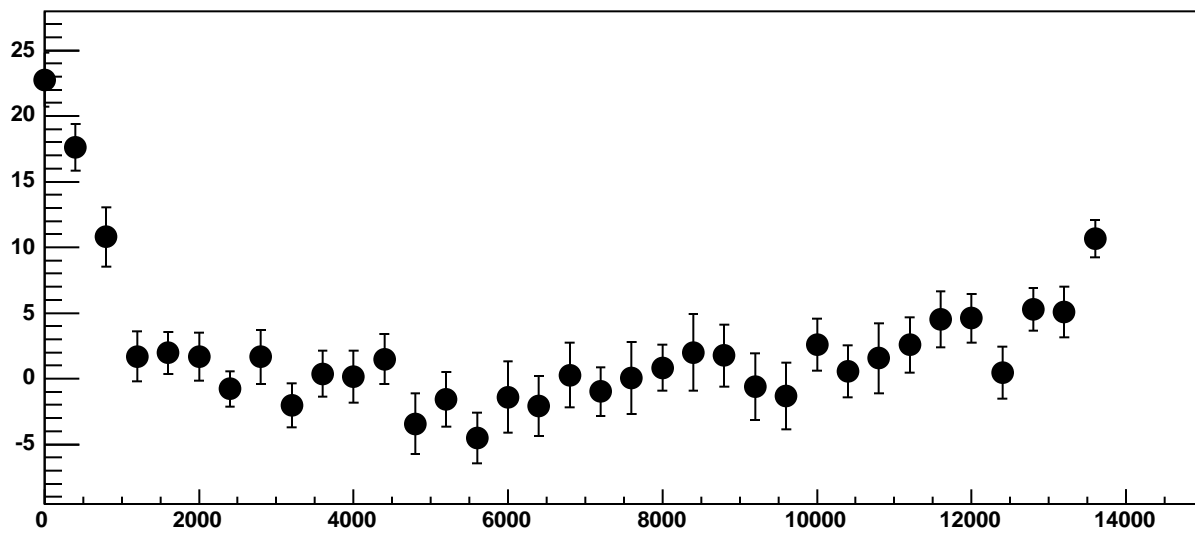


$\chi^2 / \text{ndf}$  19.72 / 23  
p0  $-72.08 \pm 0.8401$   
p1  $0.01957 \pm 0.0001382$

Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

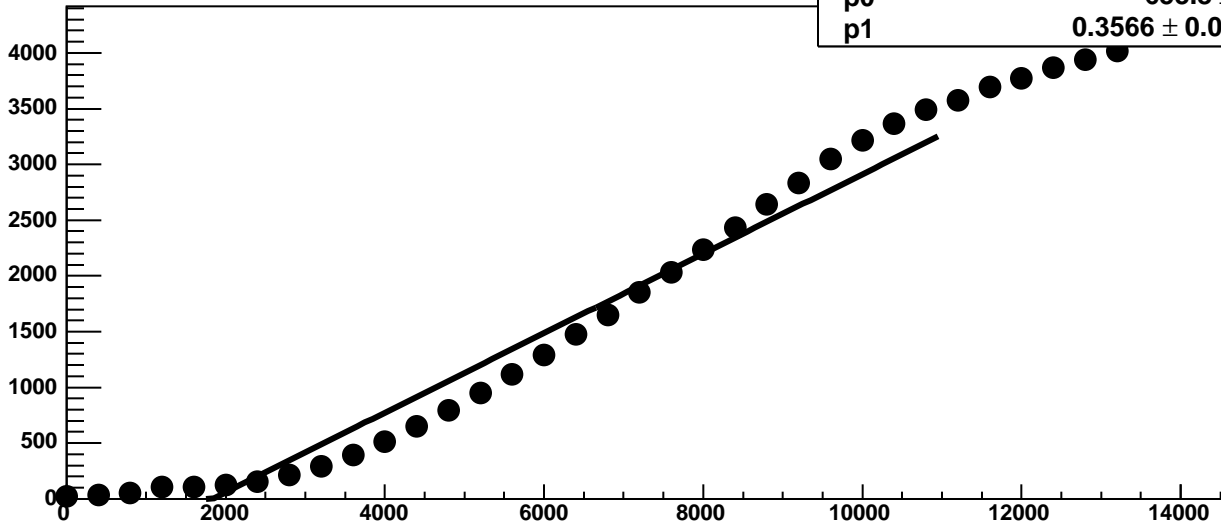


Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

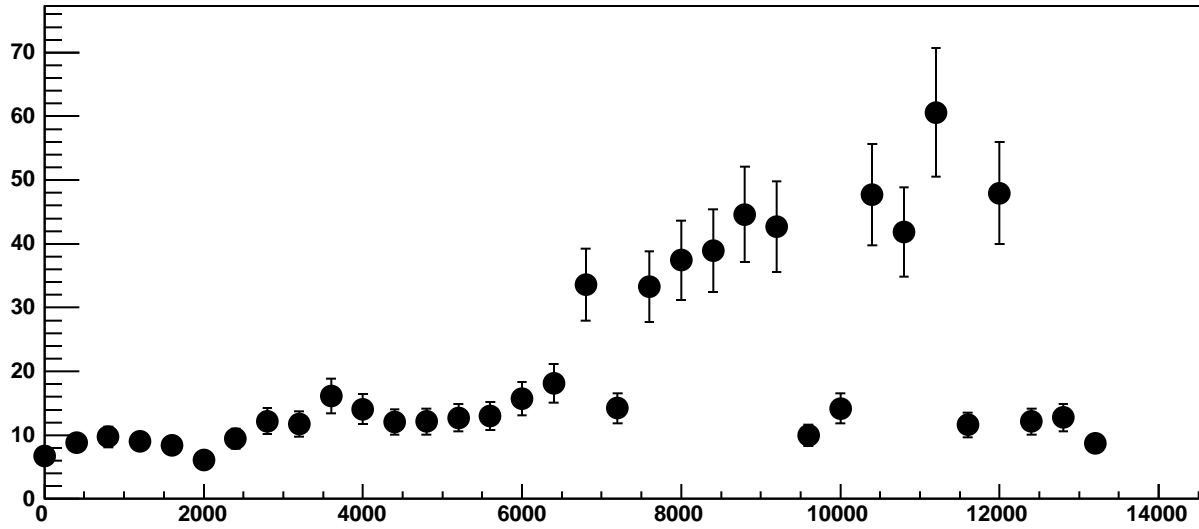


Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

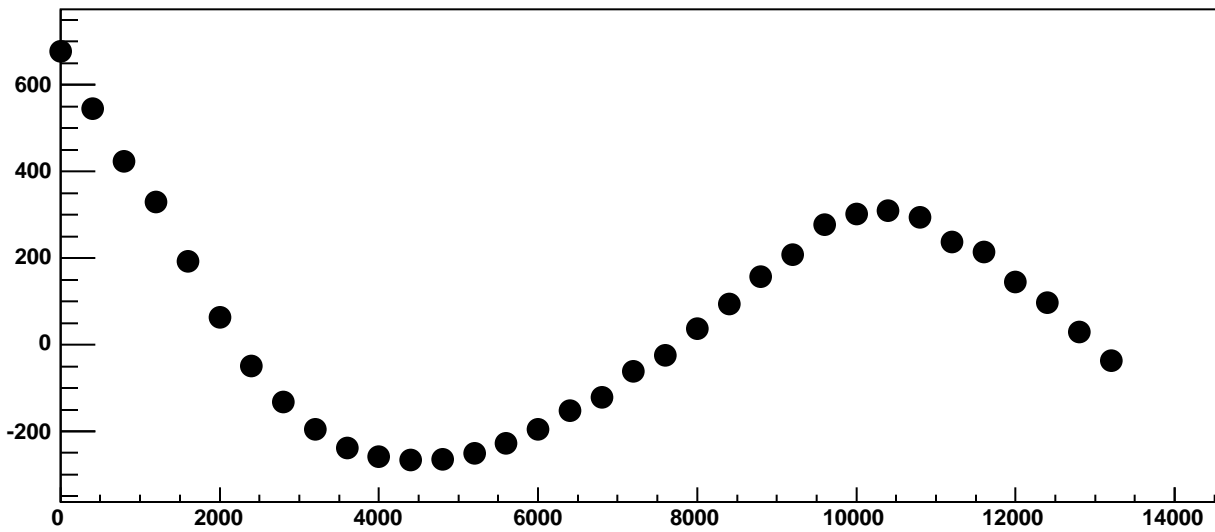
$\chi^2 / \text{ndf}$  1.178e+05 / 23  
p0  $-653.8 \pm 1.094$   
p1  $0.3566 \pm 0.0002241$



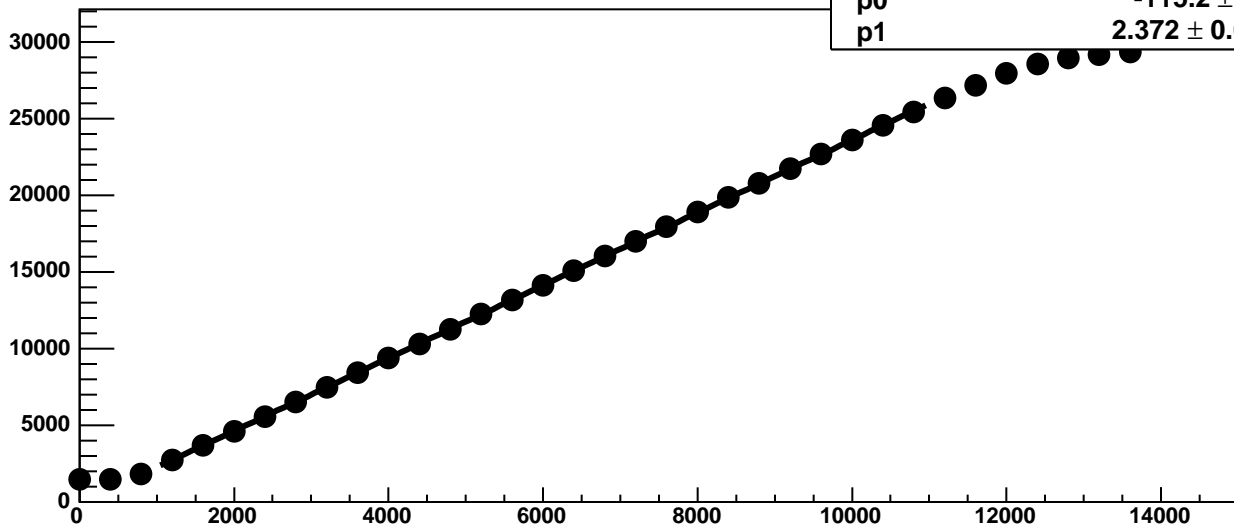
Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



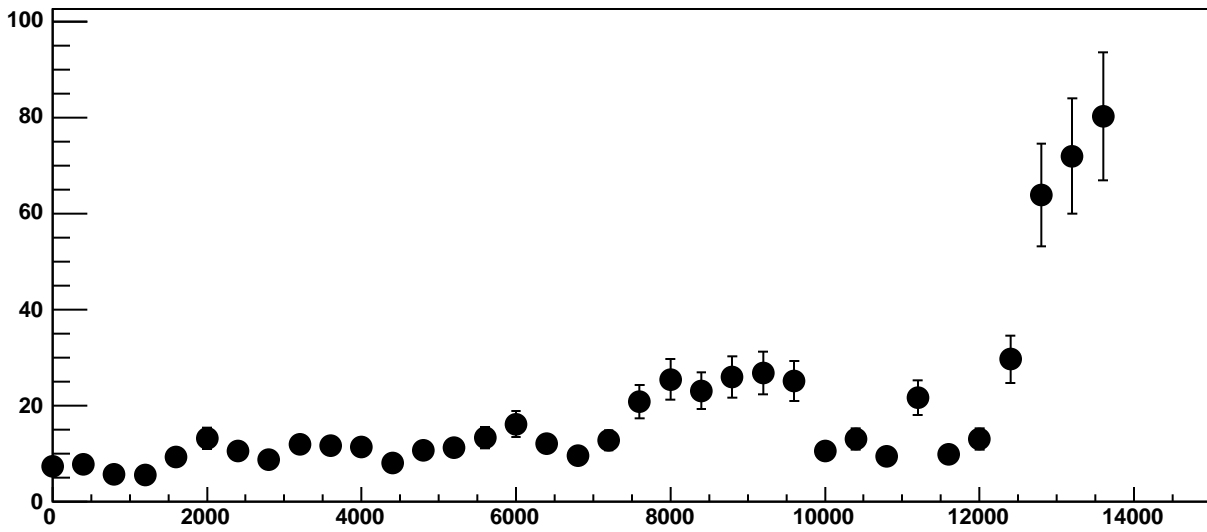
Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



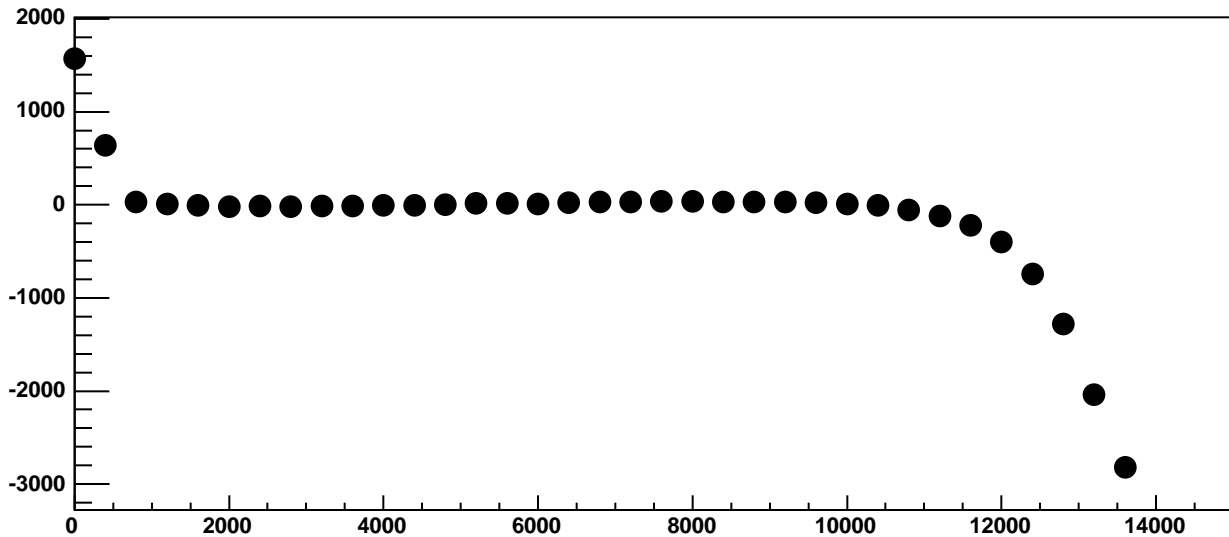
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



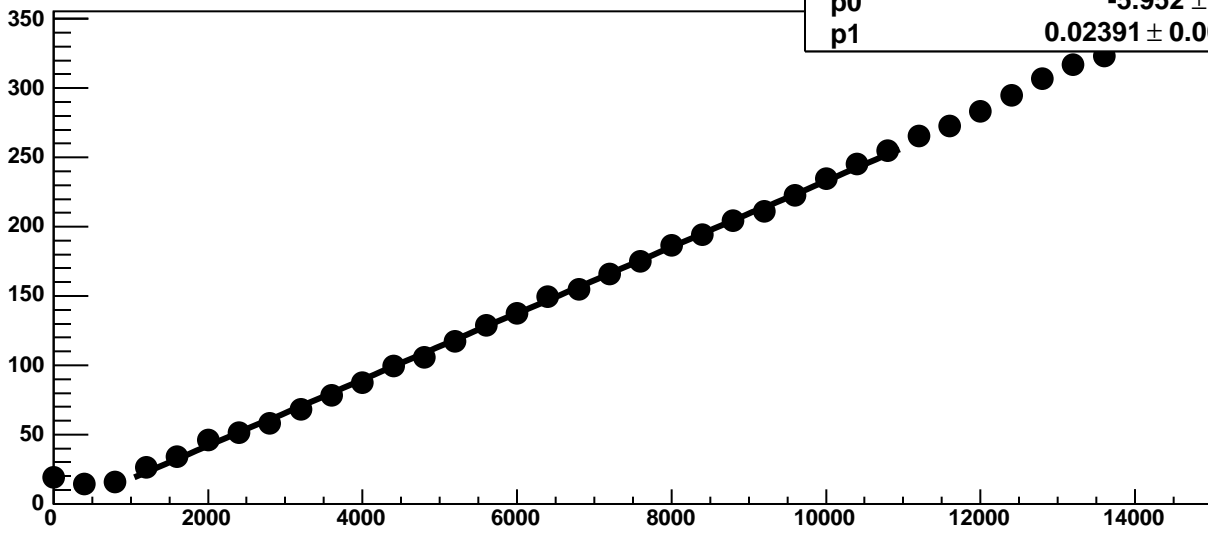
Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

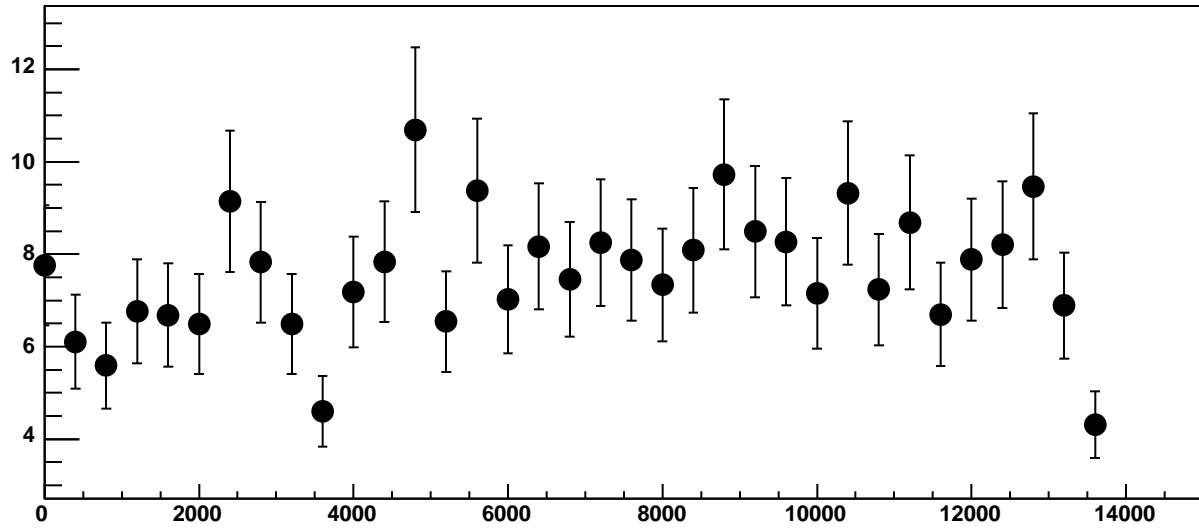


Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

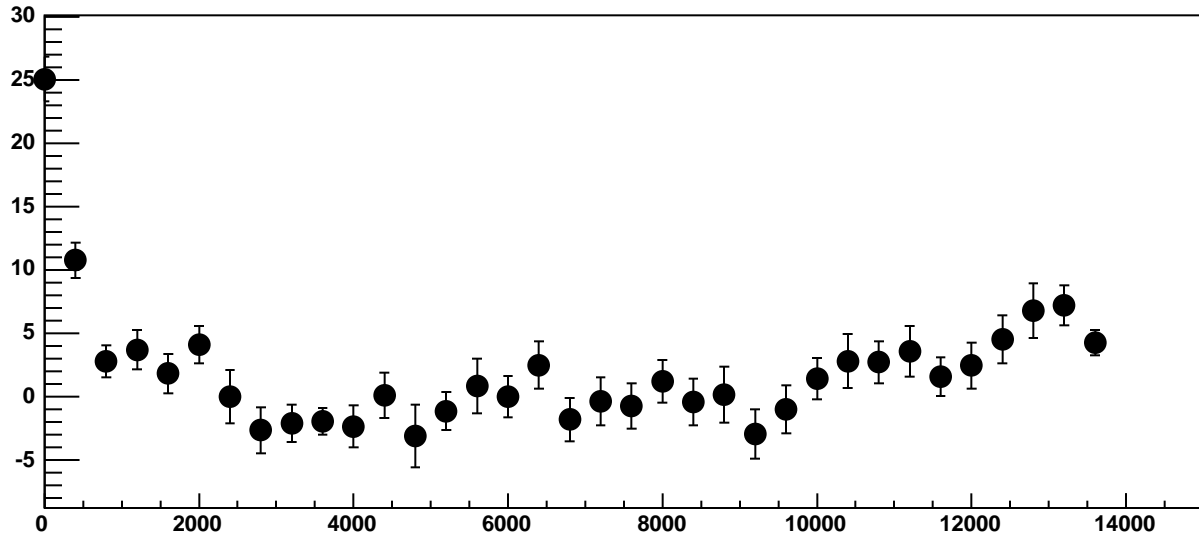


$\chi^2 / \text{ndf}$  38 / 23  
p0  $-5.952 \pm 0.7407$   
p1  $0.02391 \pm 0.0001177$

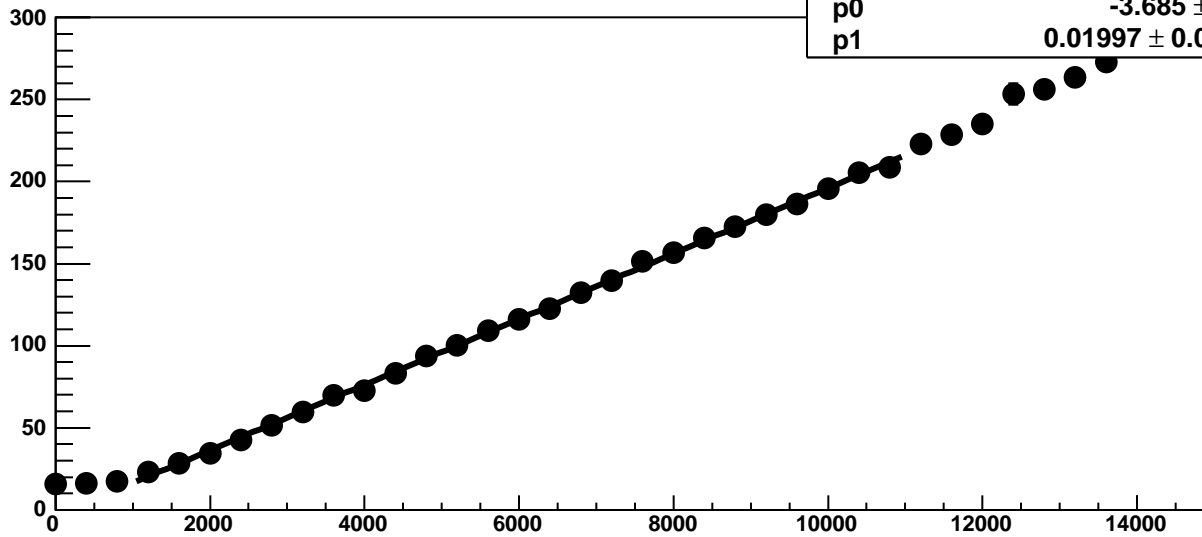
Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



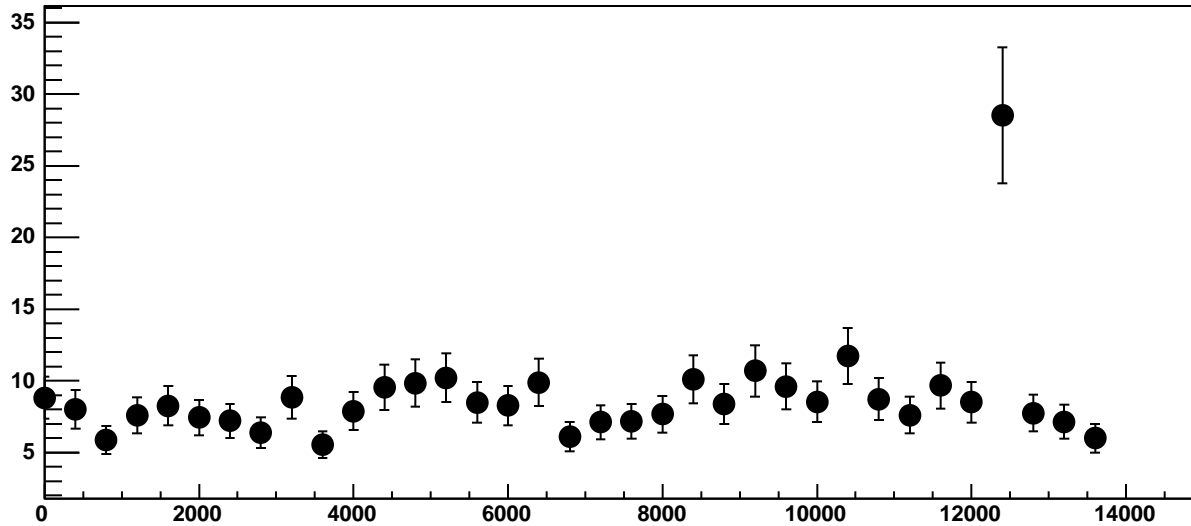
Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



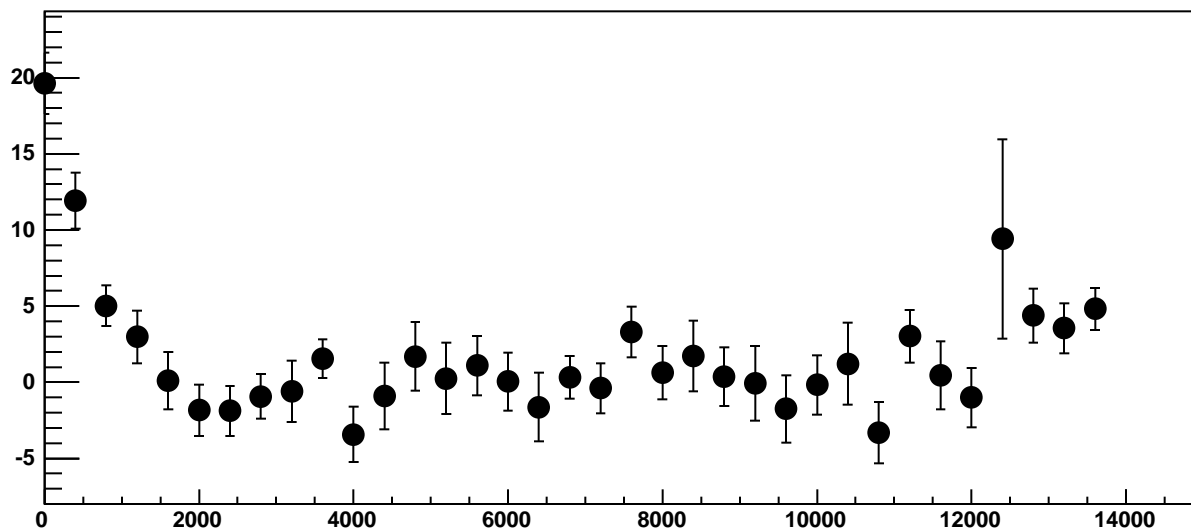
Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



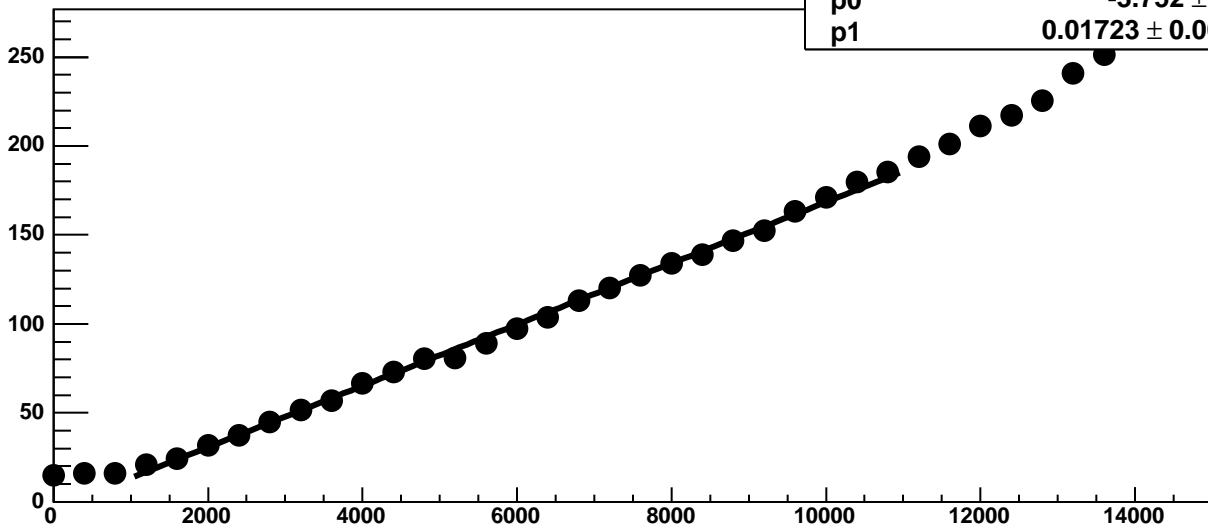
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

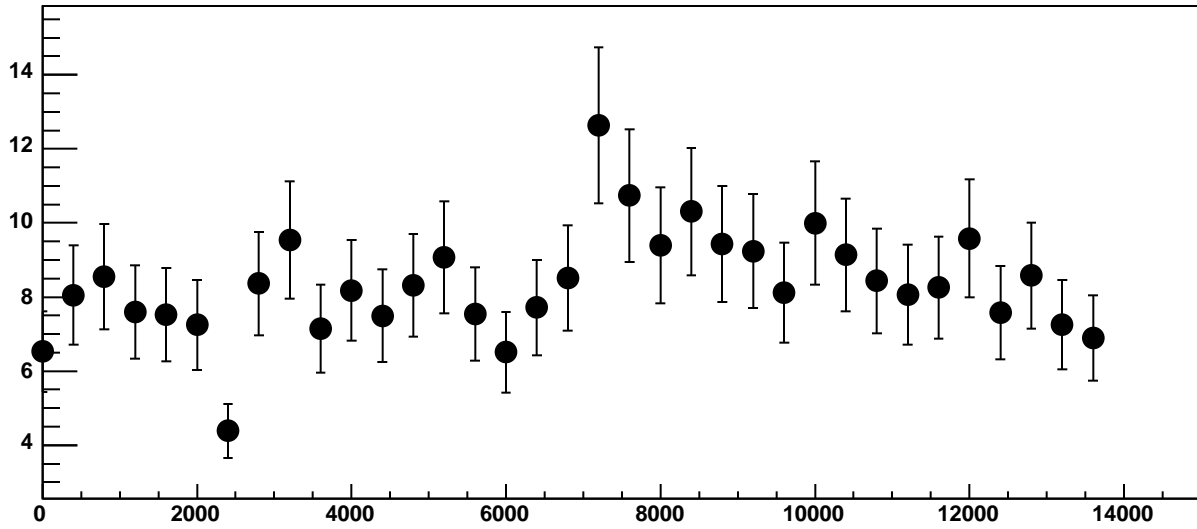


Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

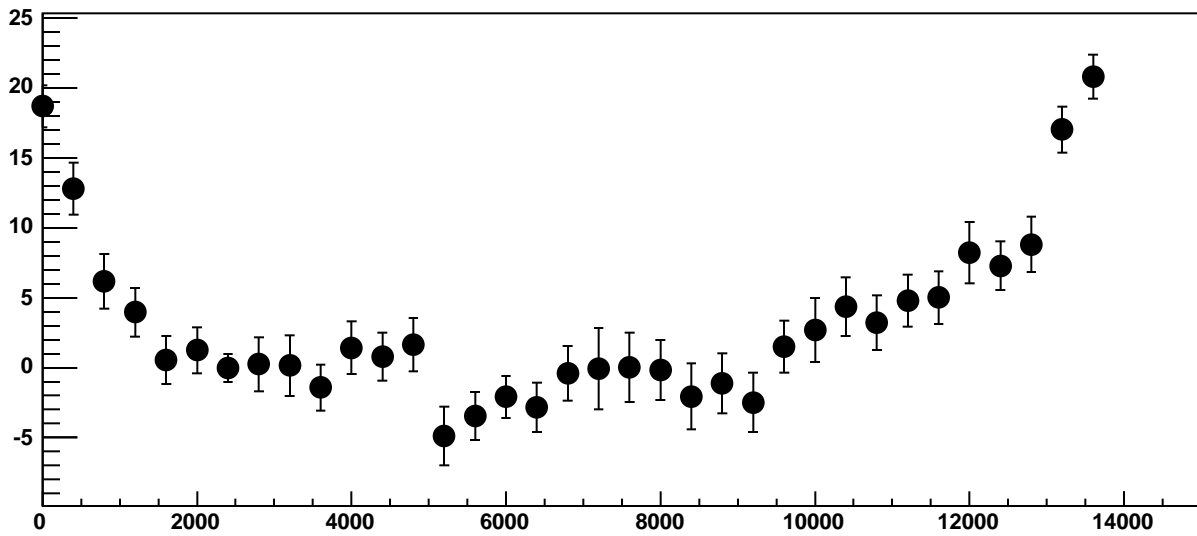


$\chi^2 / \text{ndf}$  33.78 / 23  
p0  $-3.752 \pm 0.7659$   
p1  $0.01723 \pm 0.0001268$

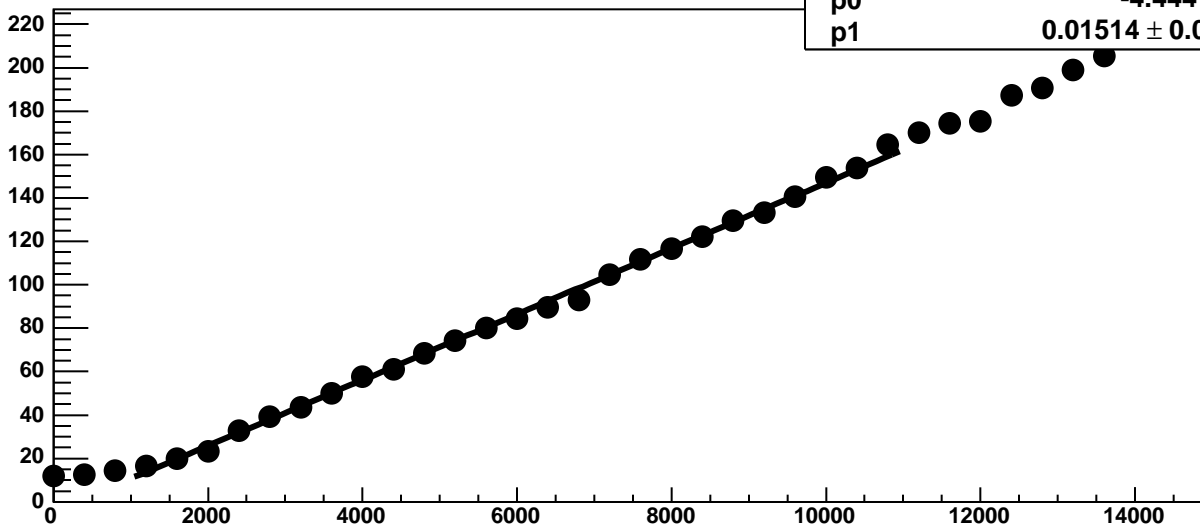
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

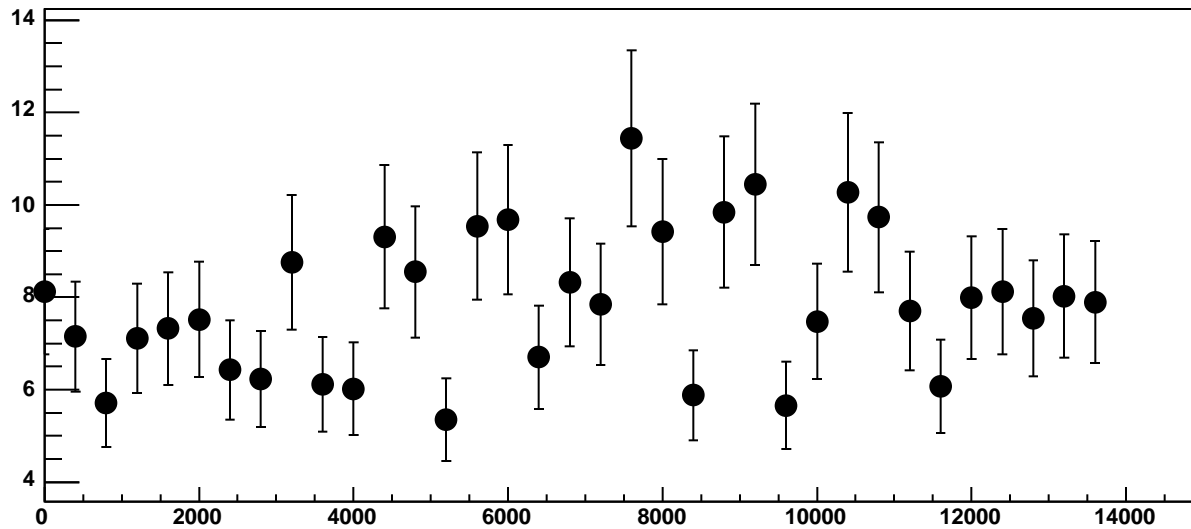


Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

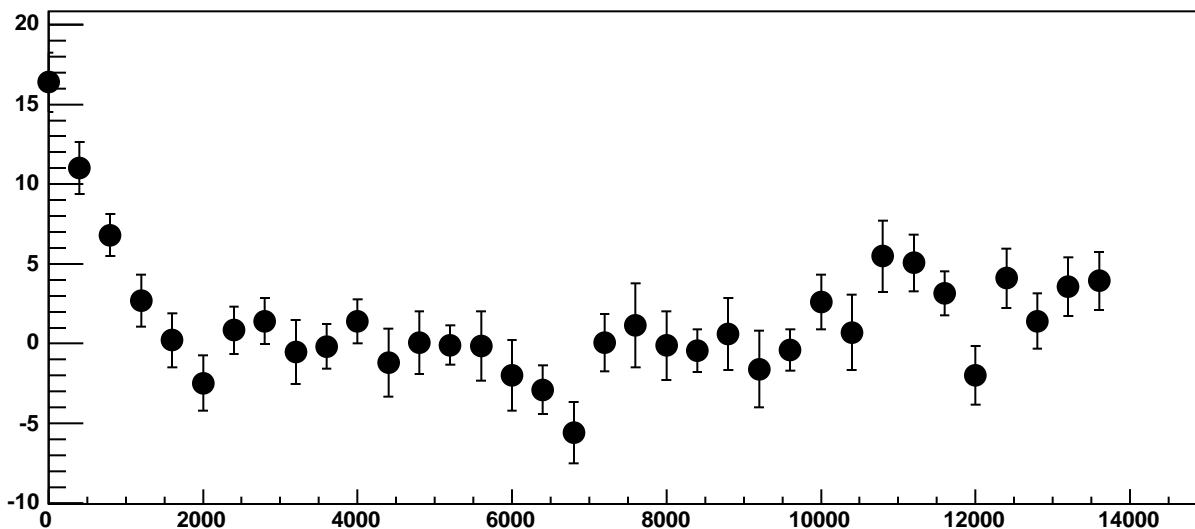


$\chi^2 / \text{ndf}$  29.72 / 23  
p0 -4.444 ± 0.762  
p1 0.01514 ± 0.0001206

Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

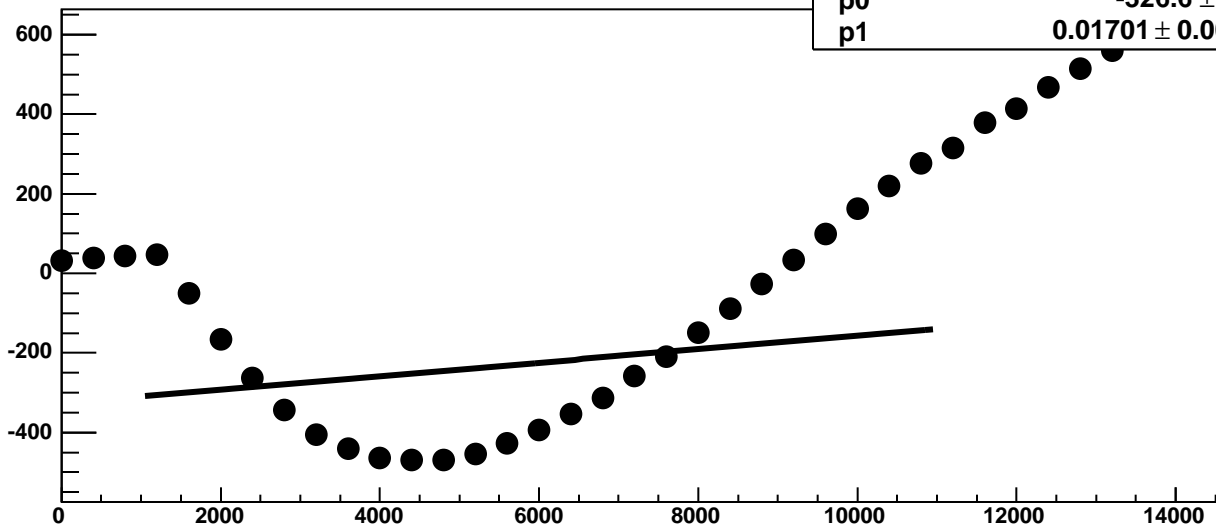


Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



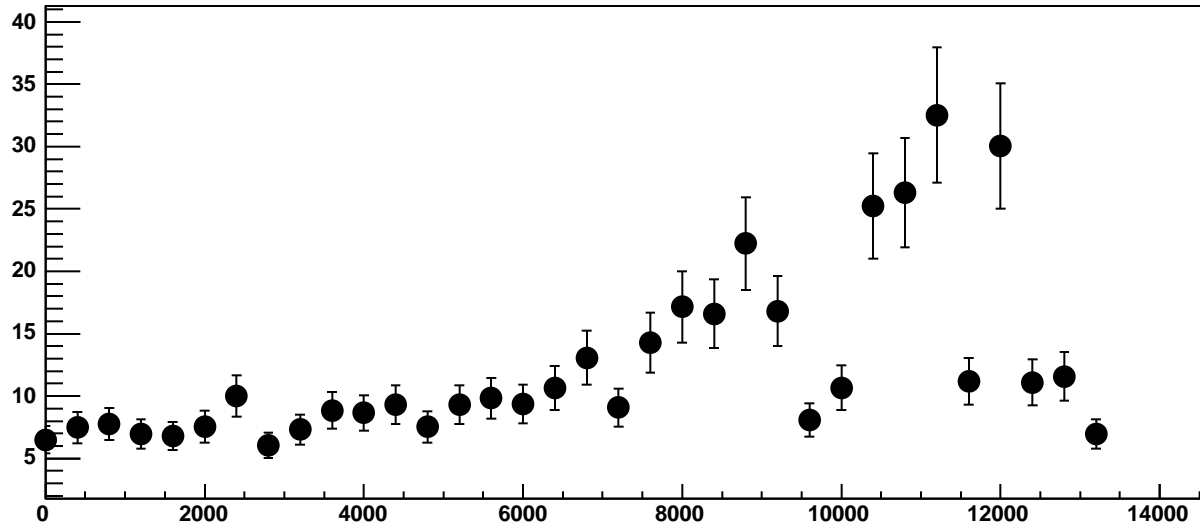


Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

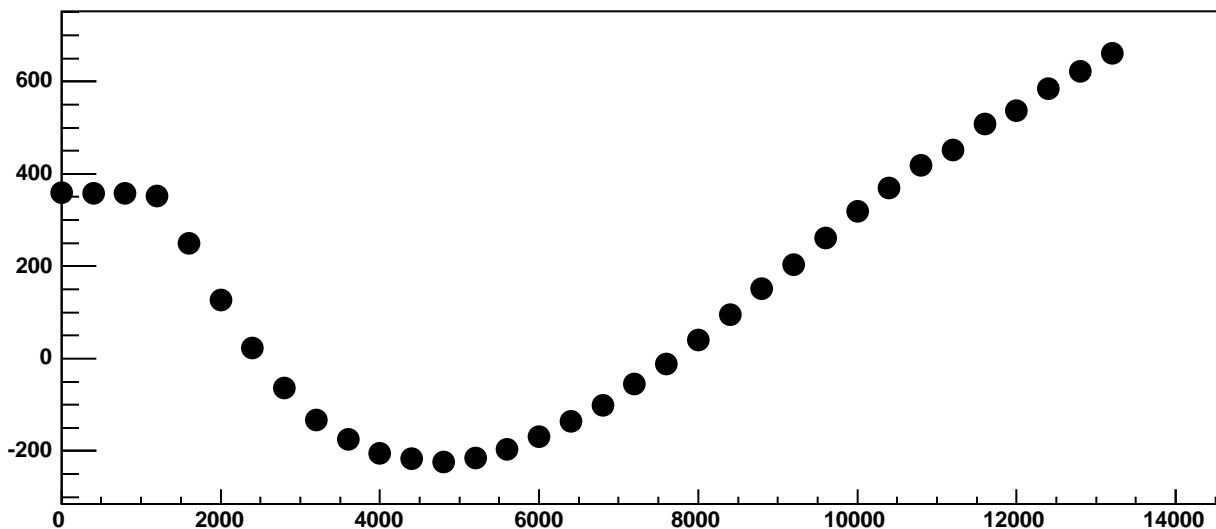


$\chi^2 / \text{ndf}$  2.128e+05 / 23  
p0 -326.6 ± 0.8803  
p1 0.01701 ± 0.0001659

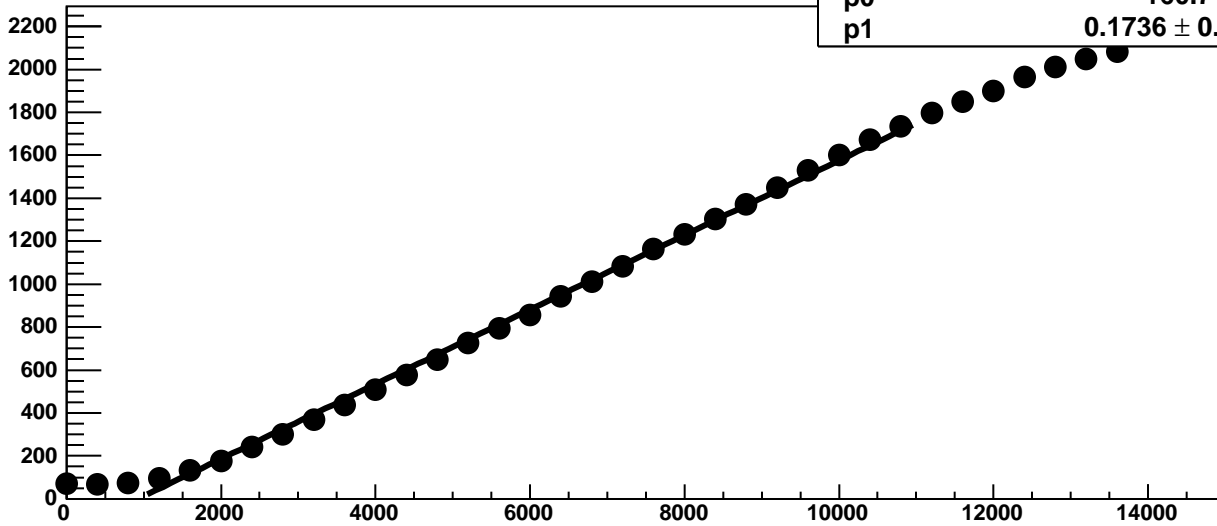
Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1431 / 23

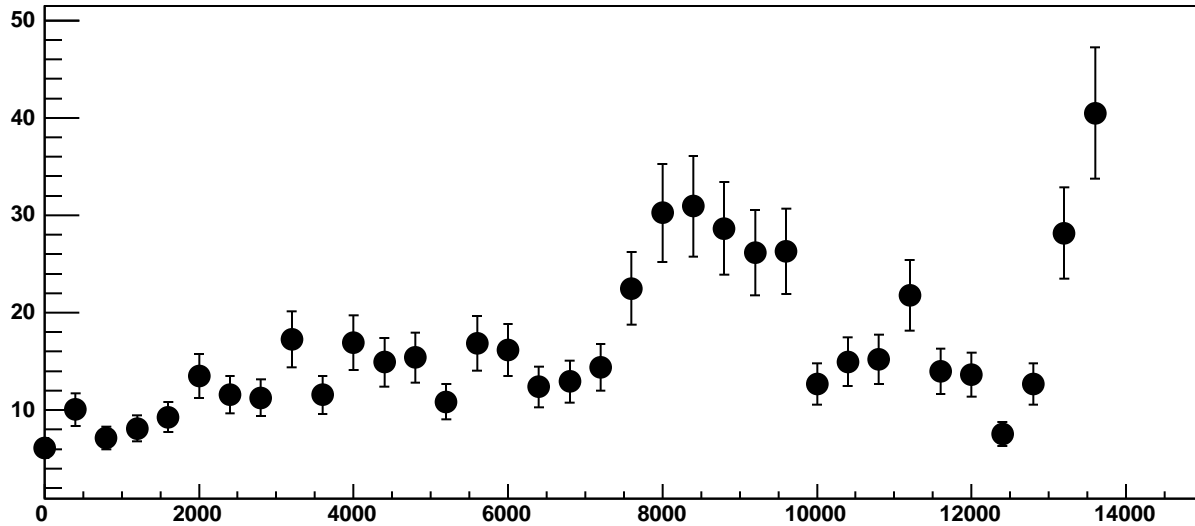
p0

$-160.7 \pm 1.223$

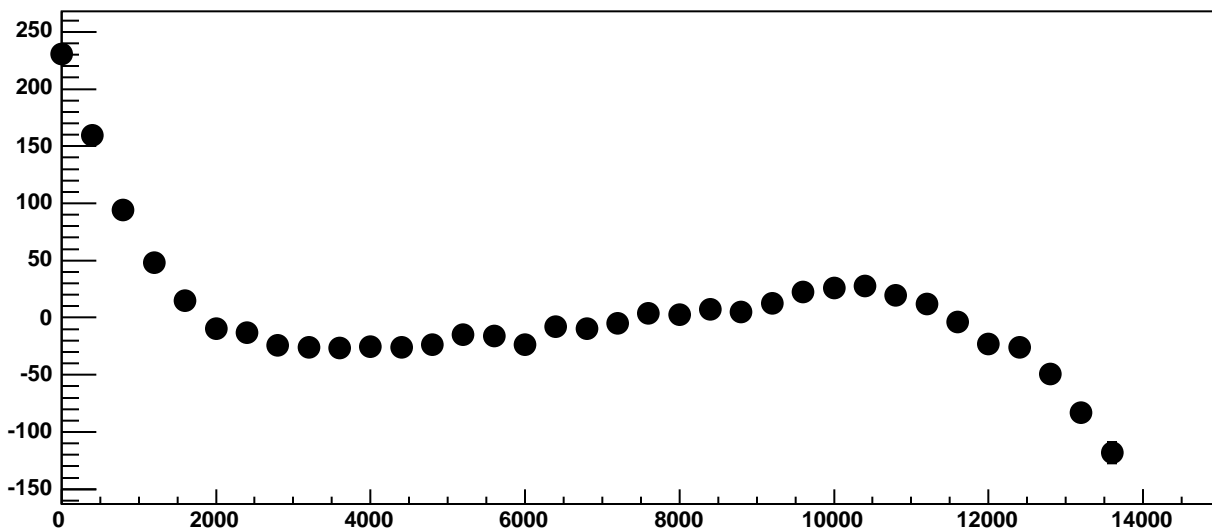
p1

$0.1736 \pm 0.000217$

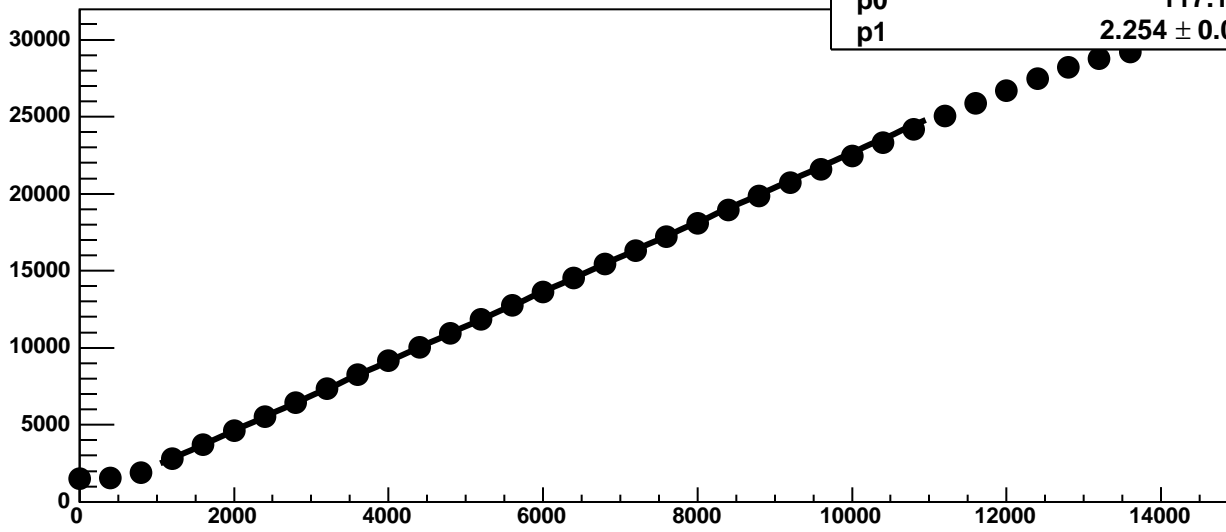
Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

259.6 / 23

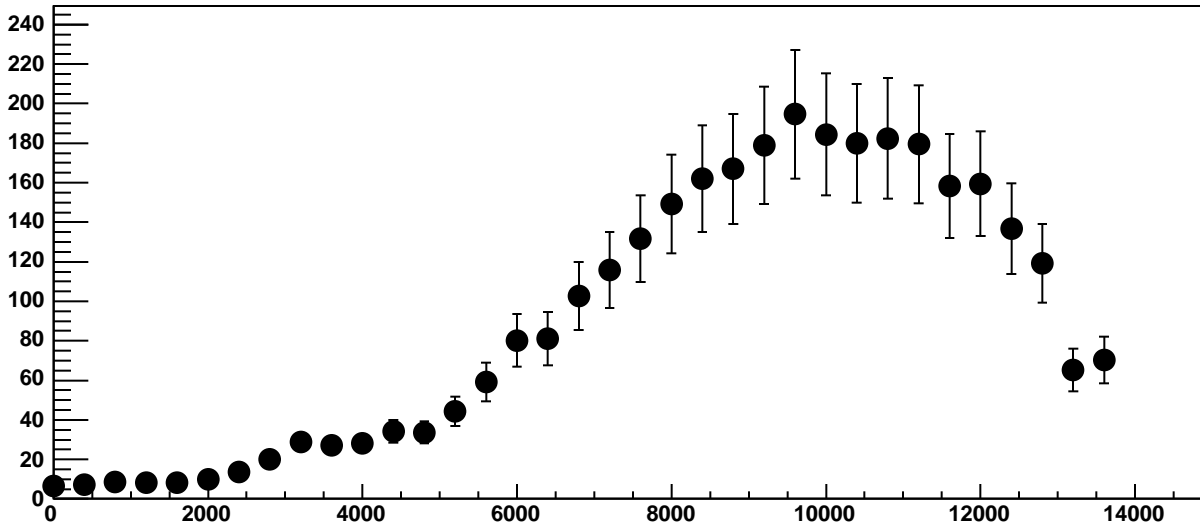
p0

$117.1 \pm 2.038$

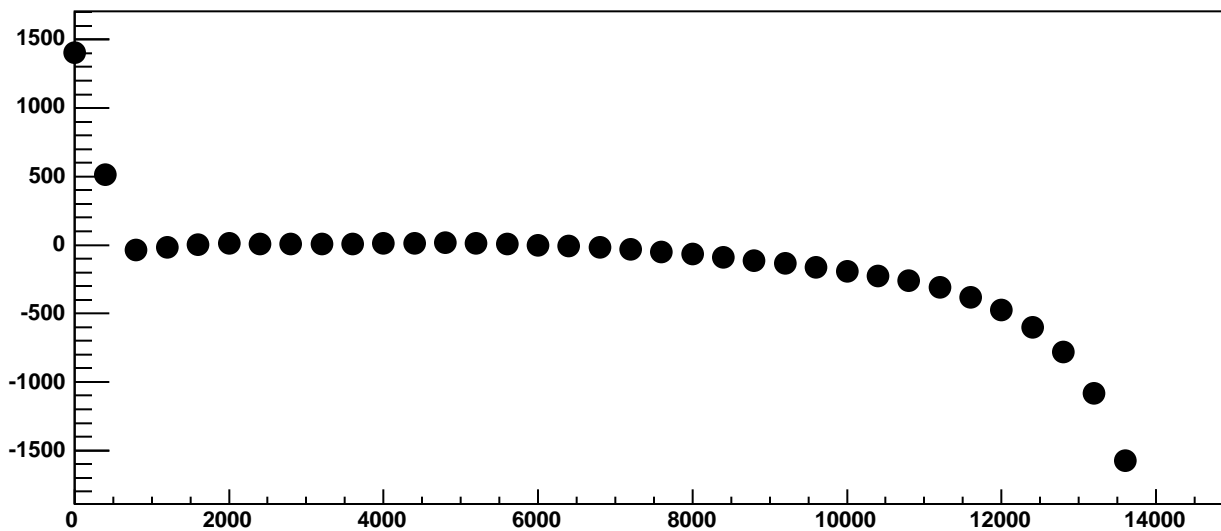
p1

$2.254 \pm 0.0008621$

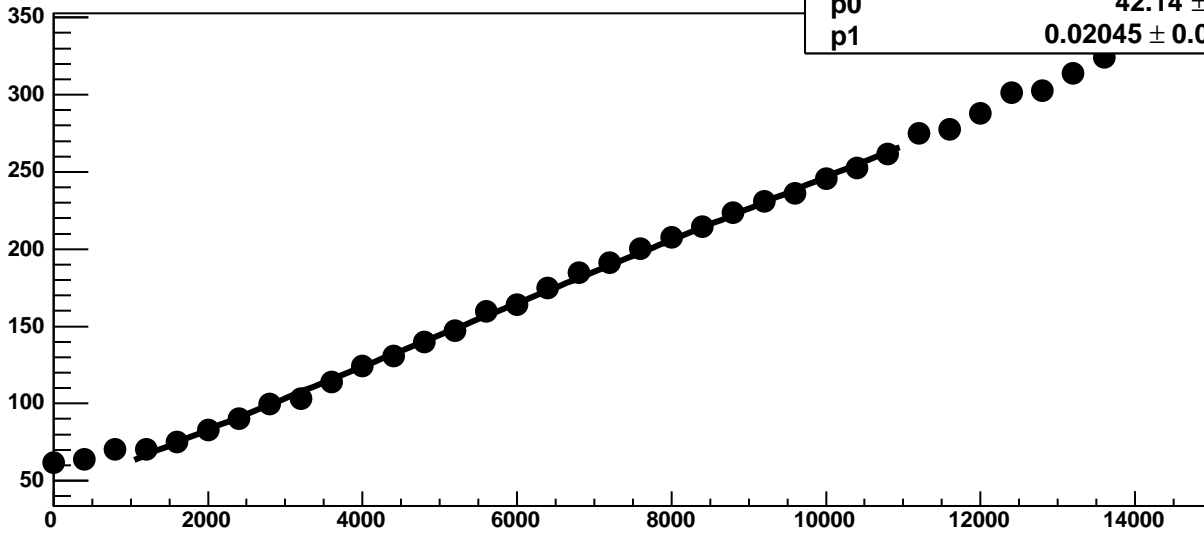
Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

29.07 / 23

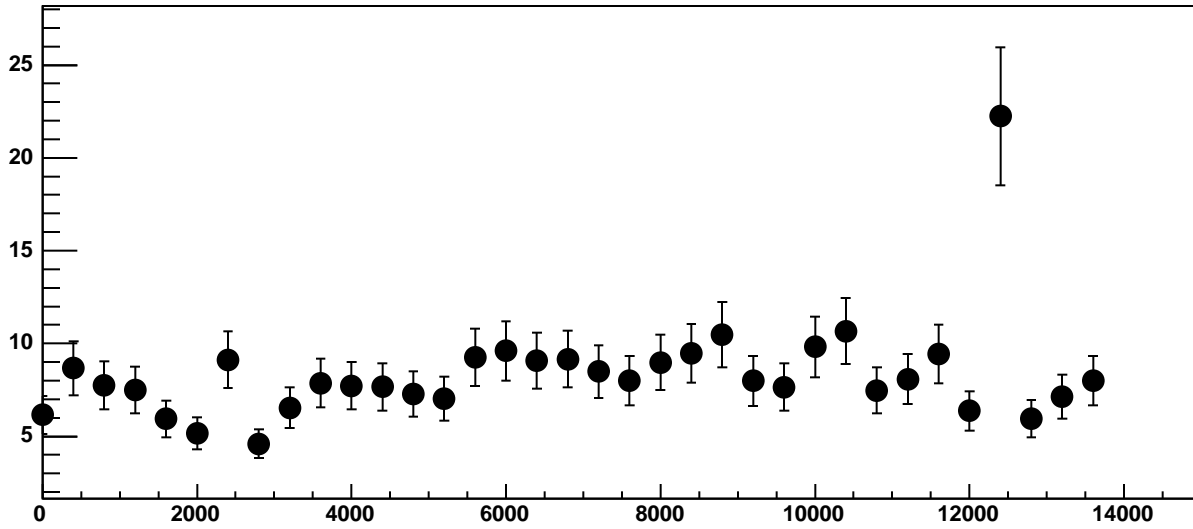
p0

$42.14 \pm 0.7085$

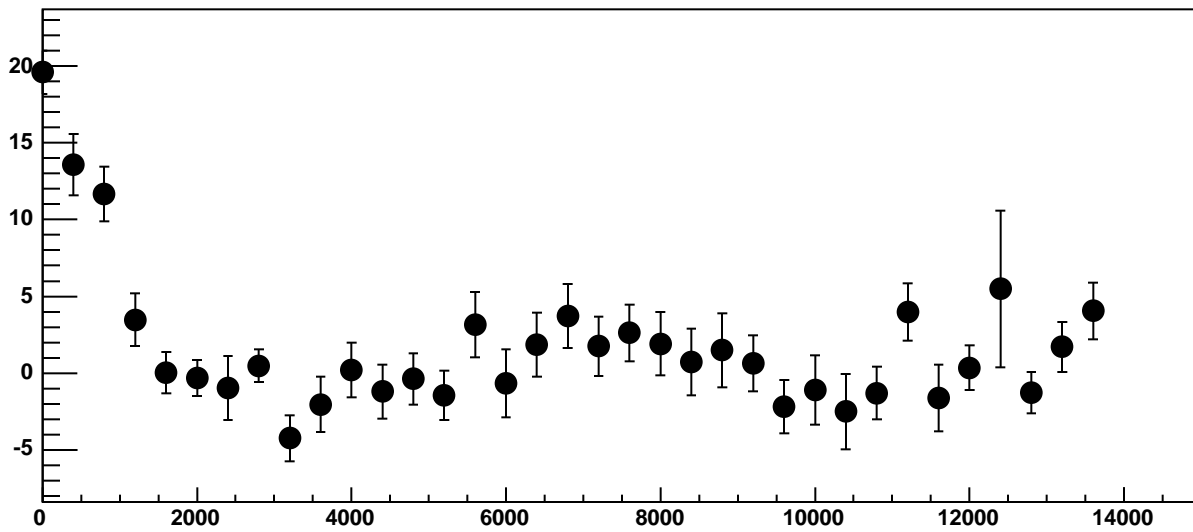
p1

$0.02045 \pm 0.0001189$

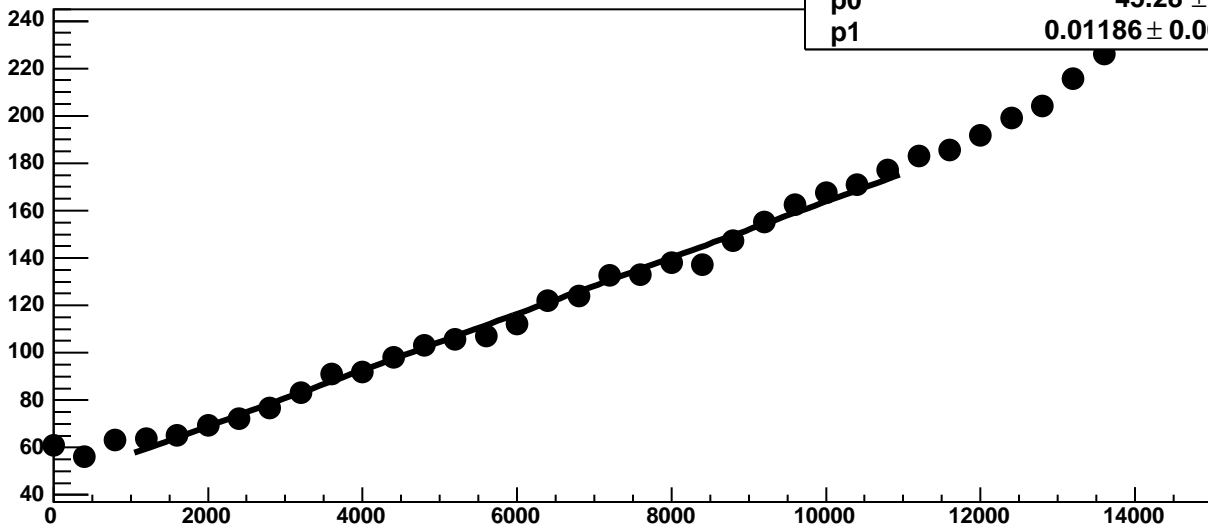
Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

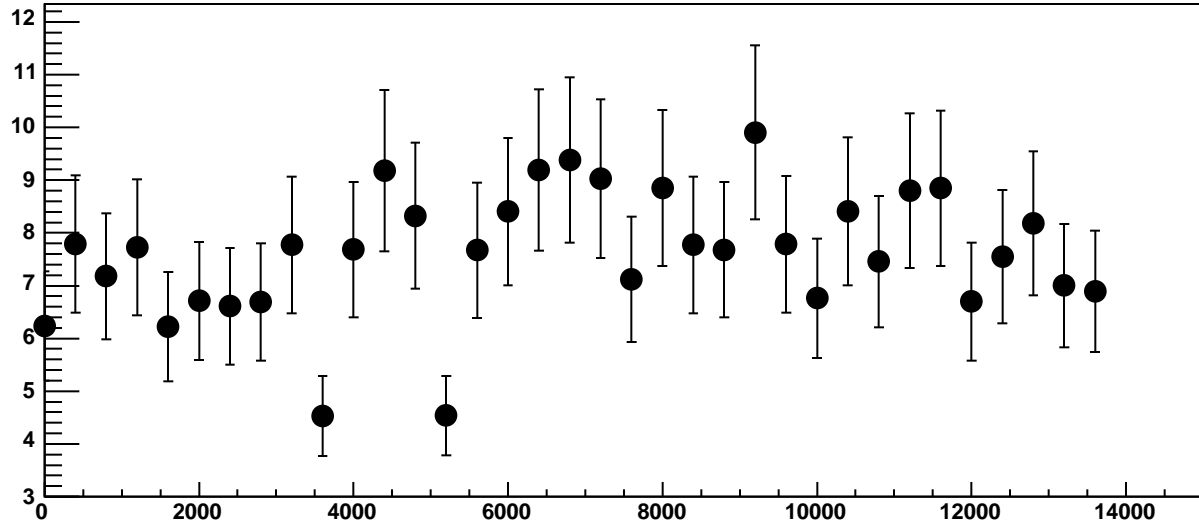


Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

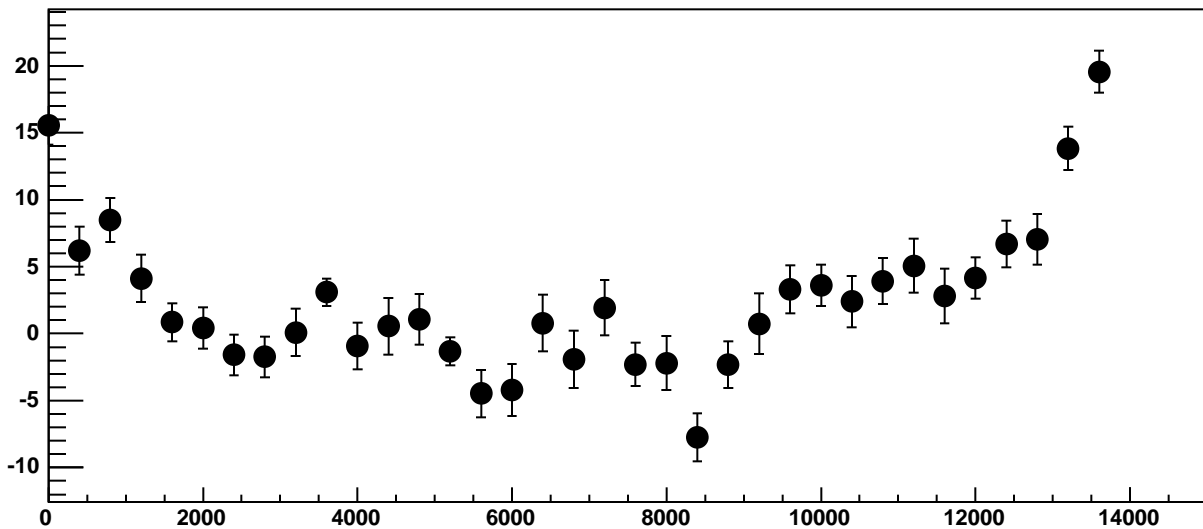


$\chi^2 / \text{ndf}$  71.69 / 23  
p0  $45.28 \pm 0.7216$   
p1  $0.01186 \pm 0.0001155$

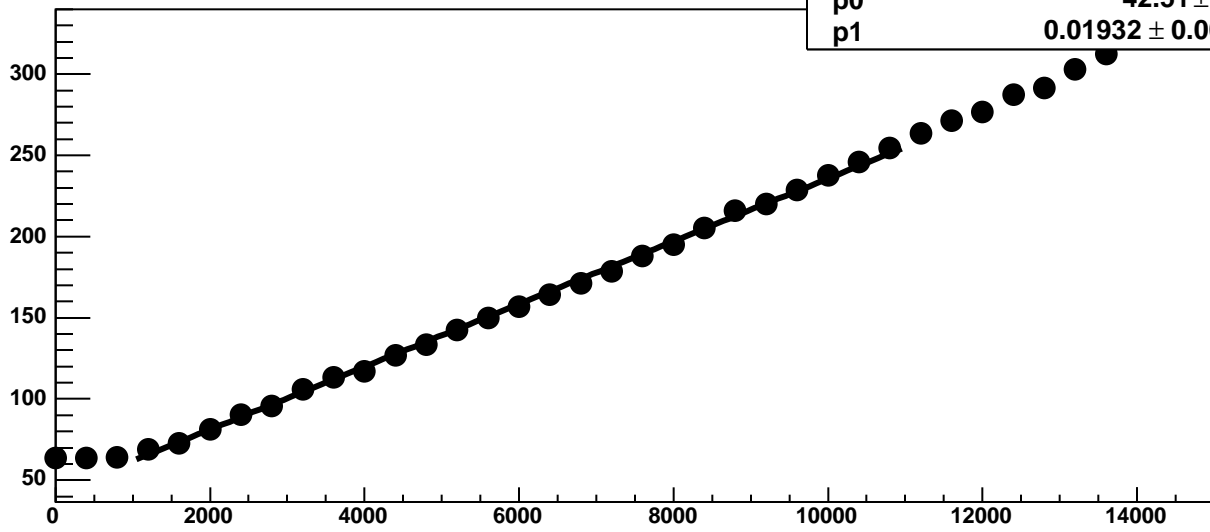
Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

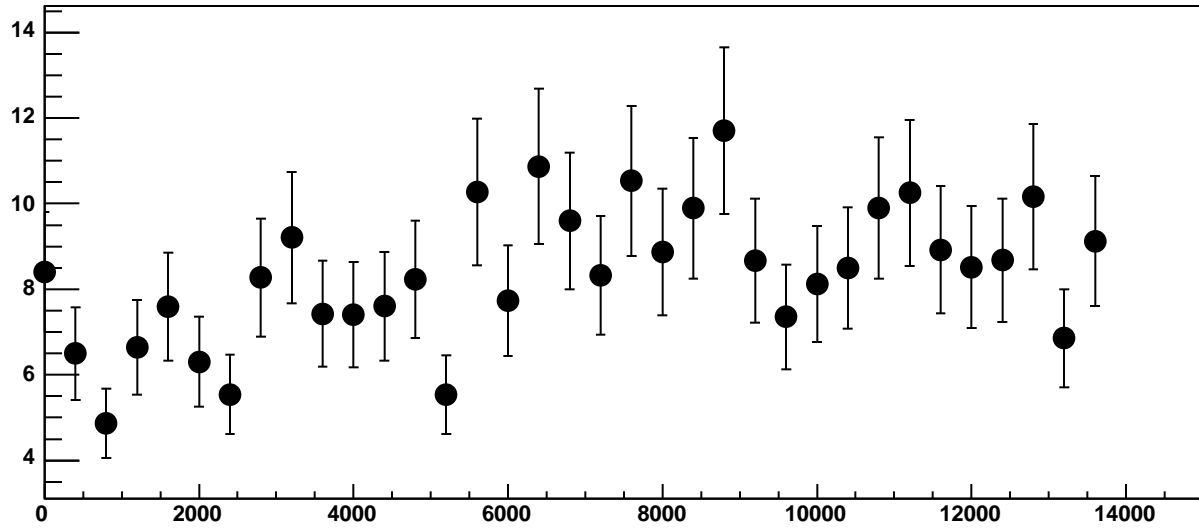


Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

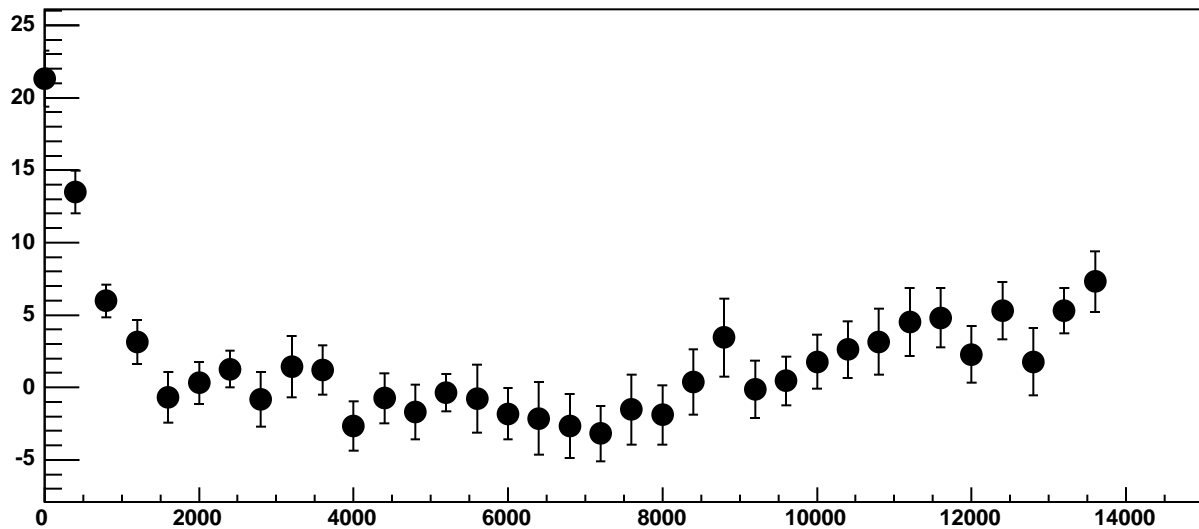


$\chi^2 / \text{ndf}$  23.86 / 23  
p0 42.51 ± 0.7643  
p1 0.01932 ± 0.0001248

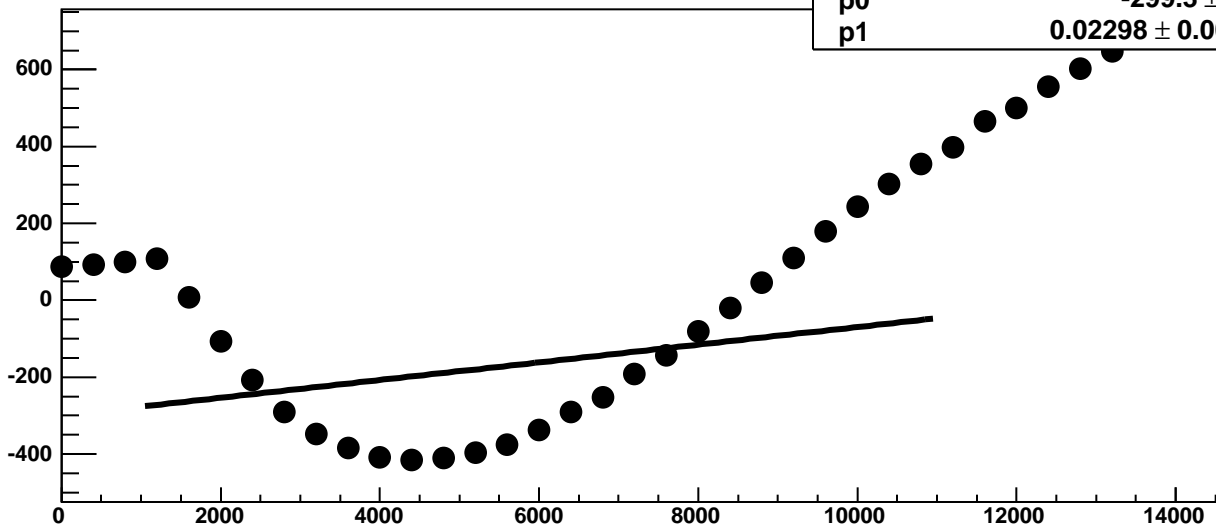
Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

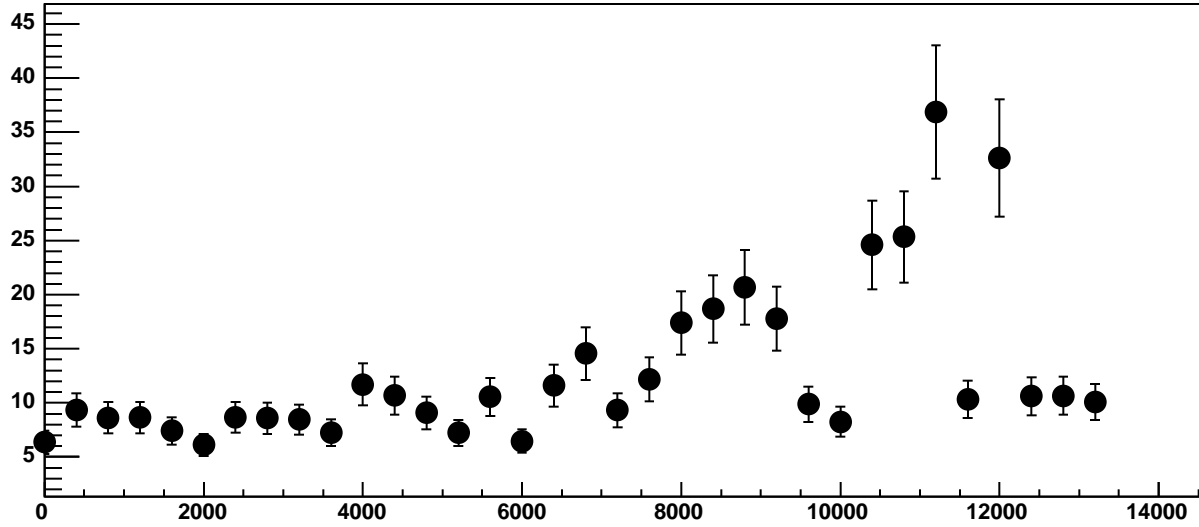


Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

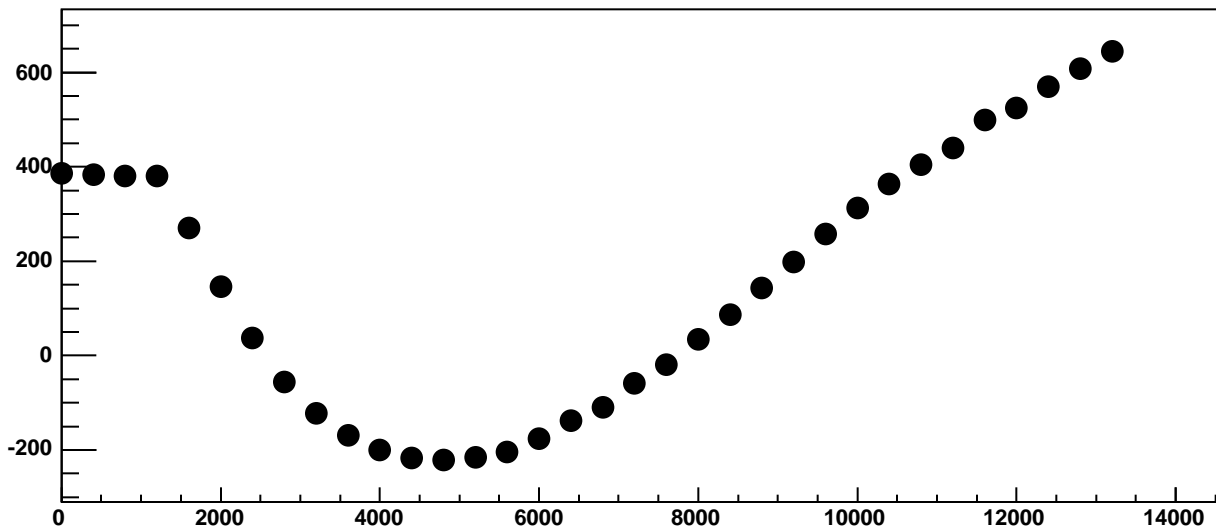


$\chi^2 / \text{ndf}$  2.089e+05 / 23  
p0 -299.3 ± 0.9191  
p1 0.02298 ± 0.0001673

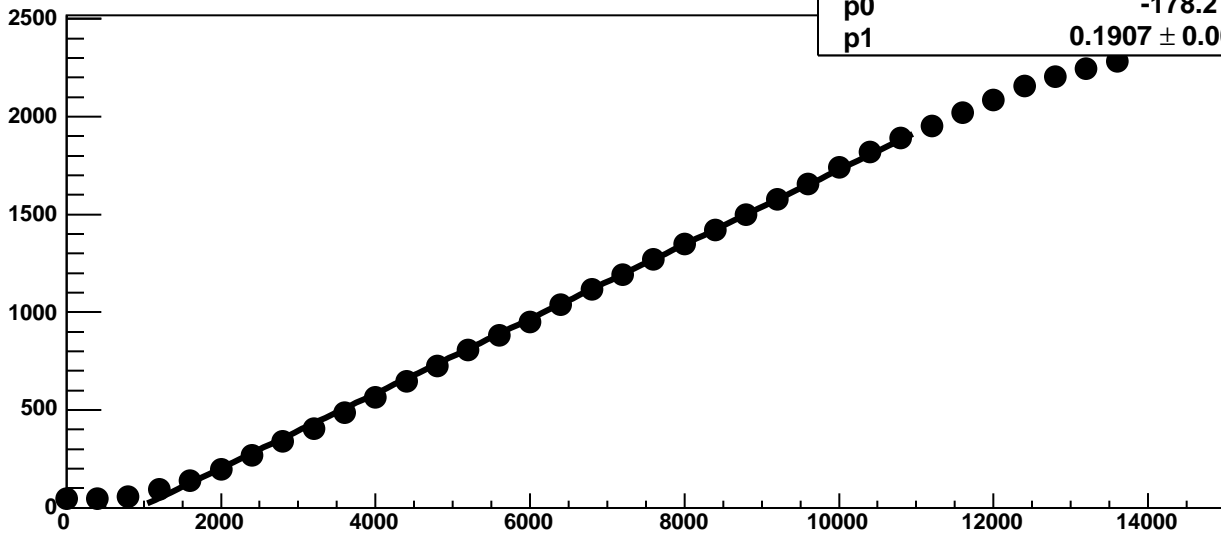
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



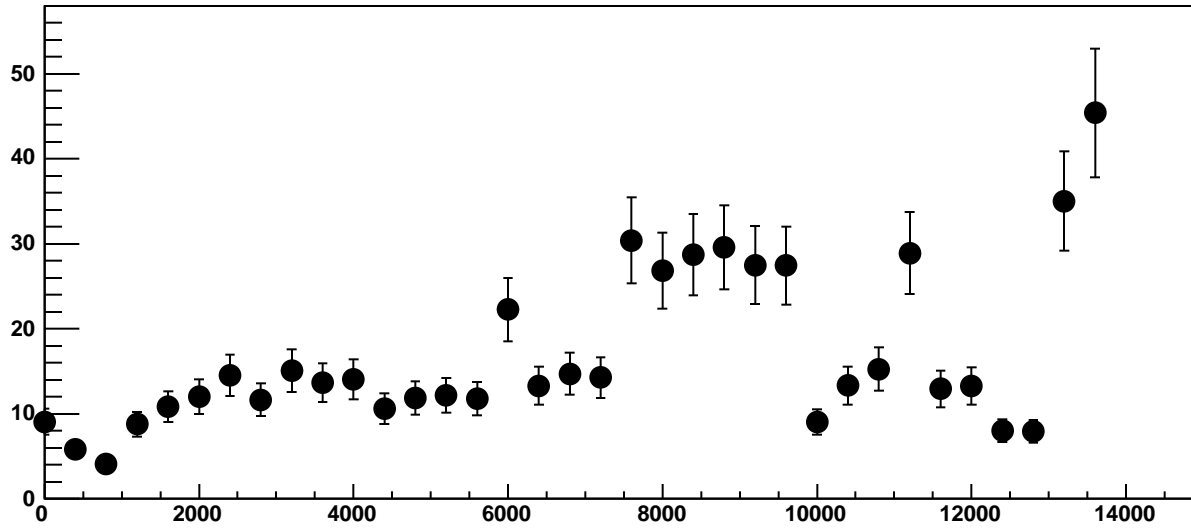
Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



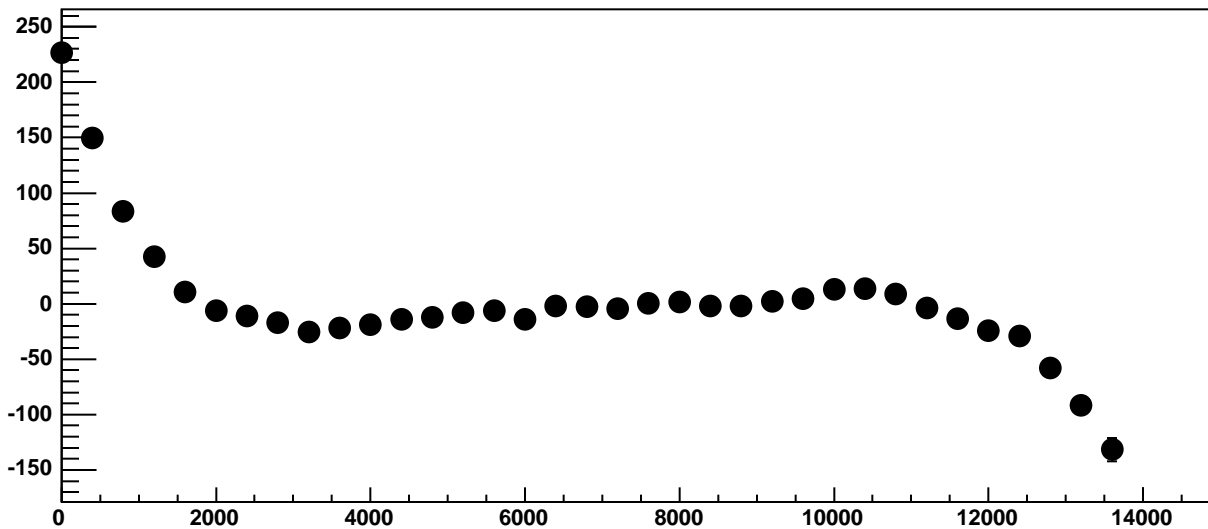
Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

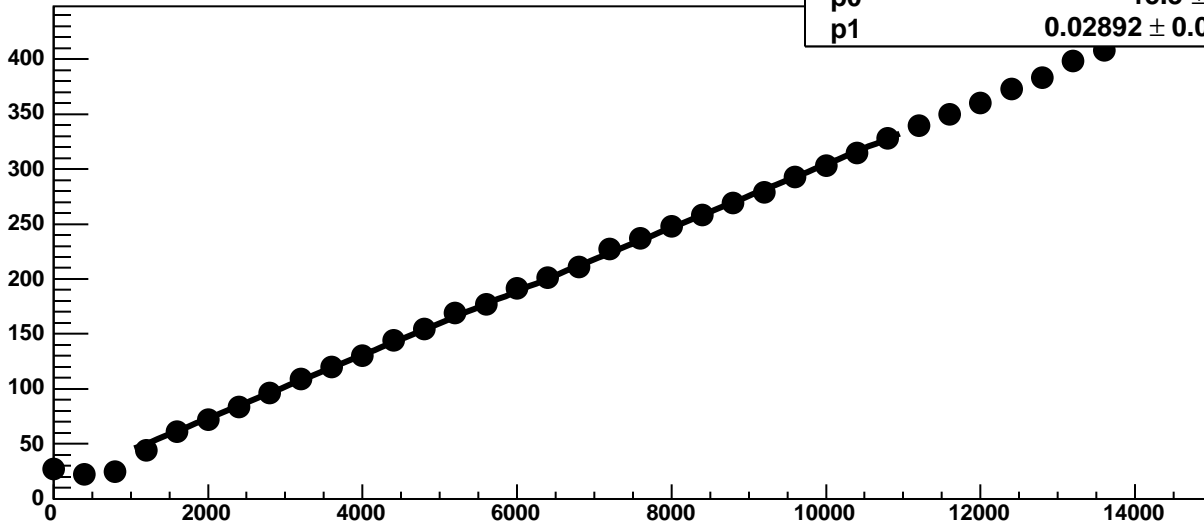


Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



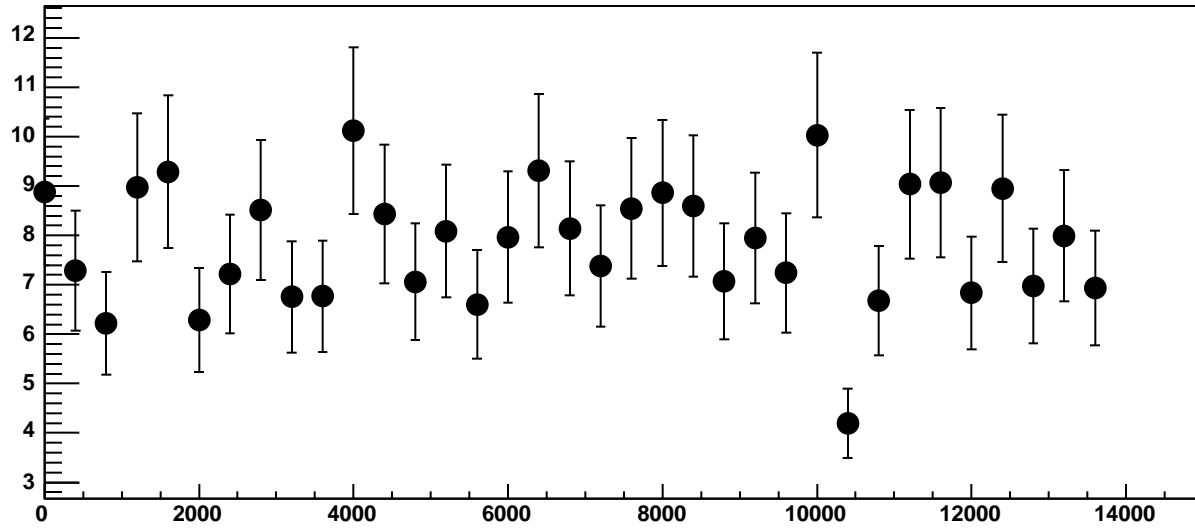


Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

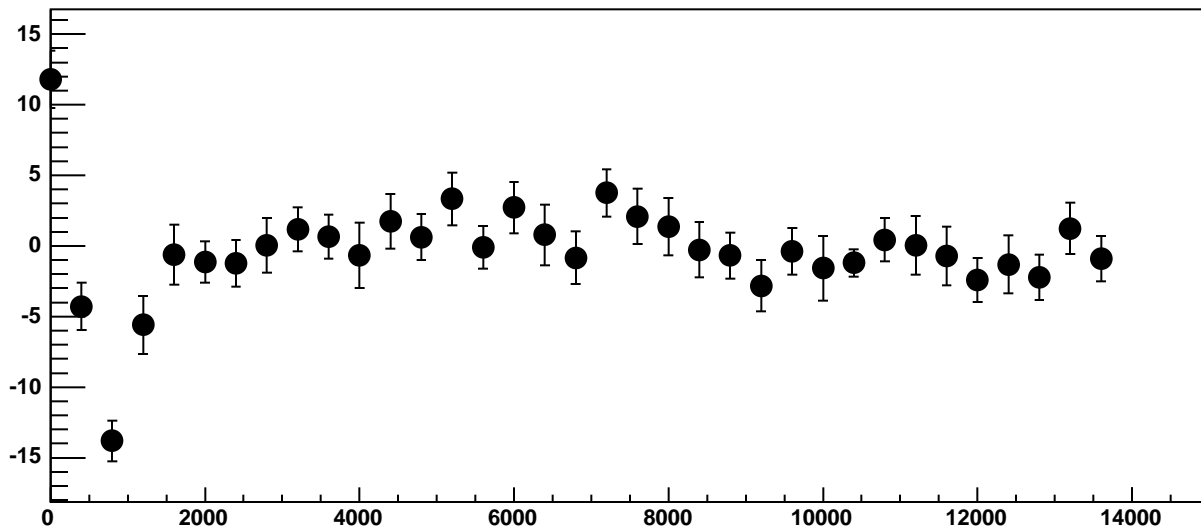


$\chi^2 / \text{ndf}$  27.37 / 23  
p0 15.3 ± 0.7896  
p1 0.02892 ± 0.0001119

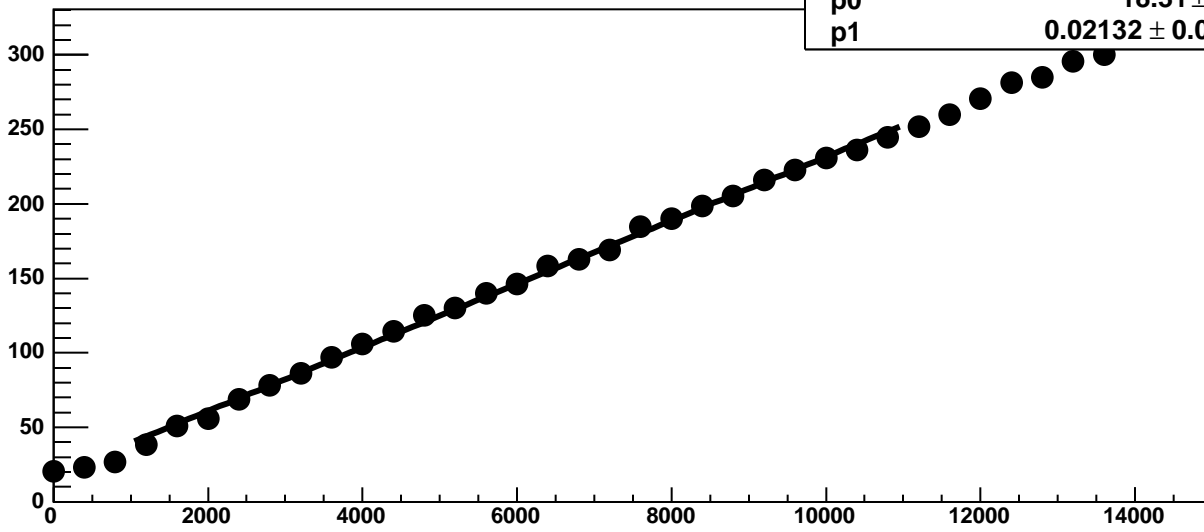
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

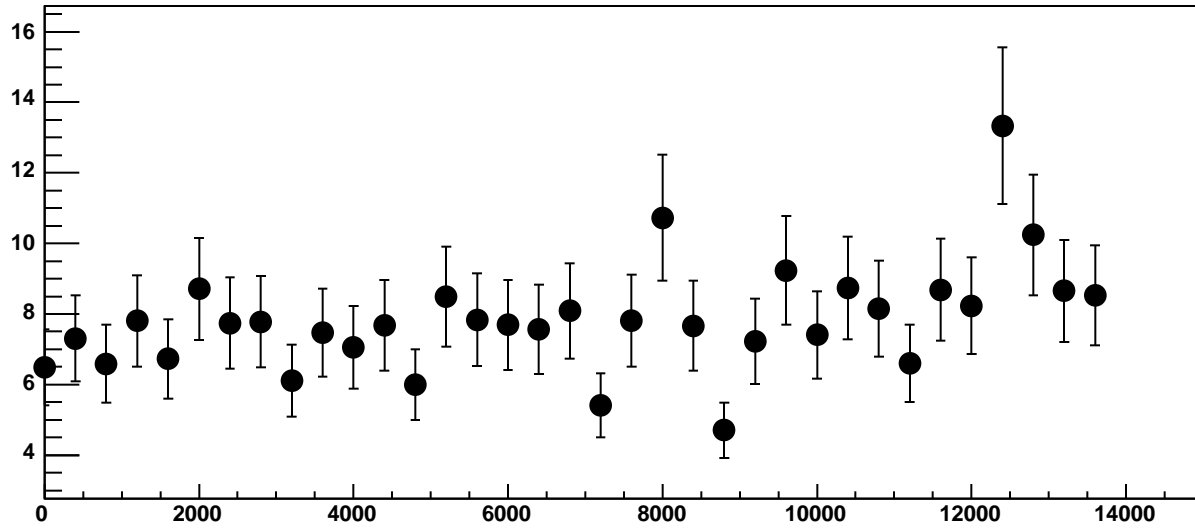


Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

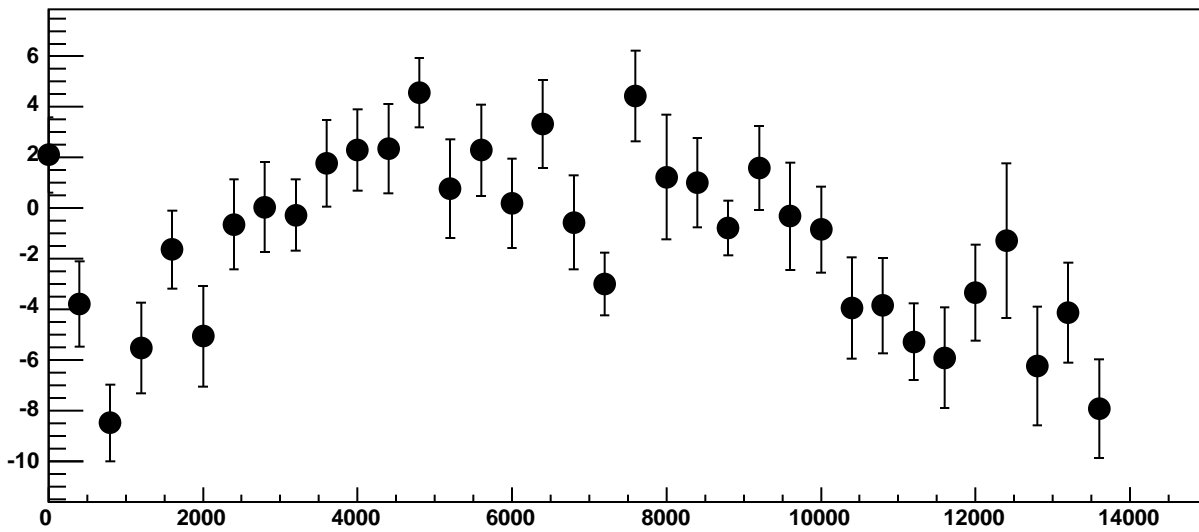


$\chi^2 / \text{ndf}$  60.94 / 23  
p0  $18.31 \pm 0.7877$   
p1  $0.02132 \pm 0.0001189$

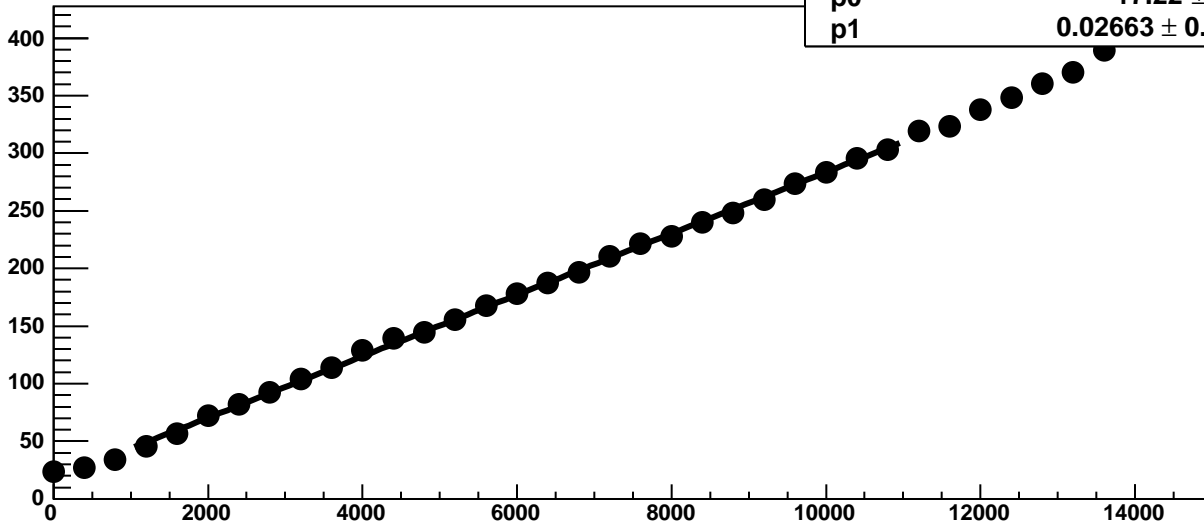
Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

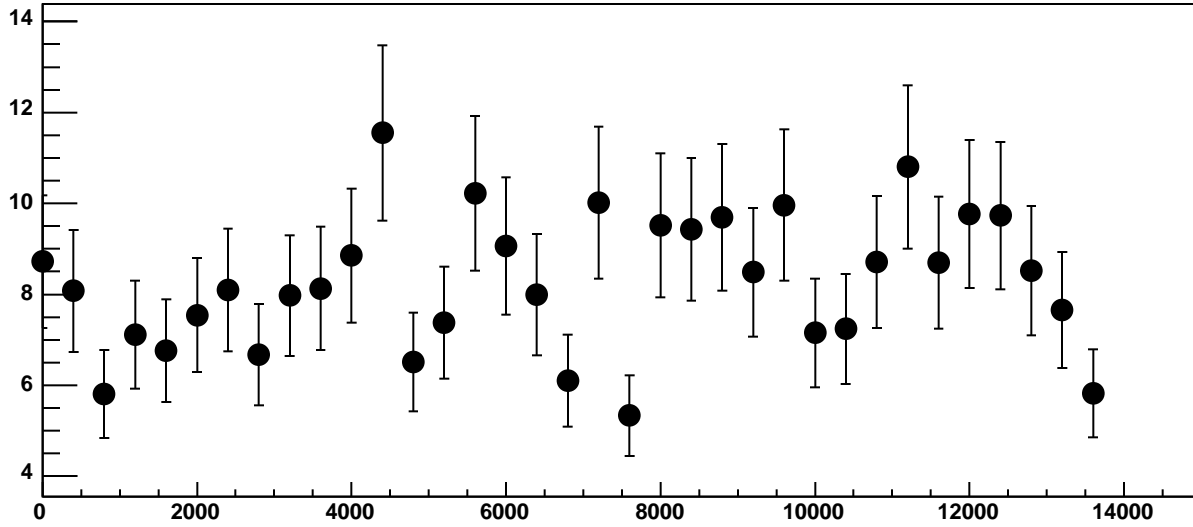


Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

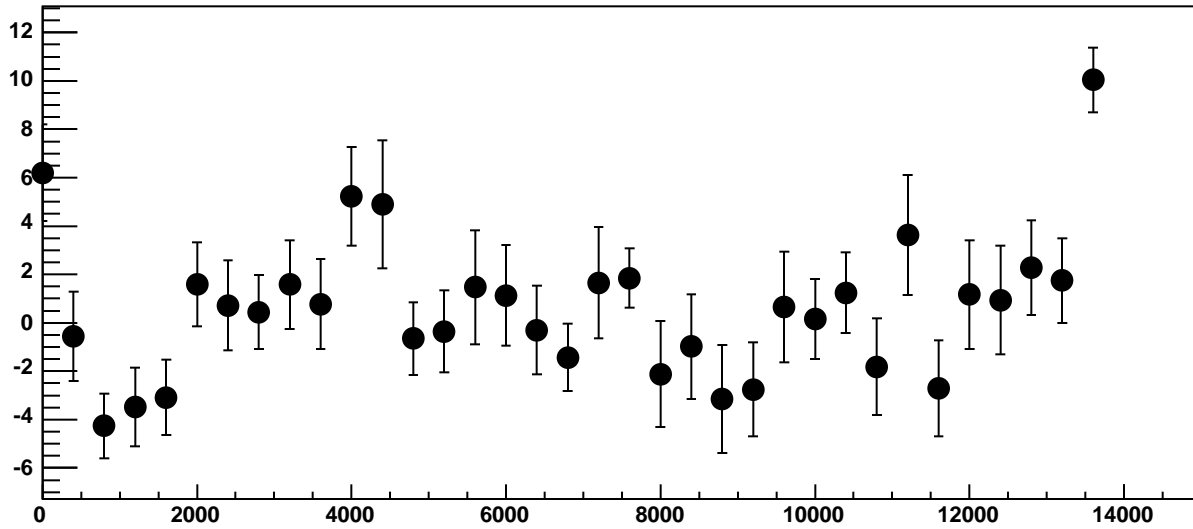


$\chi^2 / \text{ndf}$  31.96 / 23  
p0 17.22 ± 0.8015  
p1 0.02663 ± 0.000123

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

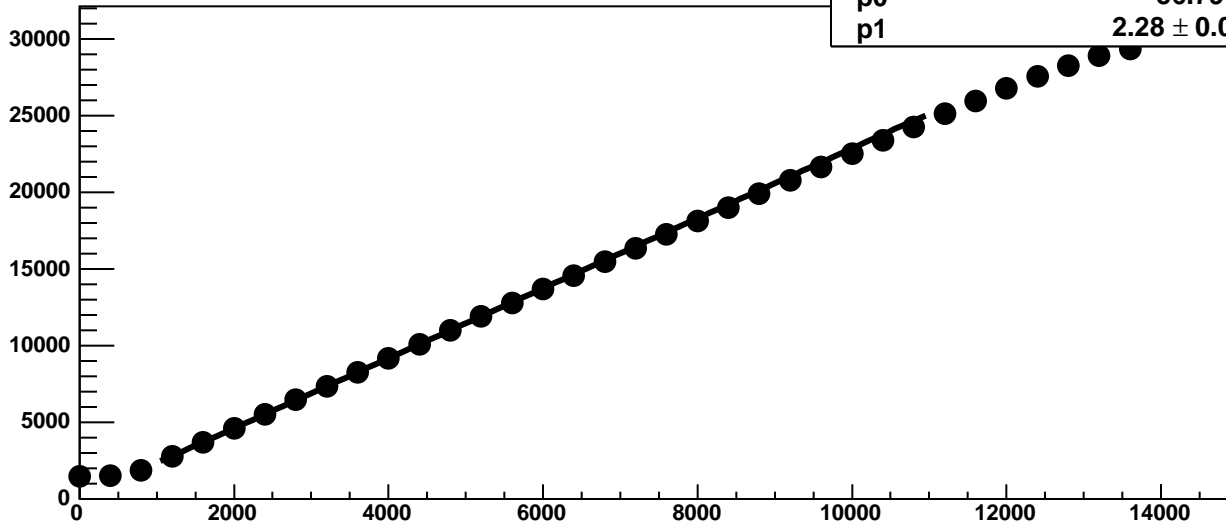


Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

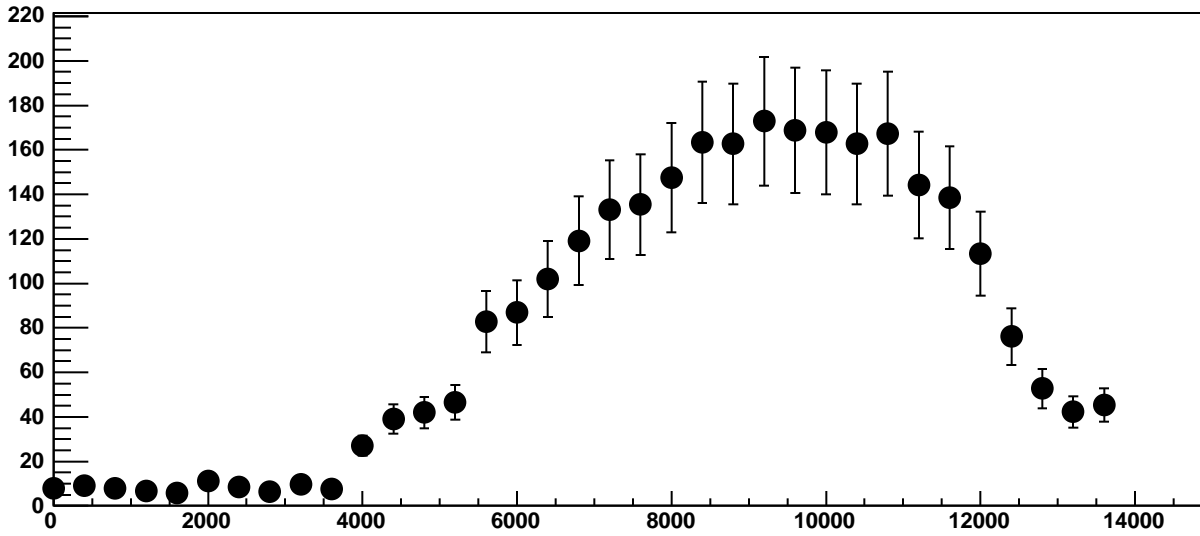


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

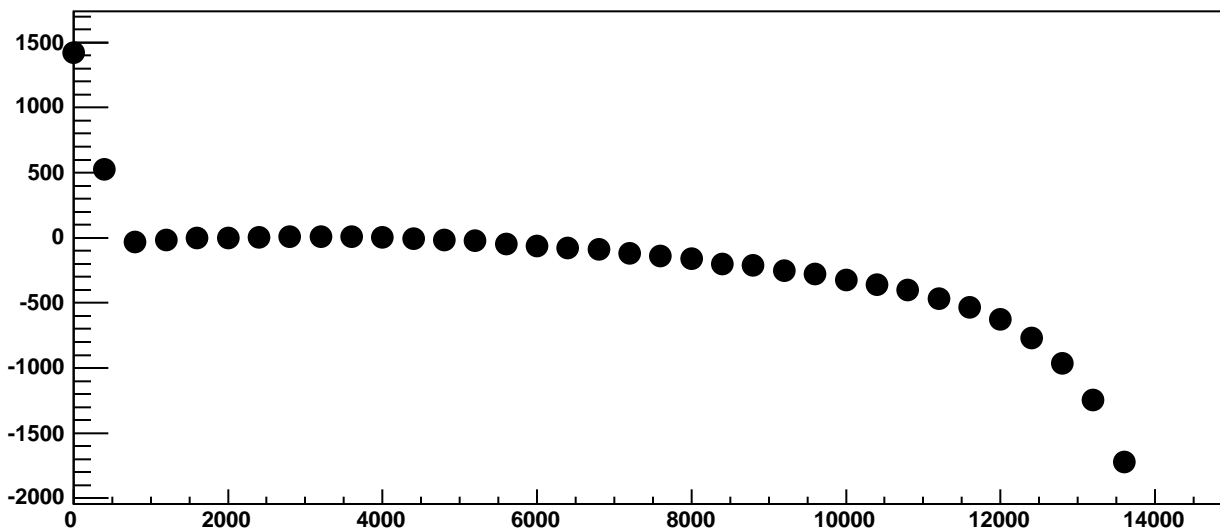
$\chi^2 / \text{ndf}$  746.1 / 23  
p0  $56.79 \pm 1.679$   
p1  $2.28 \pm 0.0006542$



Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

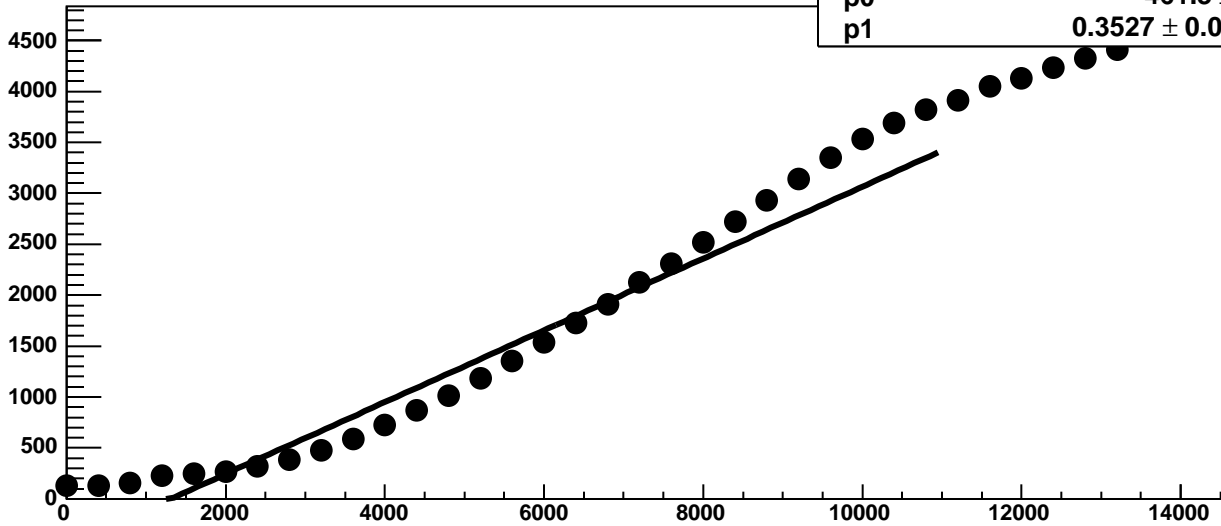


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

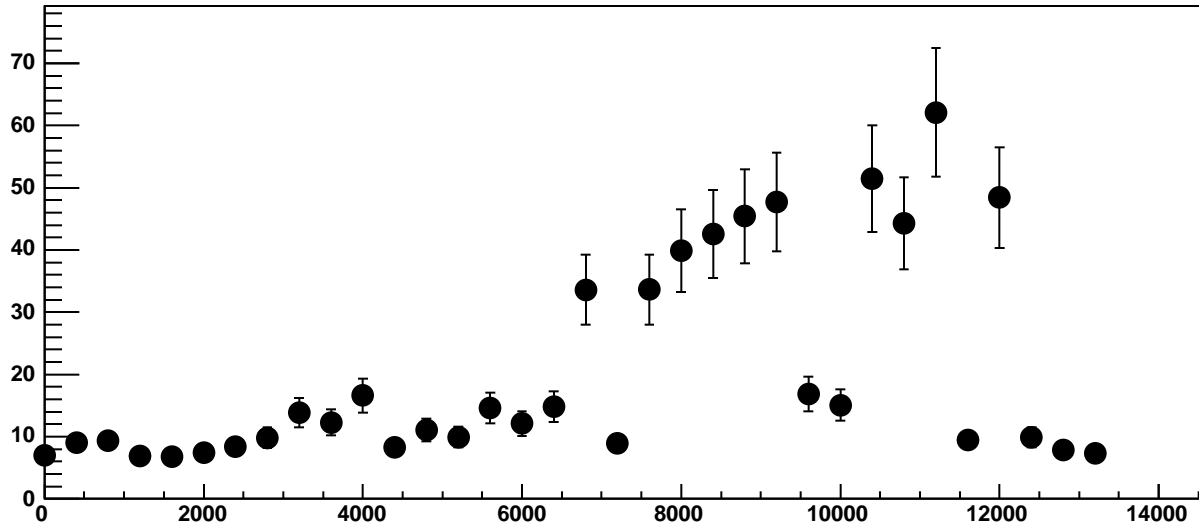


Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

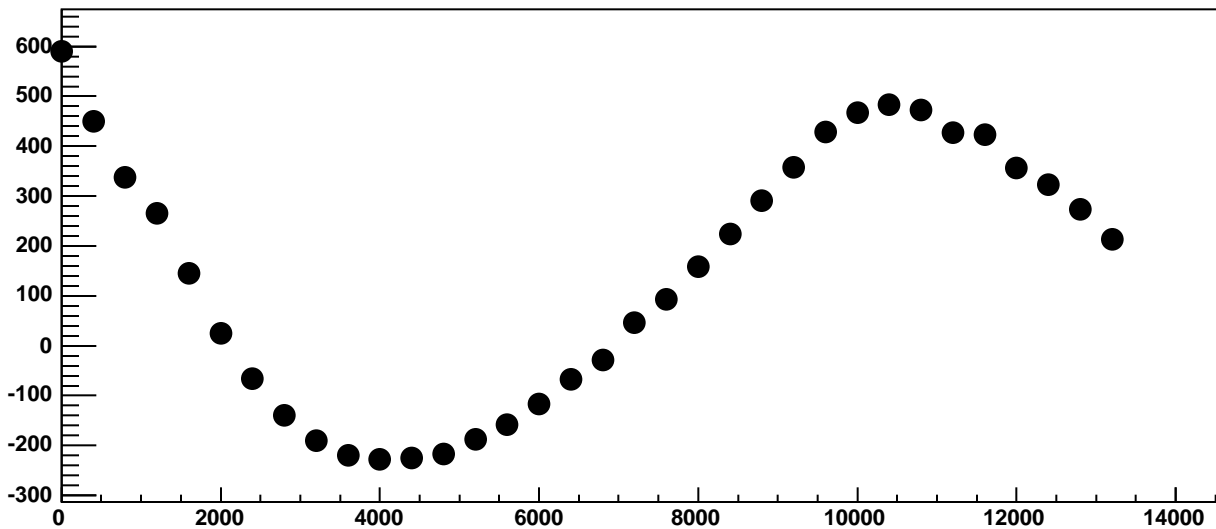
$\chi^2 / \text{ndf}$  1.253e+05 / 23  
p0 -461.3 ± 1.039  
p1 0.3527 ± 0.0002291



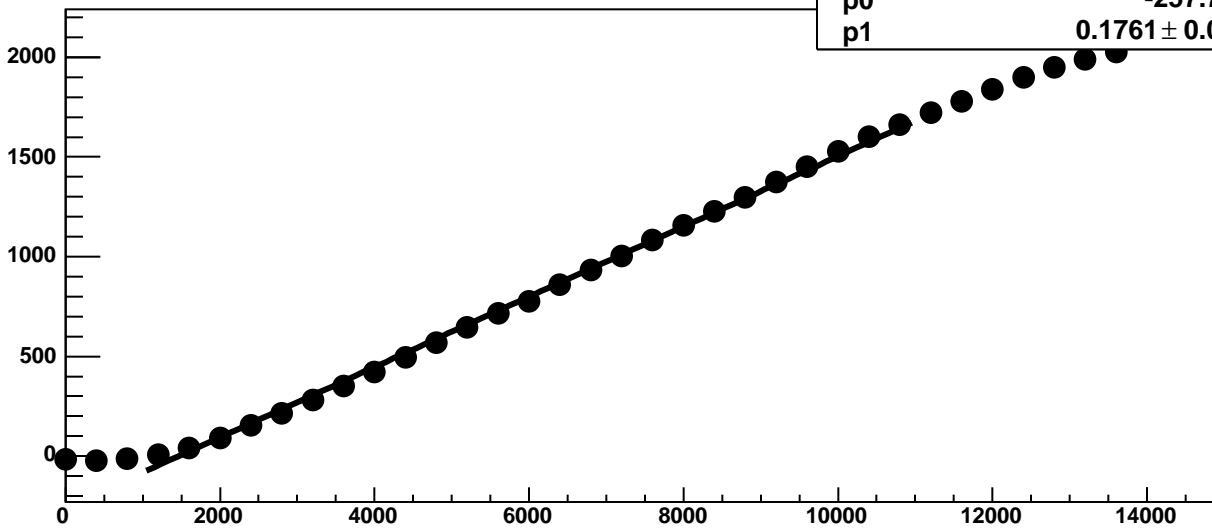
Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



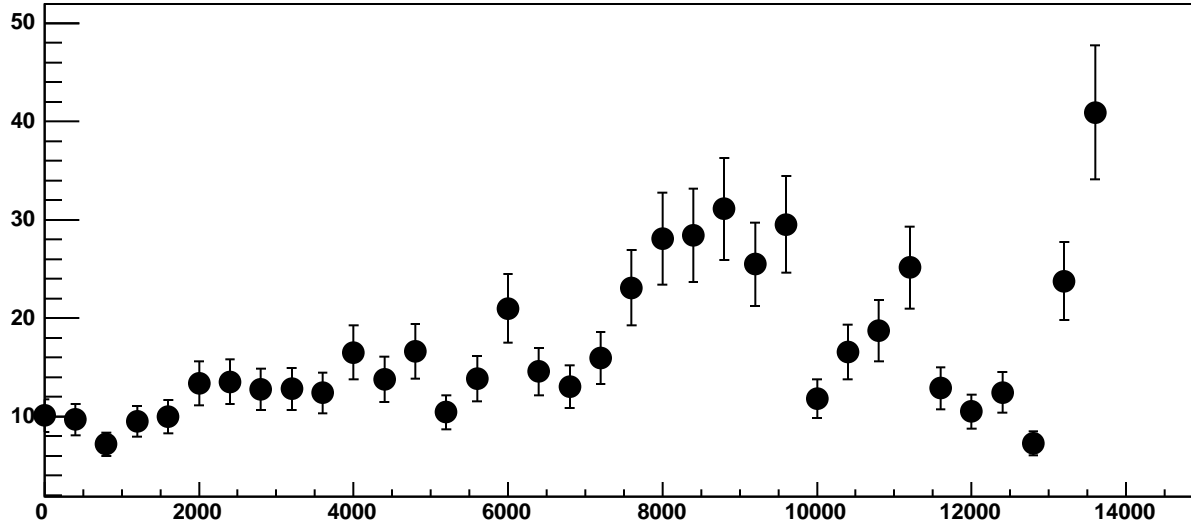
Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



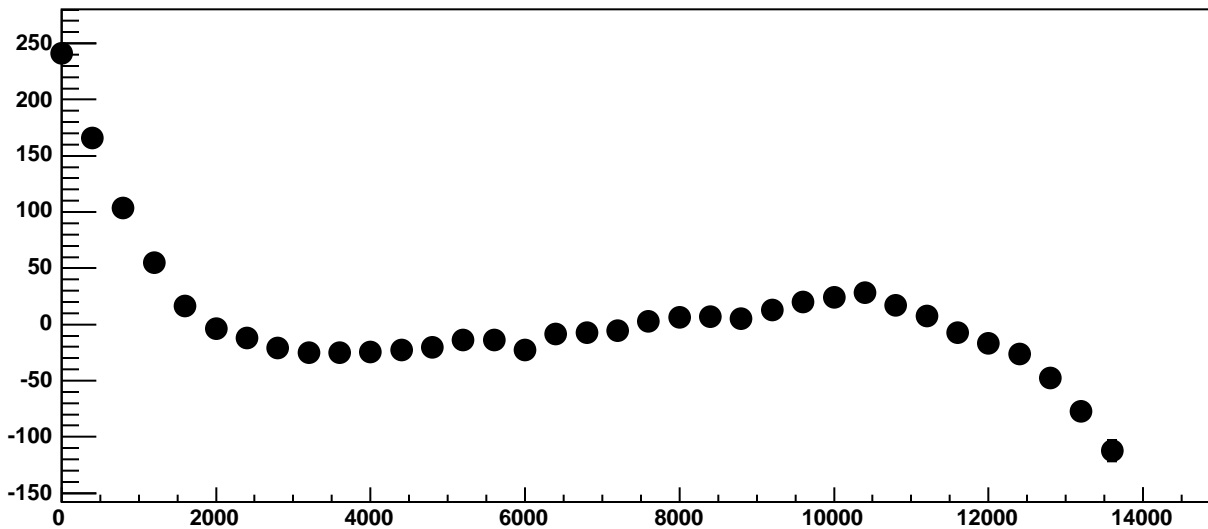
Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

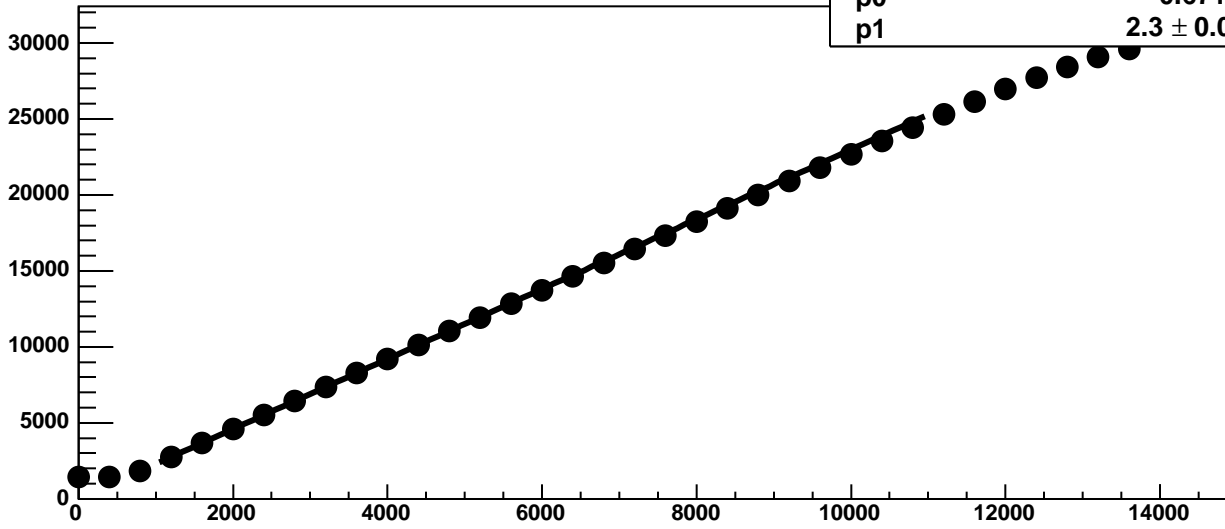


Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

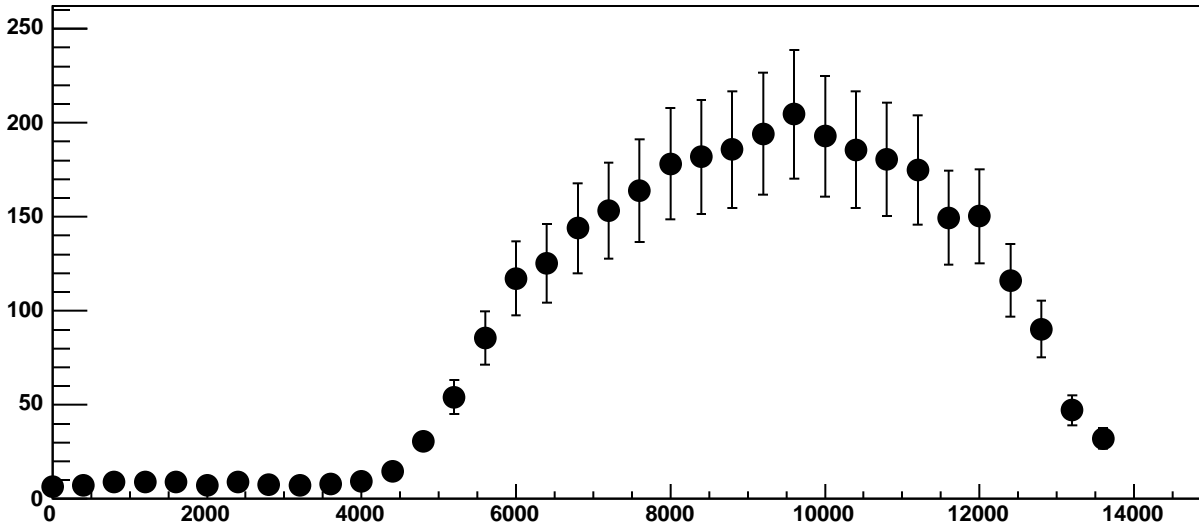


Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

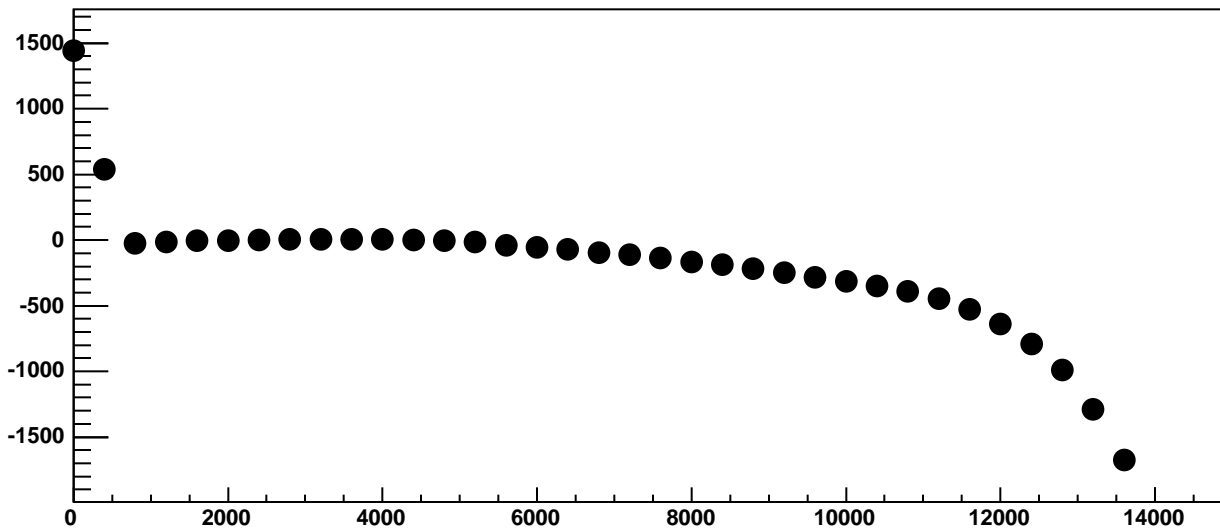
$\chi^2 / \text{ndf}$  490.2 / 23  
p0 -0.6712 ± 1.9  
p1 2.3 ± 0.0006502



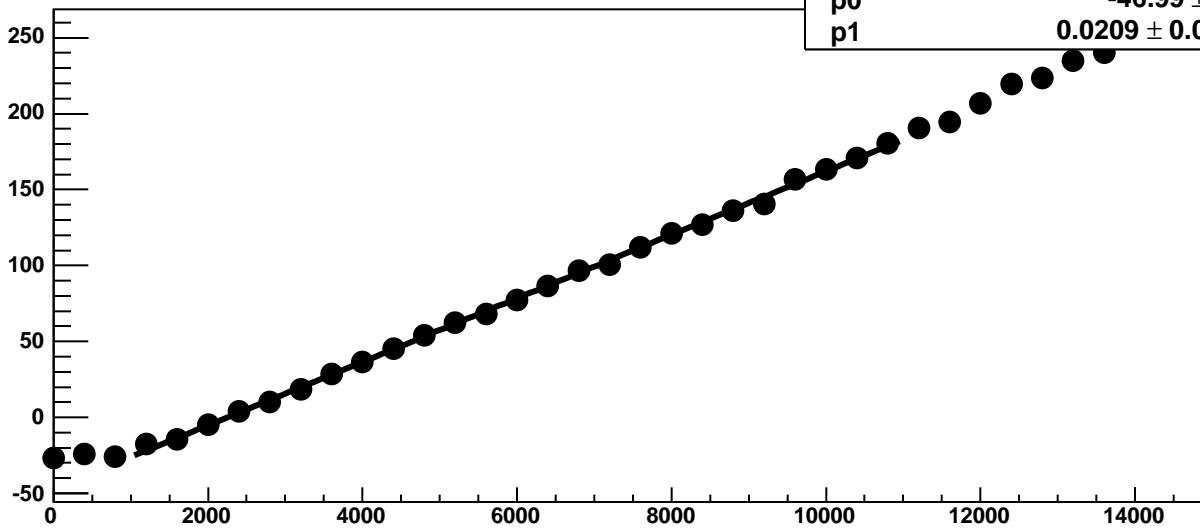
Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

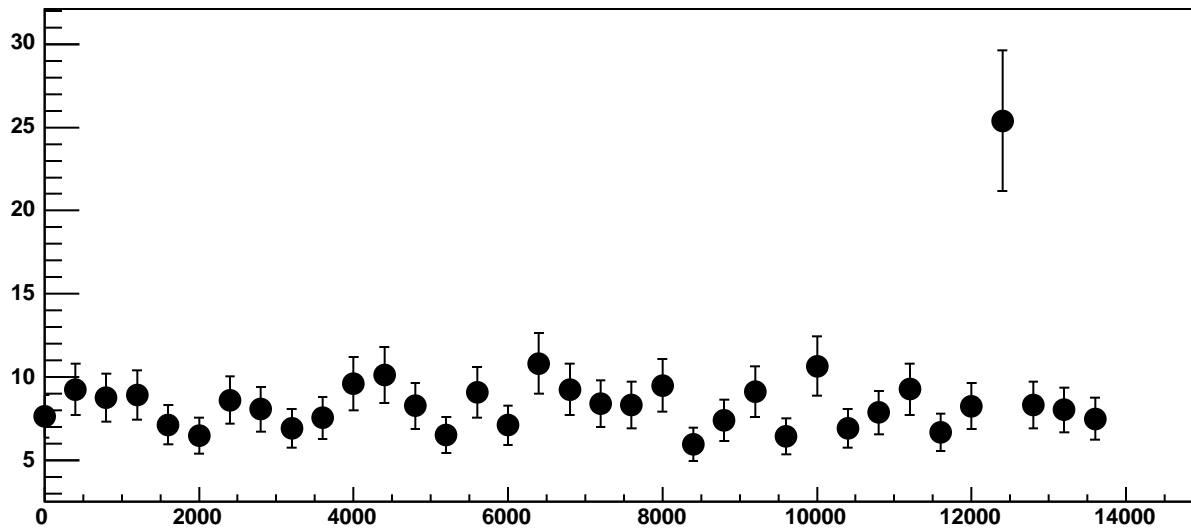


Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

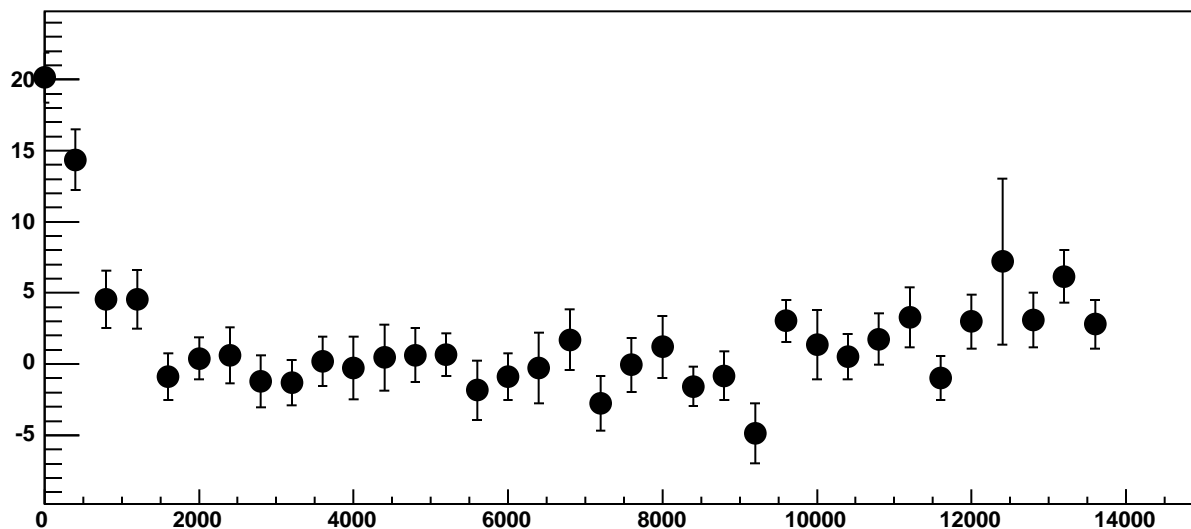


$\chi^2 / \text{ndf}$  23.48 / 23  
p0  $-46.99 \pm 0.8188$   
p1  $0.0209 \pm 0.0001222$

Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

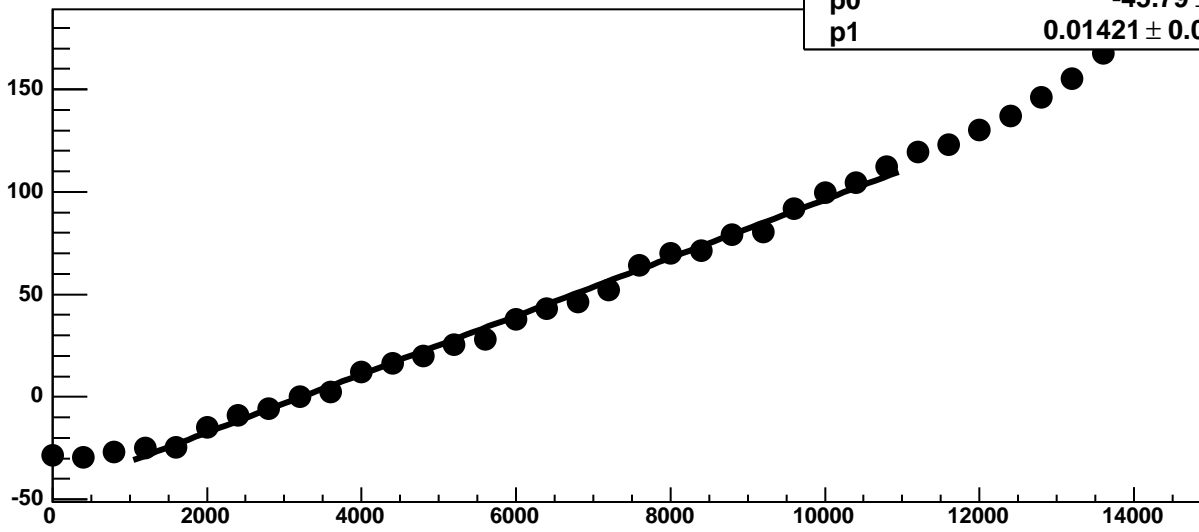


Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



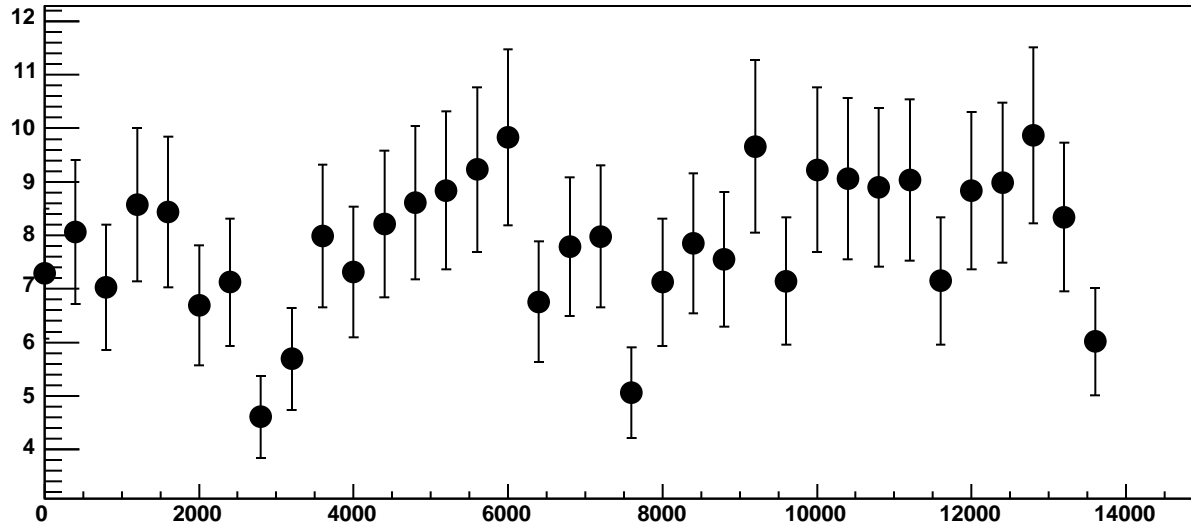


Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

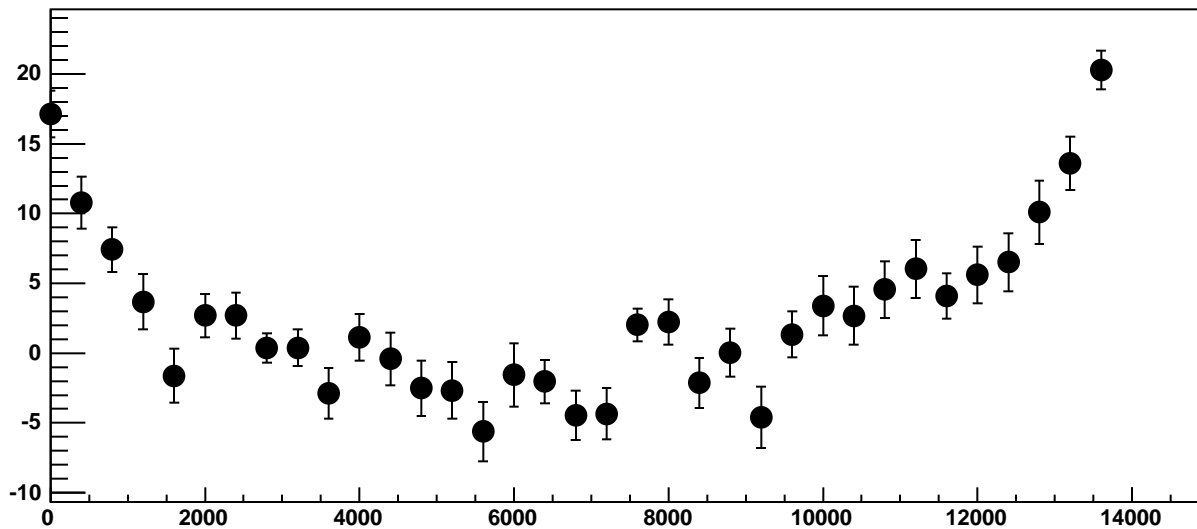


$\chi^2 / \text{ndf}$  58.12 / 23  
p0  $-45.79 \pm 0.7611$   
p1  $0.01421 \pm 0.0001205$

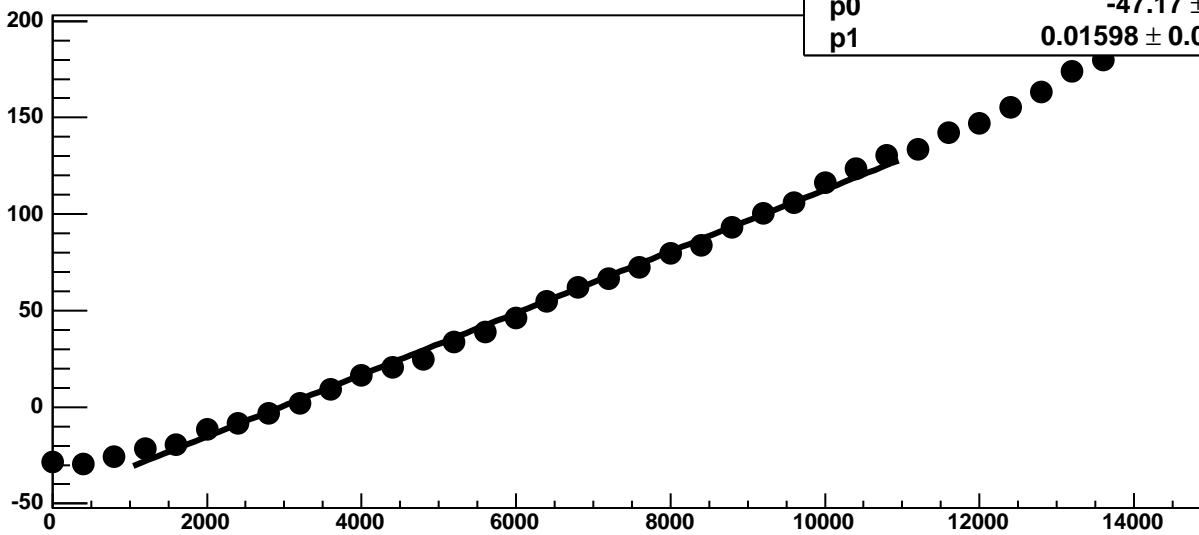
Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

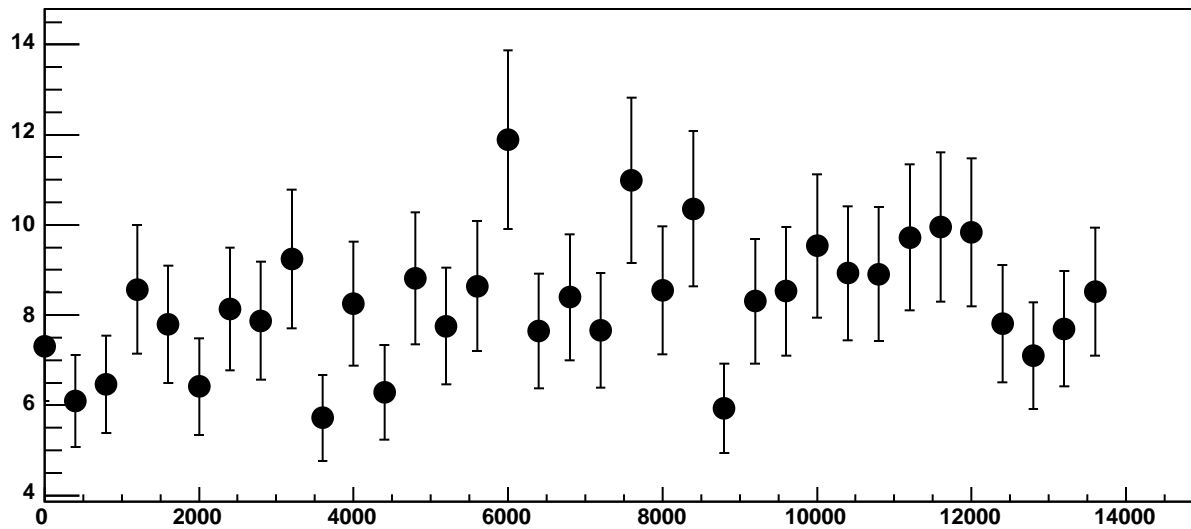


Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

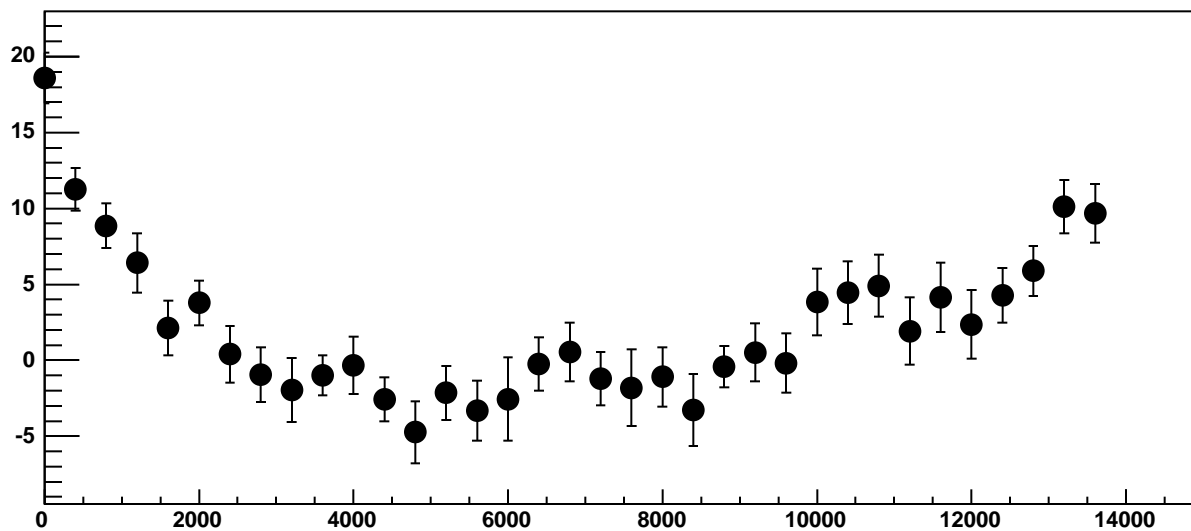


$\chi^2 / \text{ndf}$  51.28 / 23  
p0  $-47.17 \pm 0.8193$   
p1  $0.01598 \pm 0.0001283$

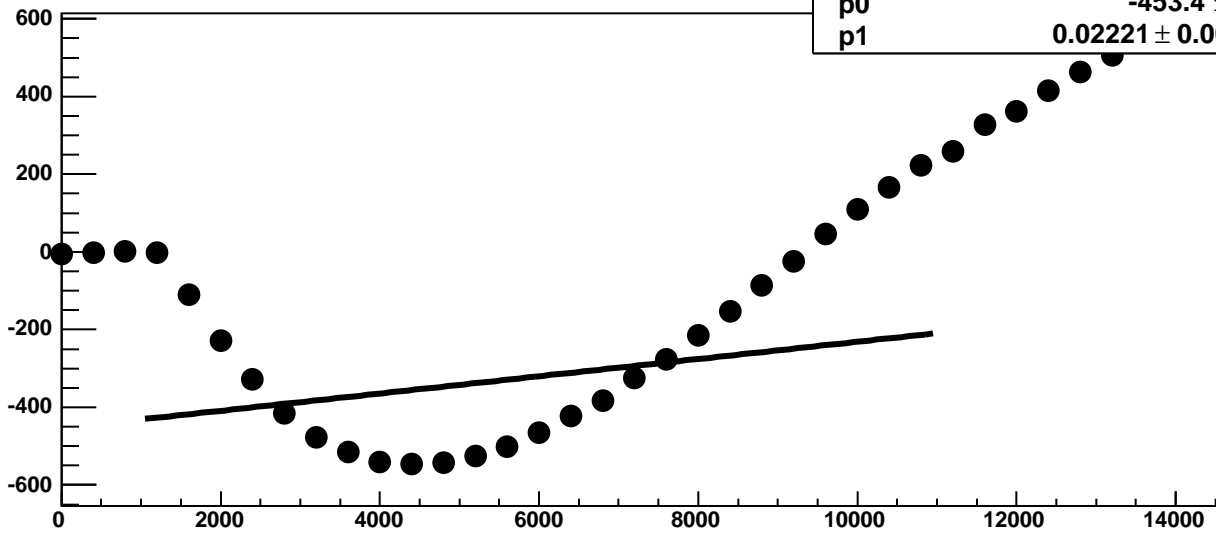
Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

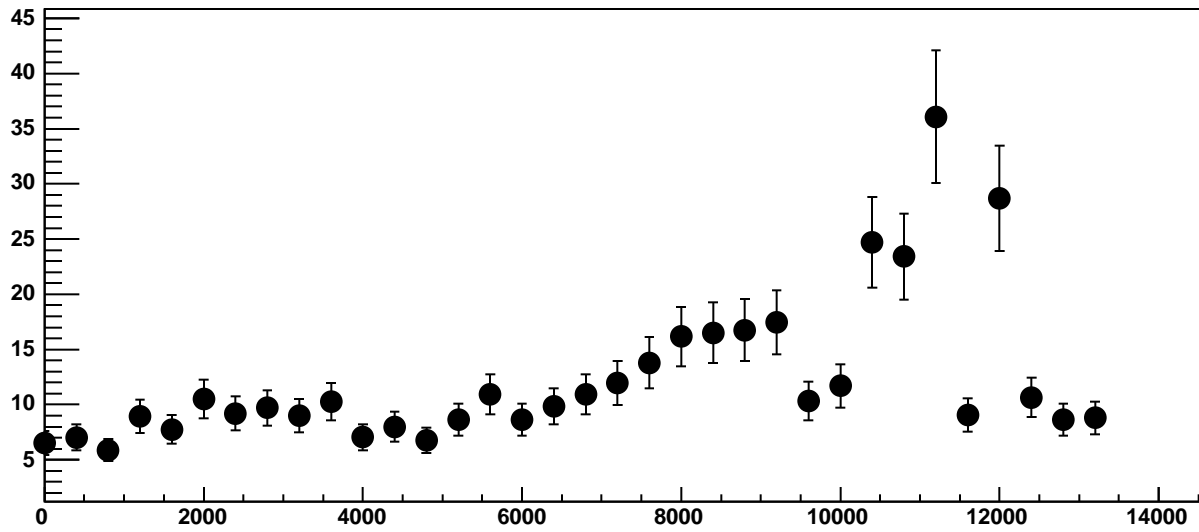


Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

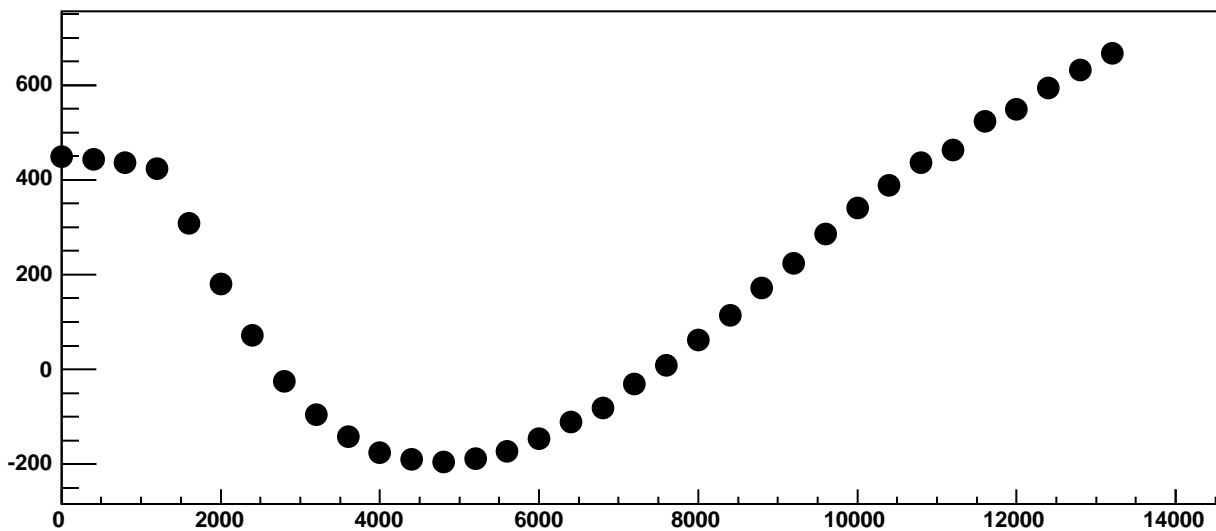


$\chi^2 / \text{ndf}$  1.951e+05 / 23  
p0 -453.4 ± 1.036  
p1 0.02221 ± 0.0001887

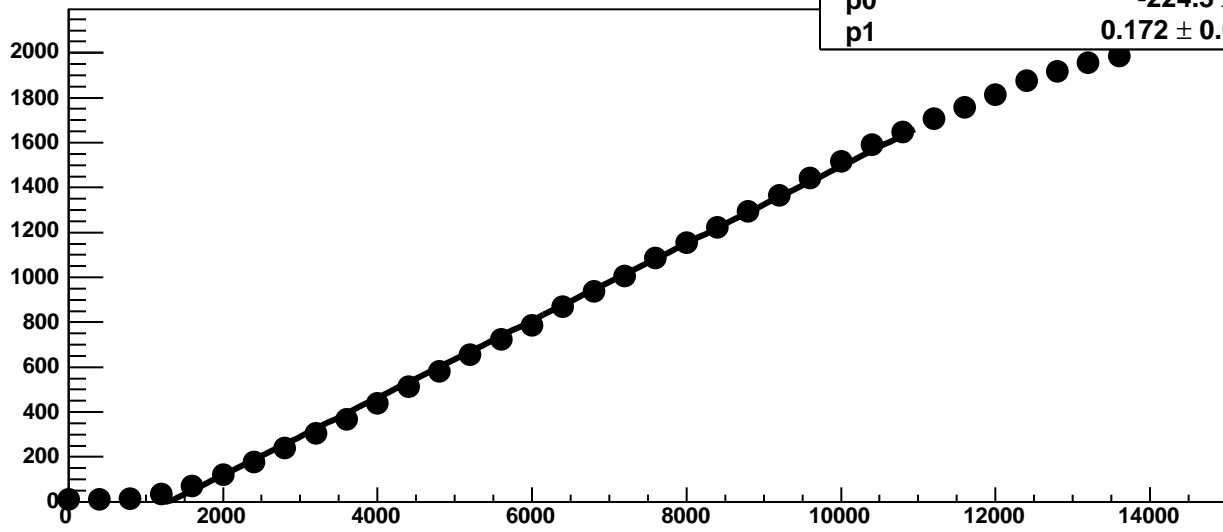
Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



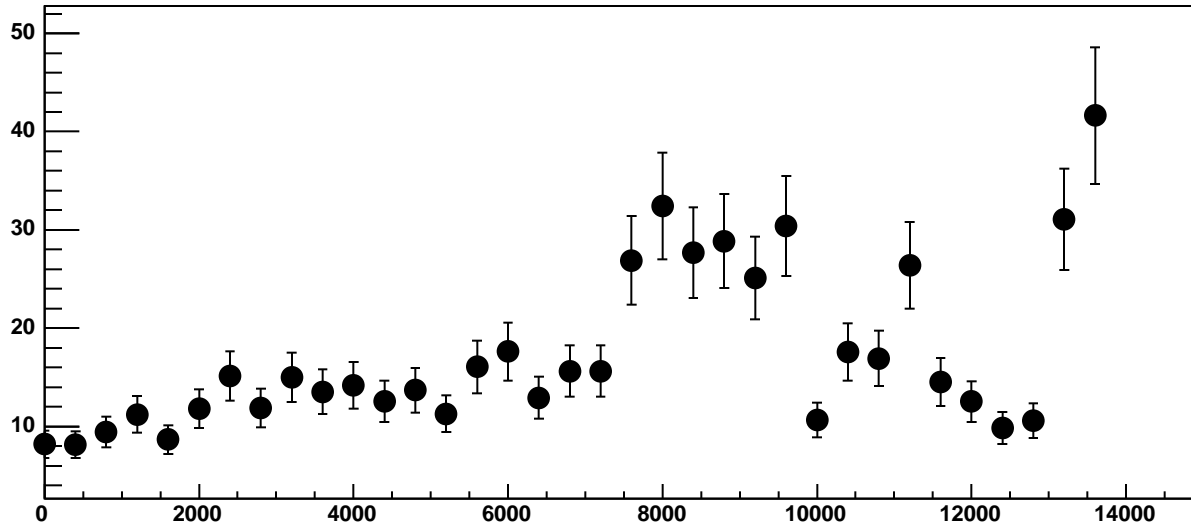
Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



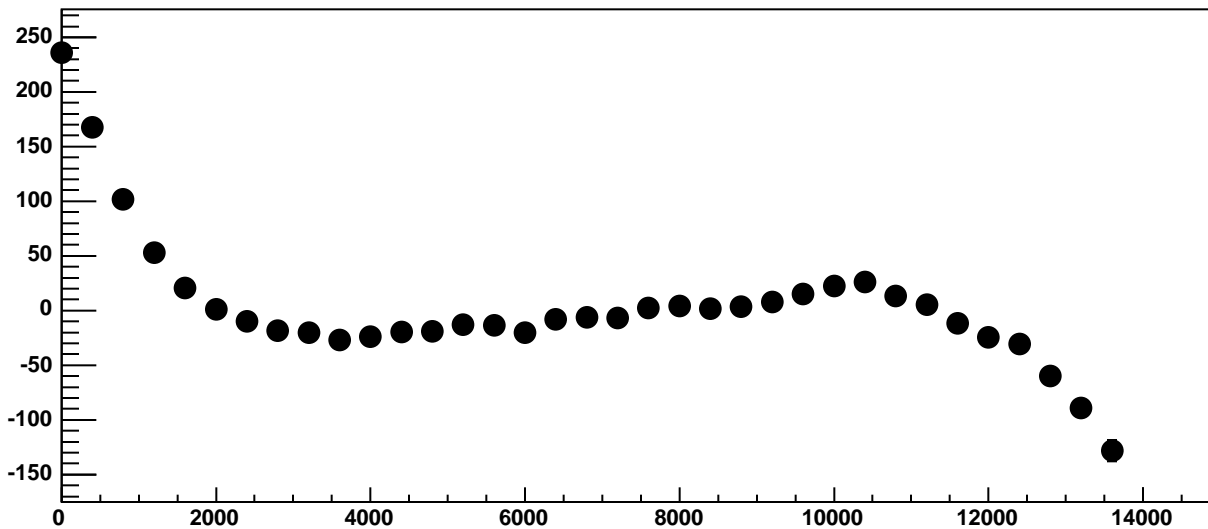
Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



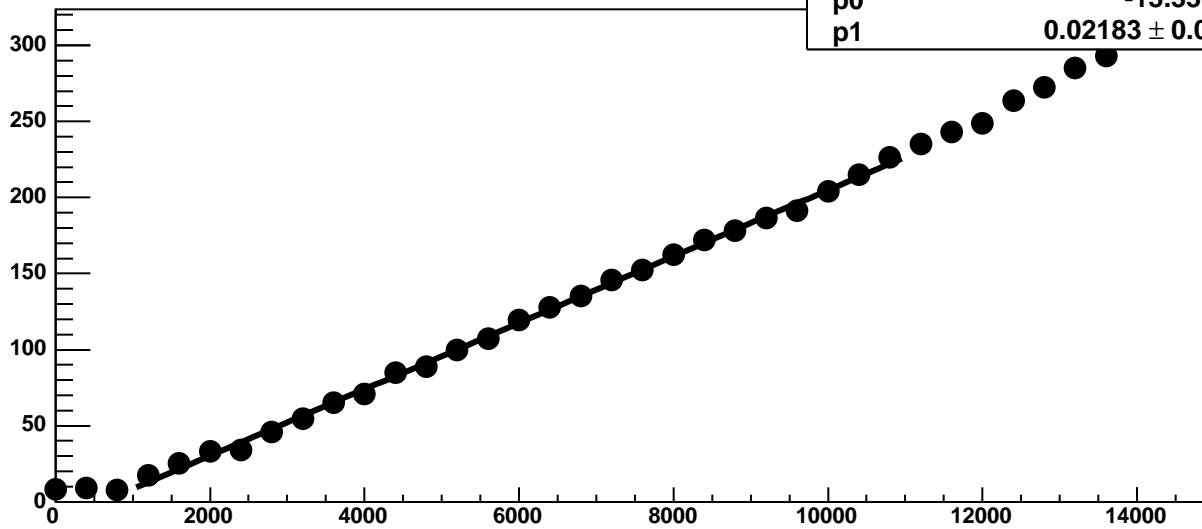
Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



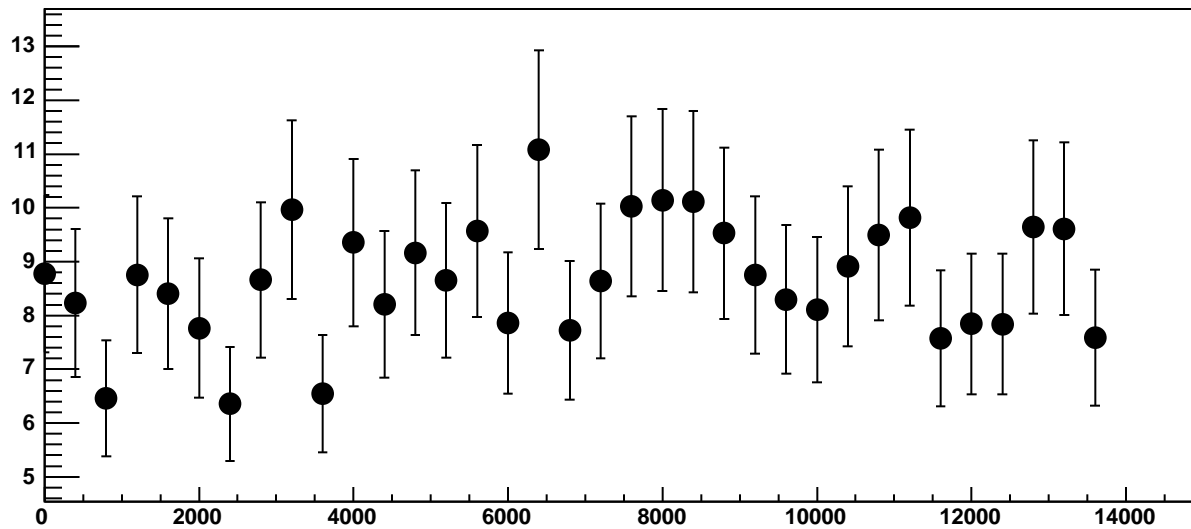
Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



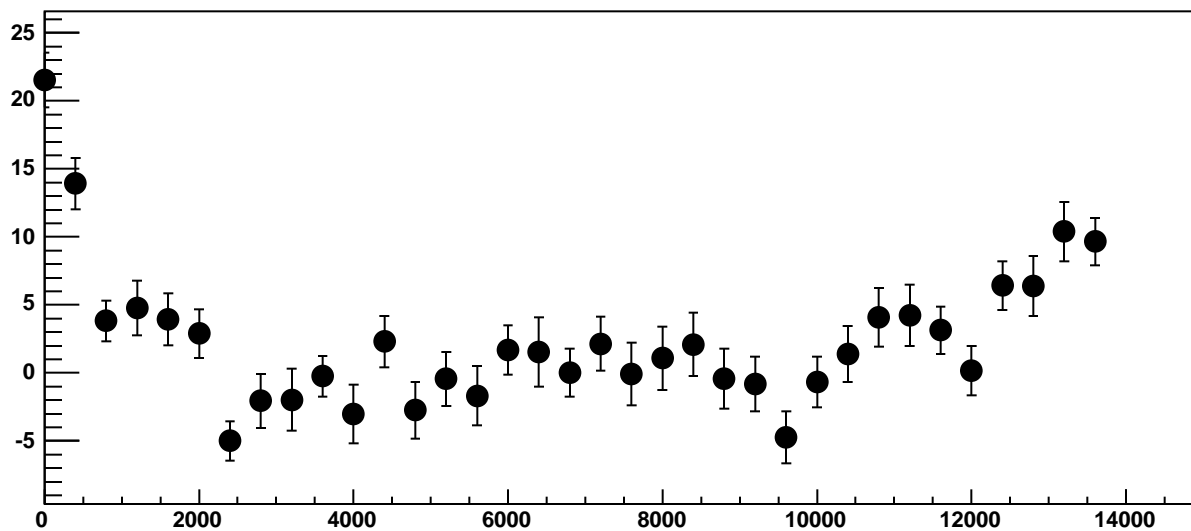
Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



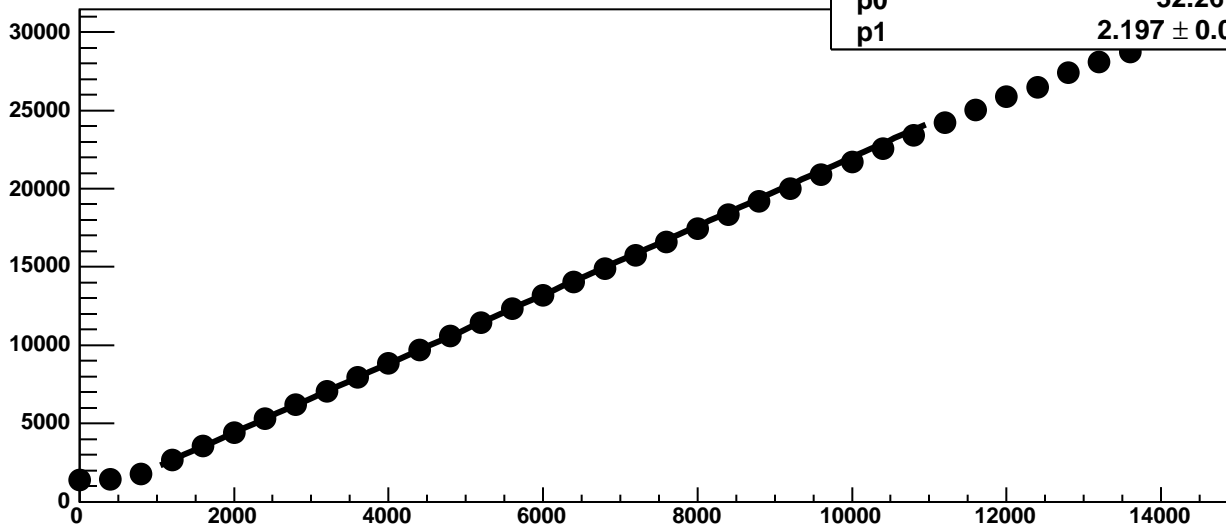
Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



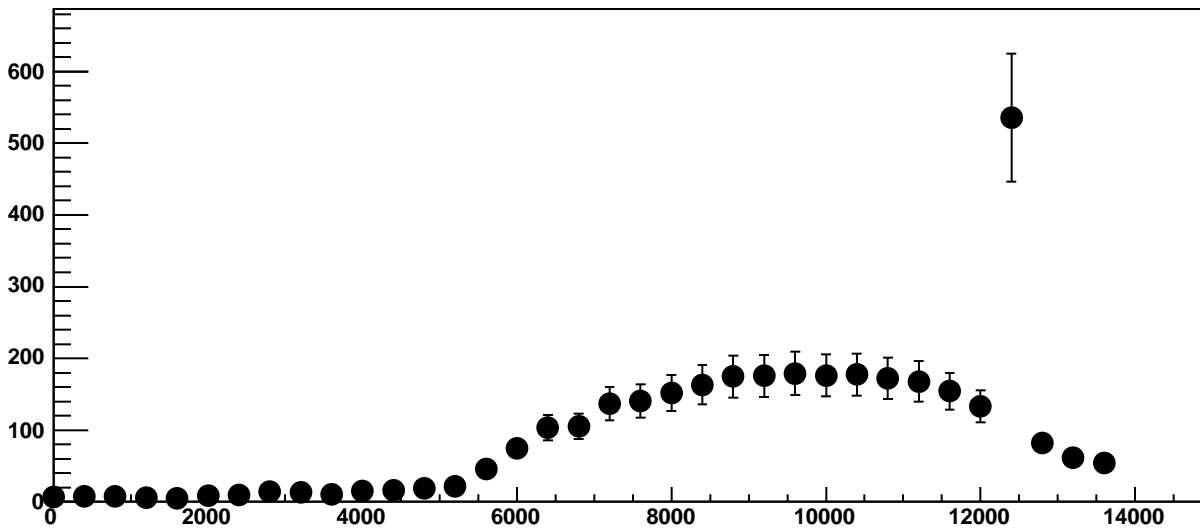
Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



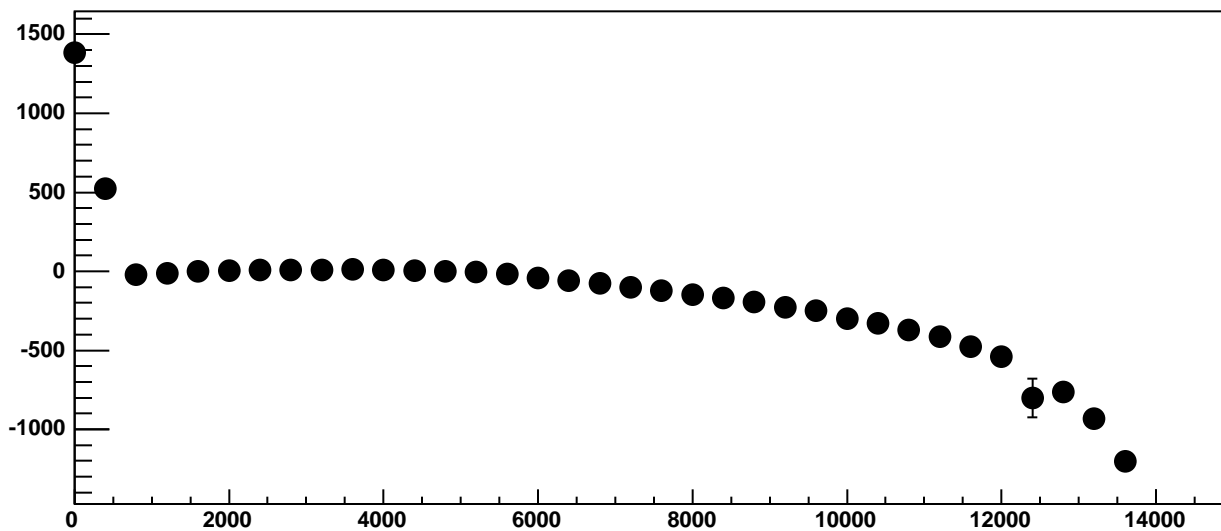
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



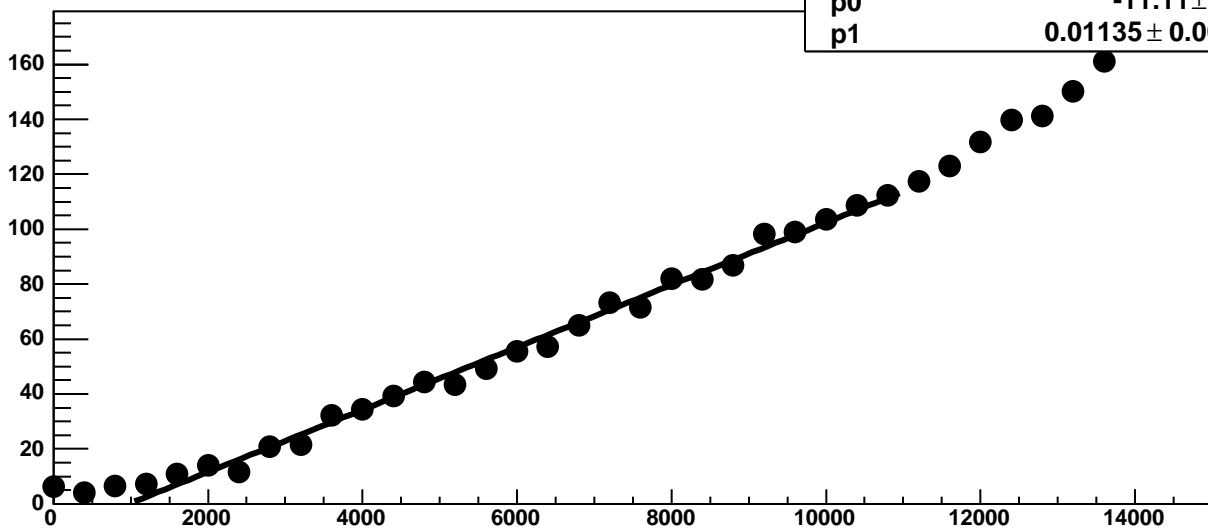
Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

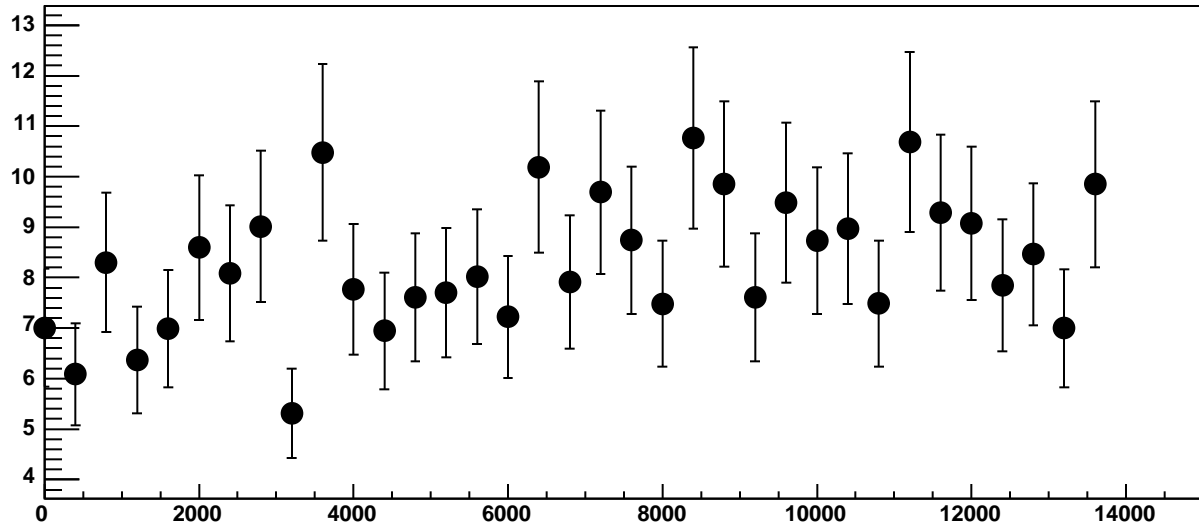


Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

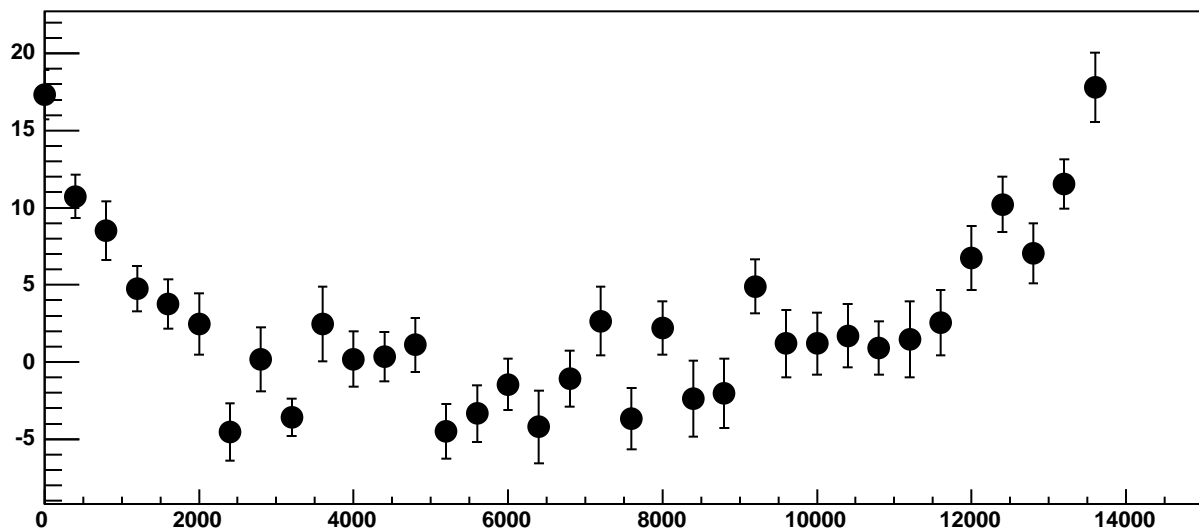


$\chi^2 / \text{ndf}$  65.53 / 23  
p0 -11.11 ± 0.7883  
p1 0.01135 ± 0.0001249

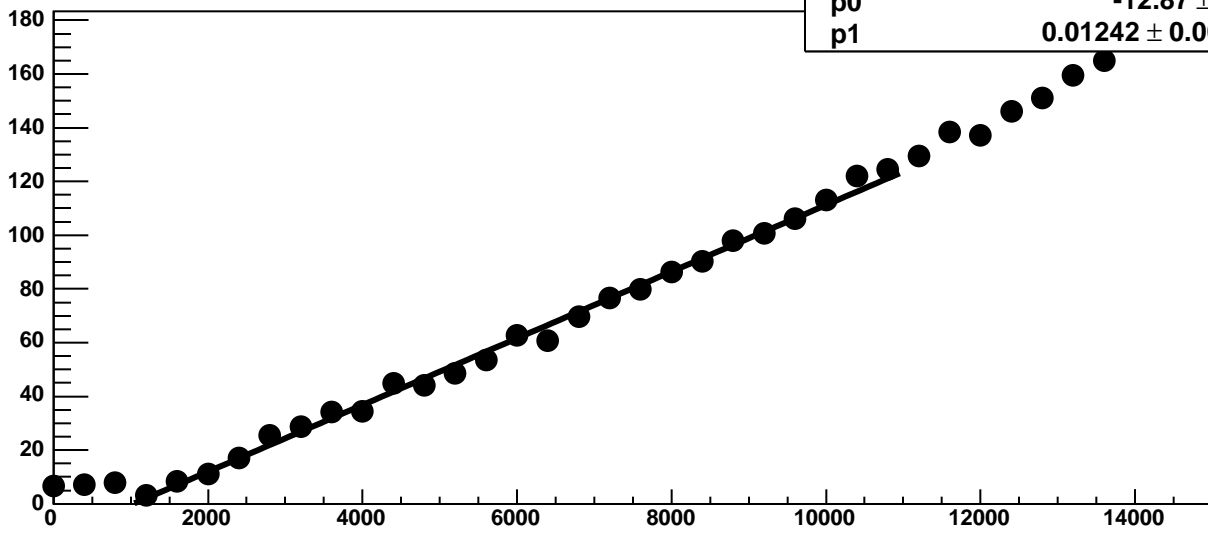
Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

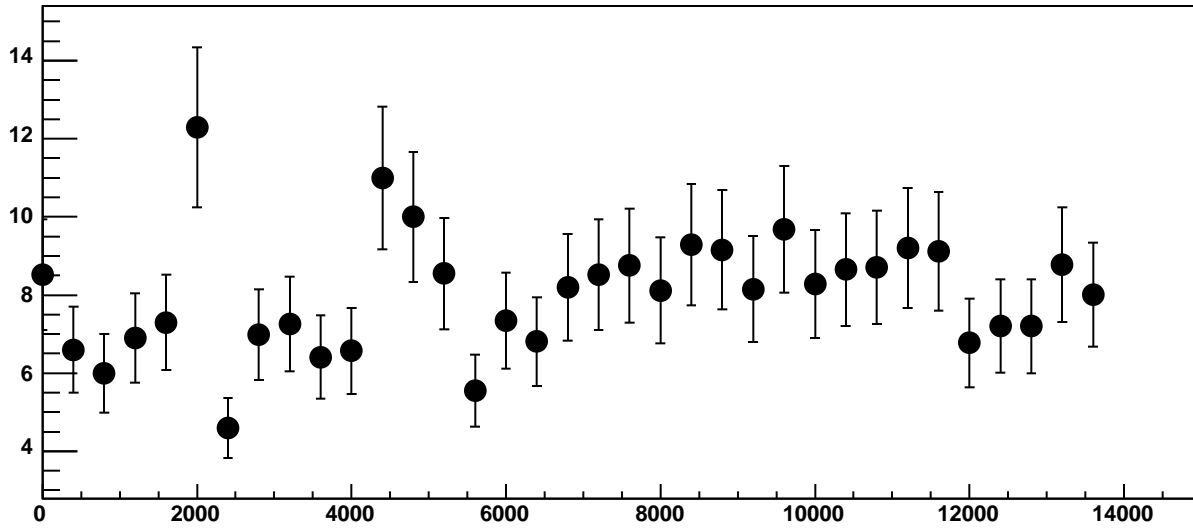


Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

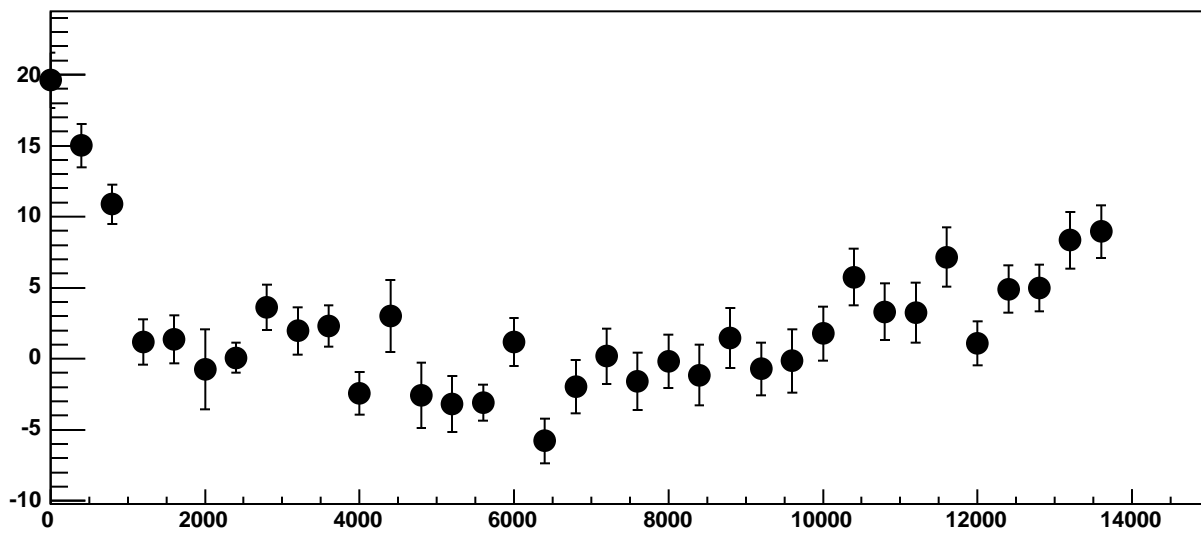


$\chi^2 / \text{ndf}$  53.02 / 23  
p0 -12.87 ± 0.7581  
p1 0.01242 ± 0.0001234

Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

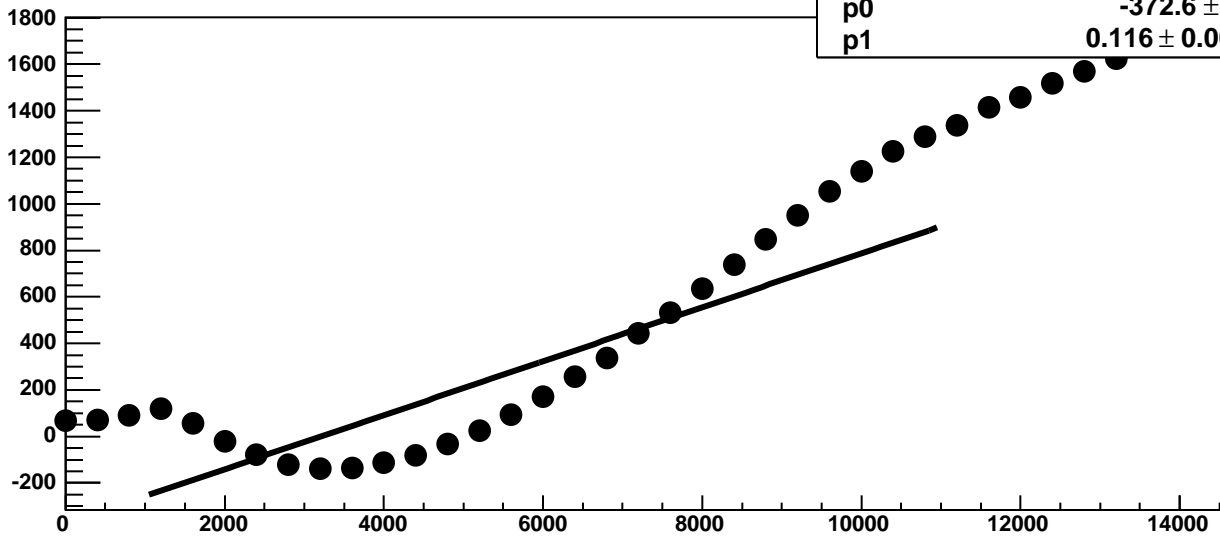


Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



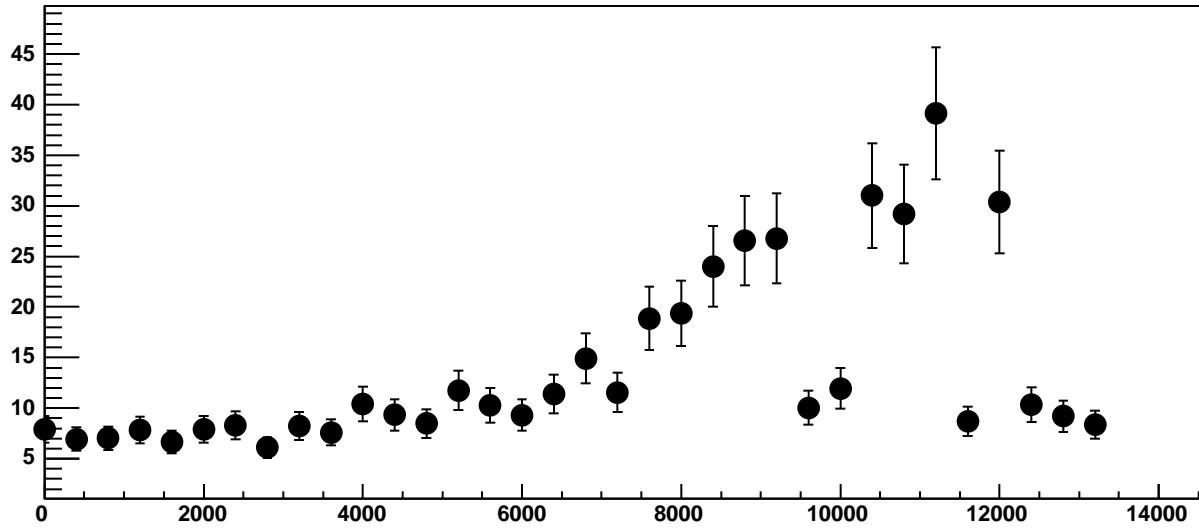


Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

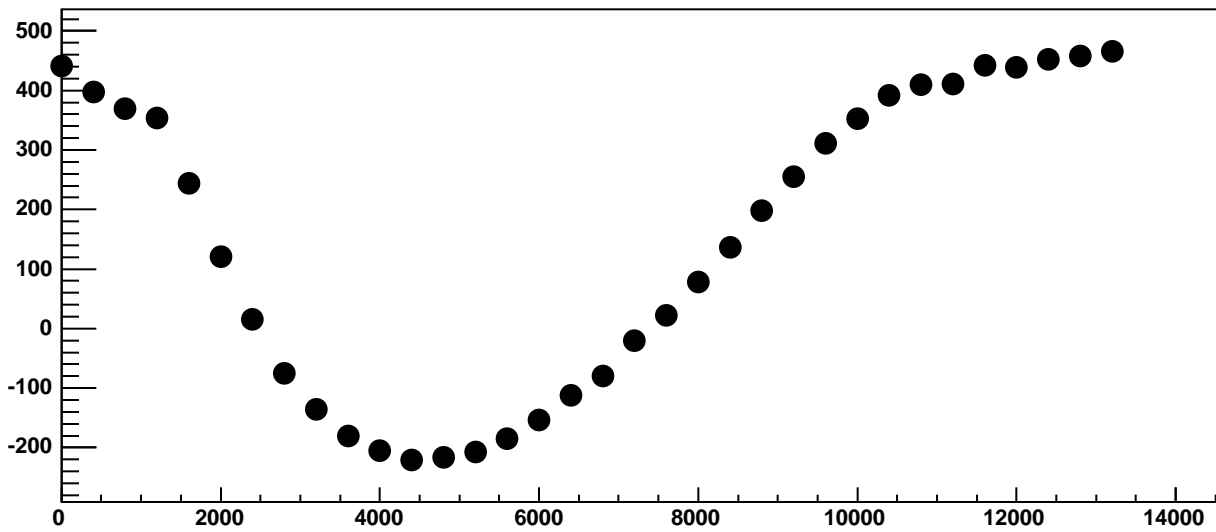


$\chi^2 / \text{ndf}$	1.826e+05 / 23
p0	-372.6 ± 0.9322
p1	0.116 ± 0.0001885

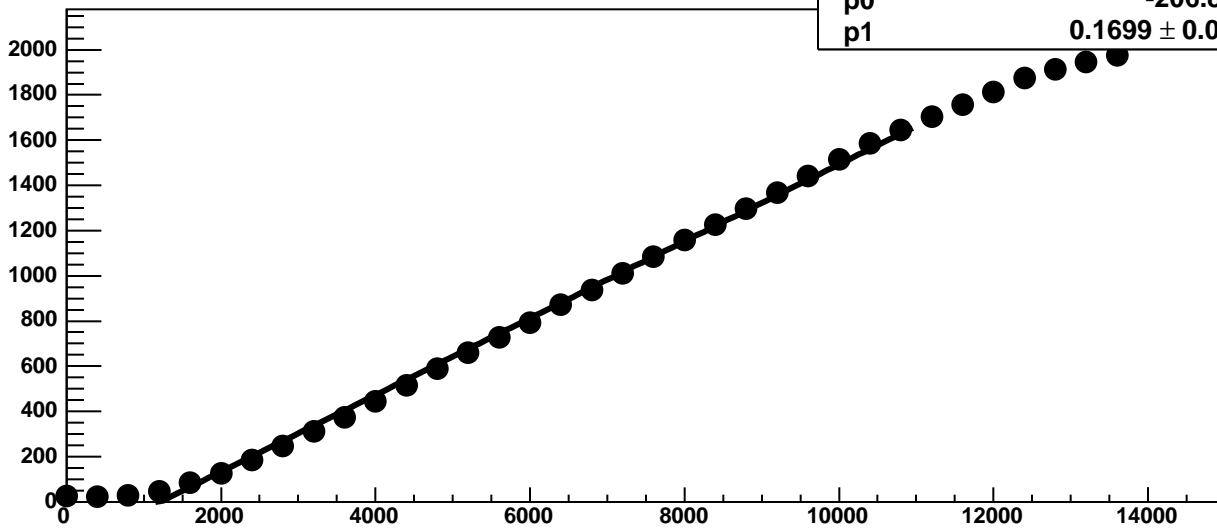
Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



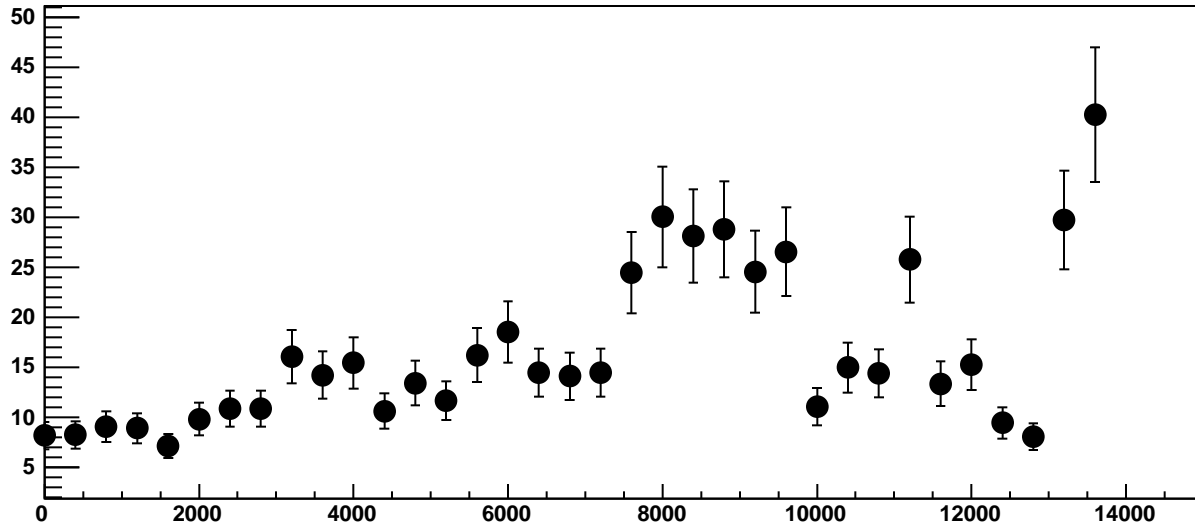
Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



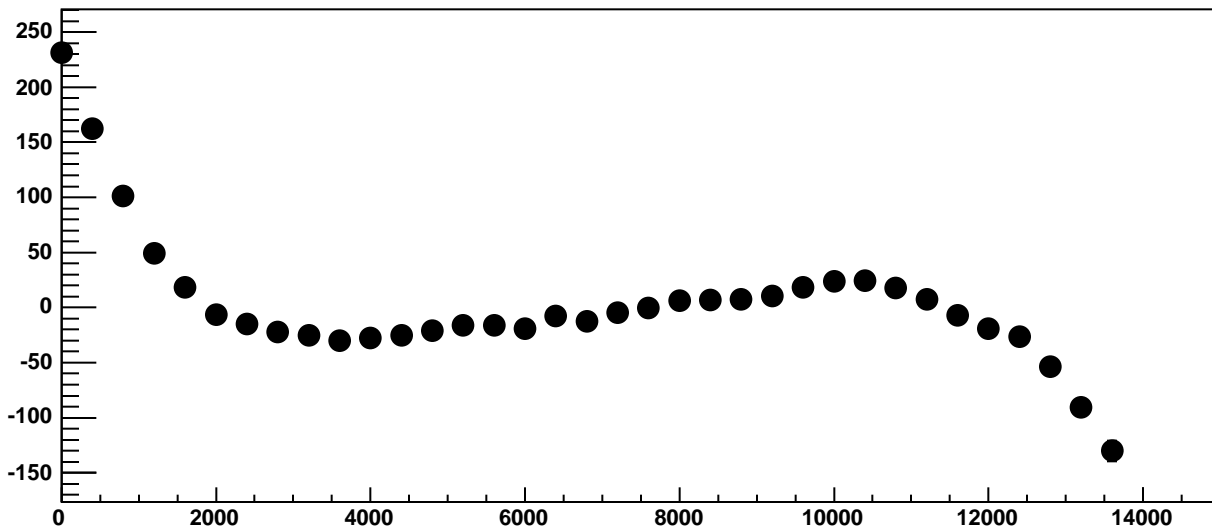
Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



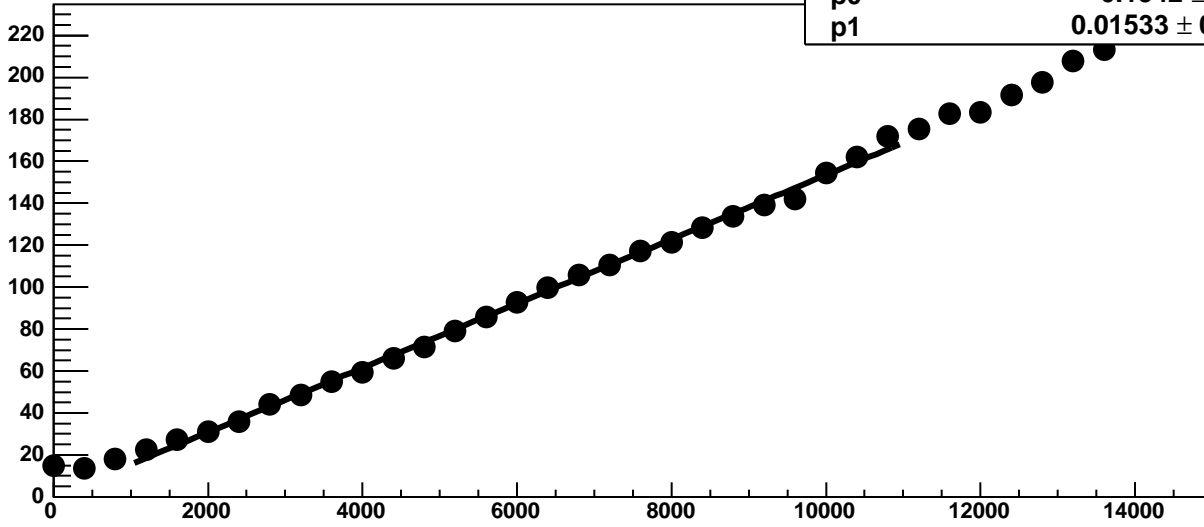
Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

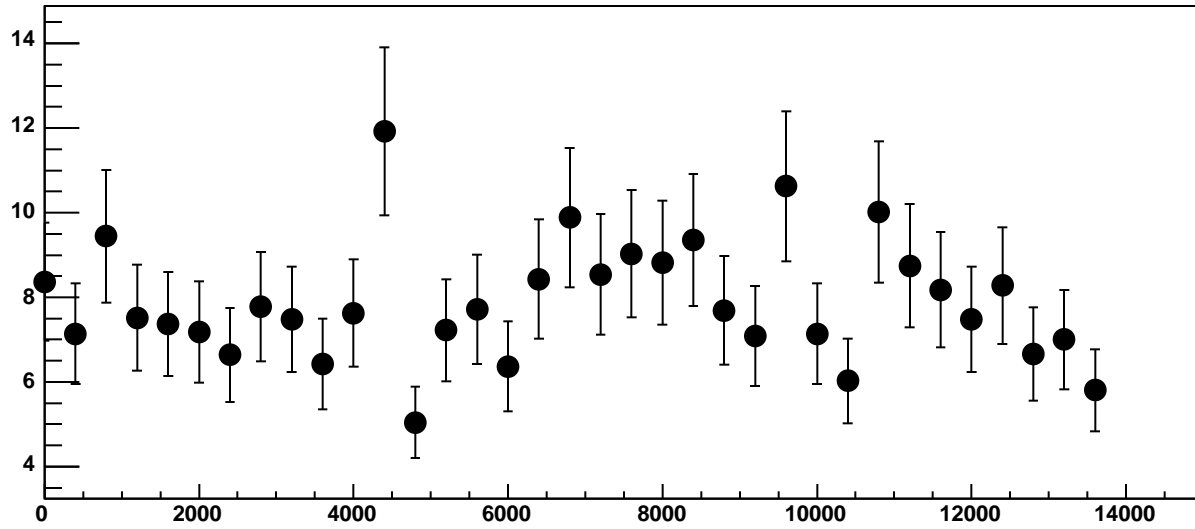


Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

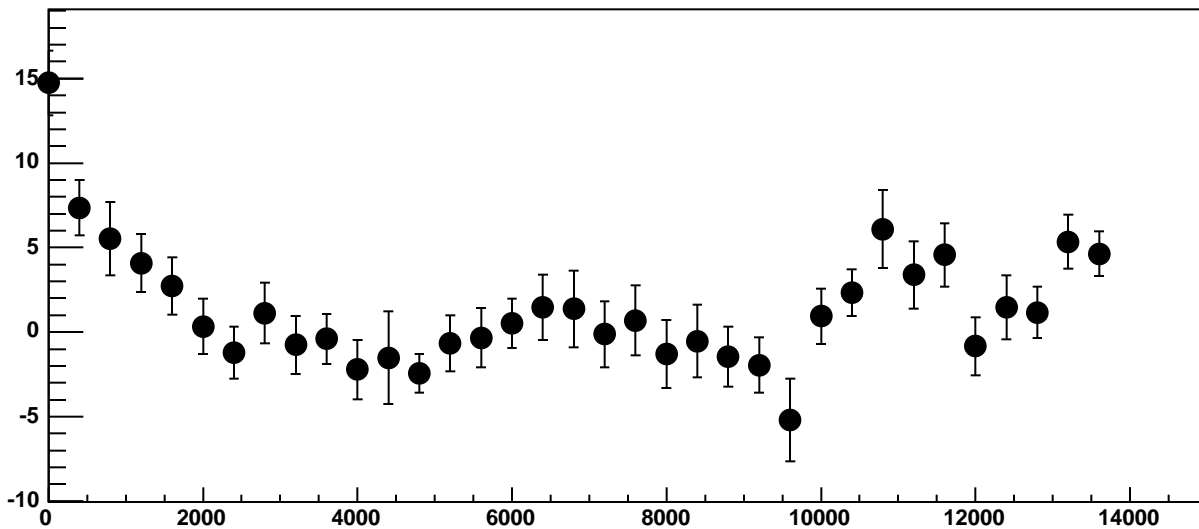


$\chi^2 / \text{ndf}$  34.62 / 23  
p0  $0.1542 \pm 0.7725$   
p1  $0.01533 \pm 0.00012$

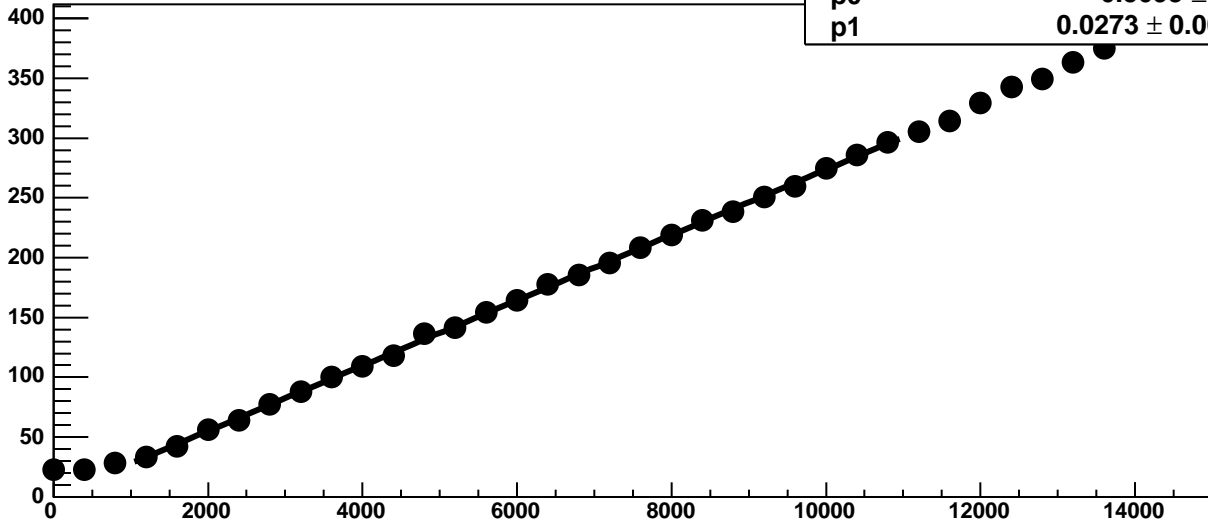
Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

20.39 / 23

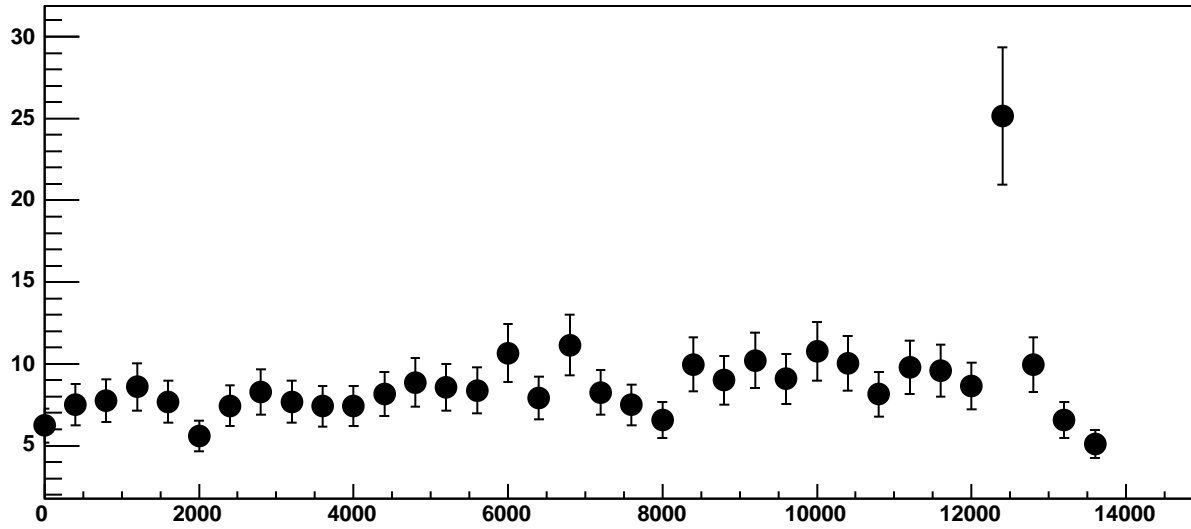
p0

$0.5093 \pm 0.8148$

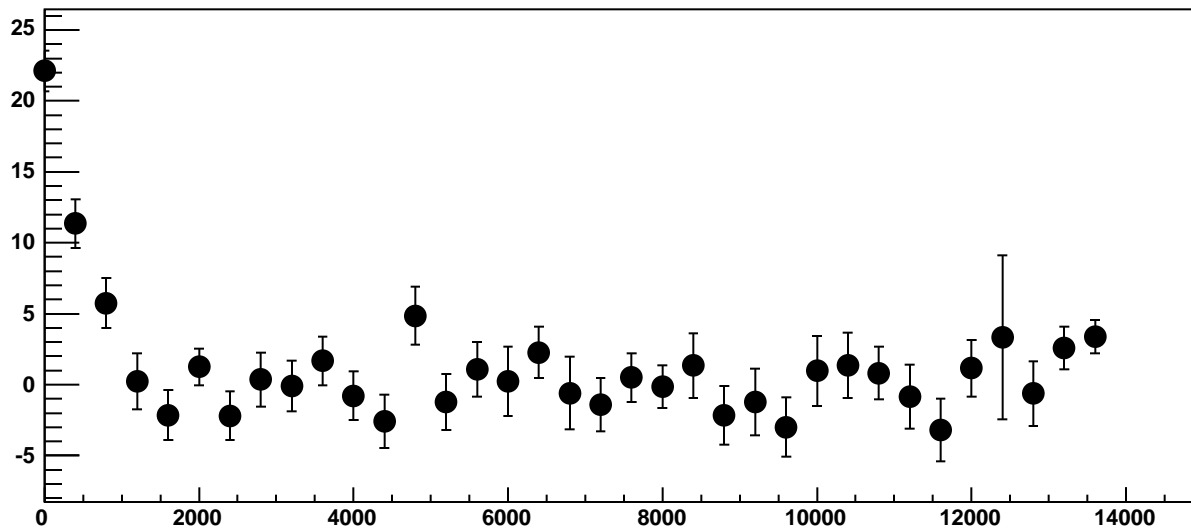
p1

$0.0273 \pm 0.0001306$

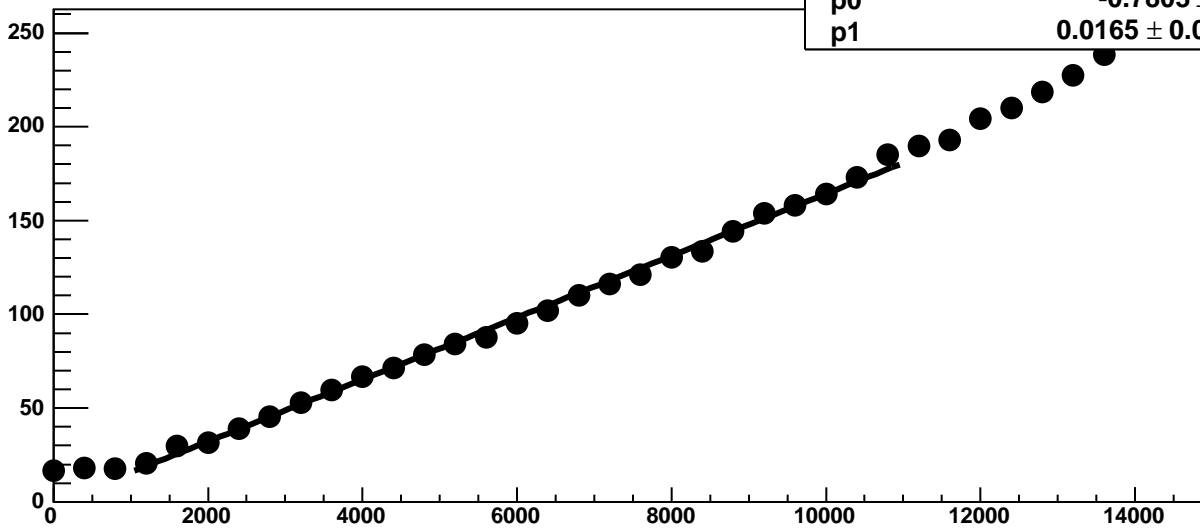
Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

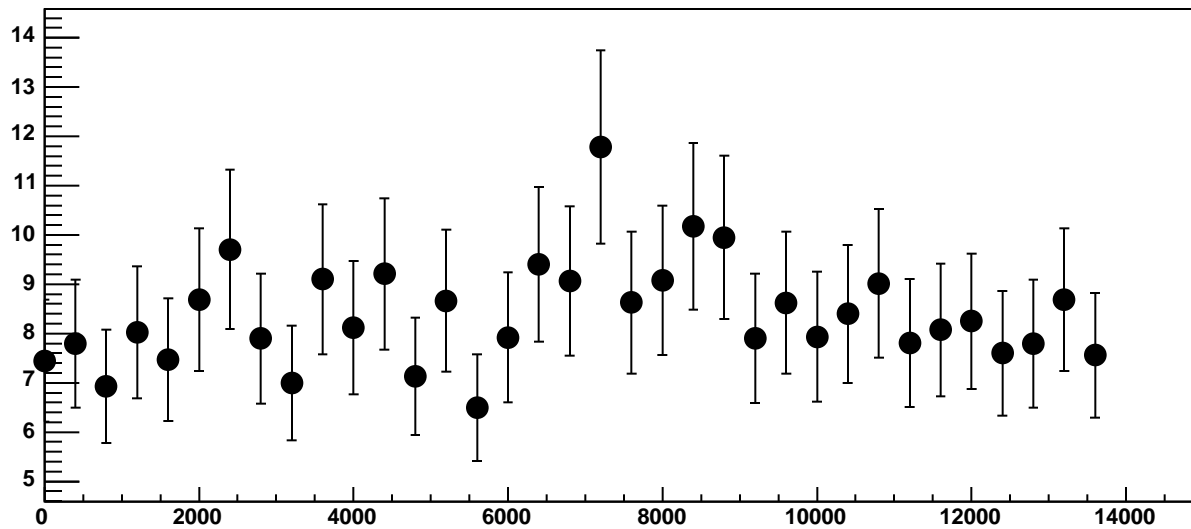


Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

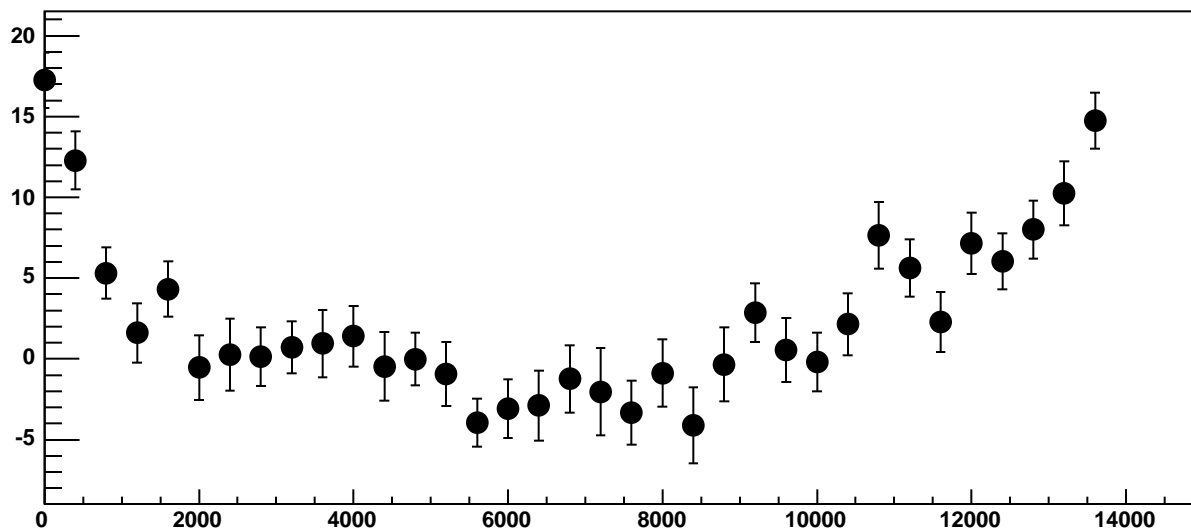


$\chi^2 / \text{ndf}$  44.69 / 23  
p0 -0.7803 ± 0.8651  
p1 0.0165 ± 0.0001335

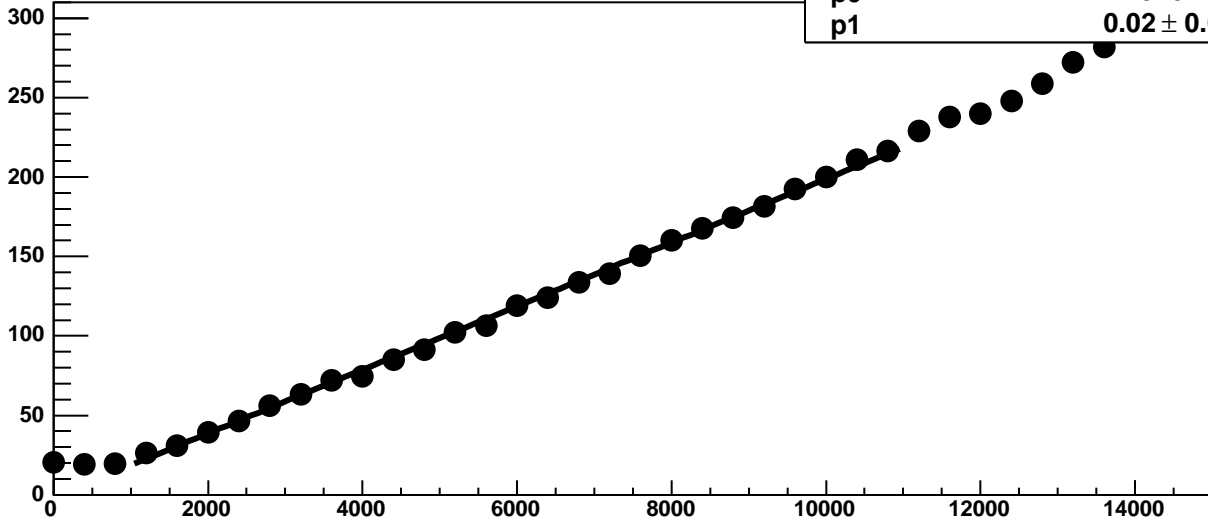
Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

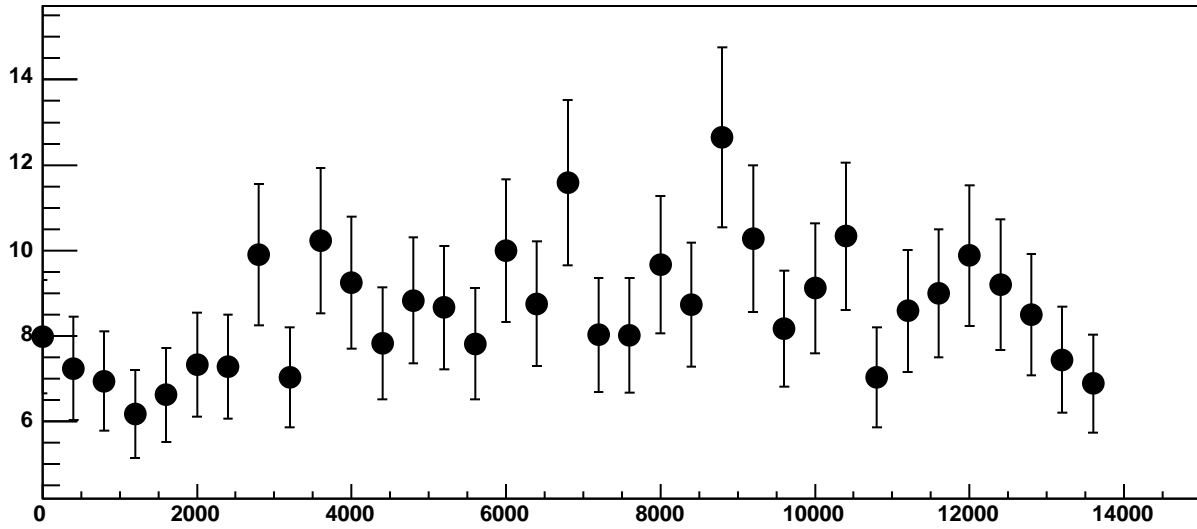


Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

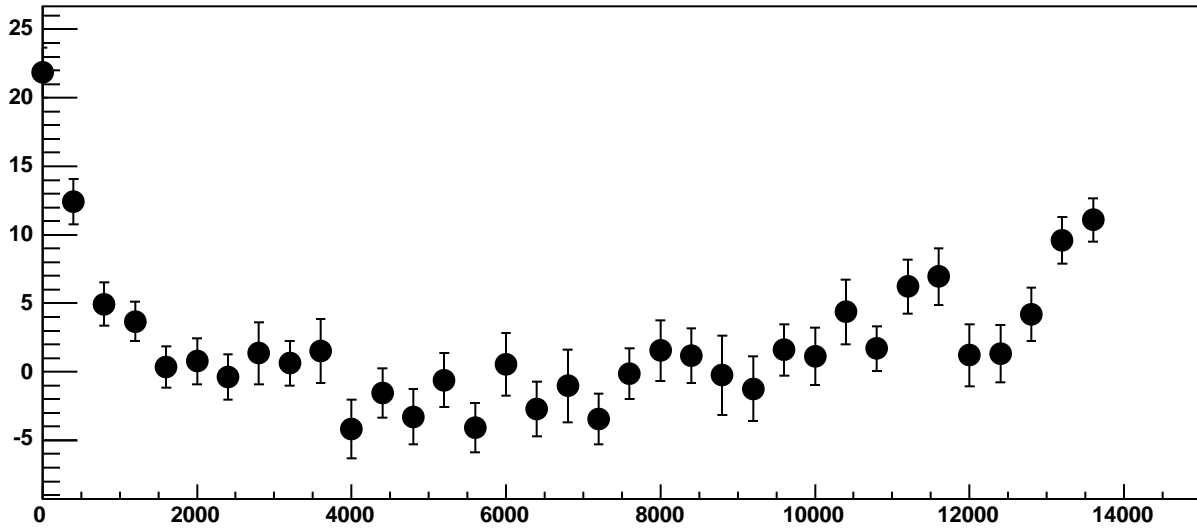


$\chi^2 / \text{ndf}$  32.66 / 23  
p0  $-1.316 \pm 0.7962$   
p1  $0.02 \pm 0.000126$

Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

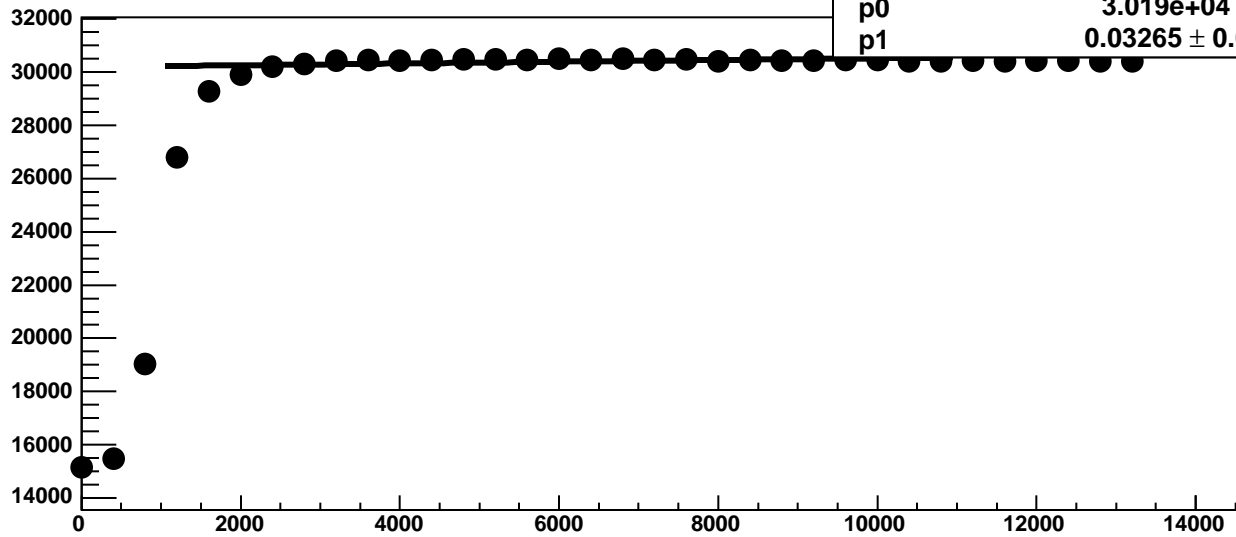
3880 / 23

p0

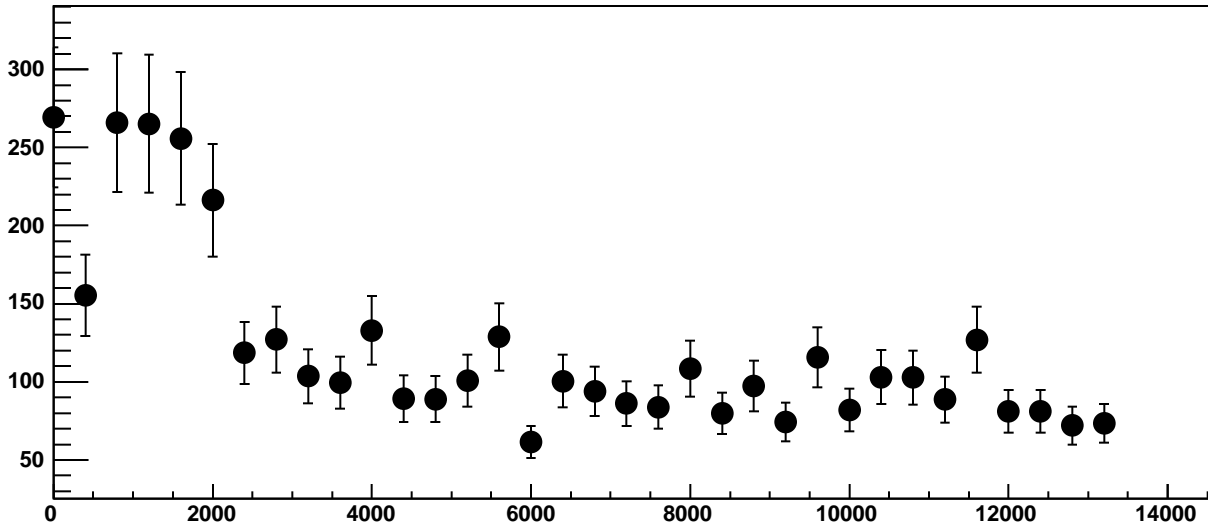
$3.019\text{e}+04 \pm 13.41$

p1

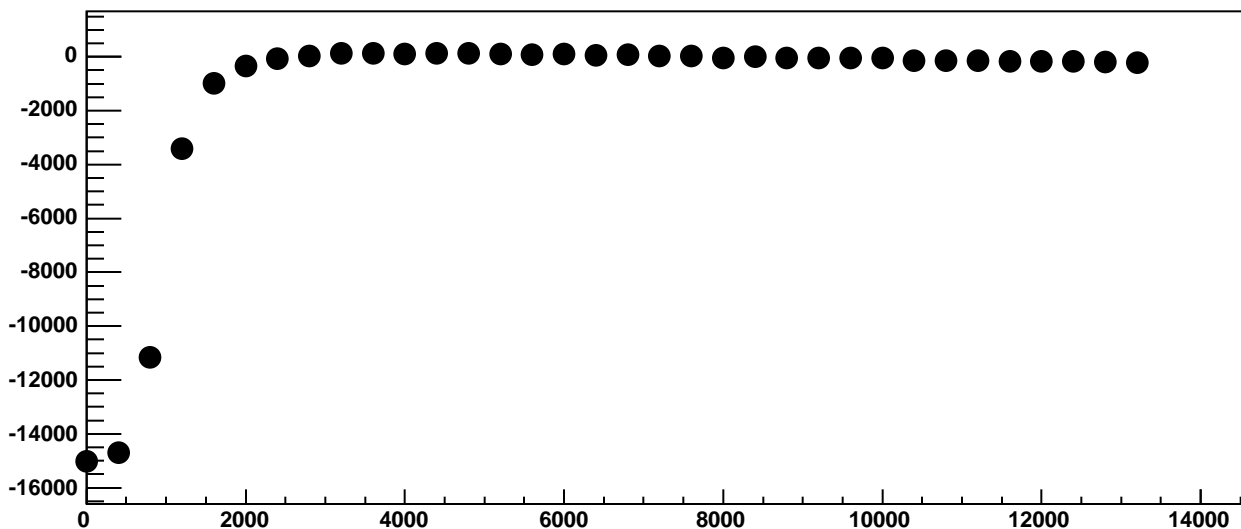
$0.03265 \pm 0.001867$



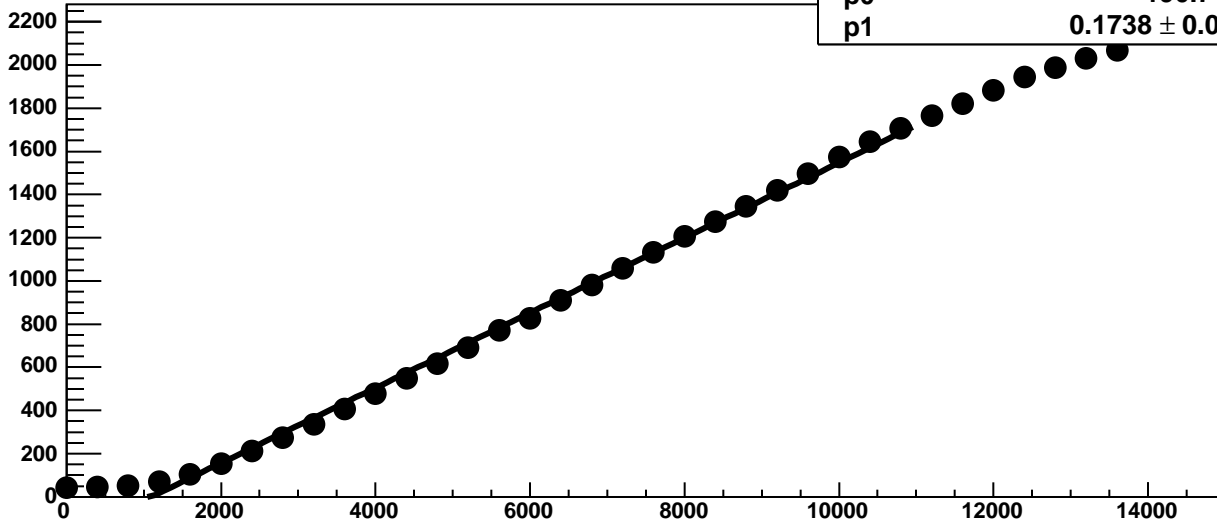
Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1461 / 23

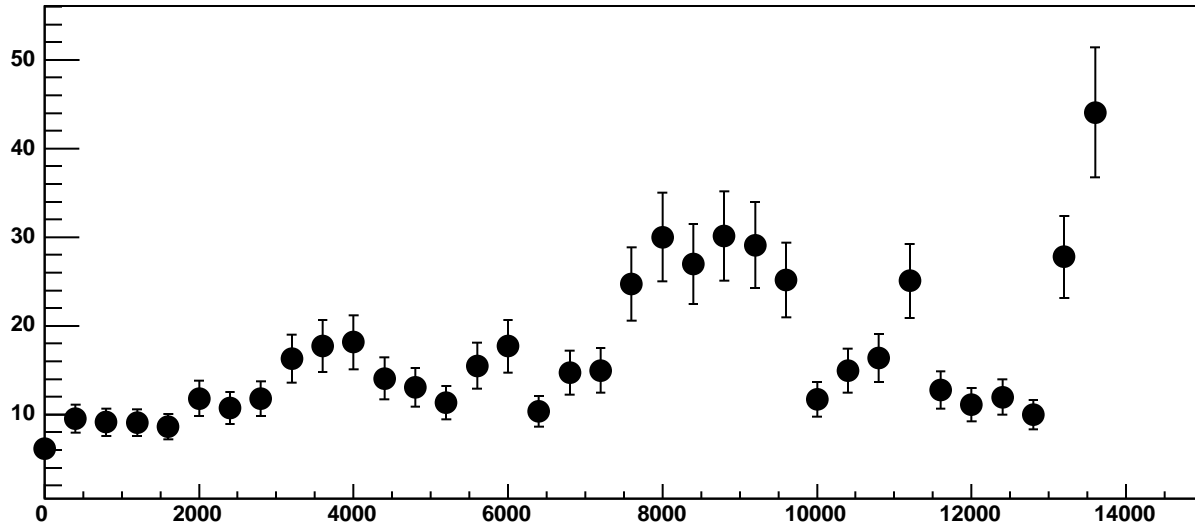
p0

$-190.7 \pm 1.234$

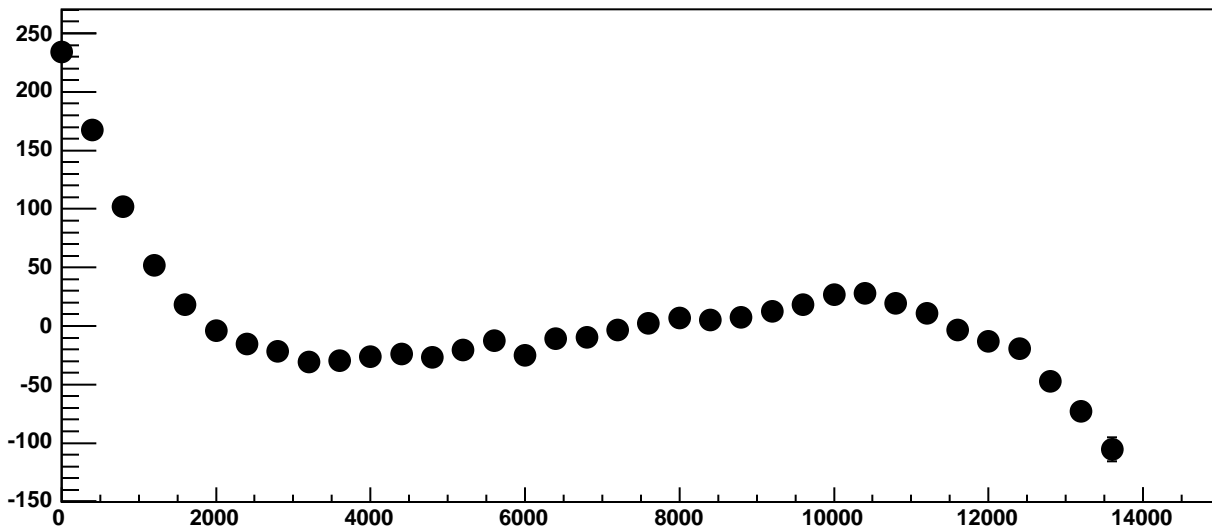
p1

$0.1738 \pm 0.0002175$

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

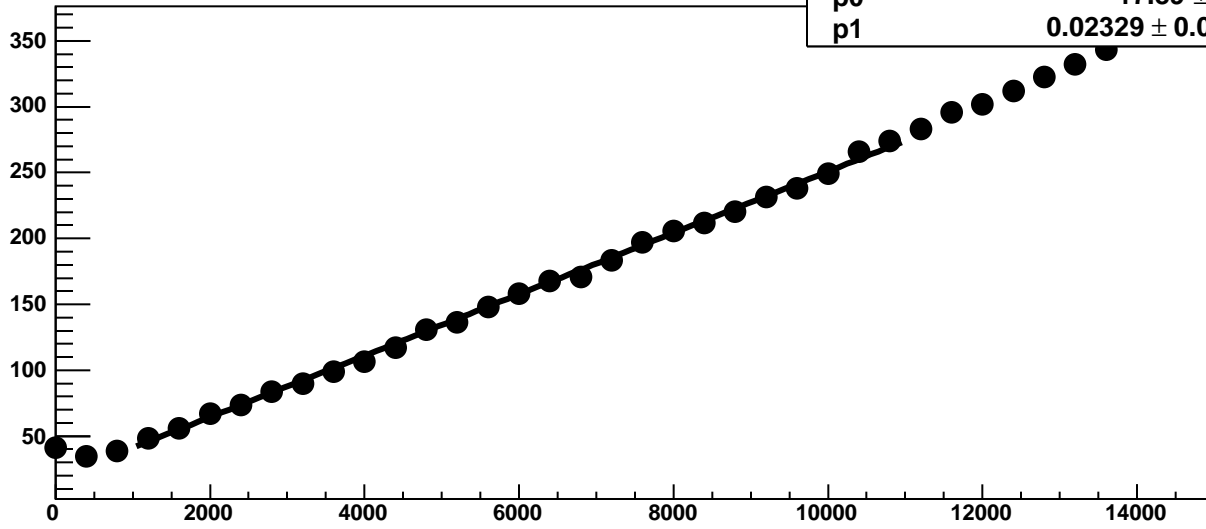


Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



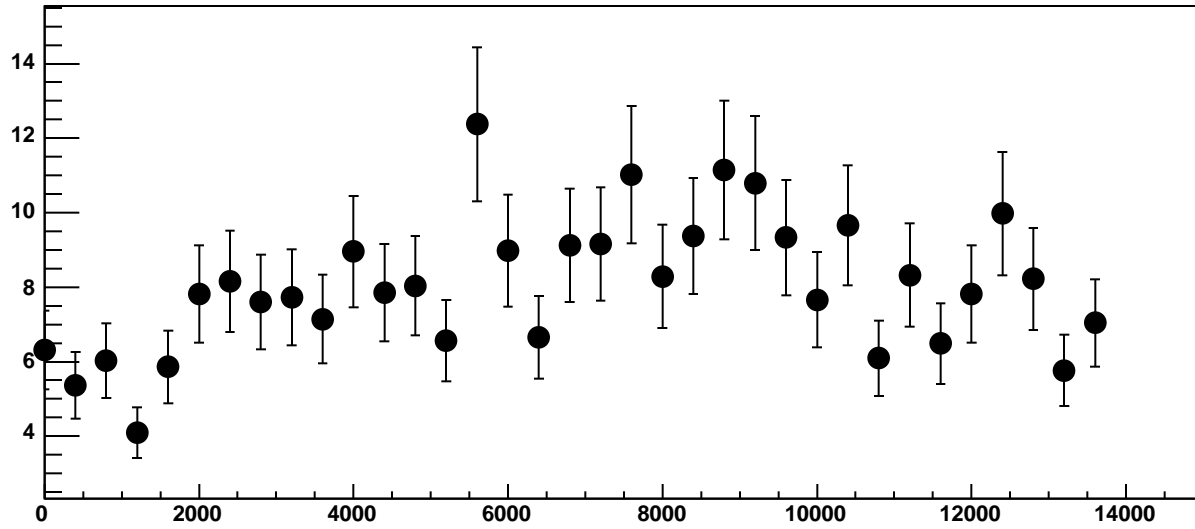


Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

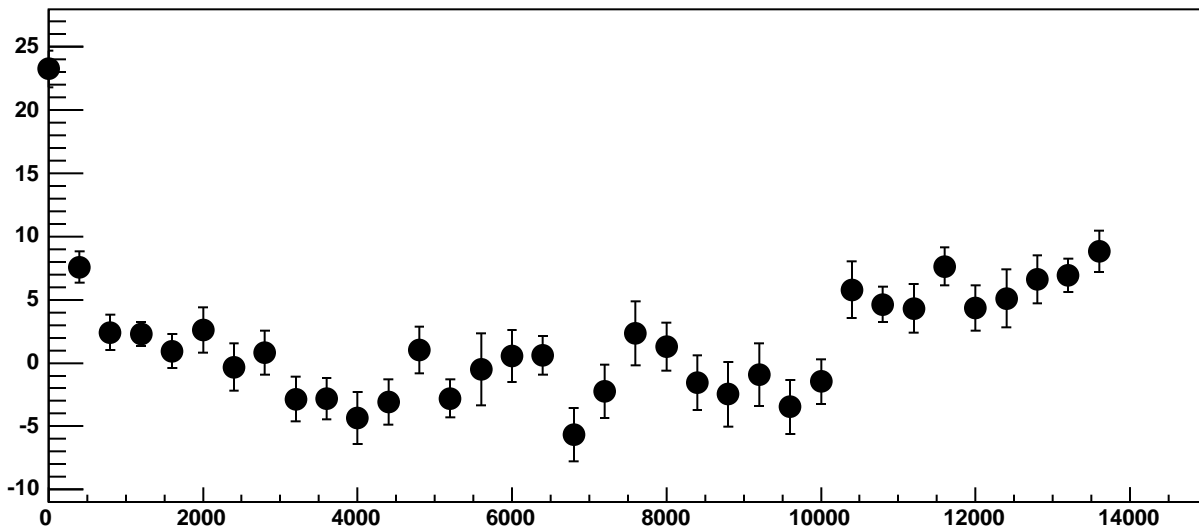


$\chi^2 / \text{ndf}$  58.38 / 23  
p0 17.89 ± 0.6784  
p1 0.02329 ± 0.0001113

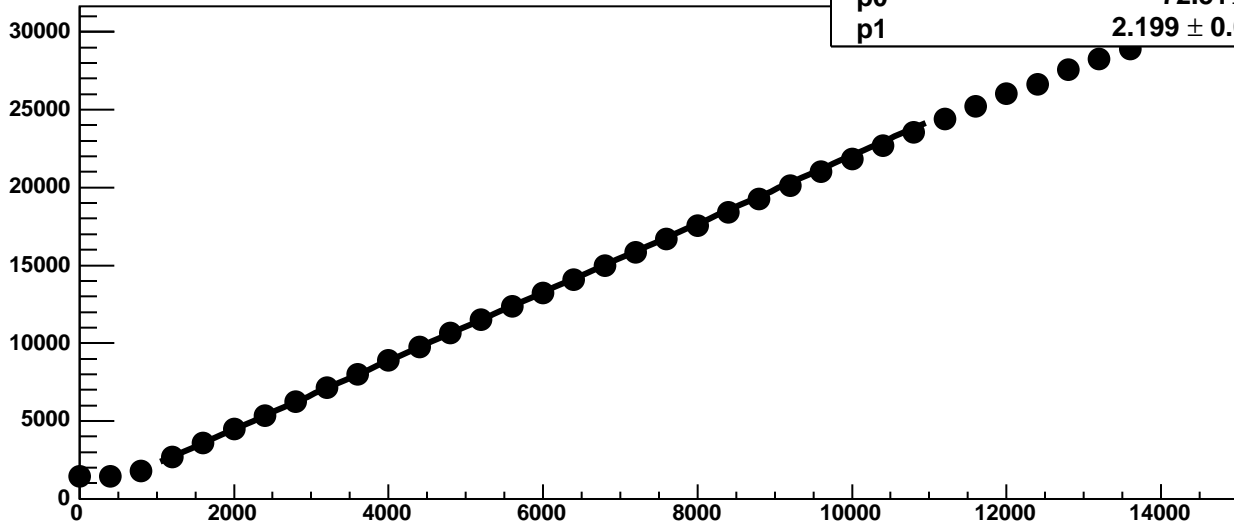
Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



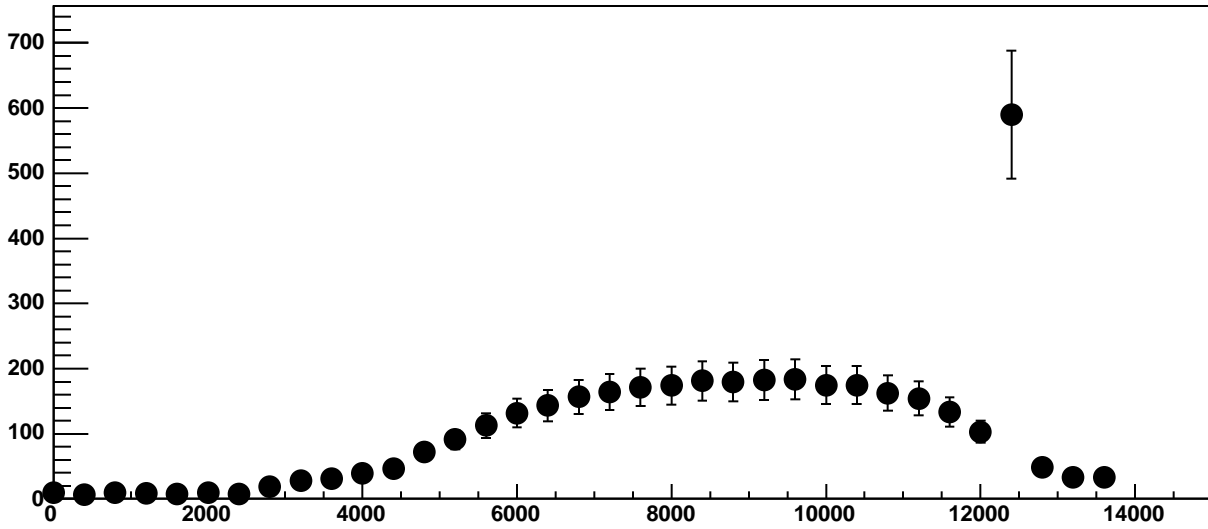
Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



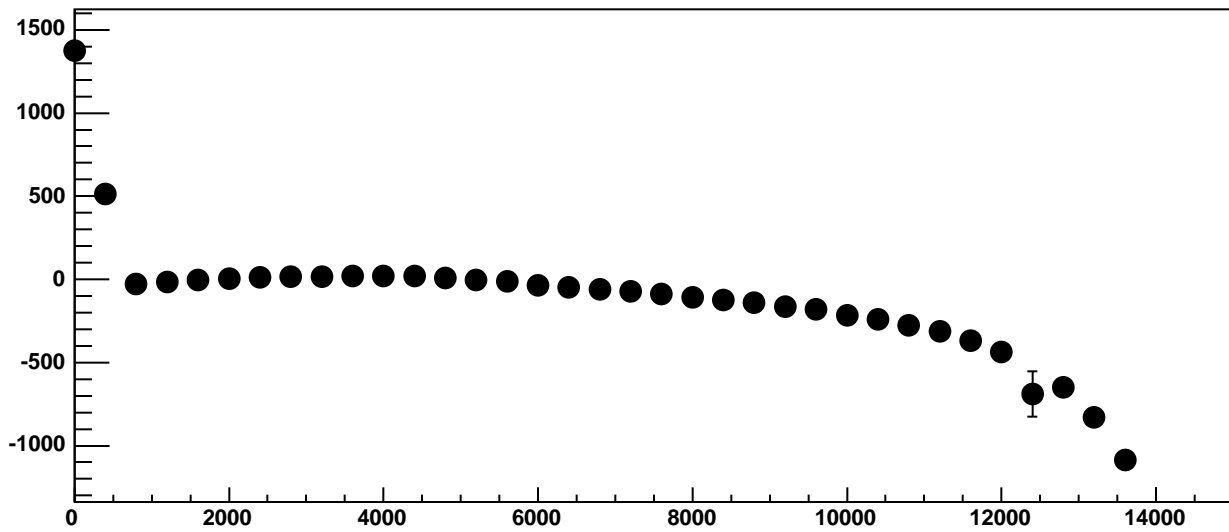
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



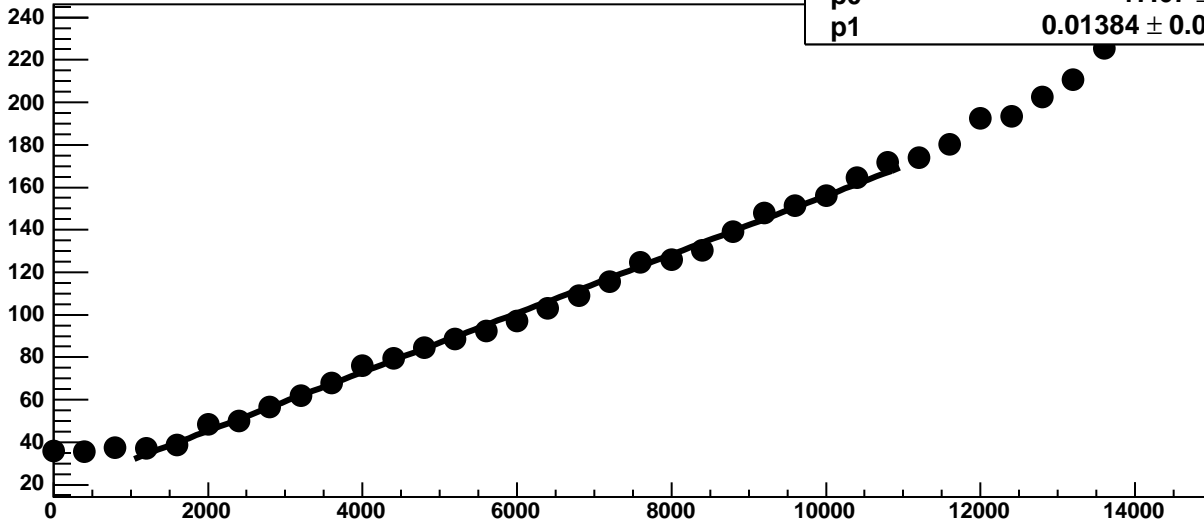
Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

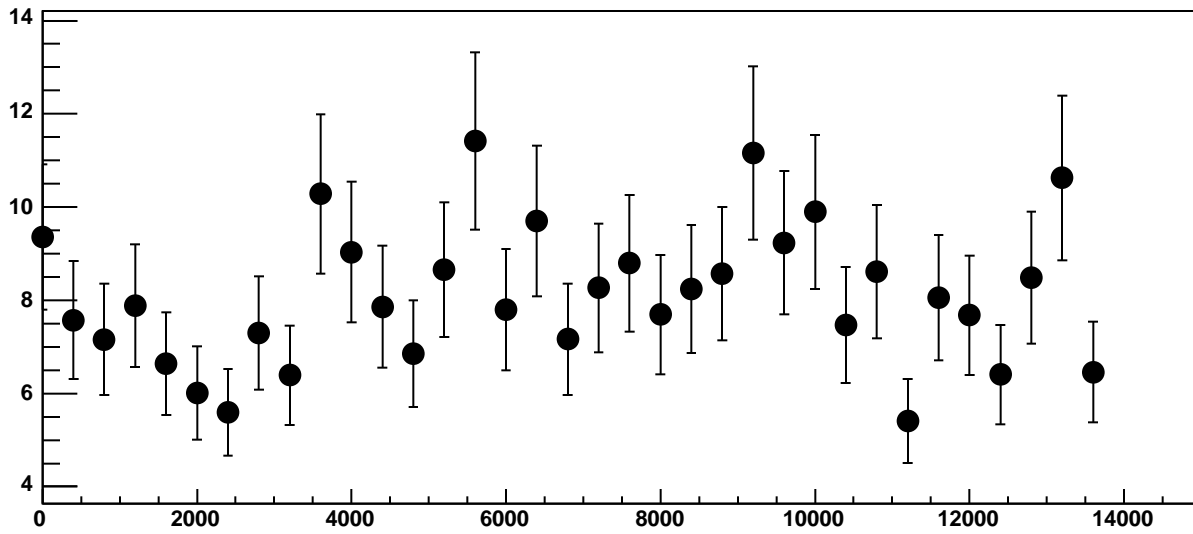


Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

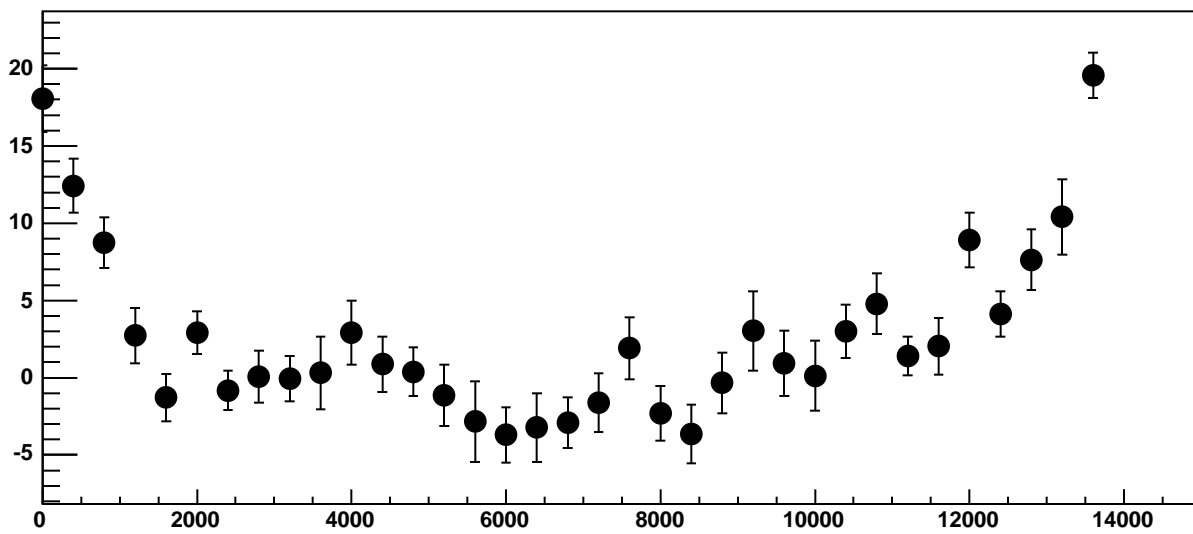


$\chi^2 / \text{ndf}$  38.8 / 23  
p0  $17.67 \pm 0.7591$   
p1  $0.01384 \pm 0.0001228$

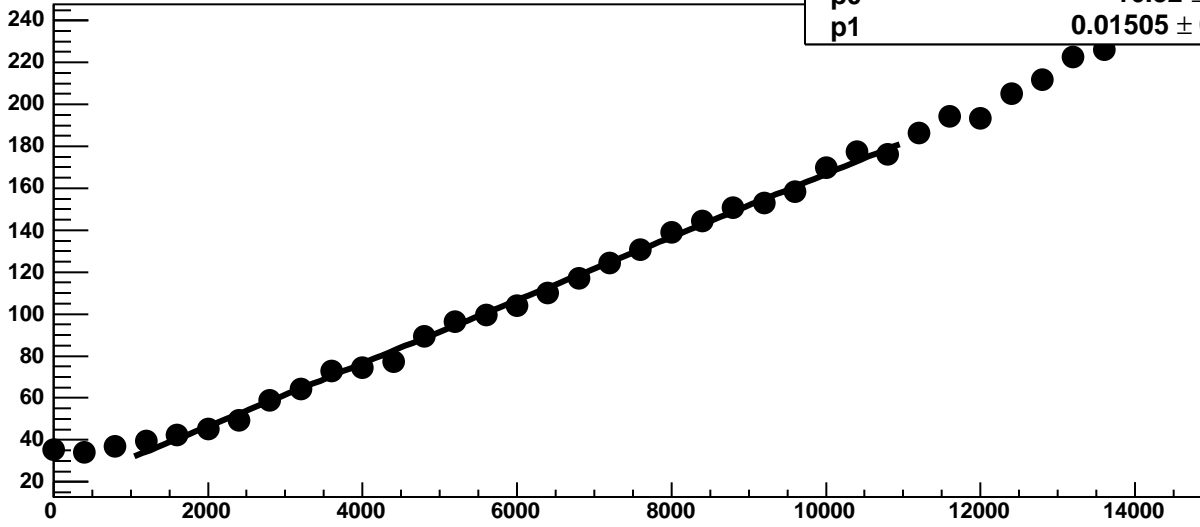
Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

56.66 / 23

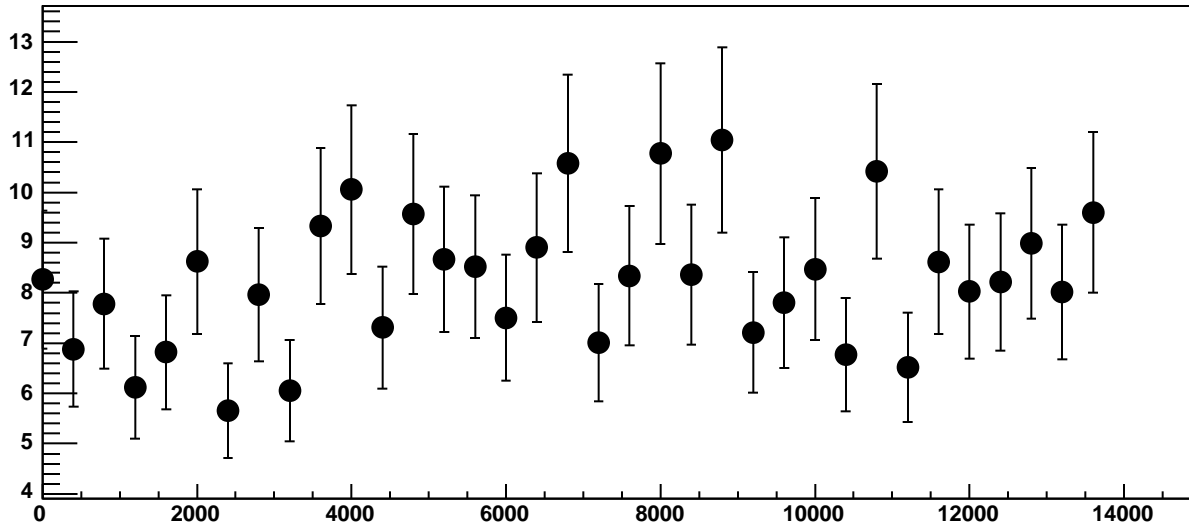
p0

$16.32 \pm 0.7606$

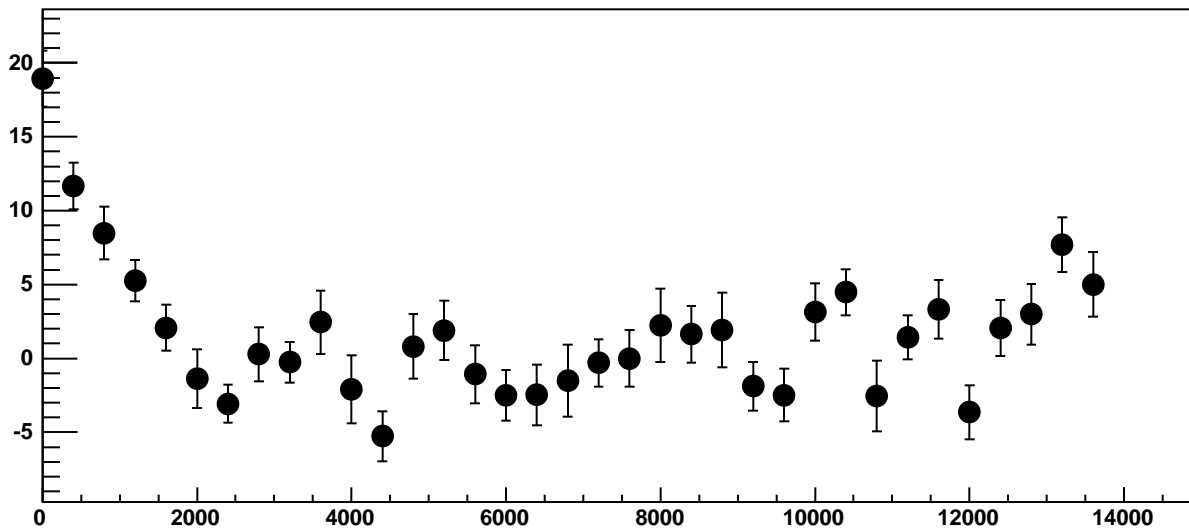
p1

$0.01505 \pm 0.00012$

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

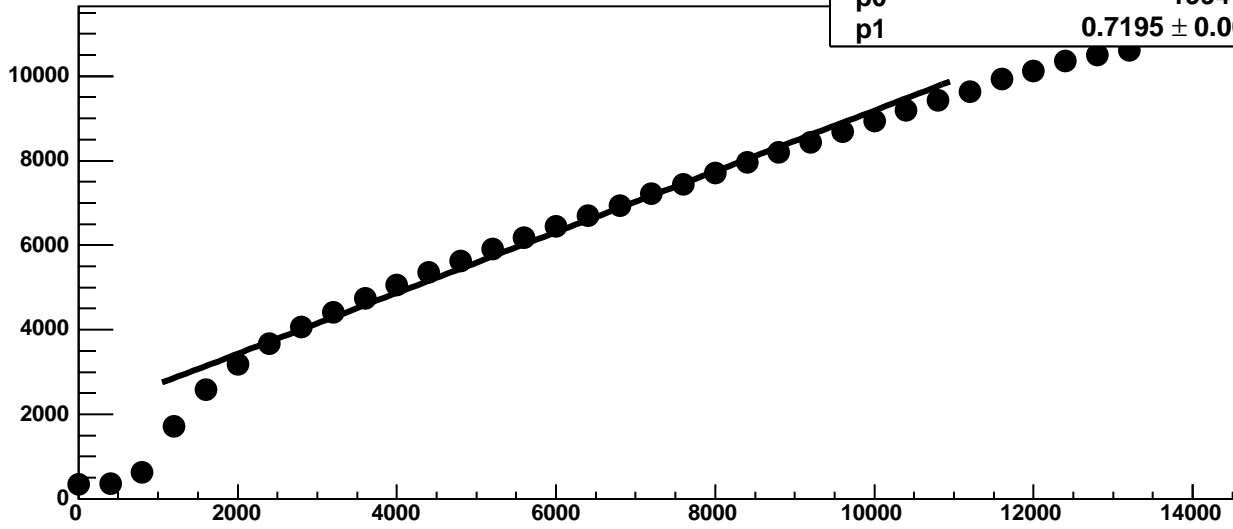


Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

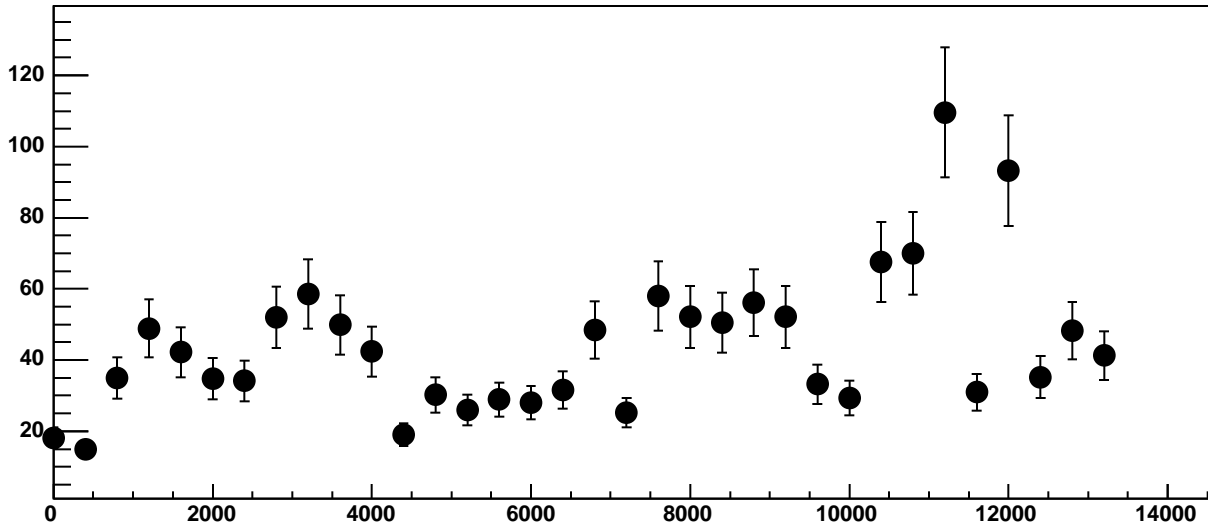


Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

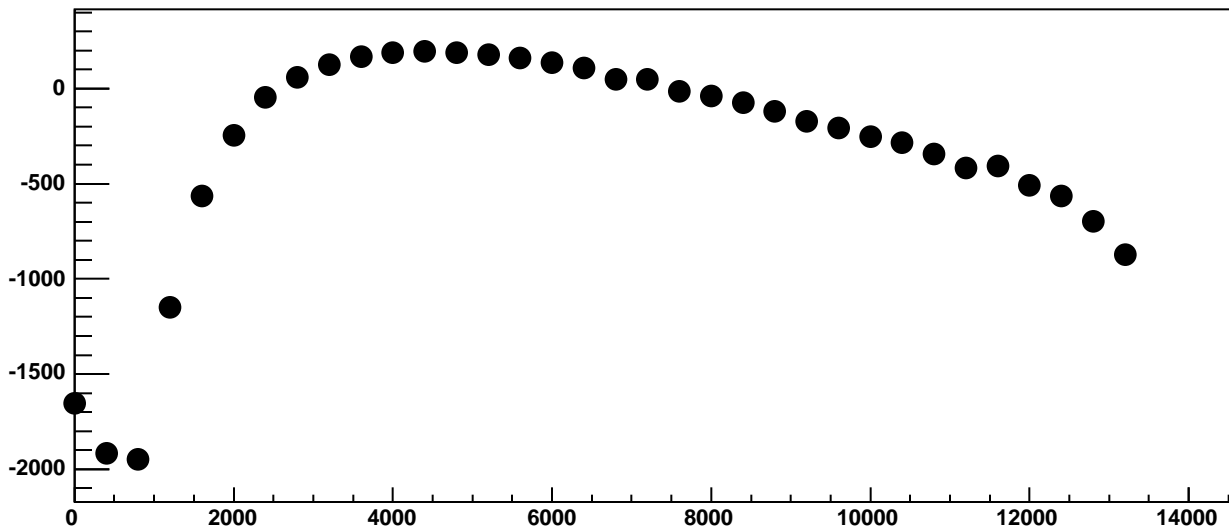
$\chi^2 / \text{ndf}$  2.386e+04 / 23  
p0 1994 ± 4.18  
p1 0.7195 ± 0.0006727



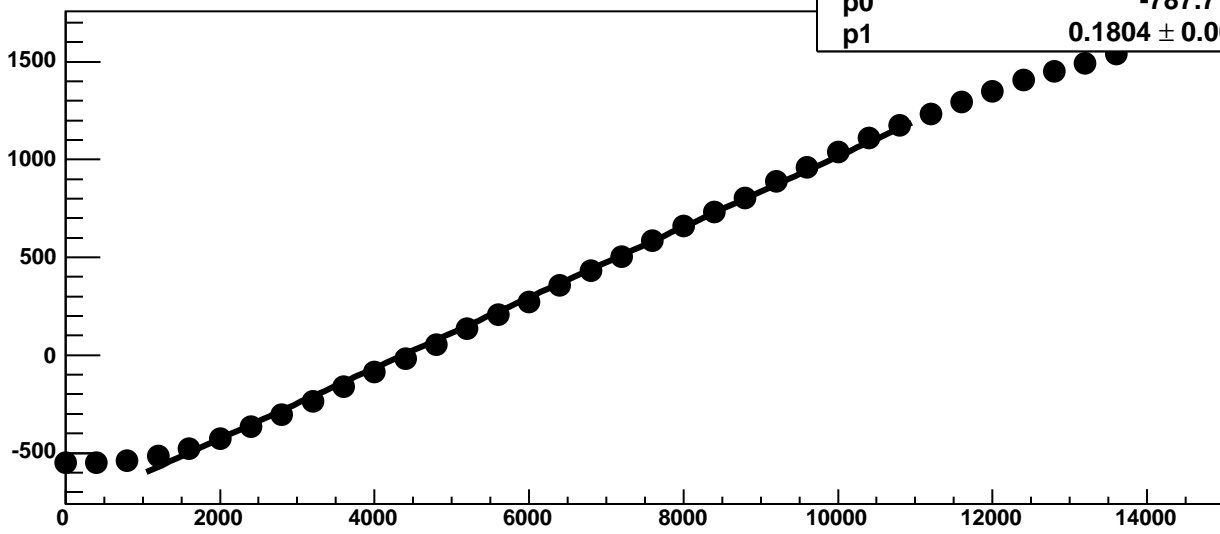
Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

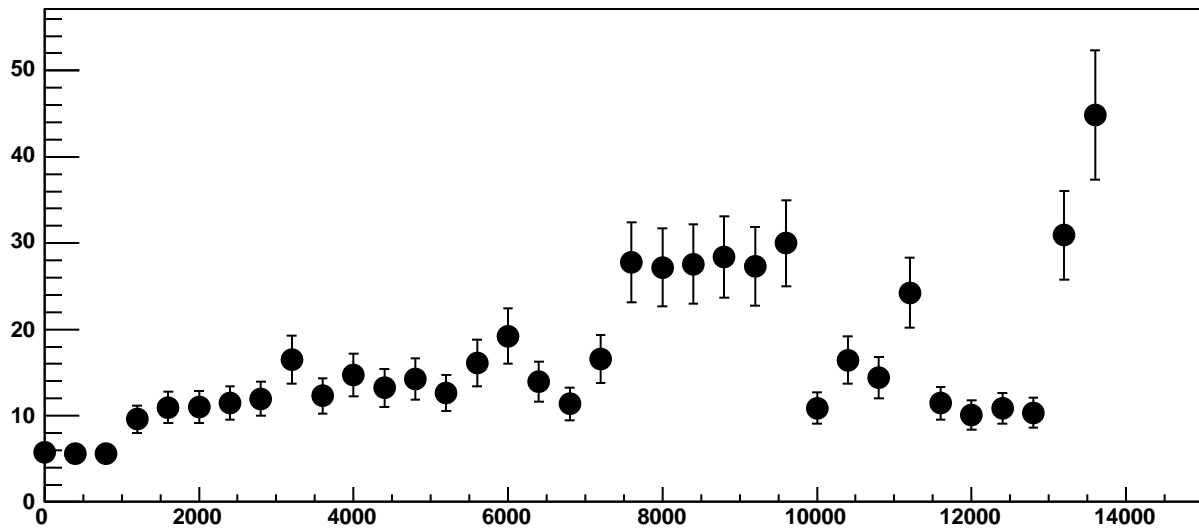


Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

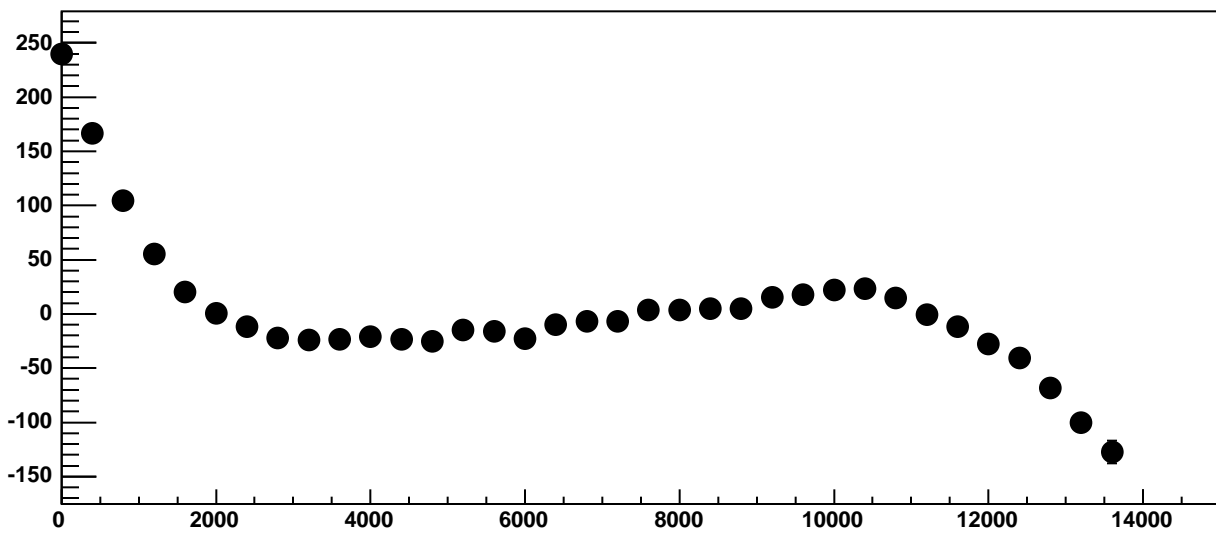


$\chi^2 / \text{ndf}$  1286 / 23  
p0  $-787.7 \pm 1.281$   
p1  $0.1804 \pm 0.0002203$

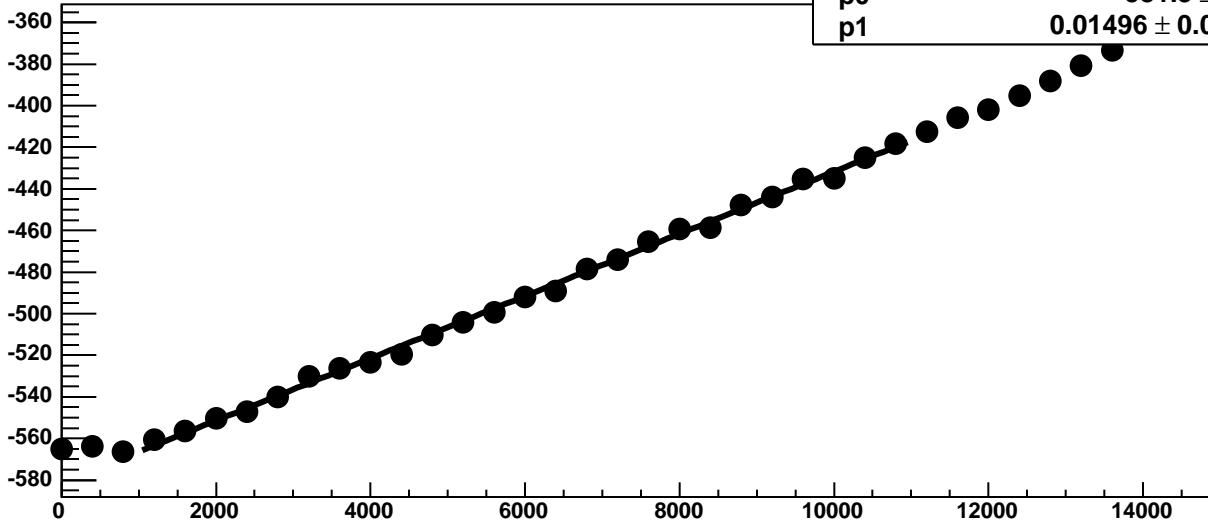
Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

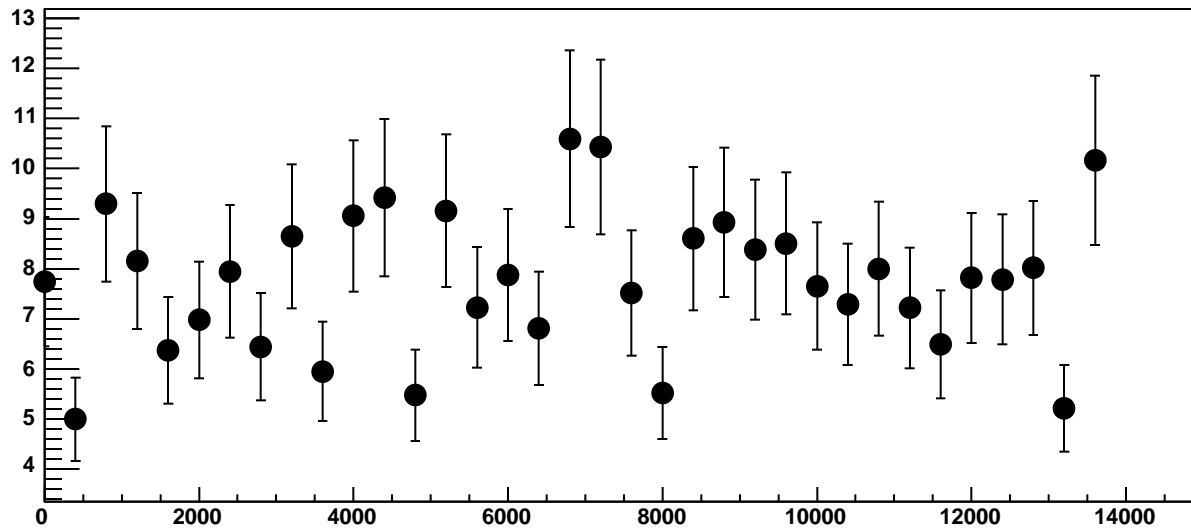


Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

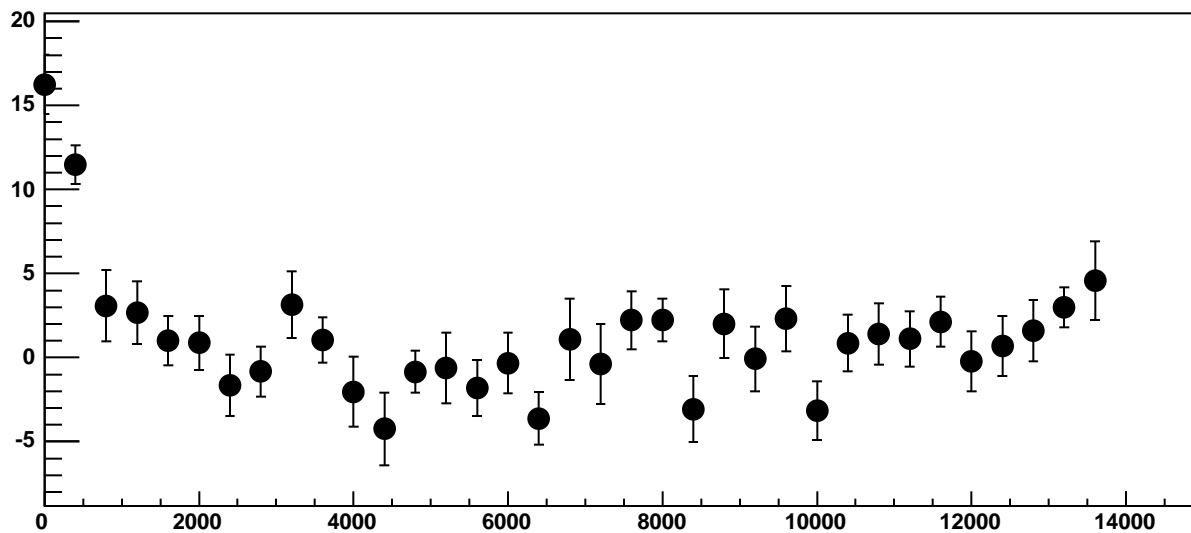


$\chi^2 / \text{ndf}$  32.83 / 23  
p0  $-581.3 \pm 0.7733$   
p1  $0.01496 \pm 0.0001195$

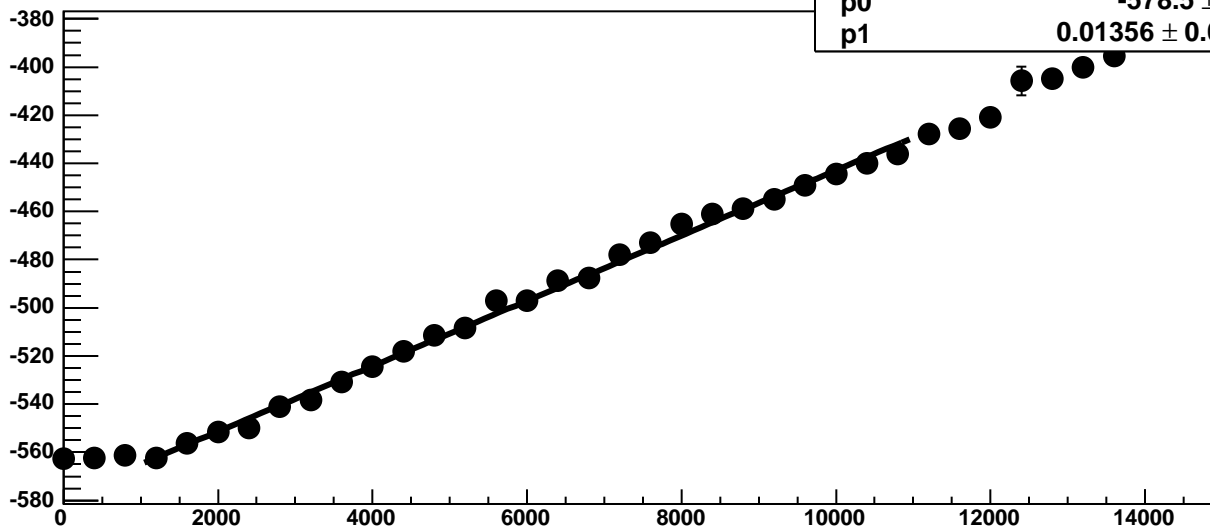
Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



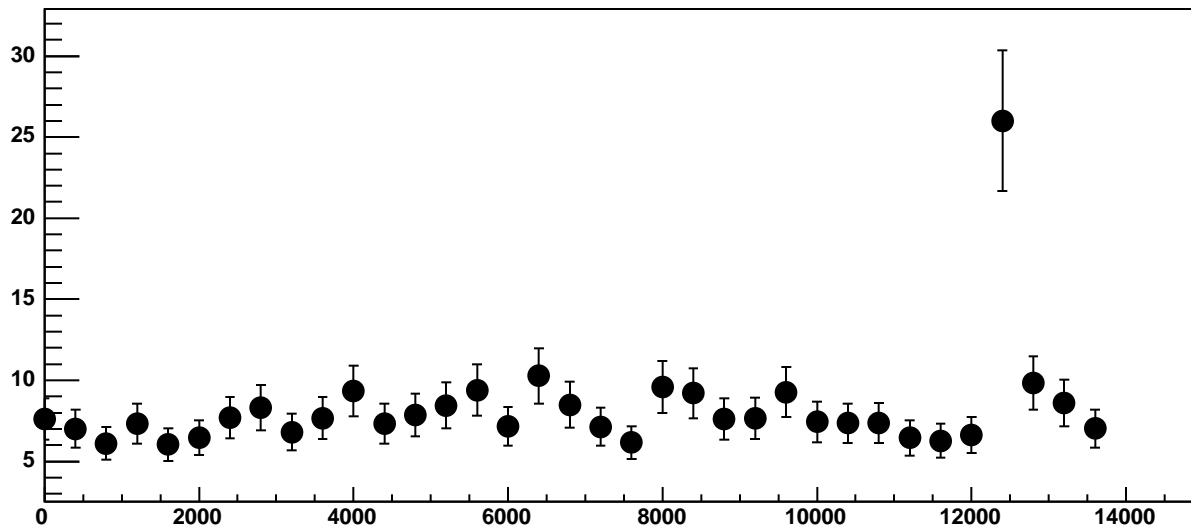
Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



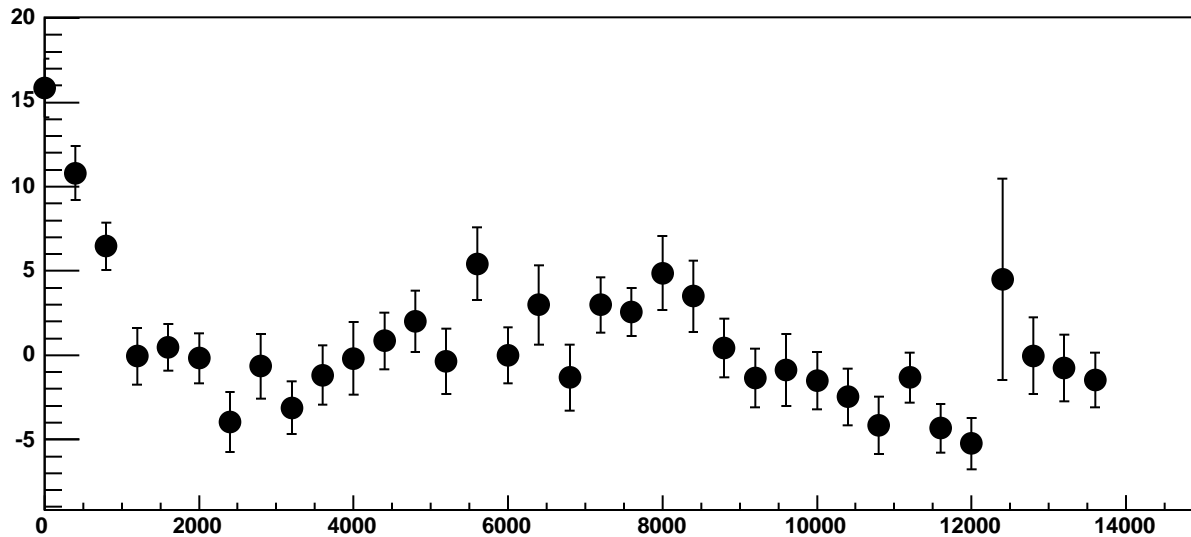
Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

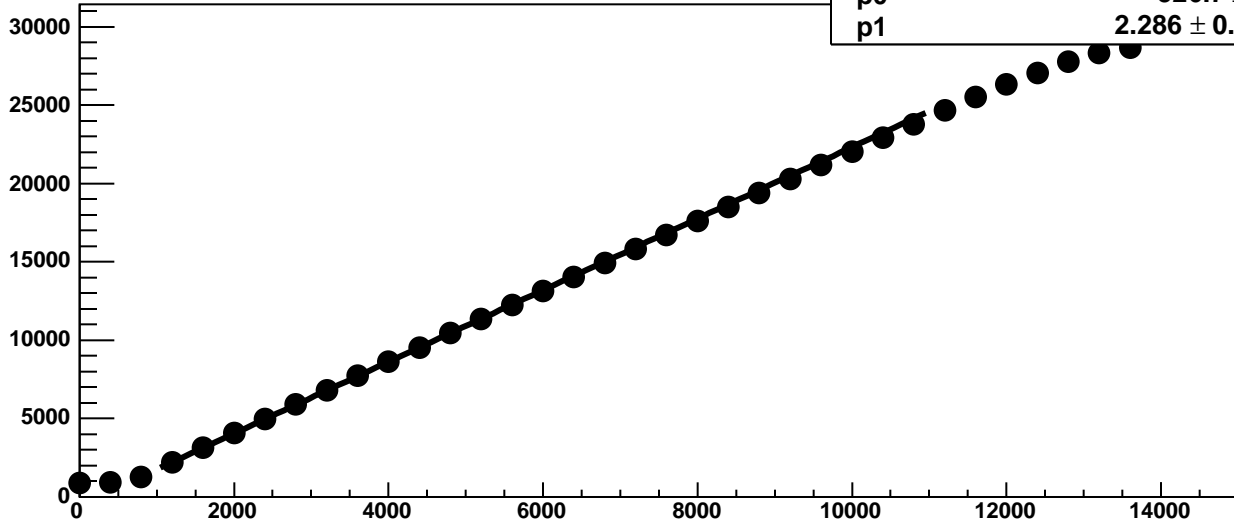


Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

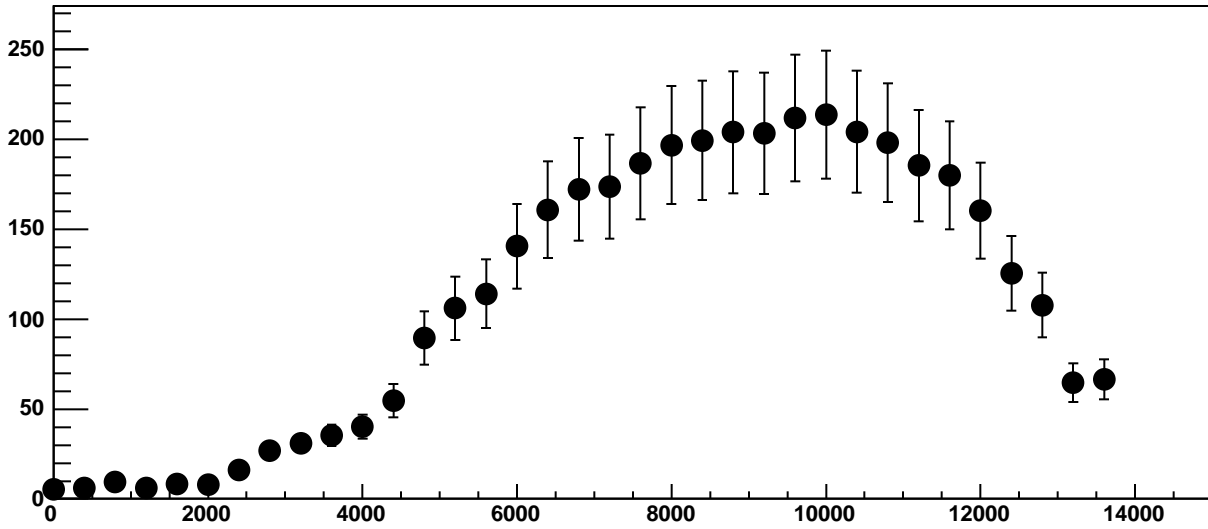




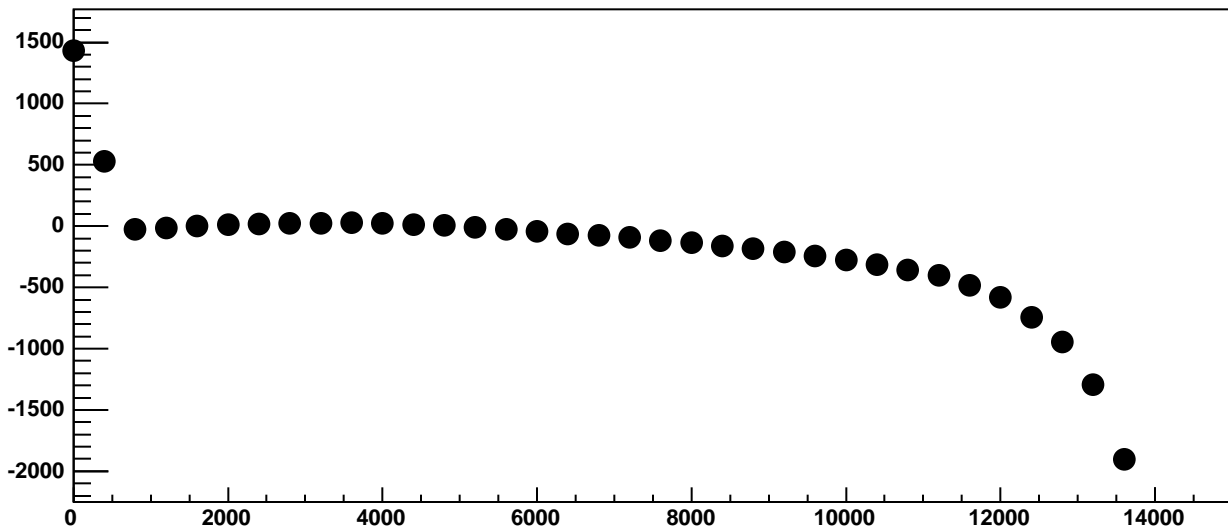
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



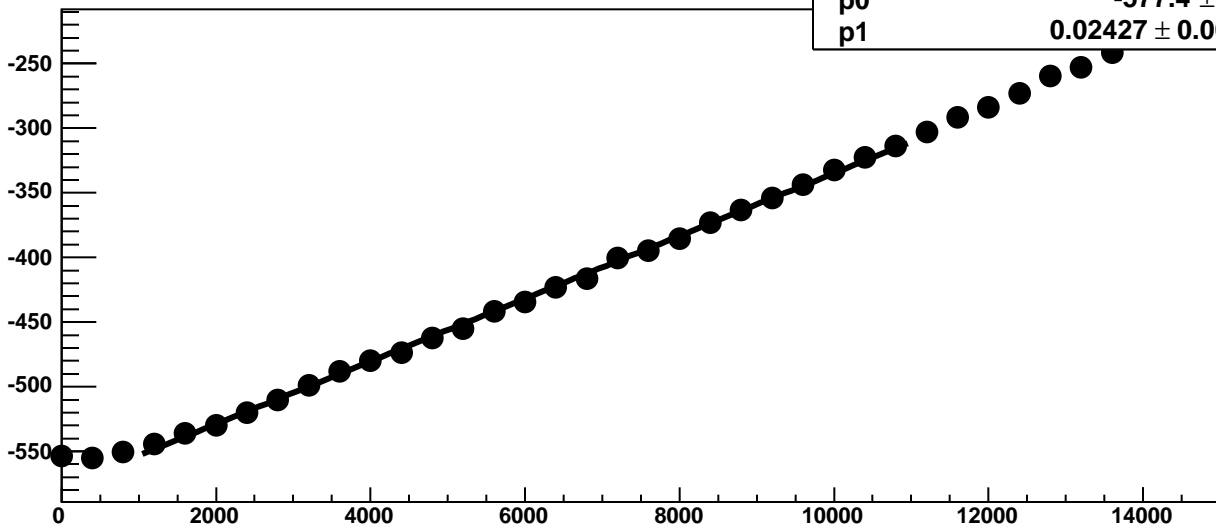
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



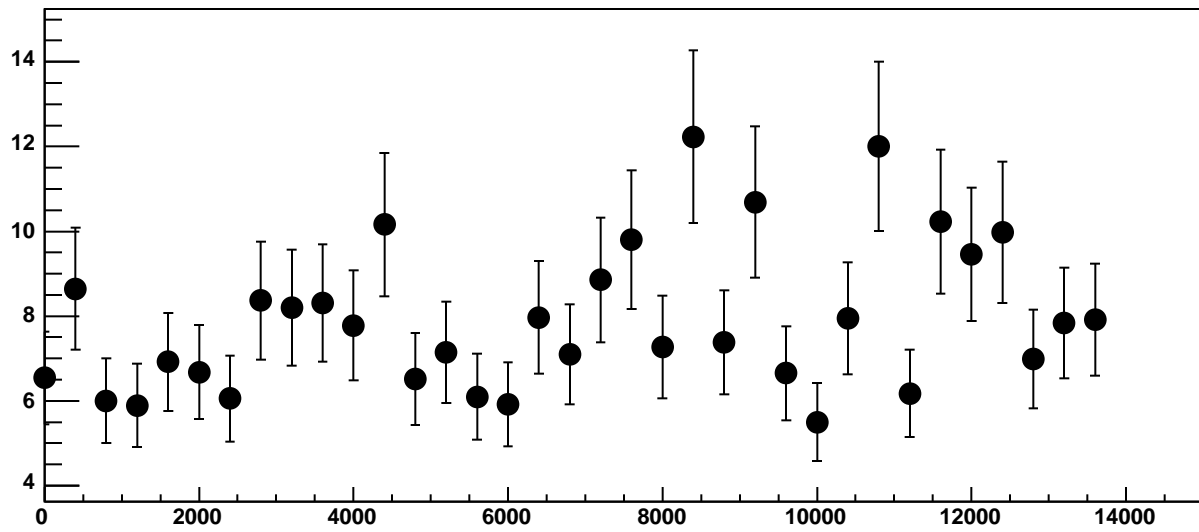
Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC



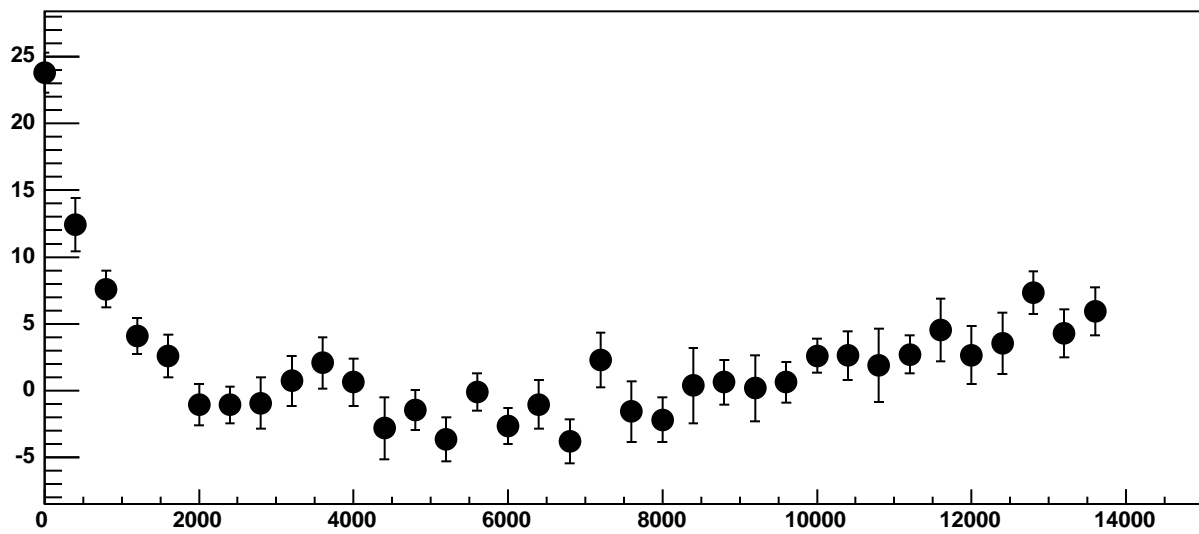
Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



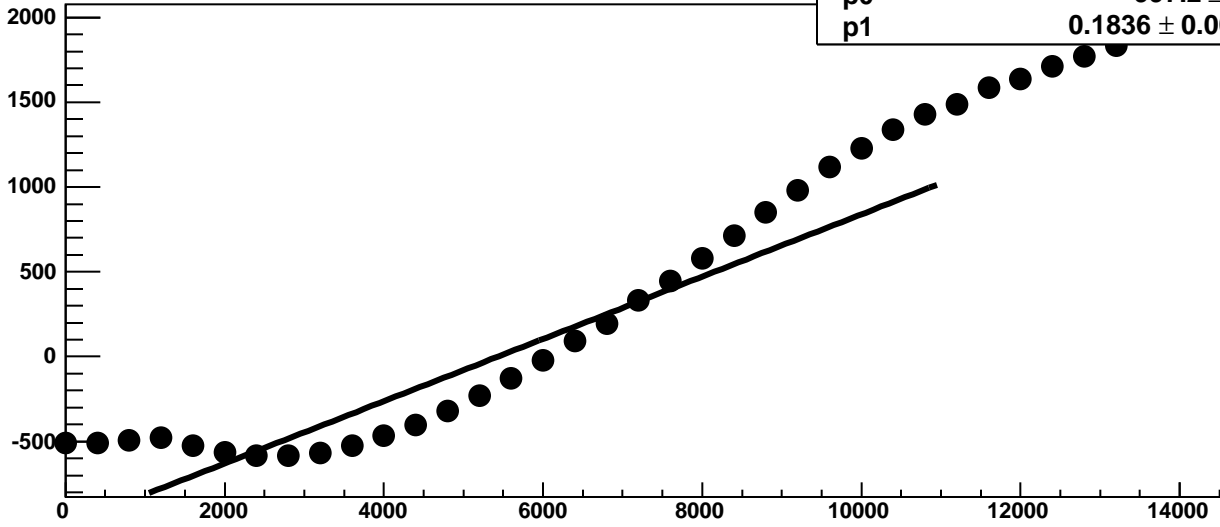
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



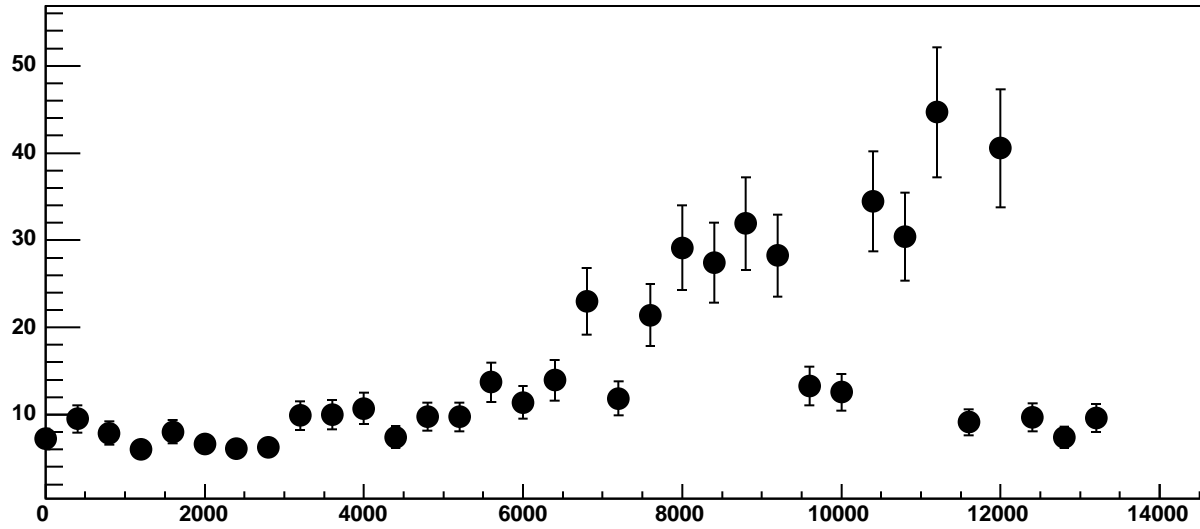
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



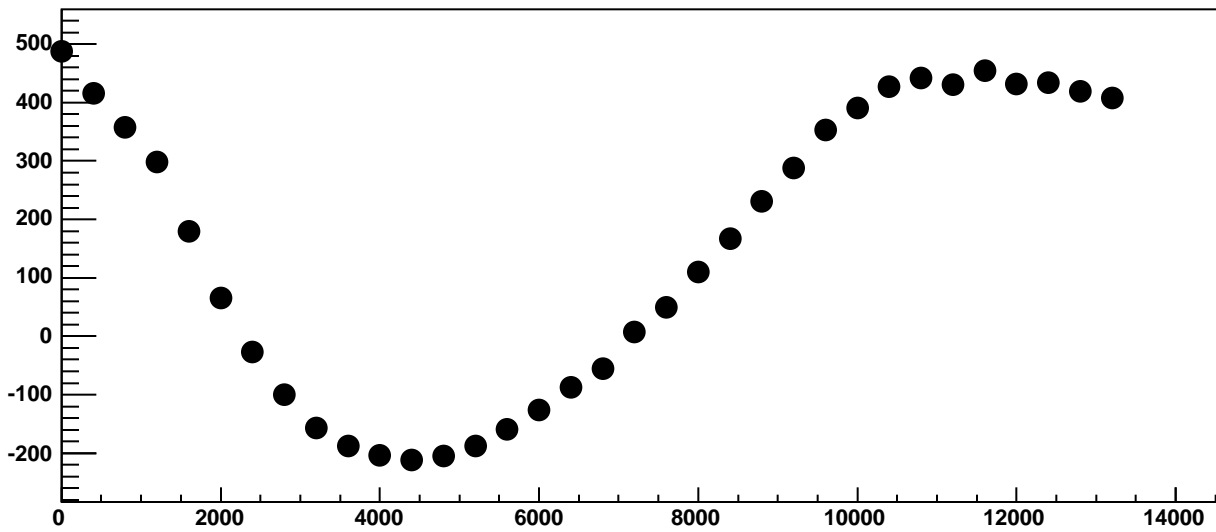
Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



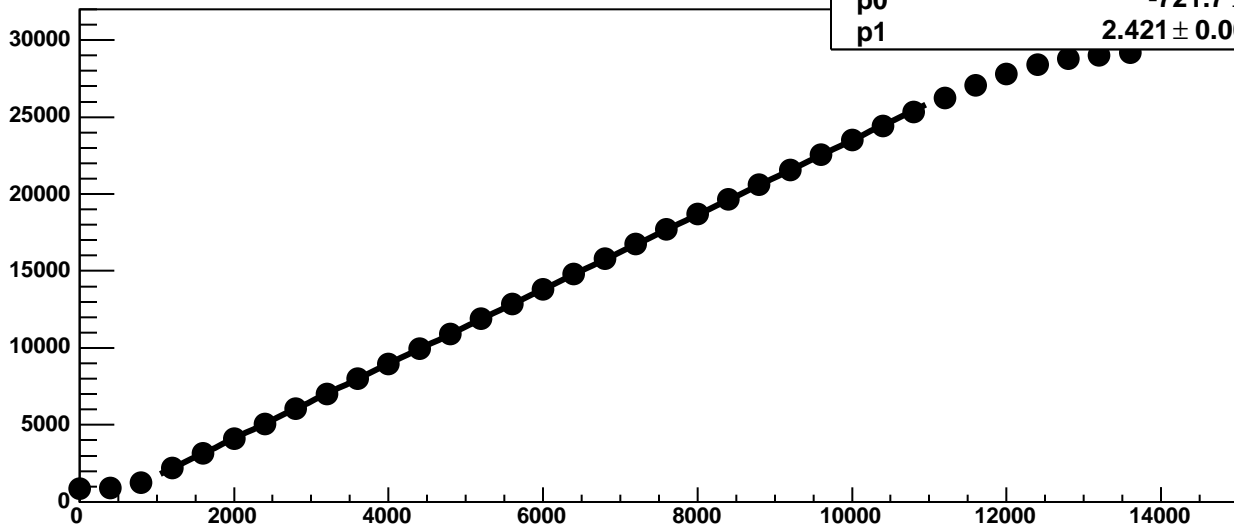
Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



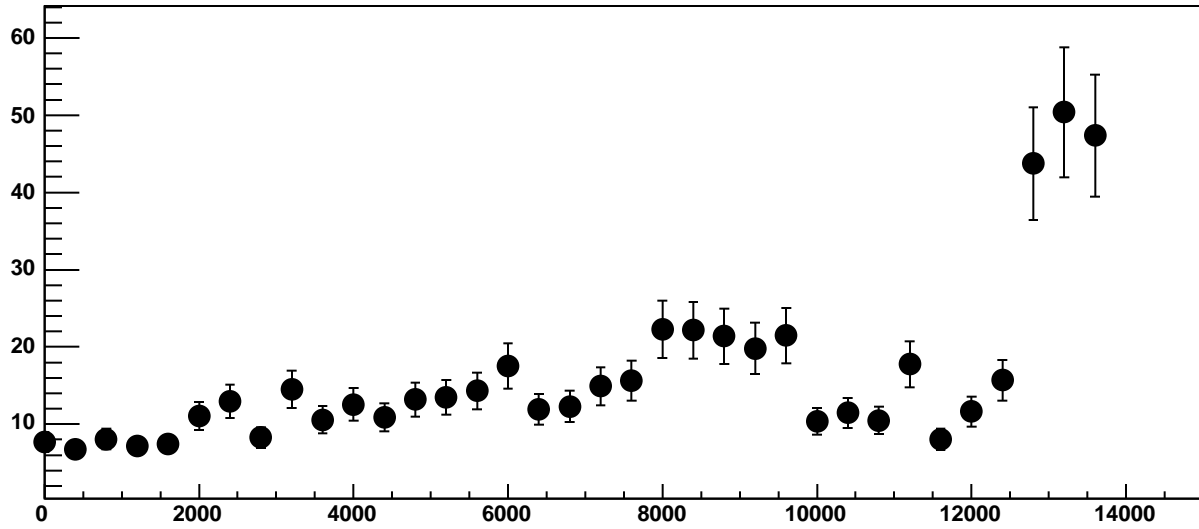
Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



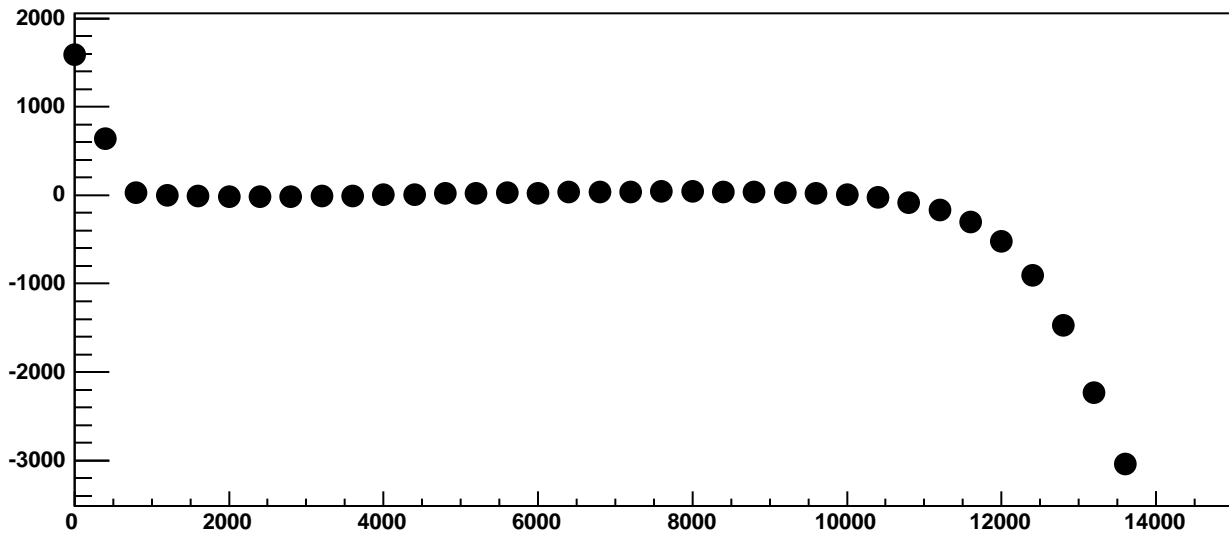
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



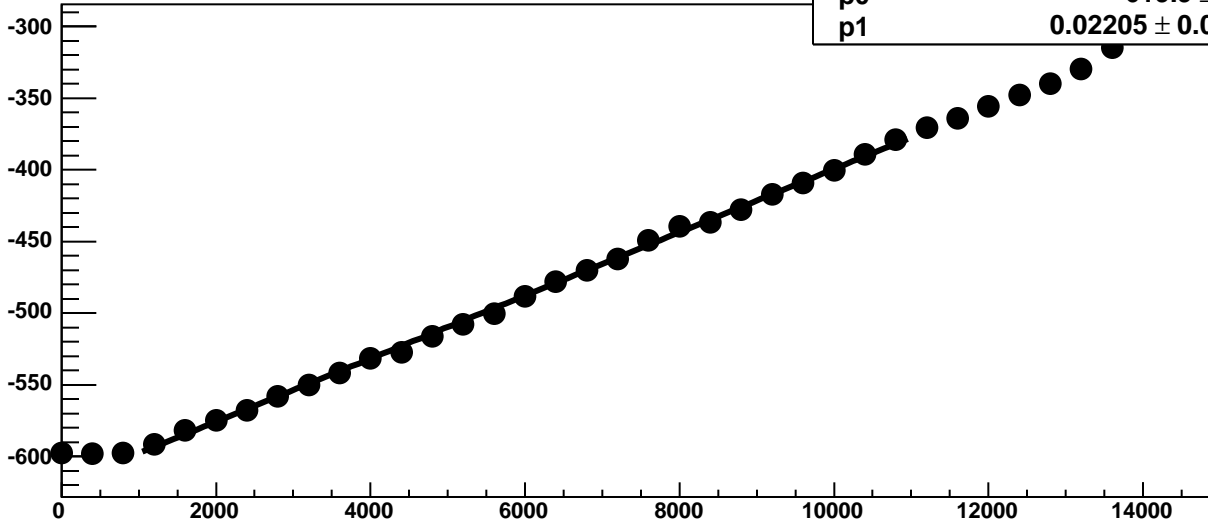
Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

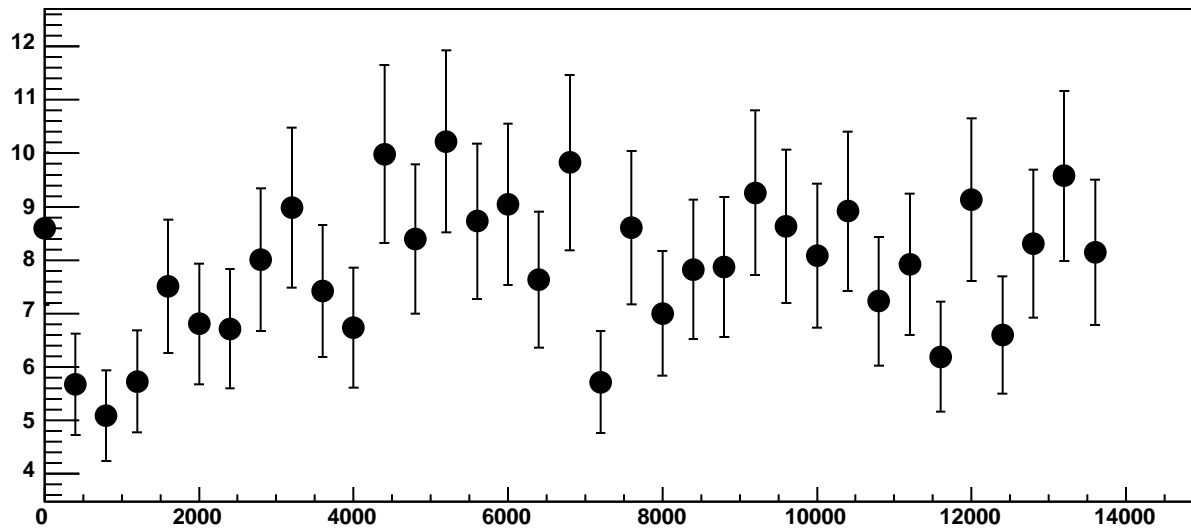


Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

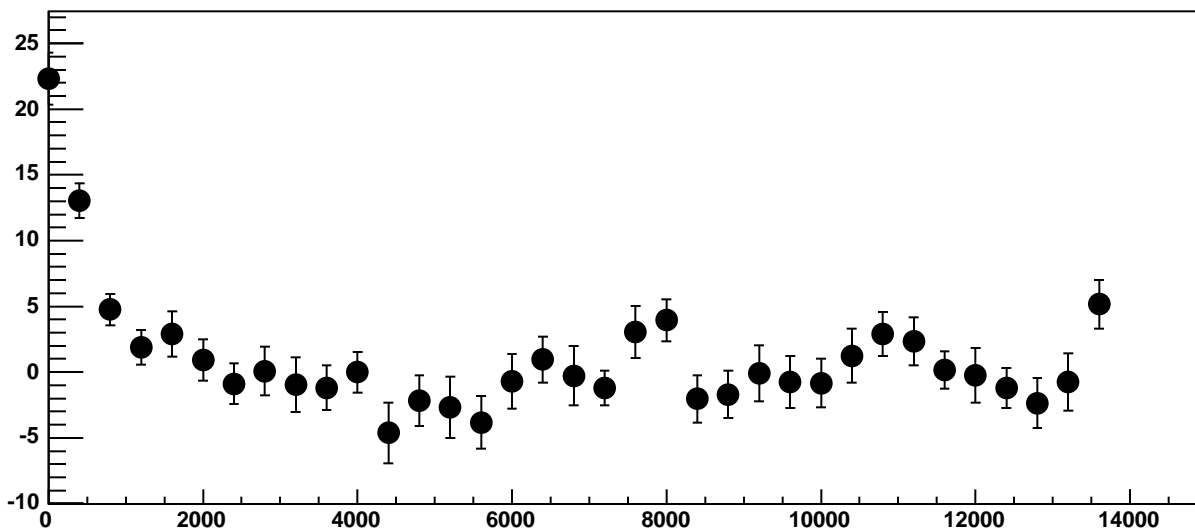


$\chi^2 / \text{ndf}$  32.16 / 23  
p0  $-619.9 \pm 0.7645$   
p1  $0.02205 \pm 0.0001183$

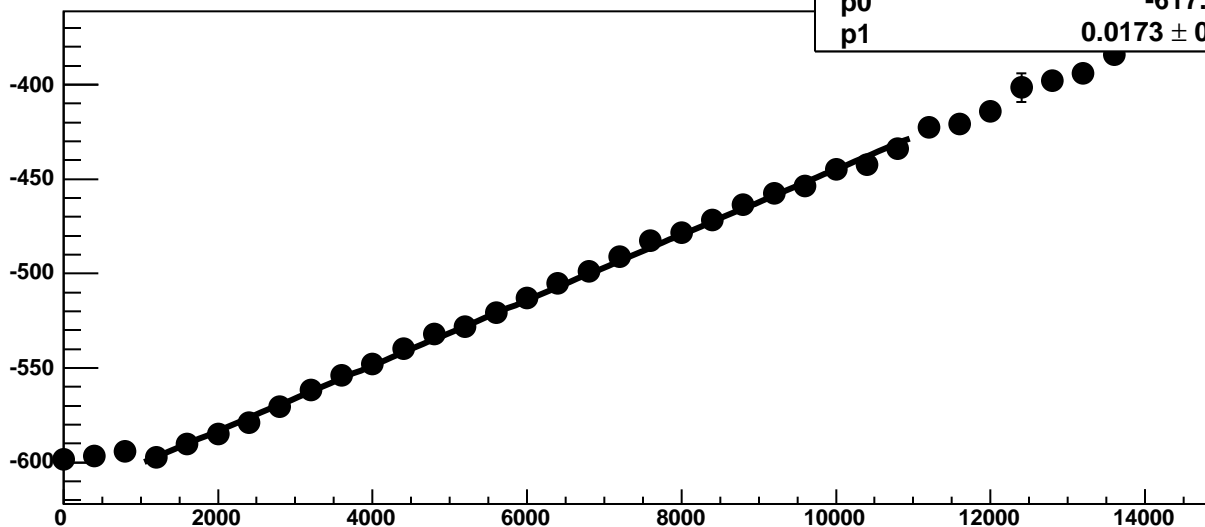
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



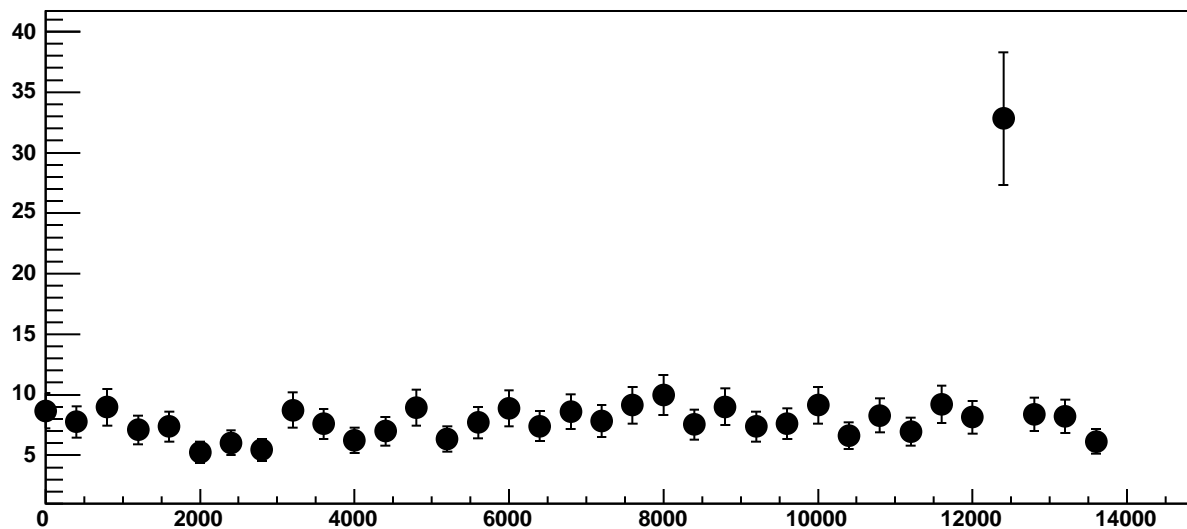
Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



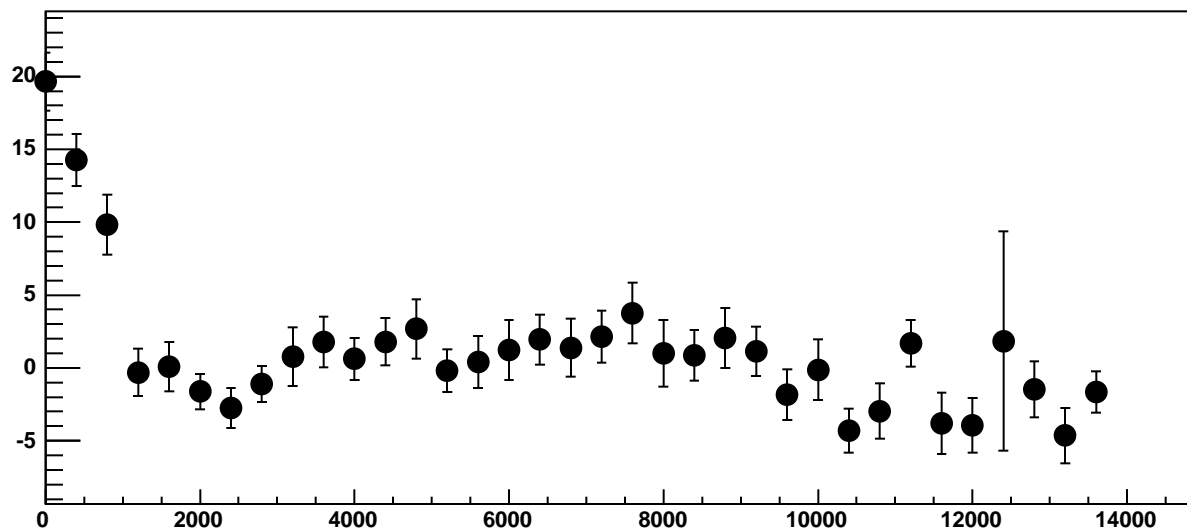
Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



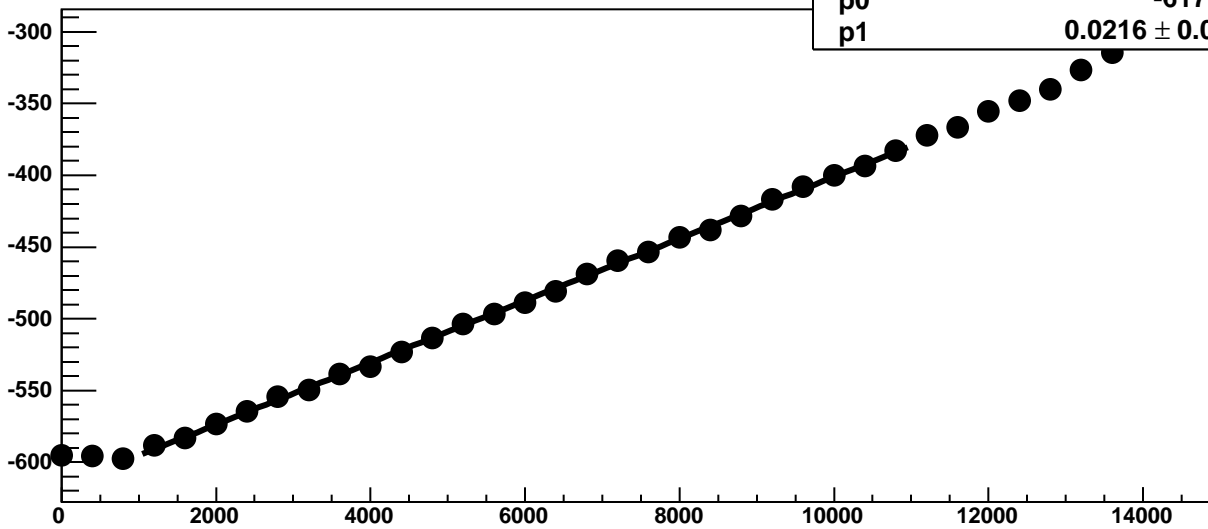
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18.63 / 23

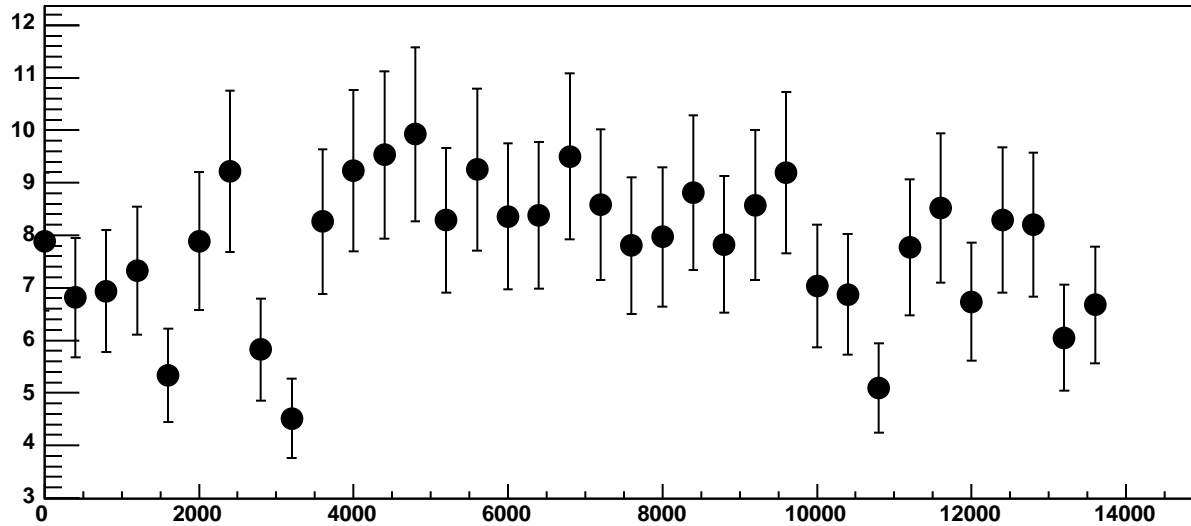
p0

$-617 \pm 0.709$

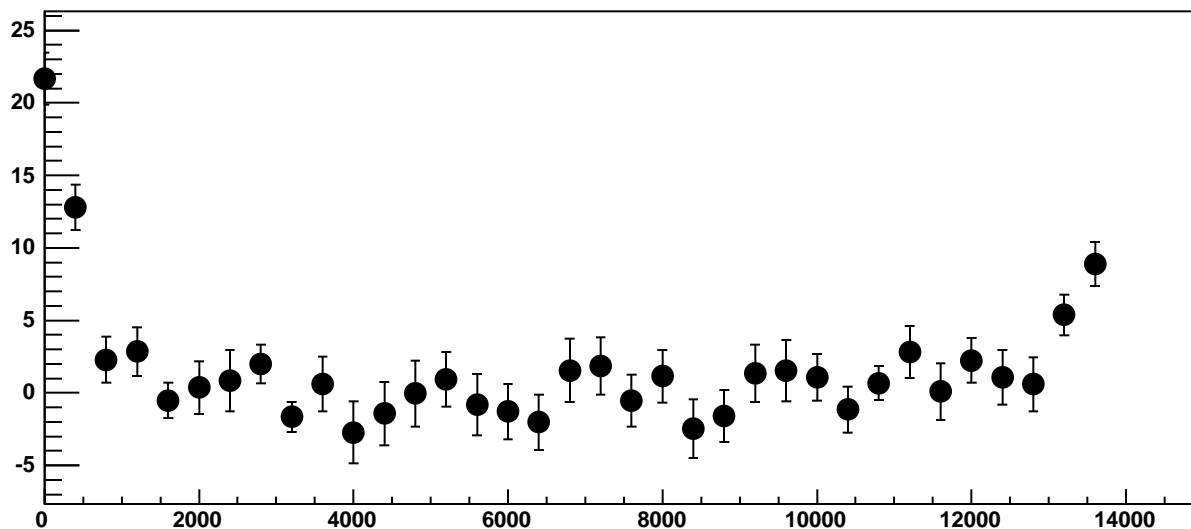
p1

$0.0216 \pm 0.0001066$

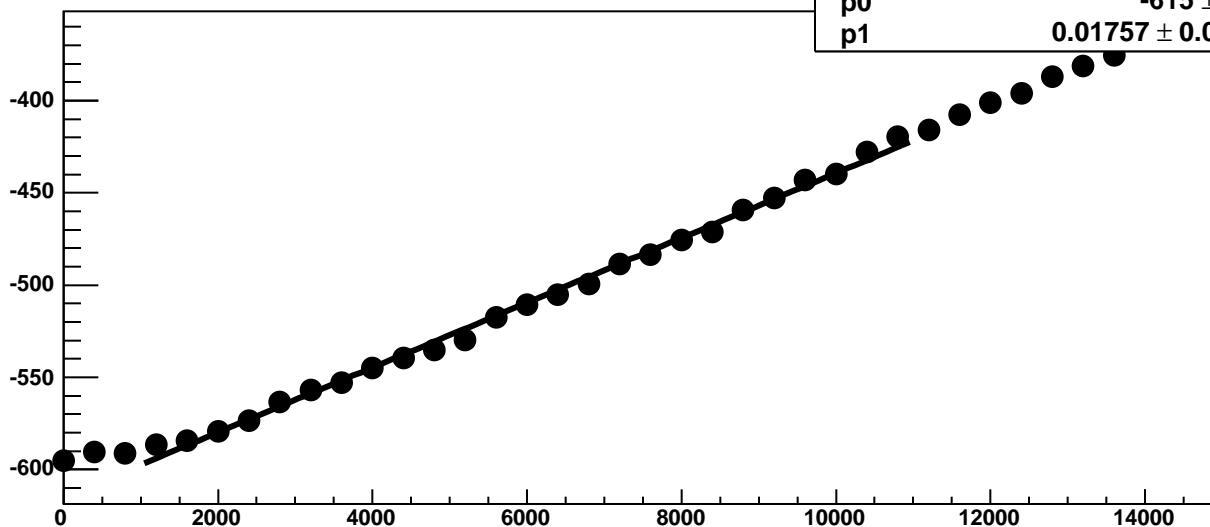
Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

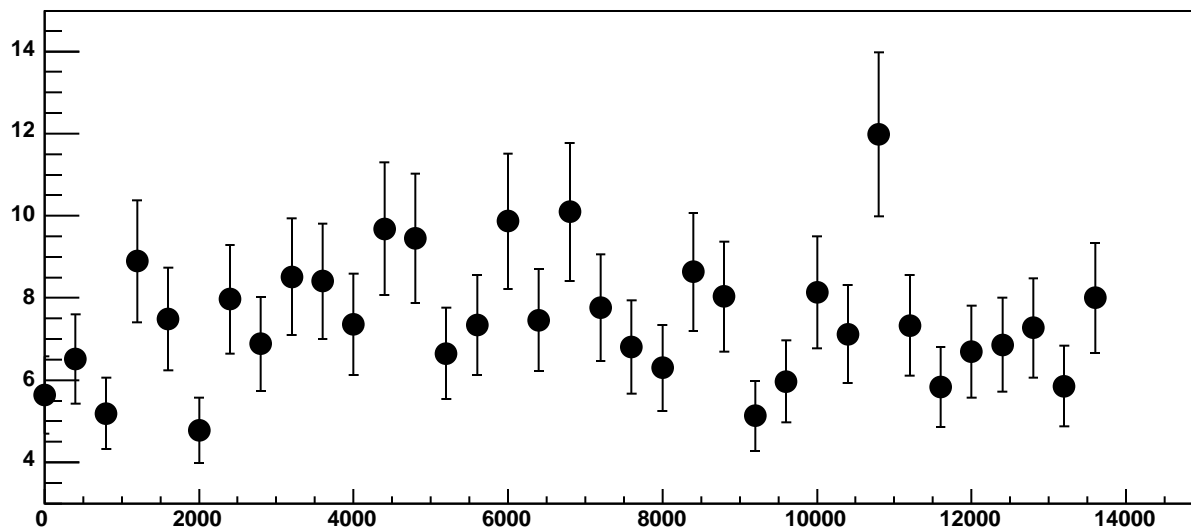


Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

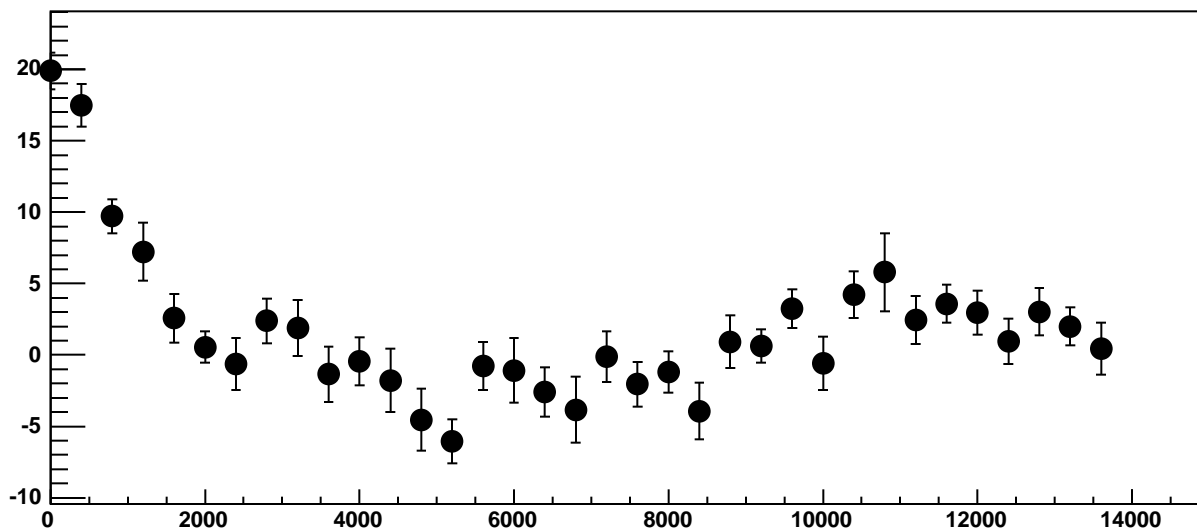


$\chi^2 / \text{ndf}$  68.85 / 23  
p0 -615 ± 0.7657  
p1 0.01757 ± 0.0001147

Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

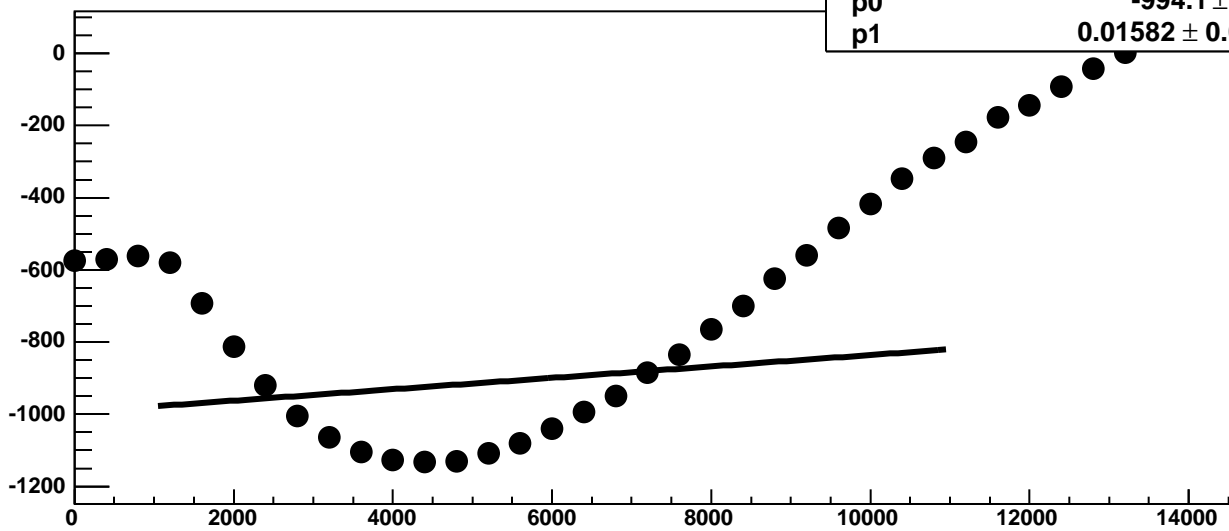


Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



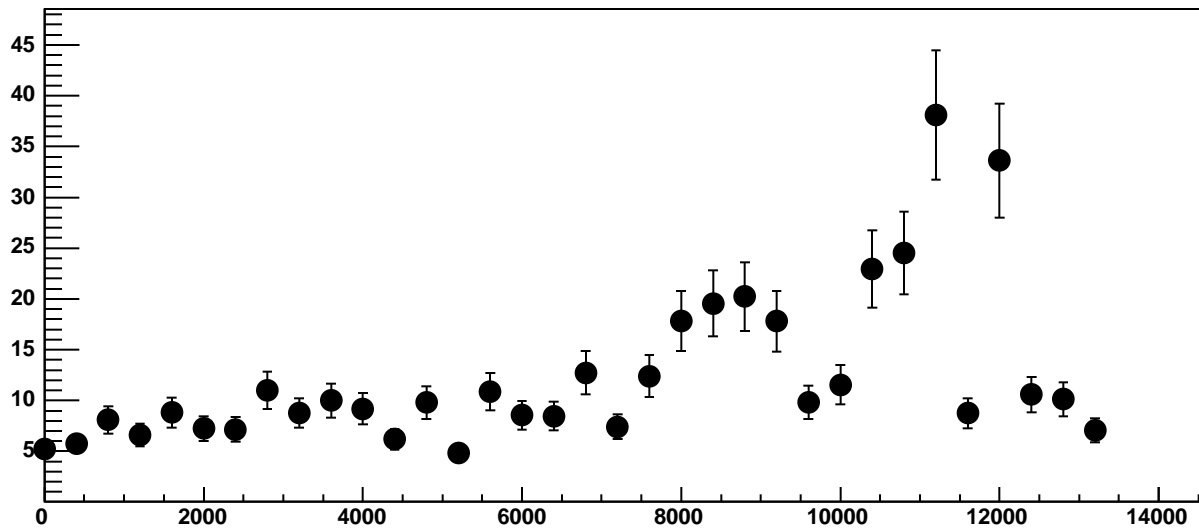


Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

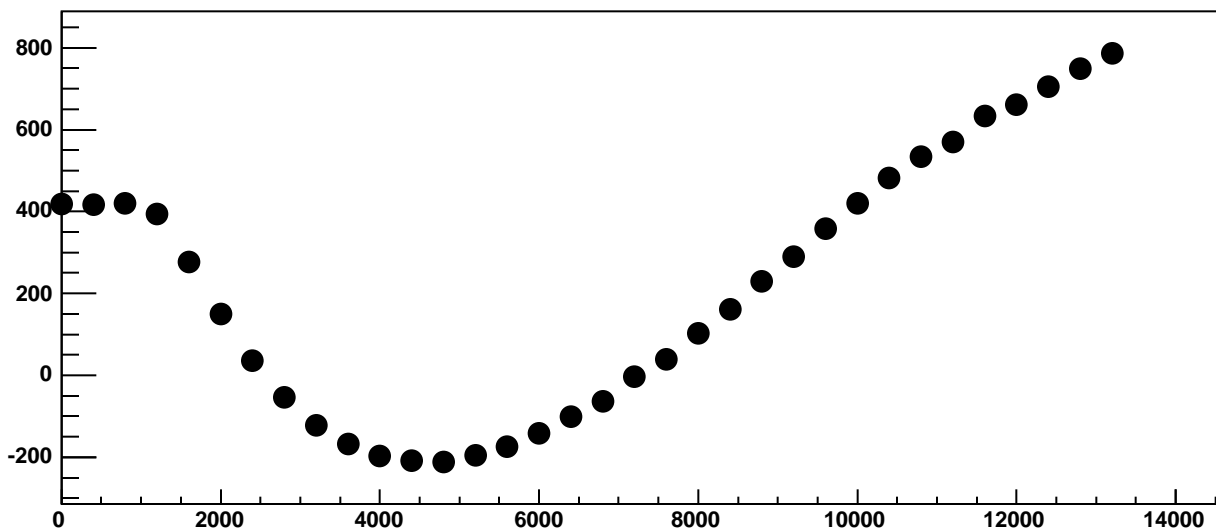


$\chi^2 / \text{ndf}$  2.651e+05 / 23  
p0 -994.1 ± 0.9292  
p1 0.01582 ± 0.000173

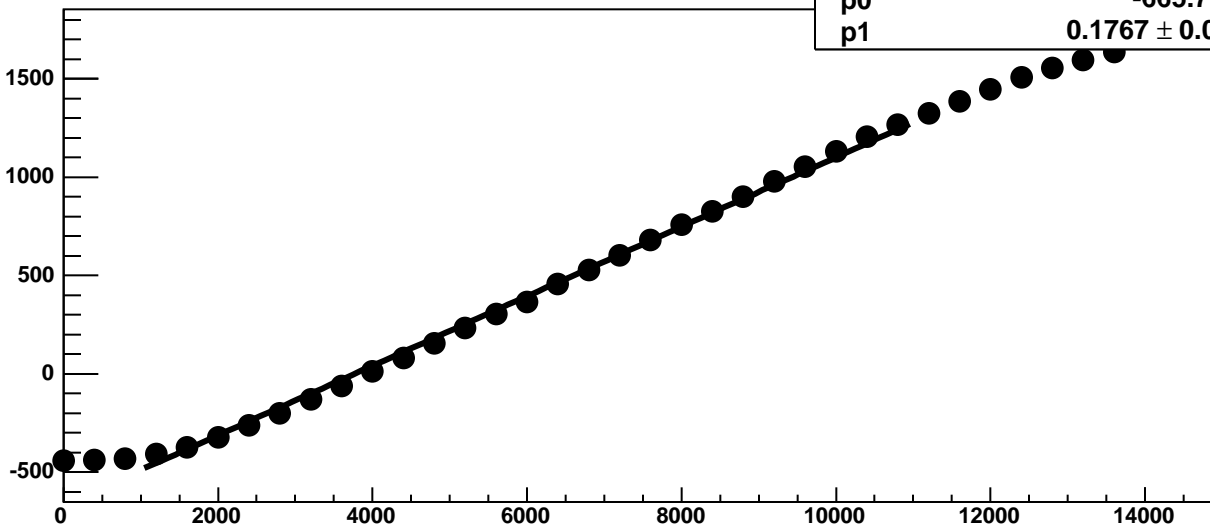
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



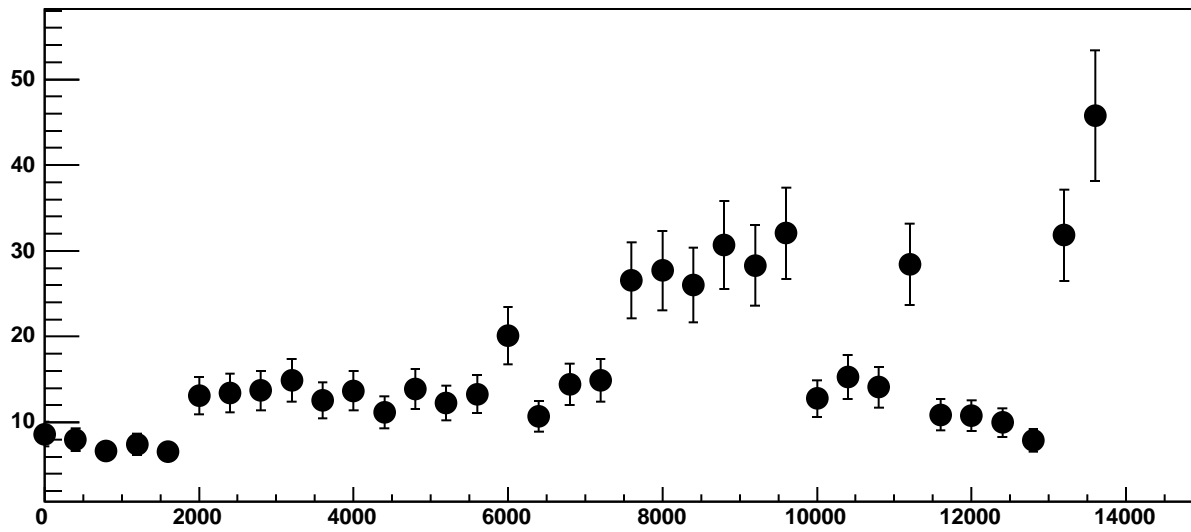
Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



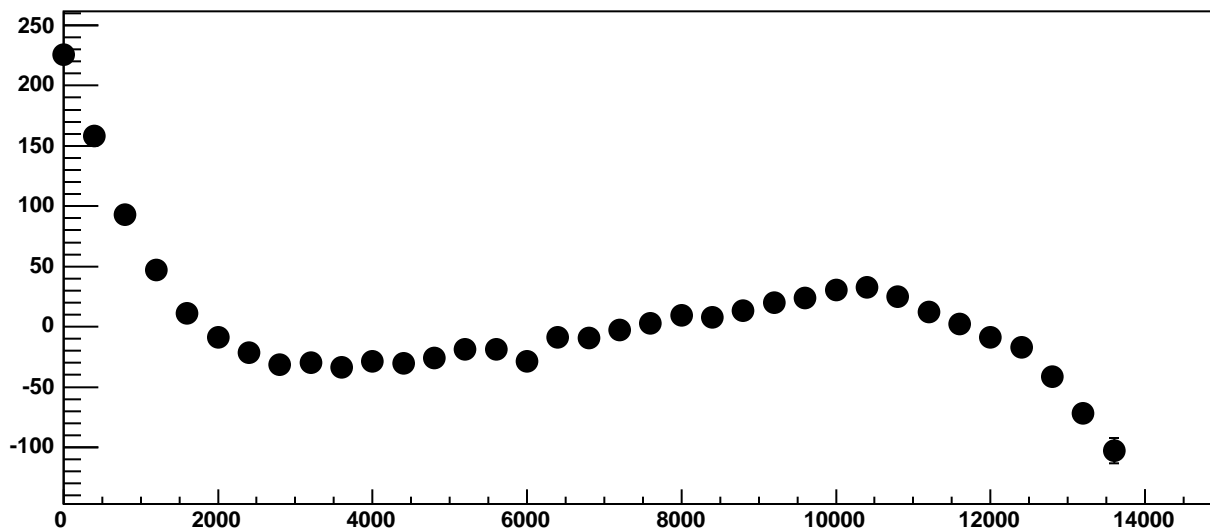
Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



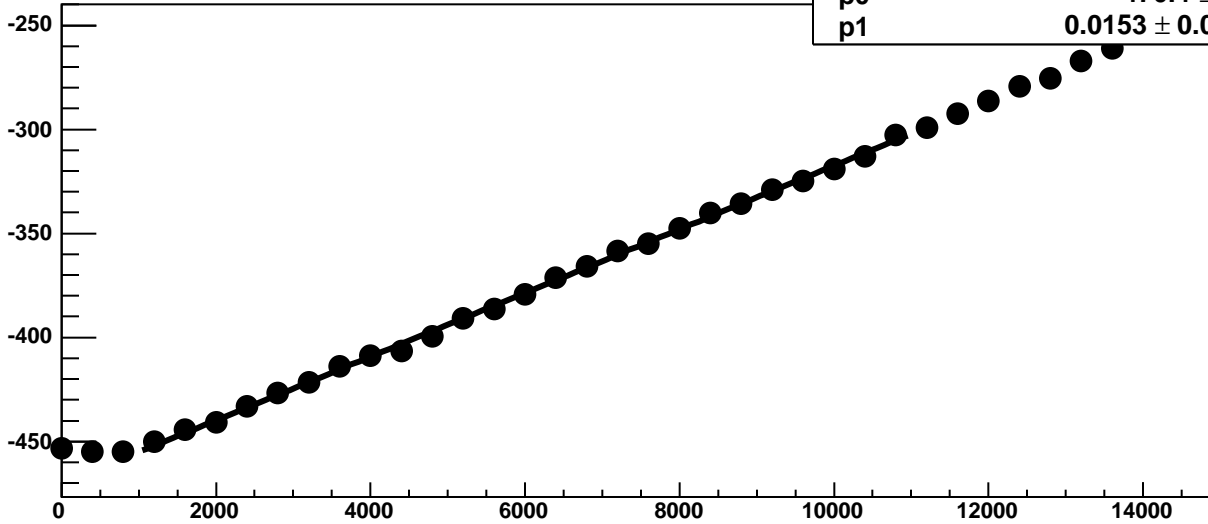
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



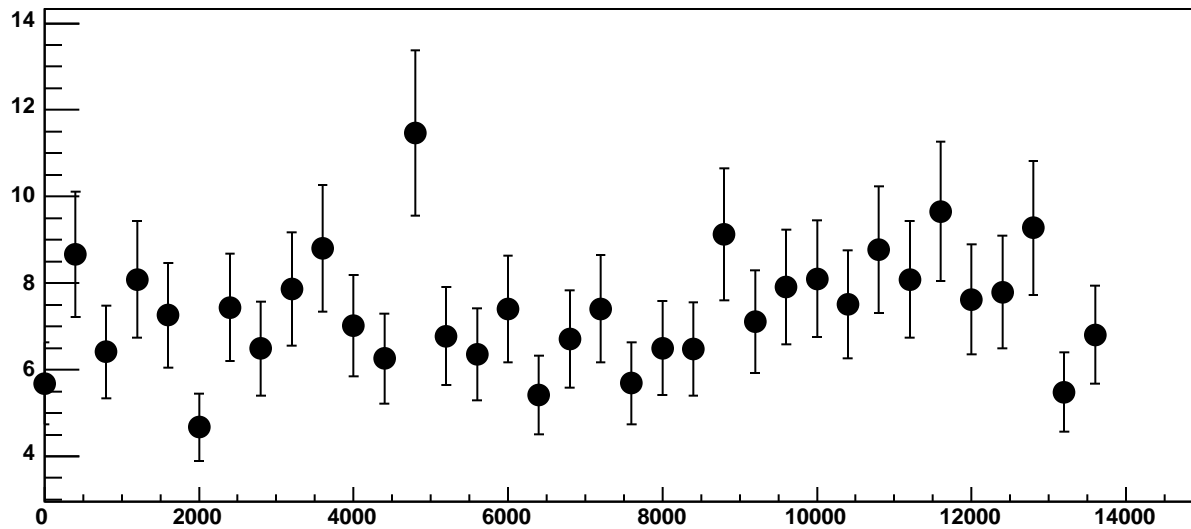
Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



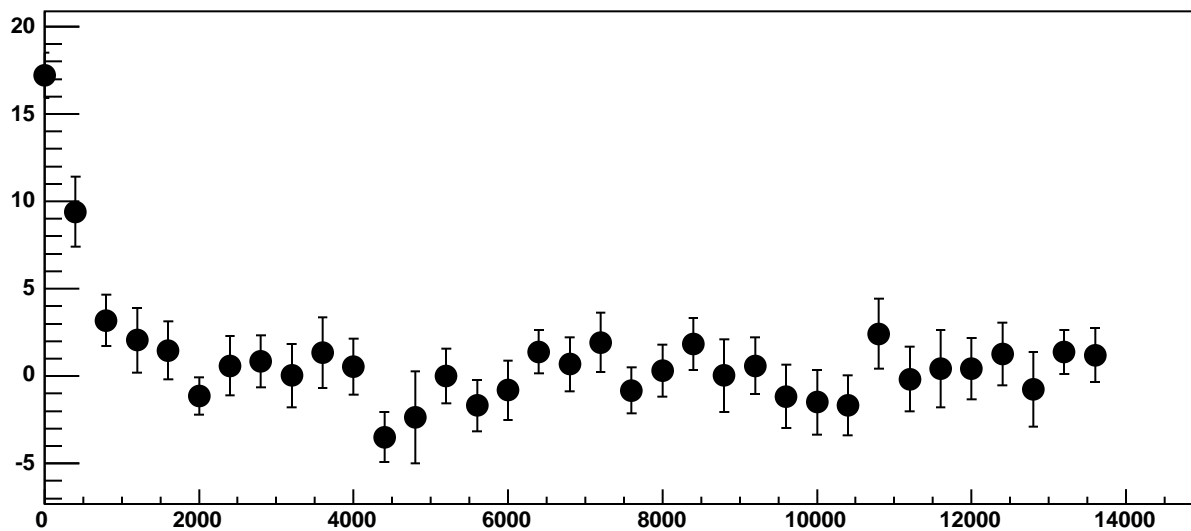
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



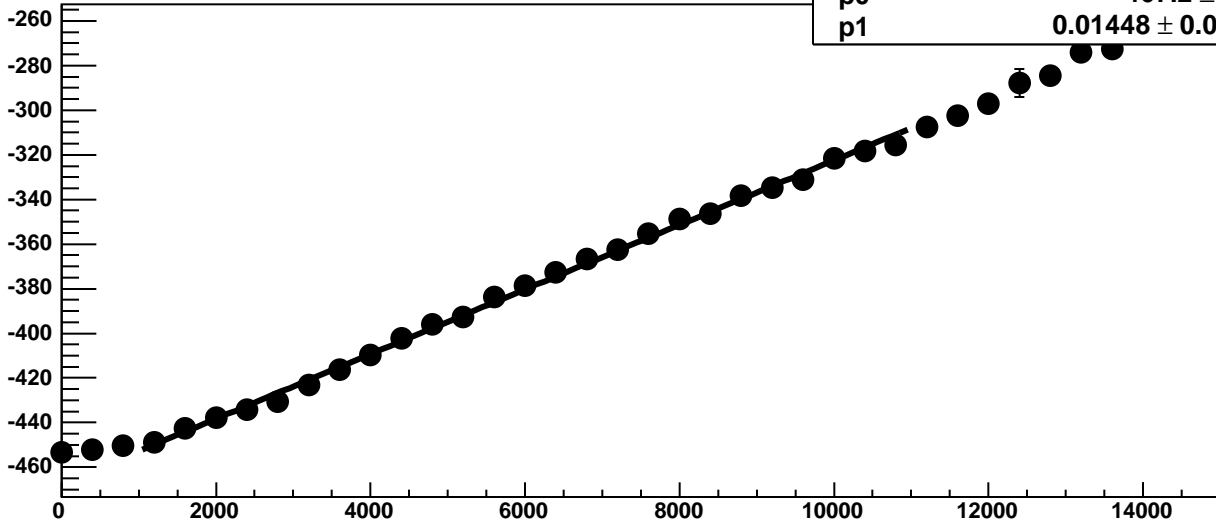
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



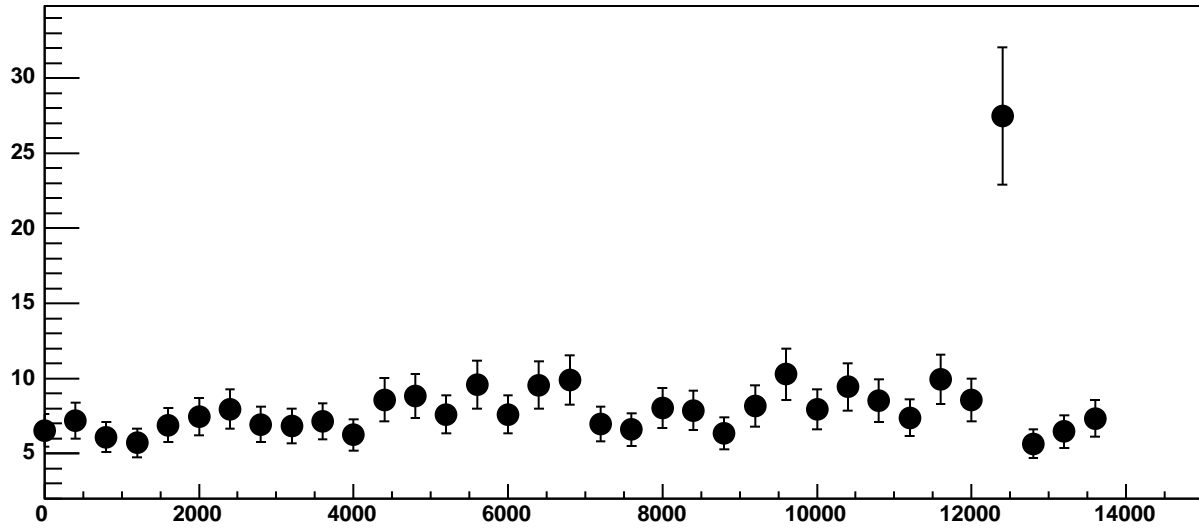
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



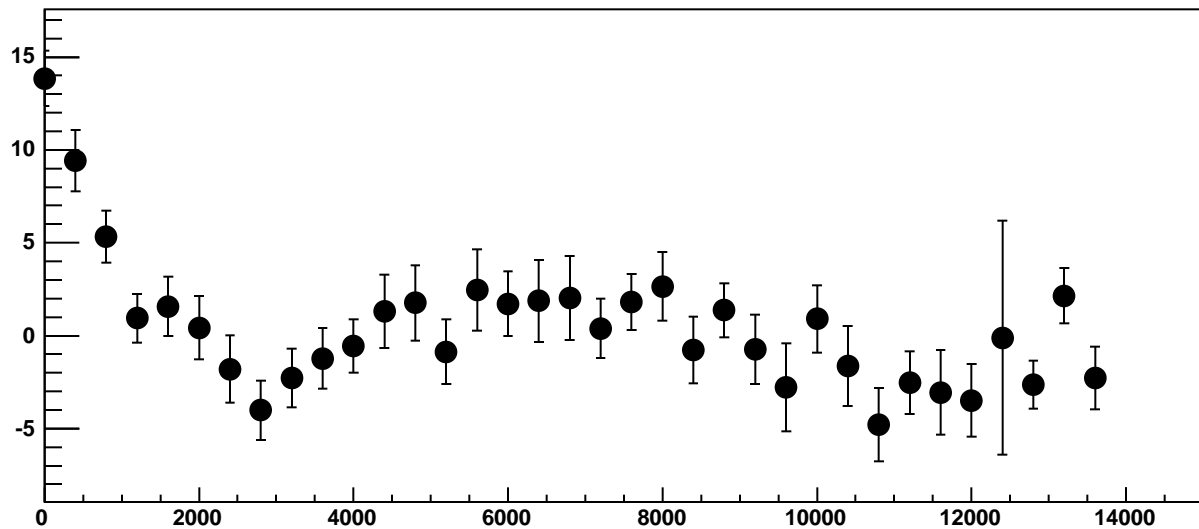
Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



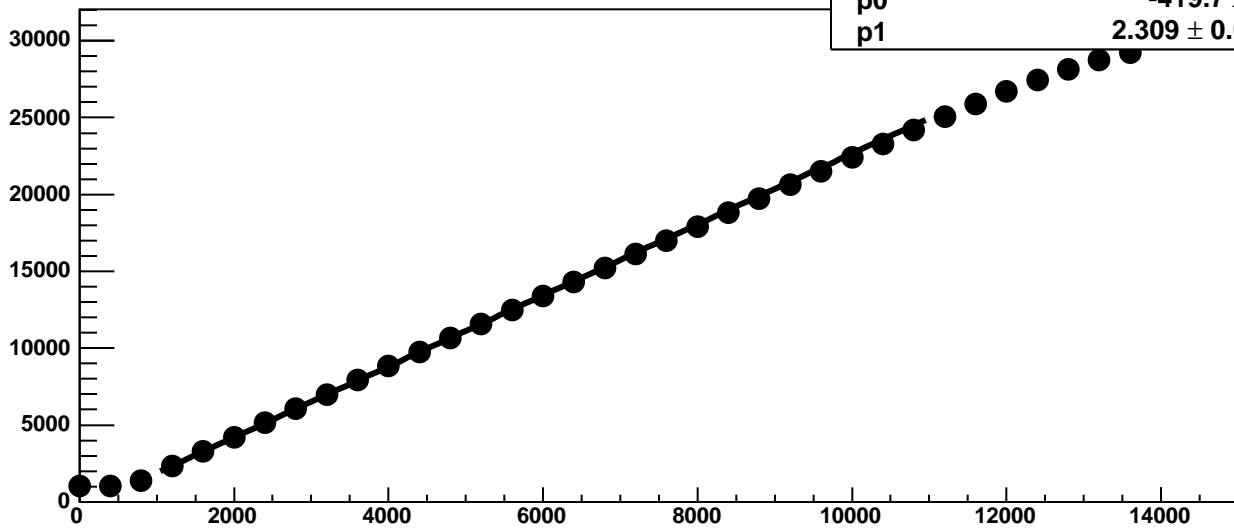
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

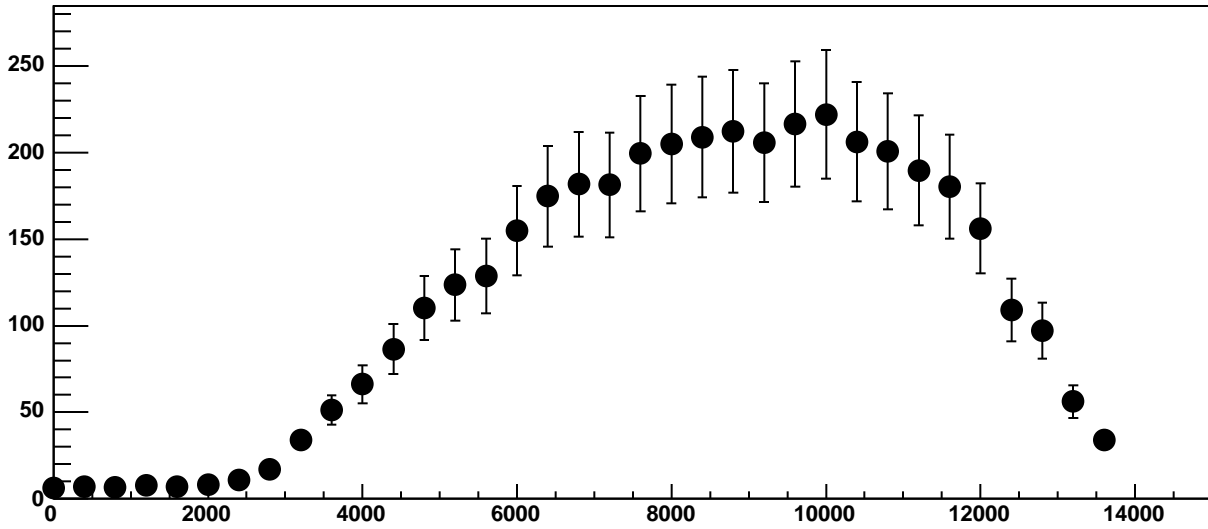


Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

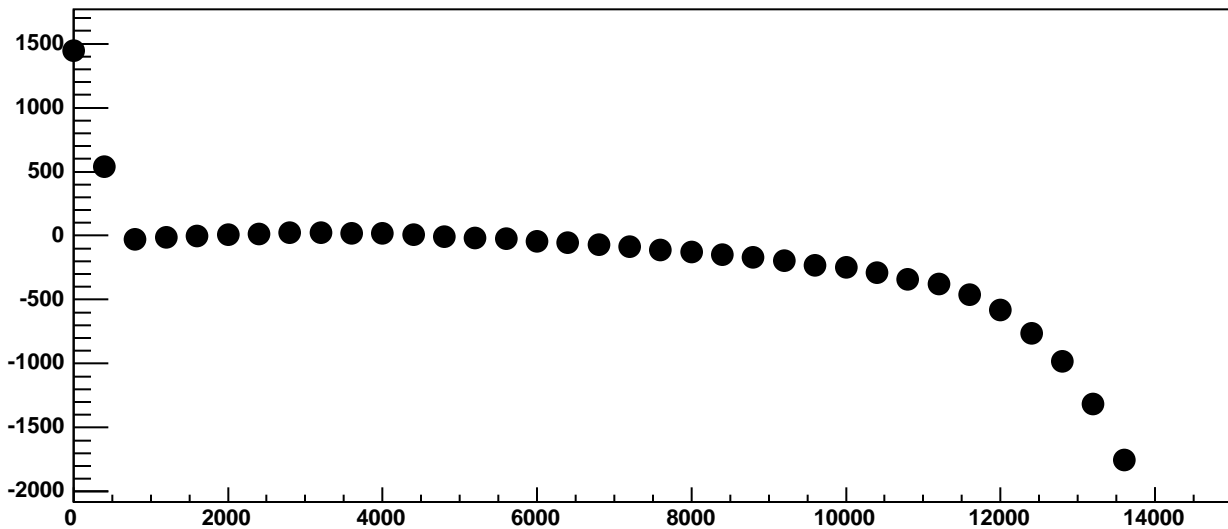


$\chi^2 / \text{ndf}$  371.3 / 23  
p0  $-419.7 \pm 2.434$   
p1  $2.309 \pm 0.001219$

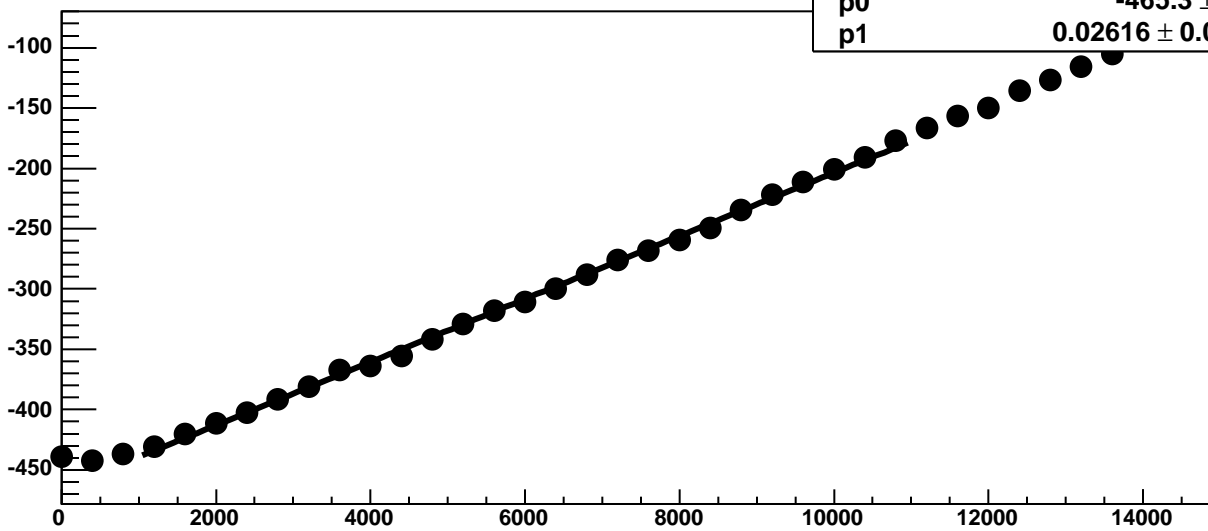
Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

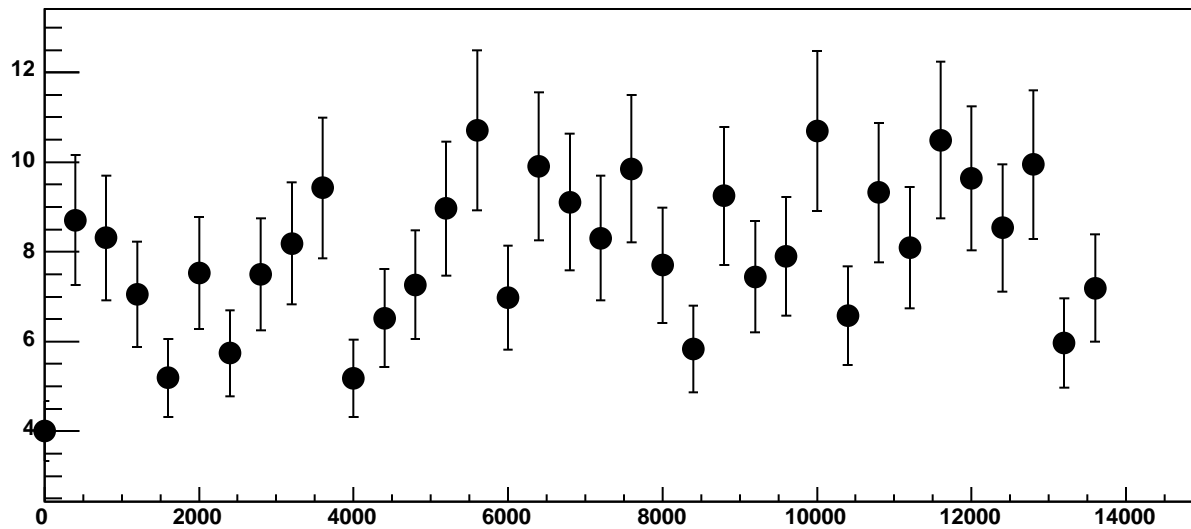


Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

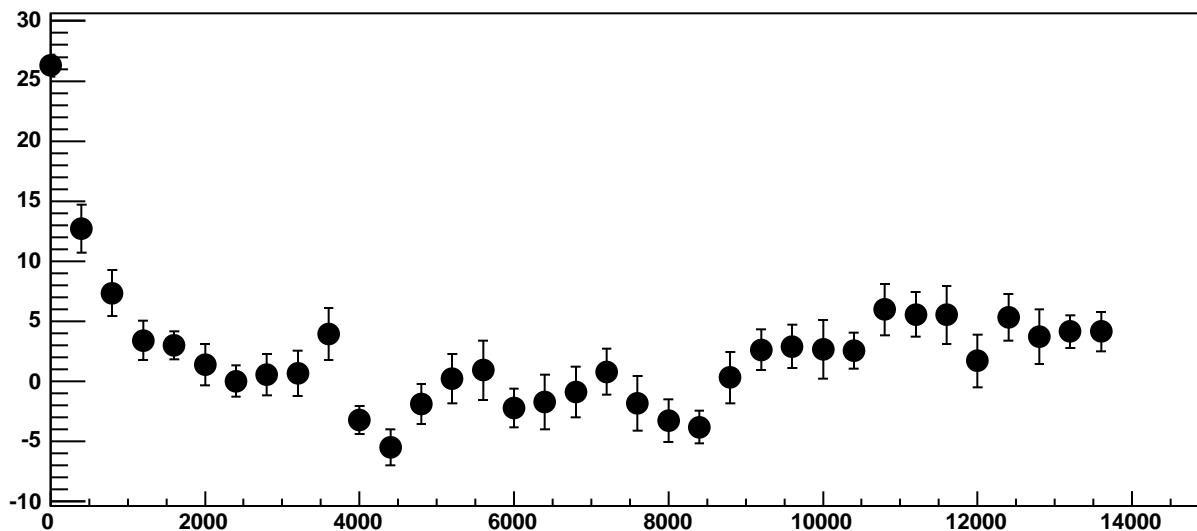


$\chi^2 / \text{ndf}$  68.95 / 23  
p0  $-465.3 \pm 0.7189$   
p1  $0.02616 \pm 0.0001149$

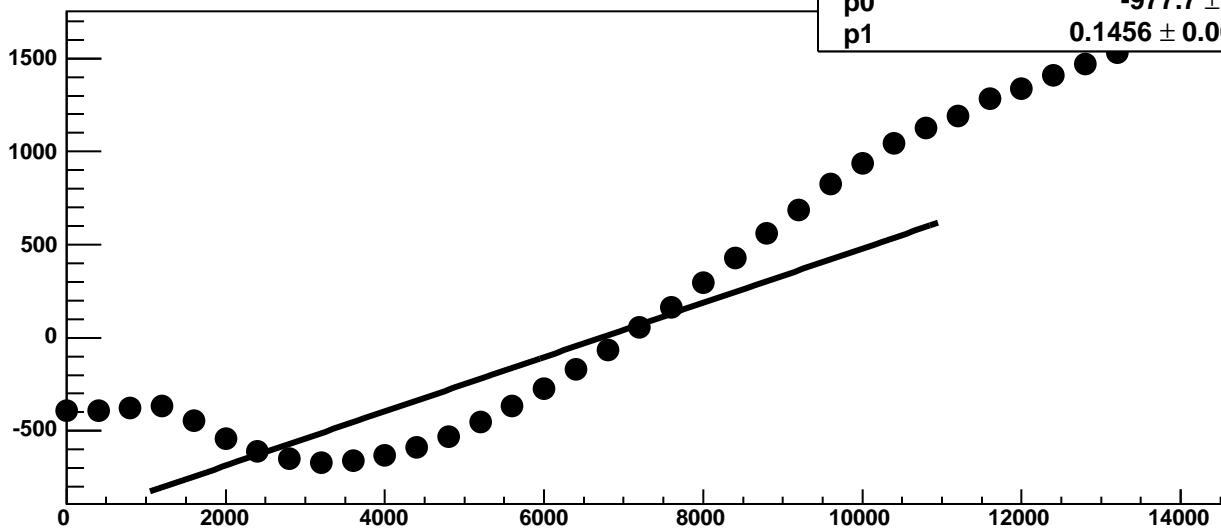
Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

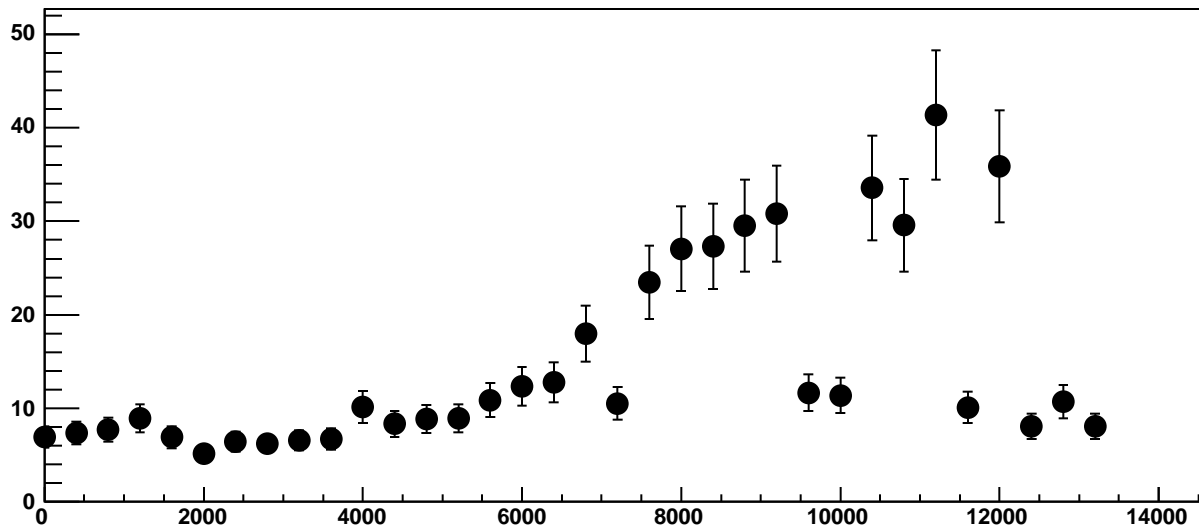


Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

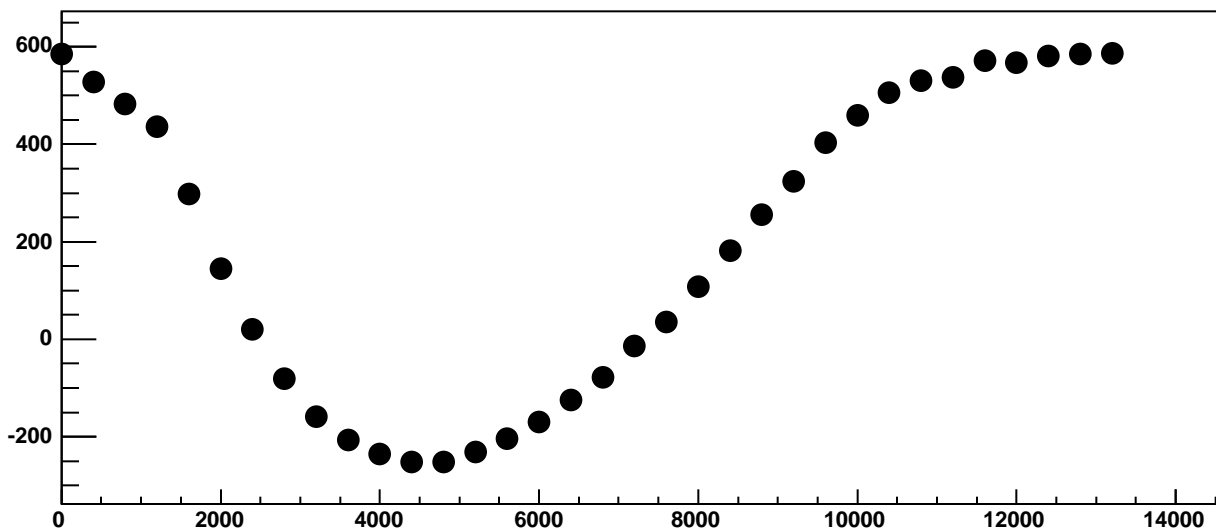


$\chi^2 / \text{ndf}$  2.643e+05 / 23  
p0 -977.7 ± 0.8718  
p1 0.1456 ± 0.0001914

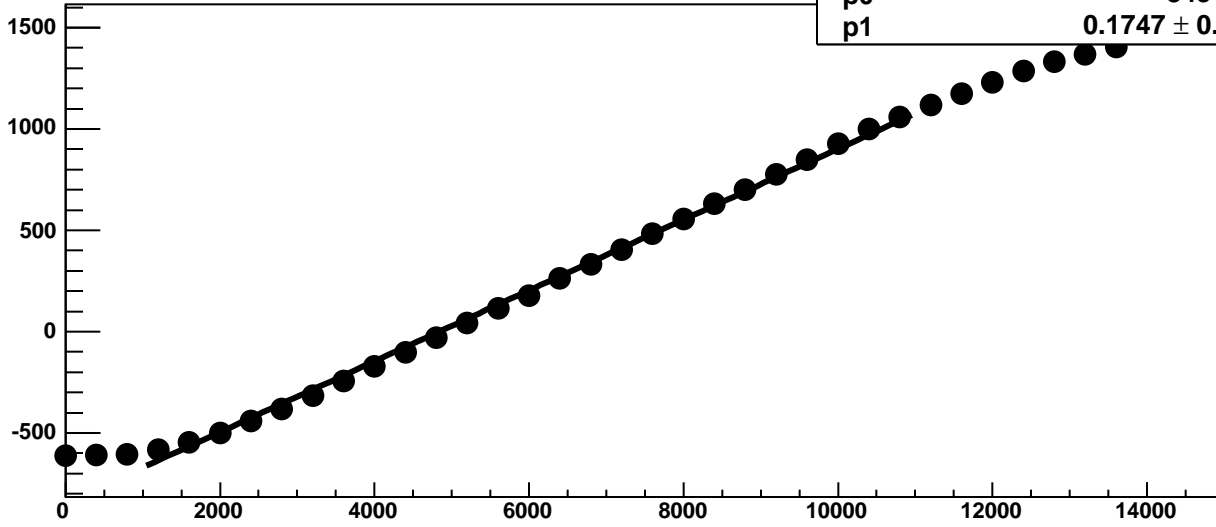
Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

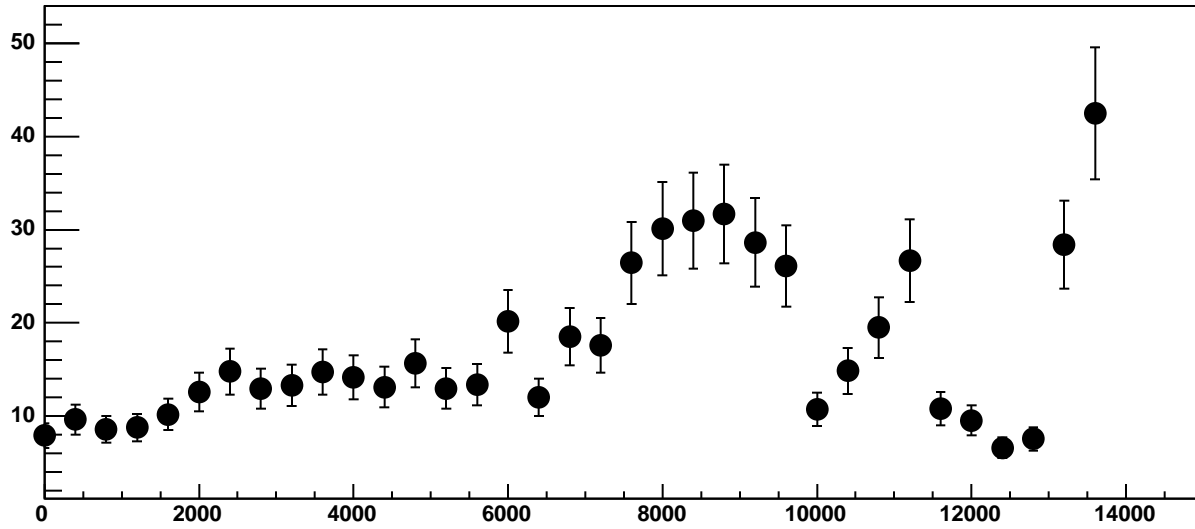


Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

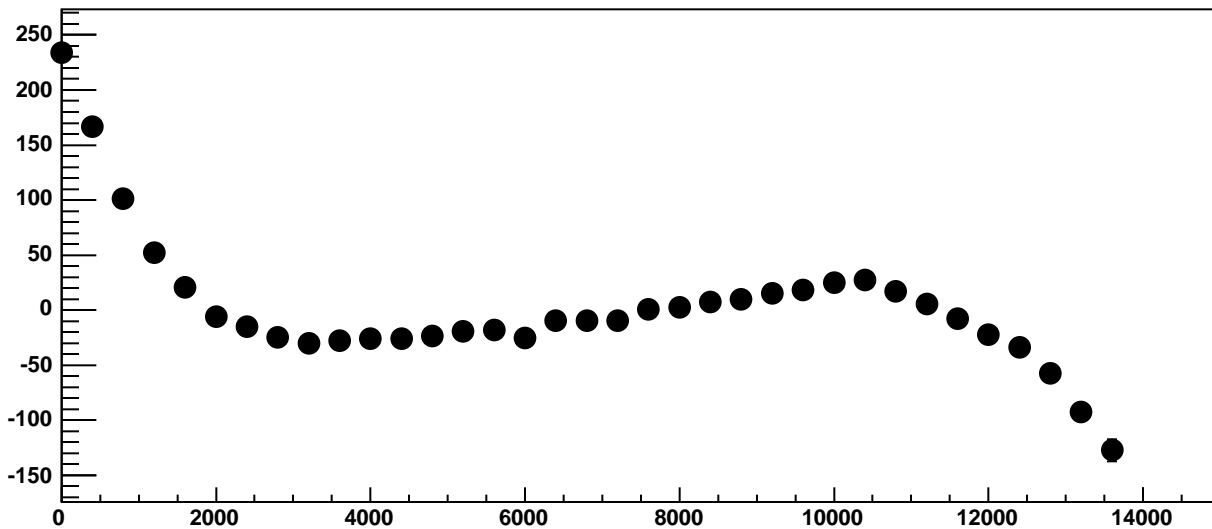


$\chi^2 / \text{ndf}$  1524 / 23  
p0  $-845 \pm 1.294$   
p1  $0.1747 \pm 0.000226$

Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

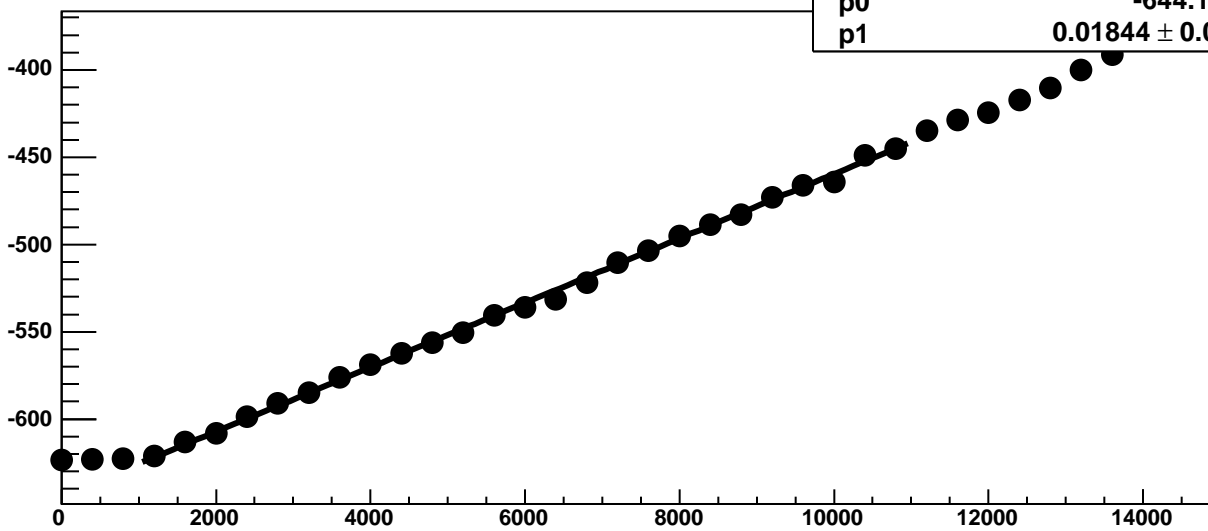


Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

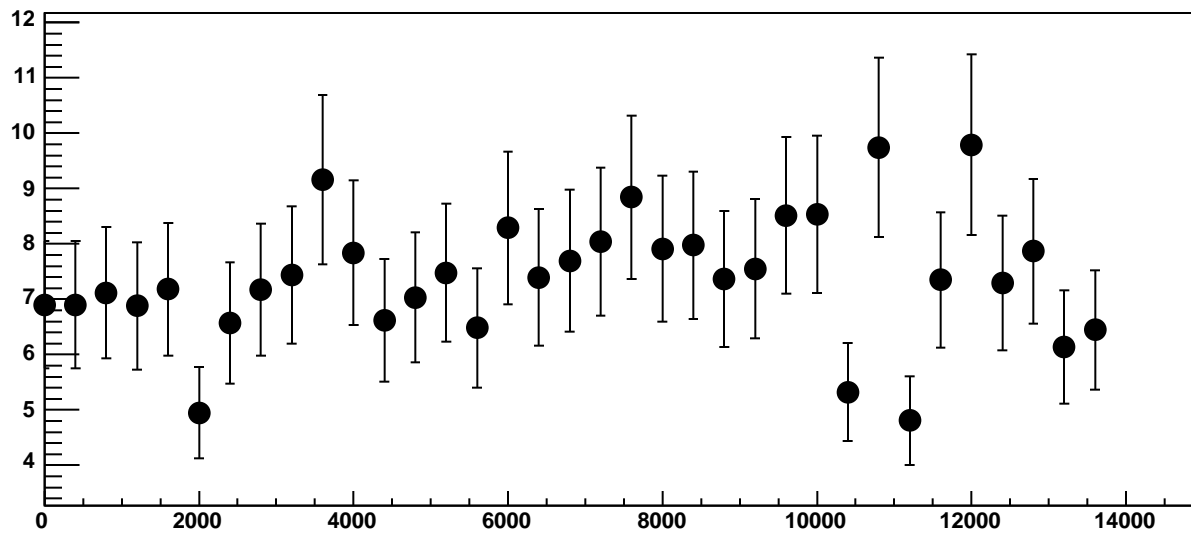




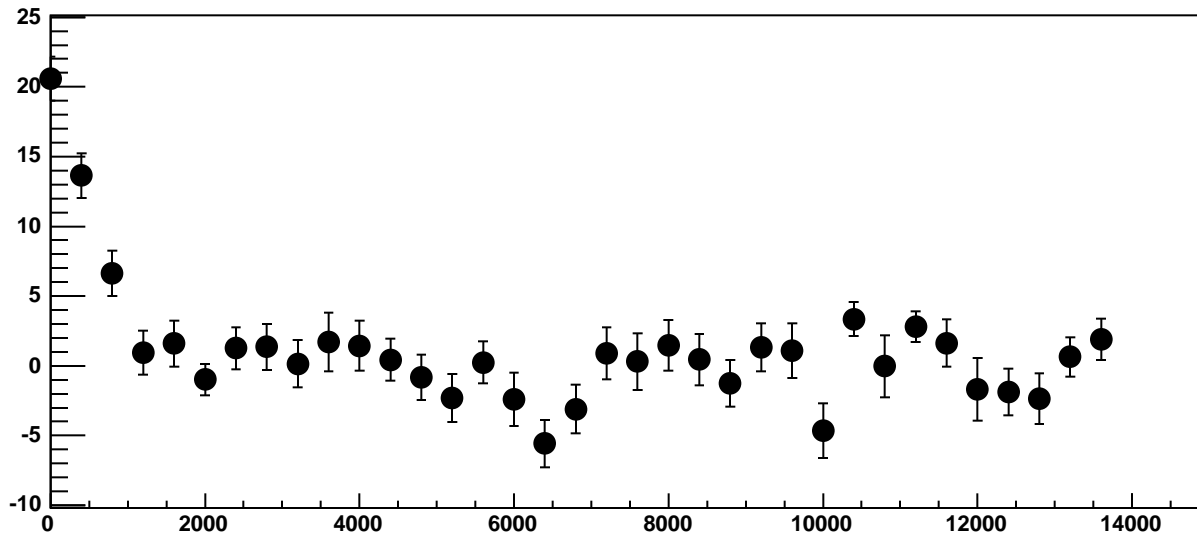
Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



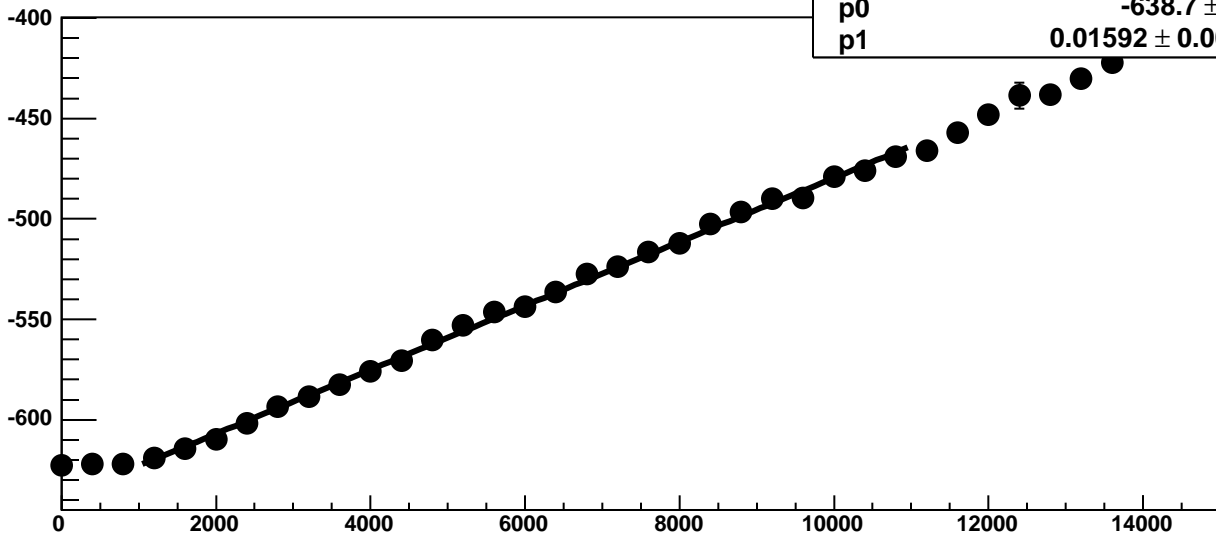
Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



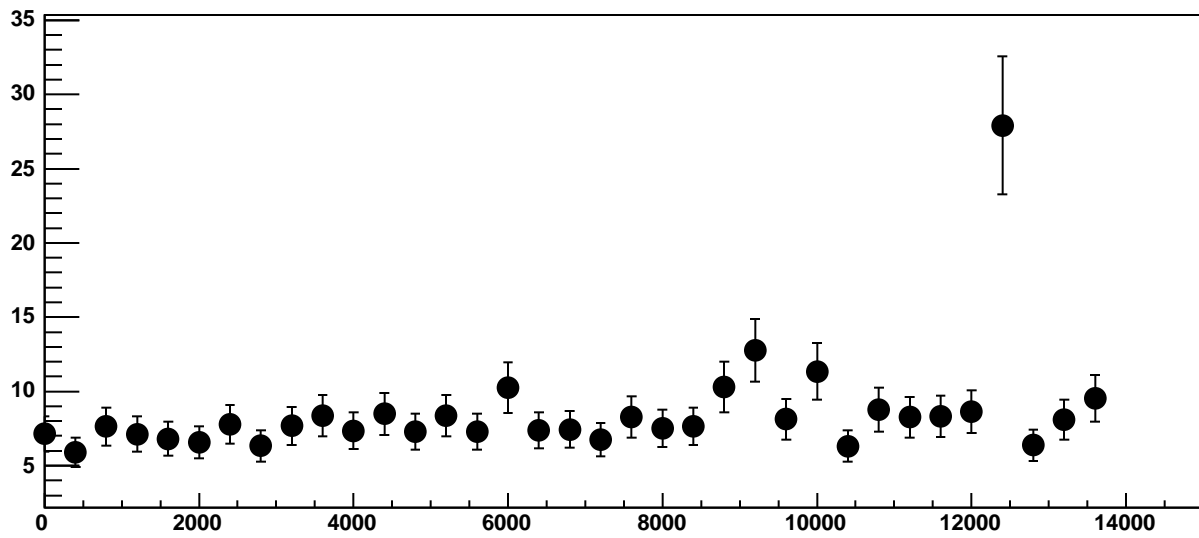
Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



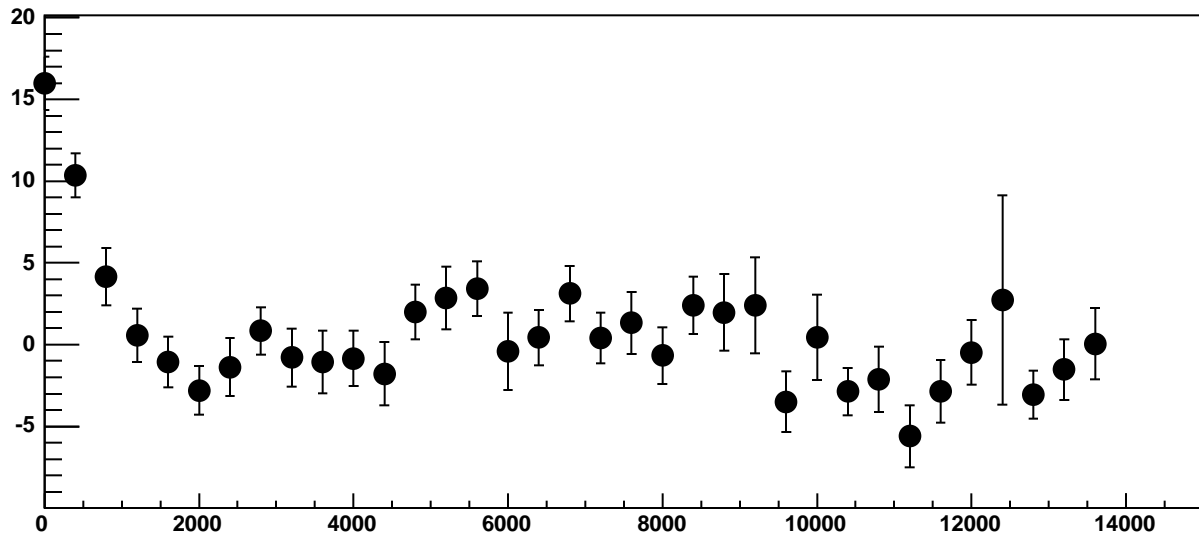
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



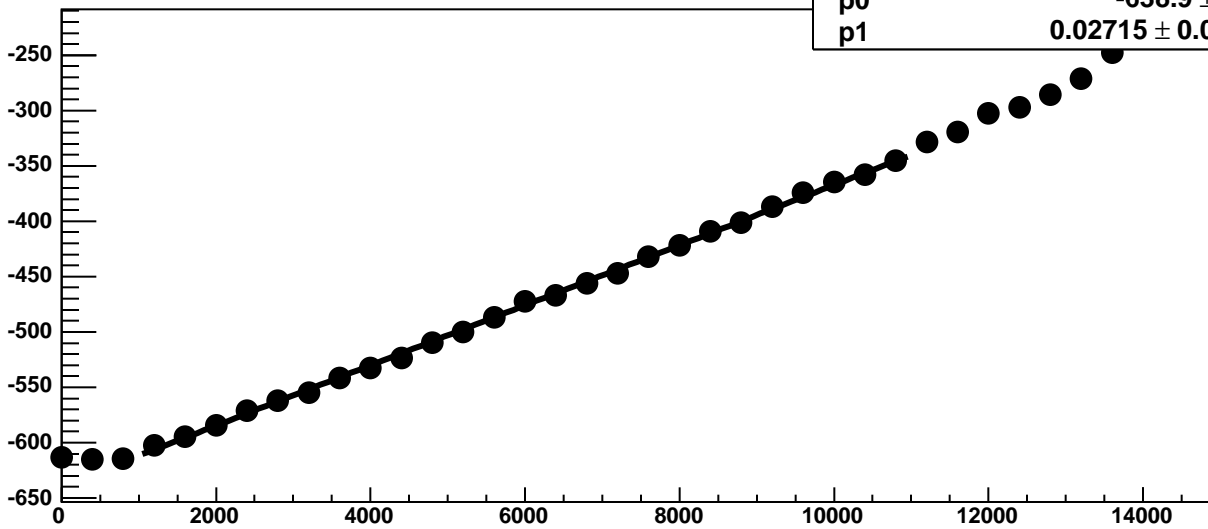
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

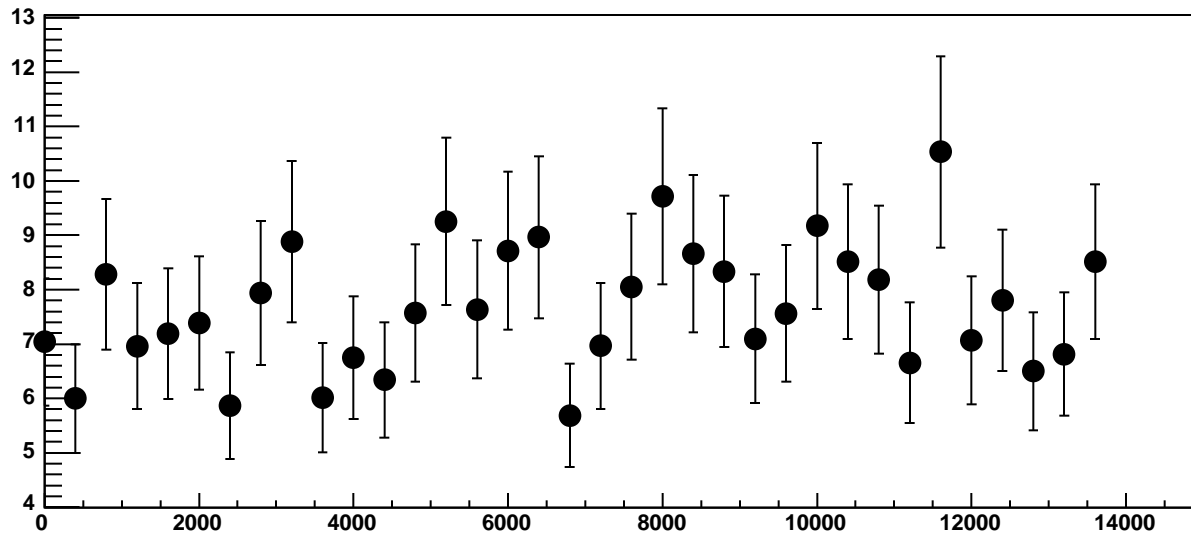


Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

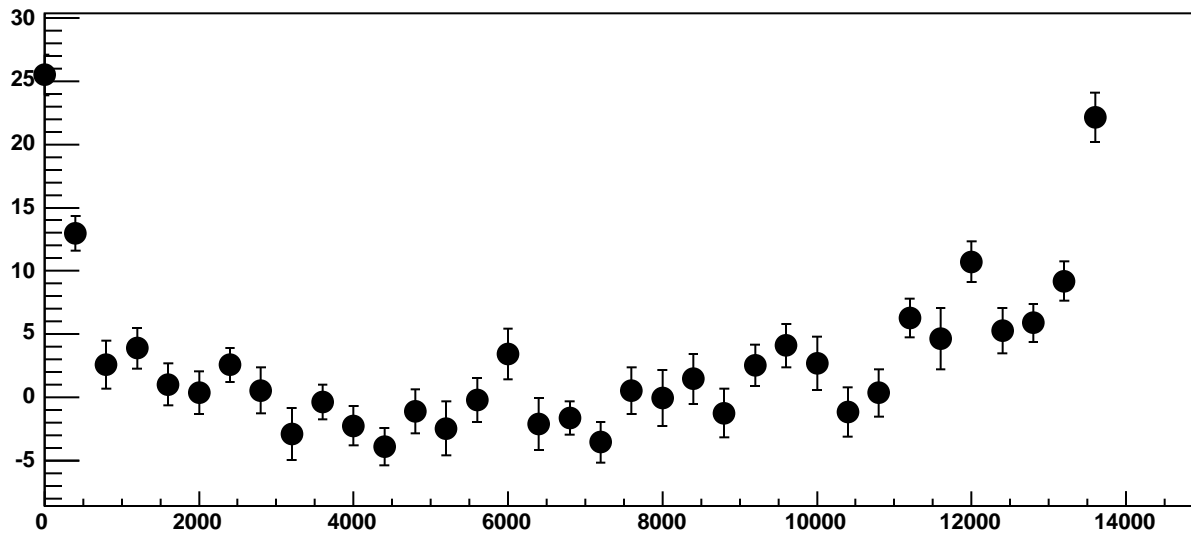


$\chi^2 / \text{ndf}$  44.61 / 23  
p0  $-638.9 \pm 0.7624$   
p1  $0.02715 \pm 0.0001205$

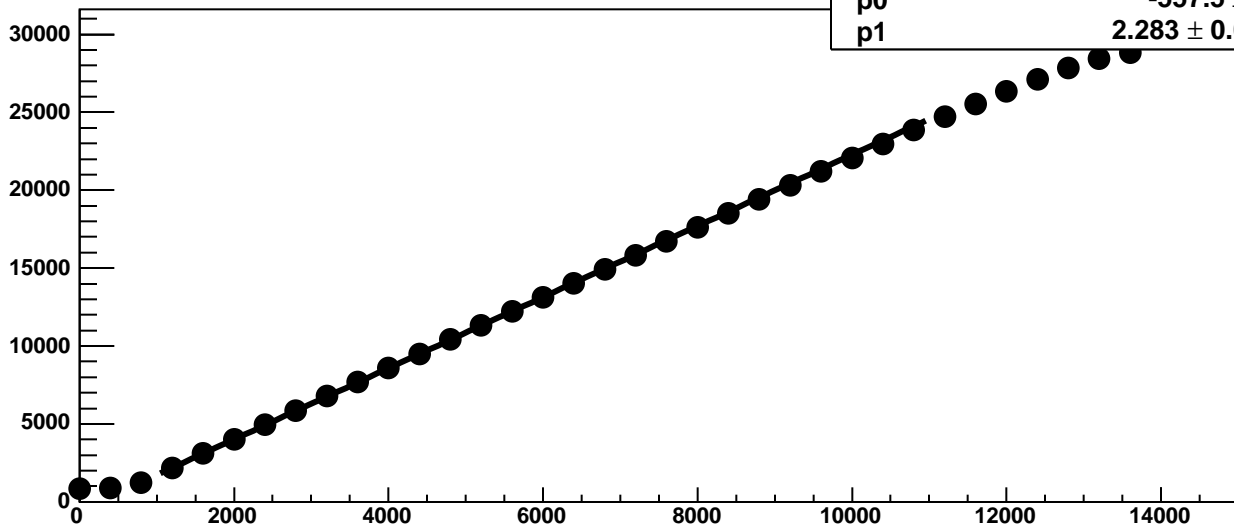
Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

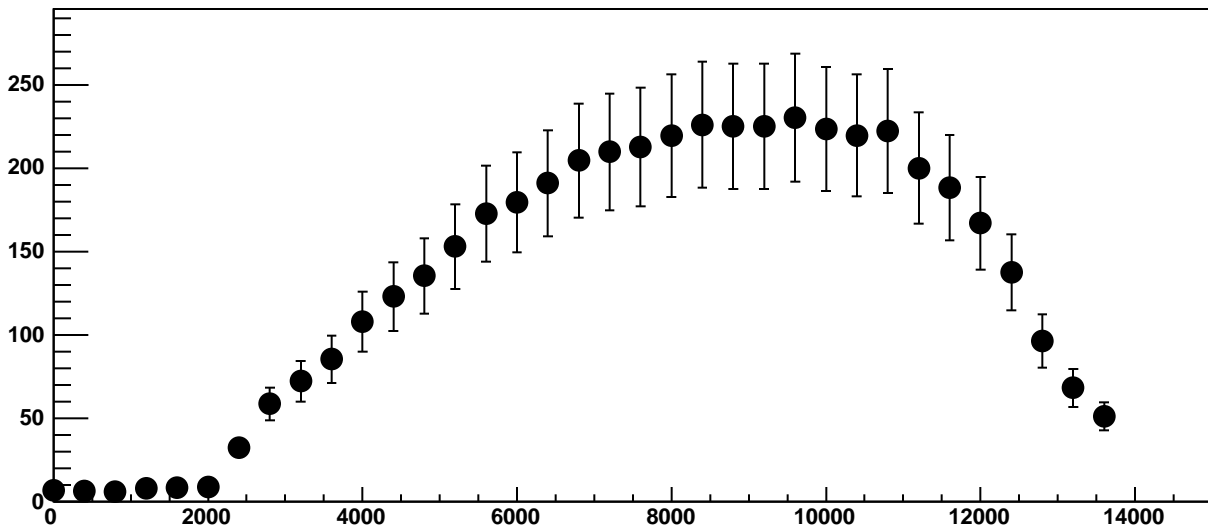


Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

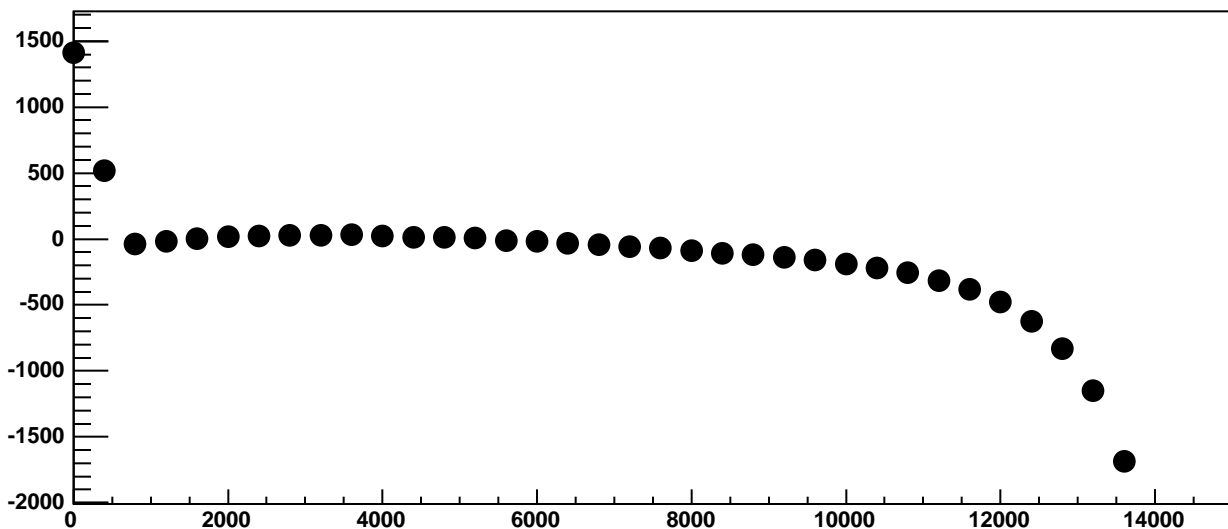


$\chi^2 / \text{ndf}$  260.4 / 23  
p0  $-557.5 \pm 2.808$   
p1  $2.283 \pm 0.001543$

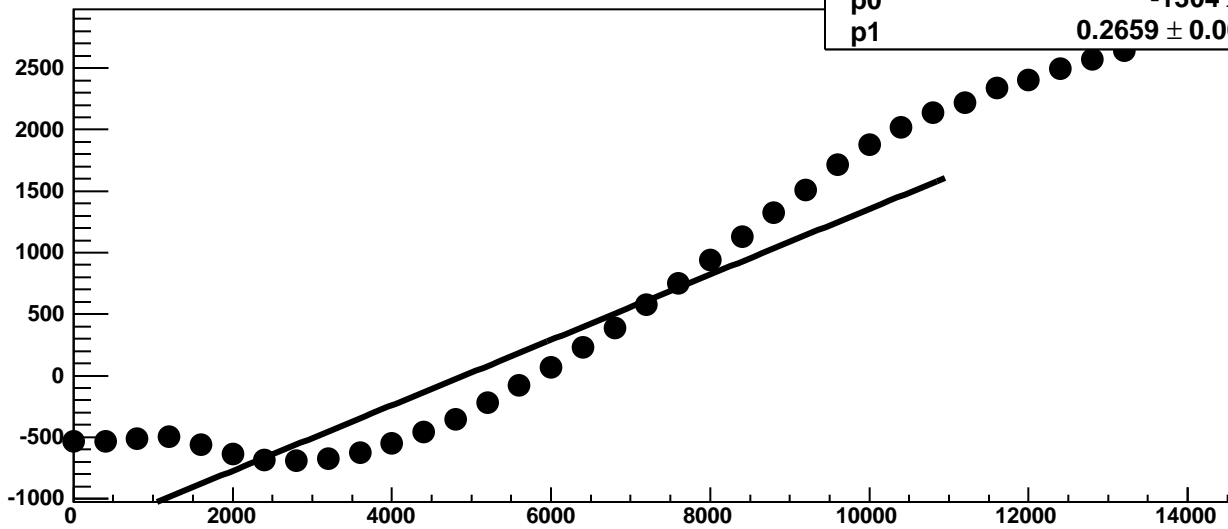
Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

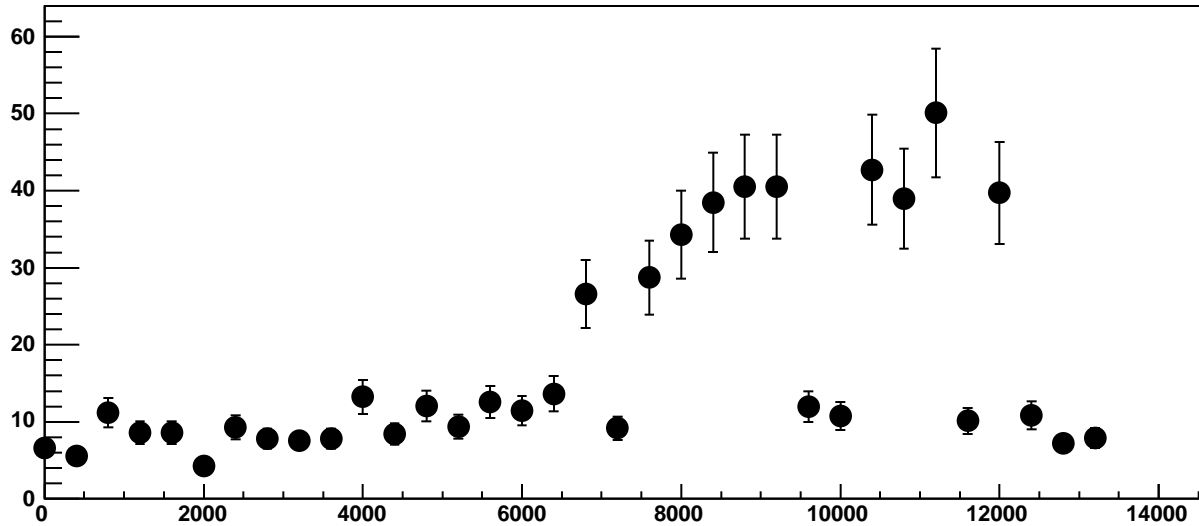


Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

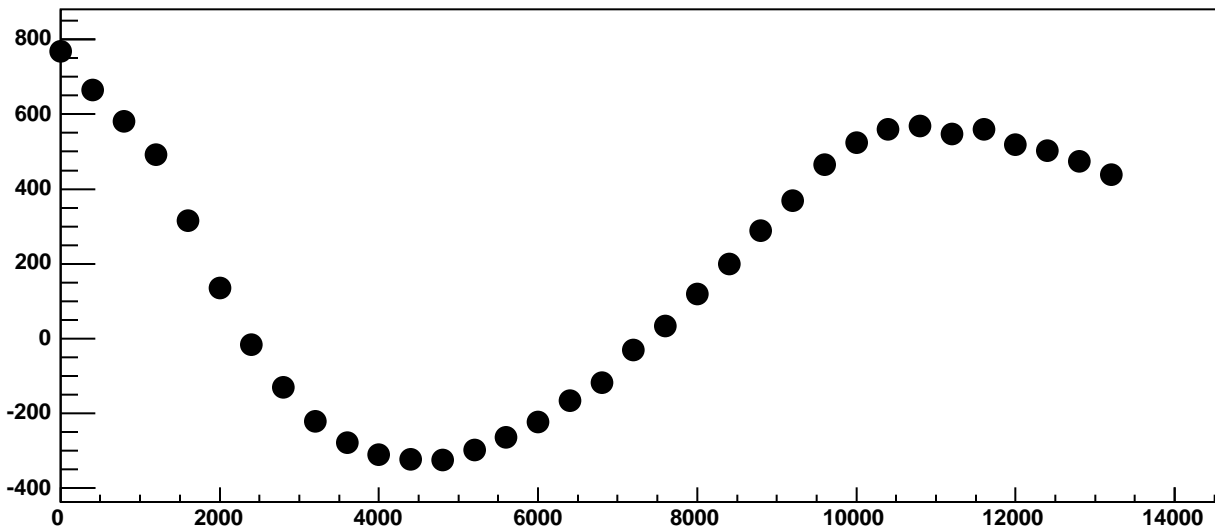


$\chi^2 / \text{ndf}$  3.262e+05 / 23  
p0 -1304 ± 0.909  
p1 0.2659 ± 0.0001972

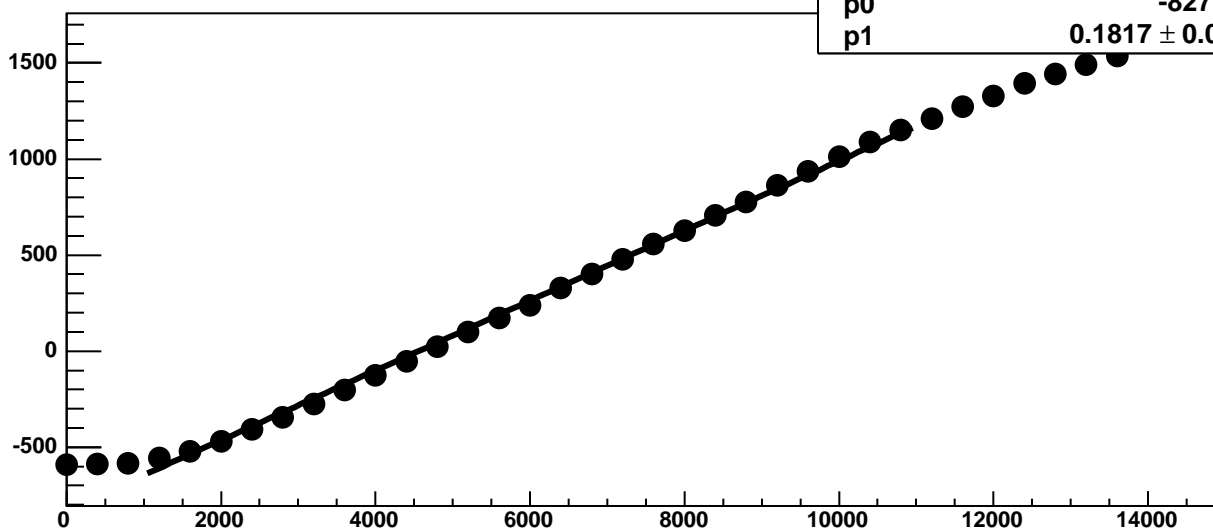
Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



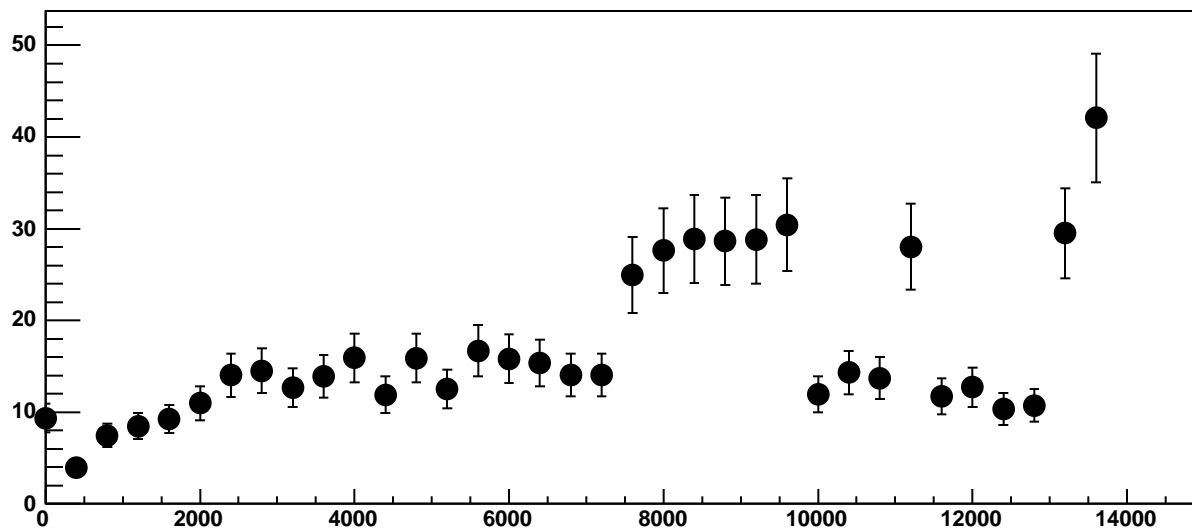
Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



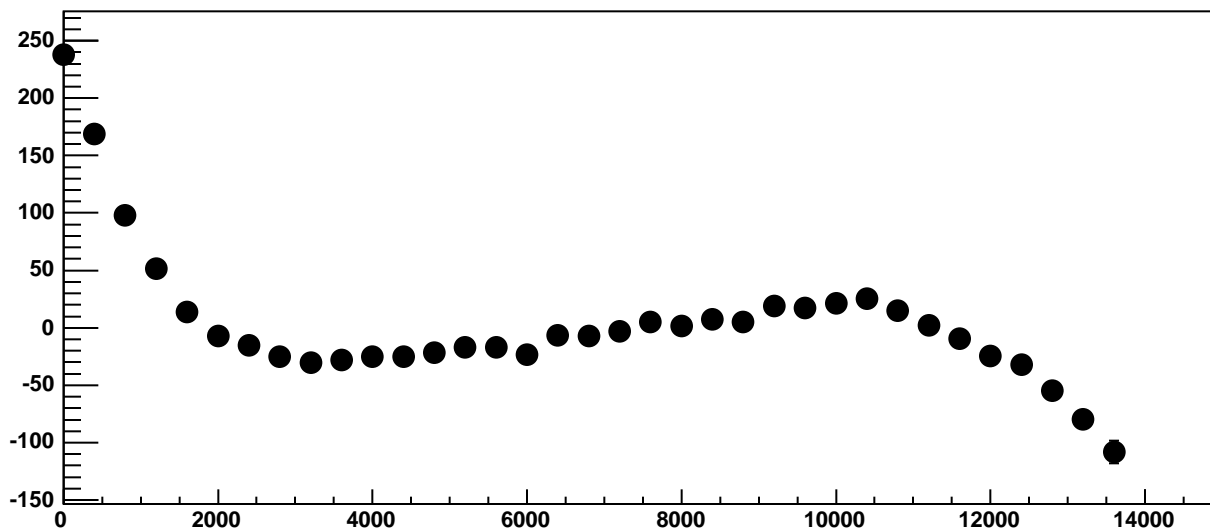
Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



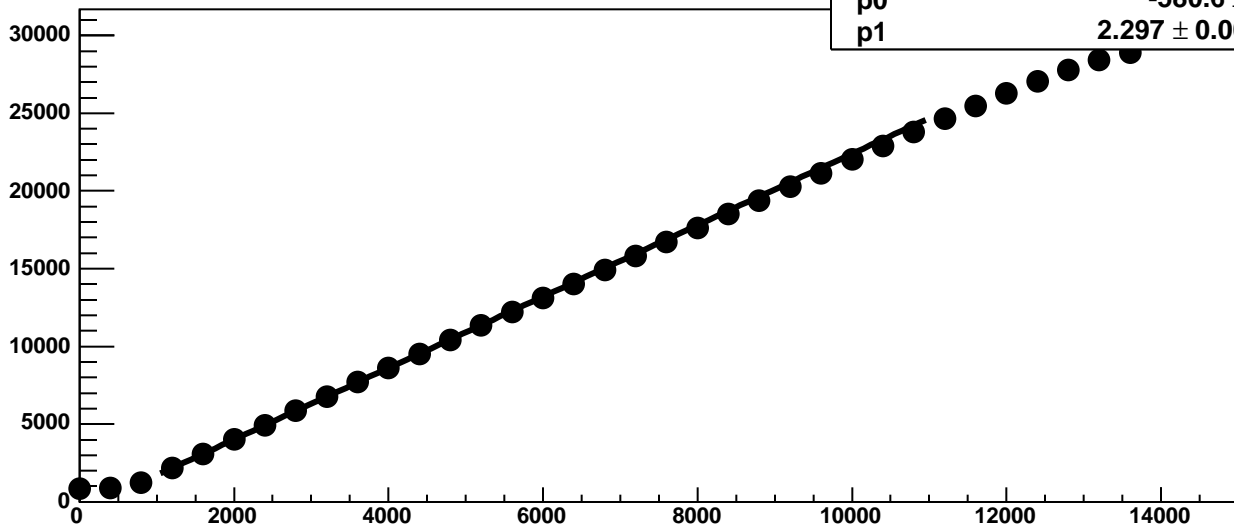
Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



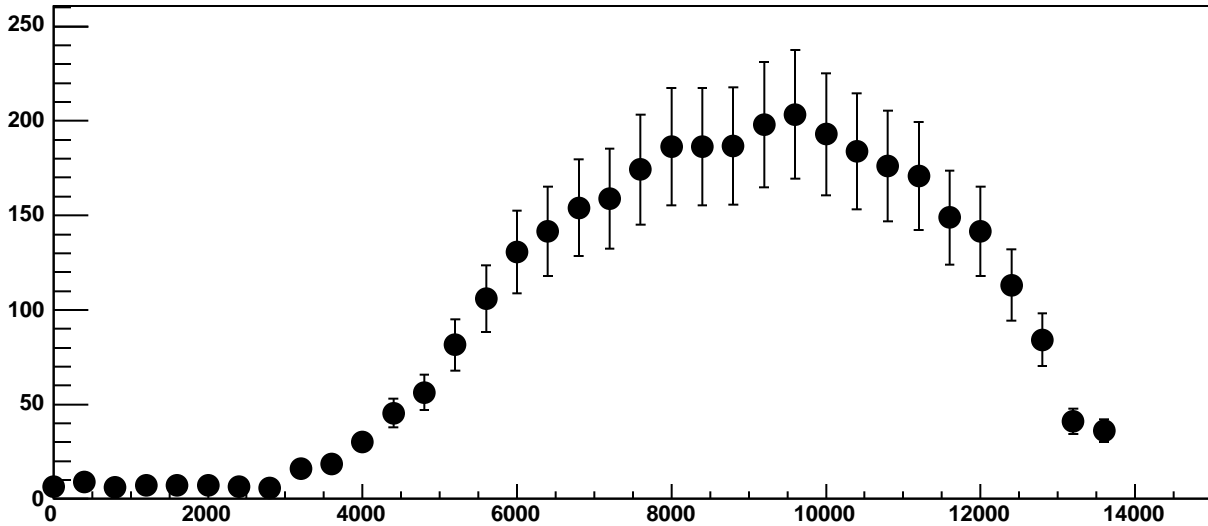
Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



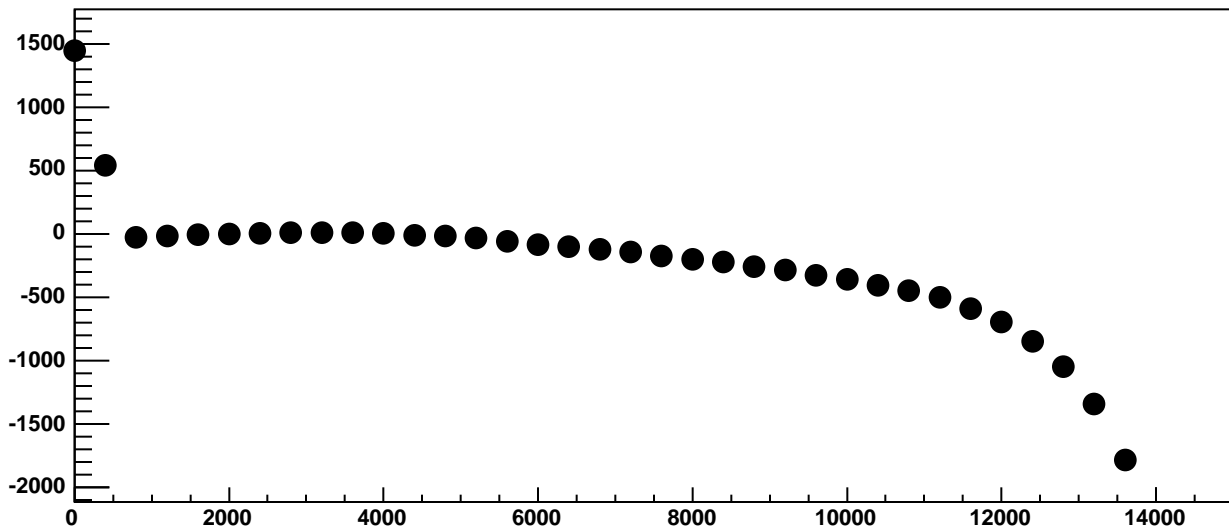
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



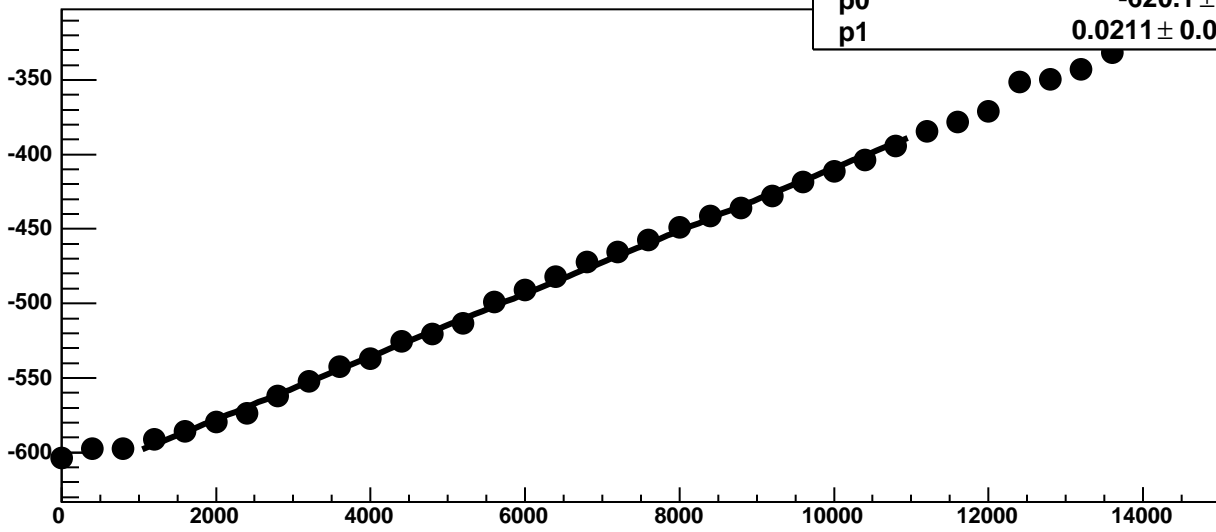
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



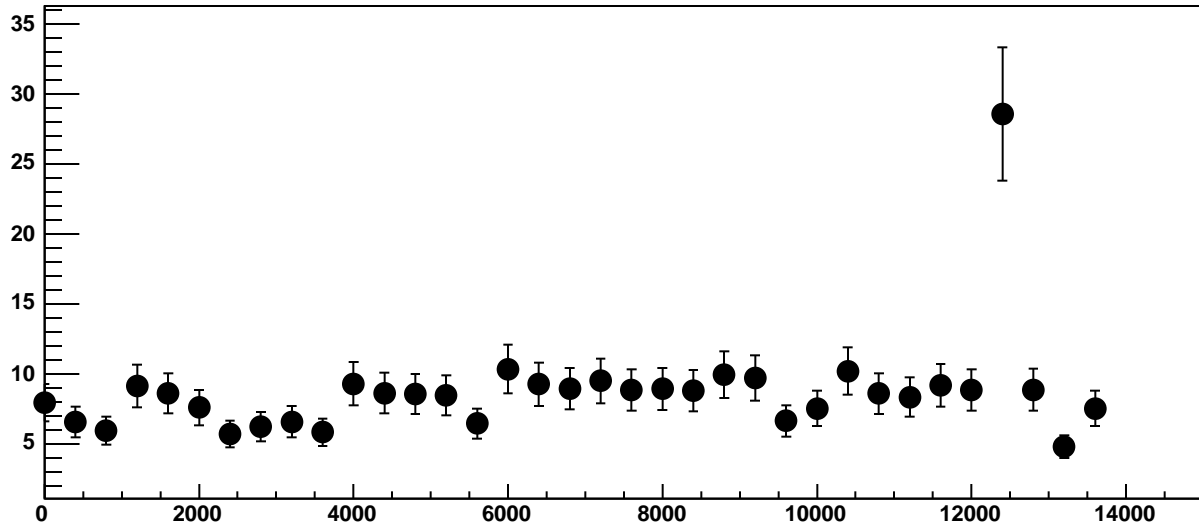
Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



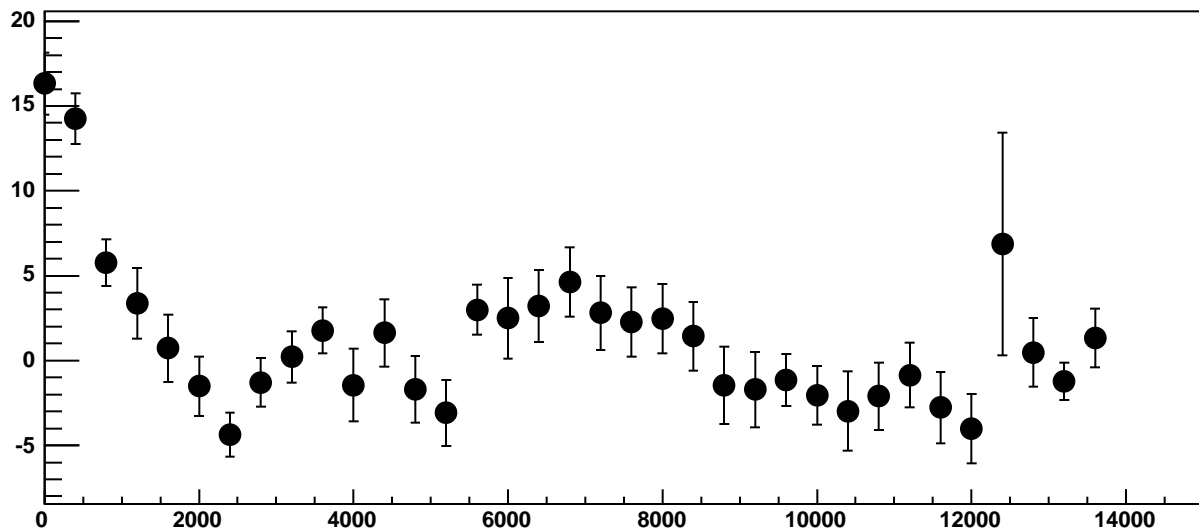
Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

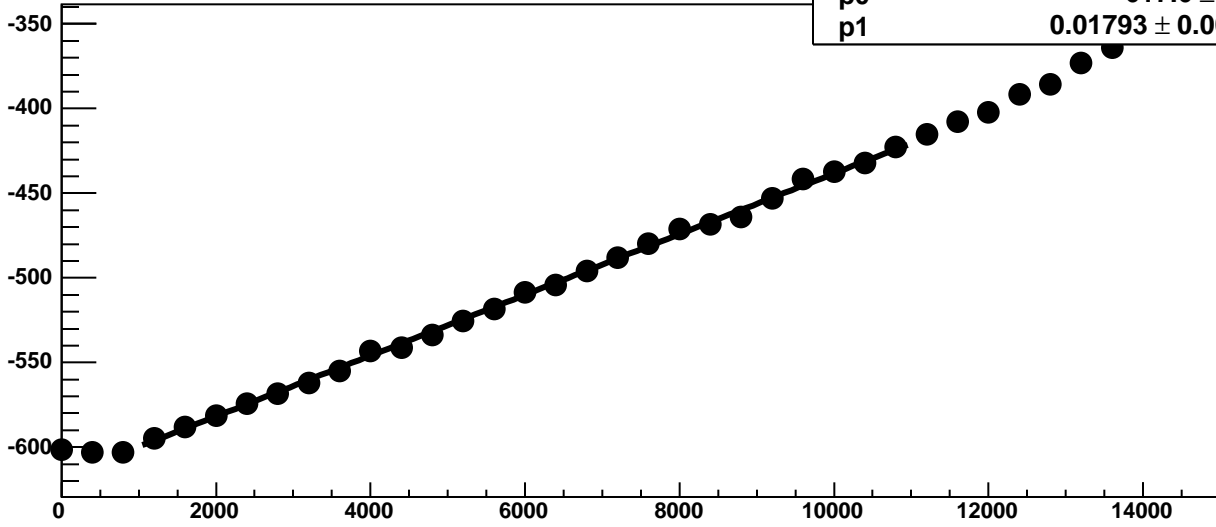


Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



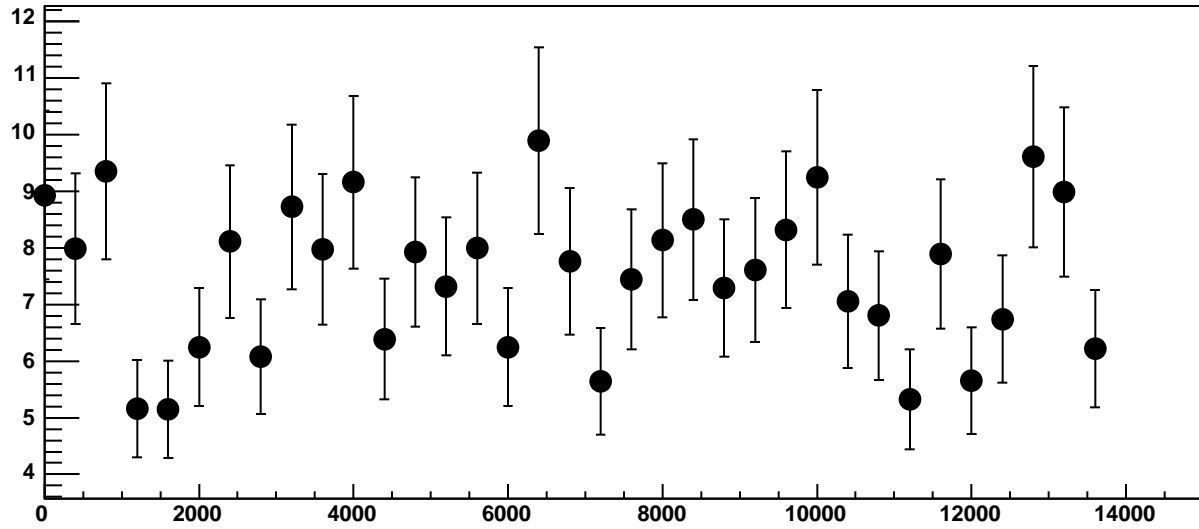


Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

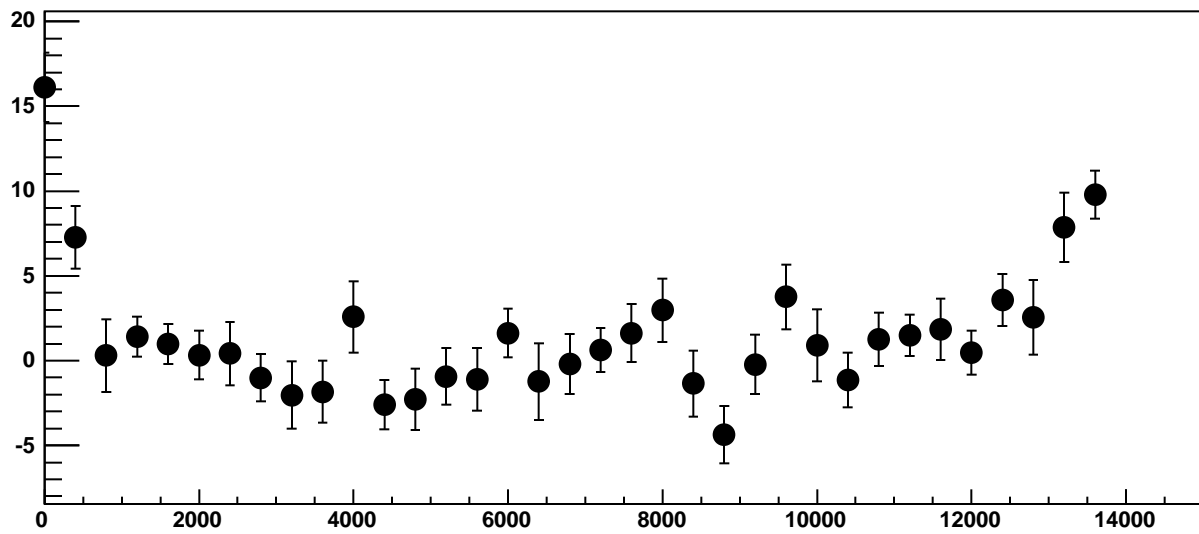


$\chi^2 / \text{ndf}$  29.44 / 23  
p0  $-617.6 \pm 0.6805$   
p1  $0.01793 \pm 0.0001076$

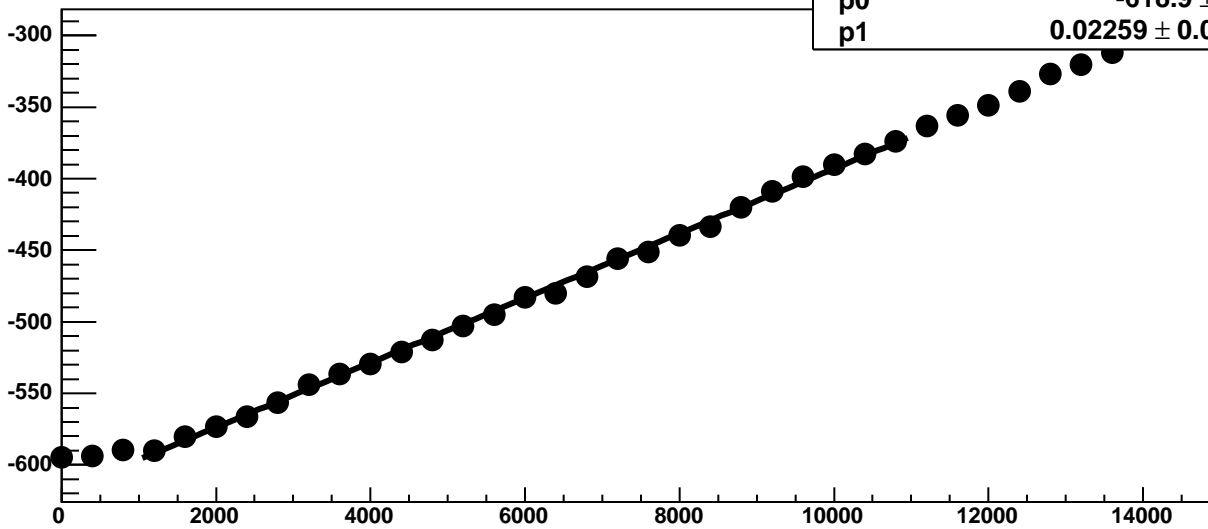
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

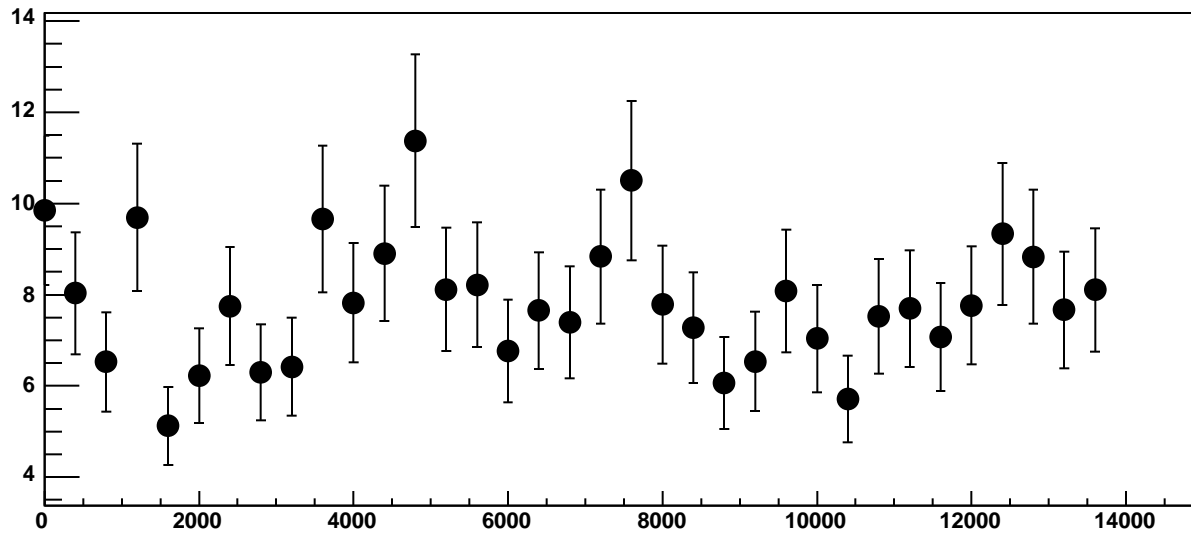


Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

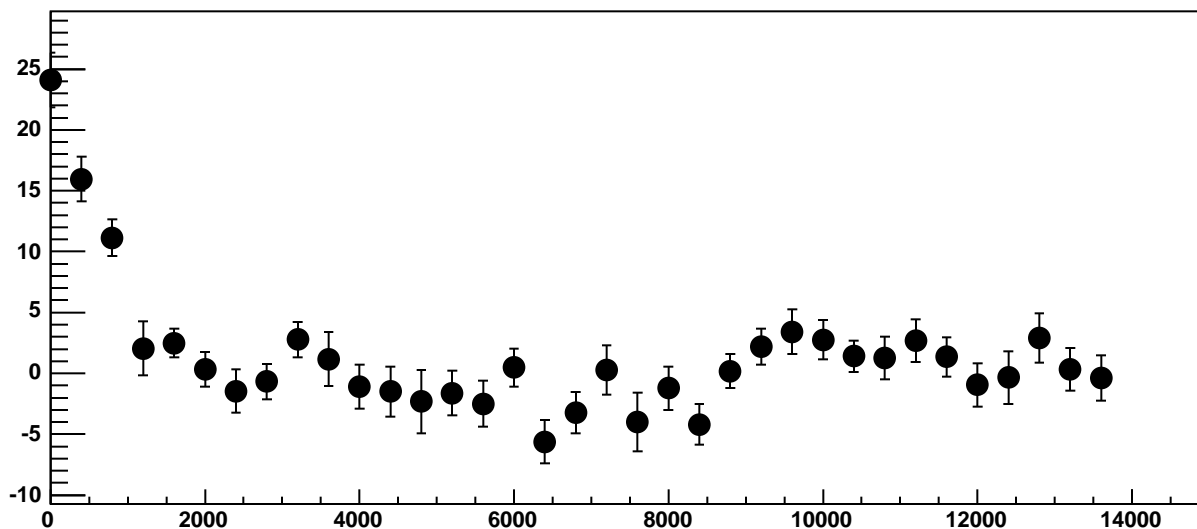


$\chi^2 / \text{ndf}$  47.99 / 23  
p0  $-618.9 \pm 0.7338$   
p1  $0.02259 \pm 0.0001088$

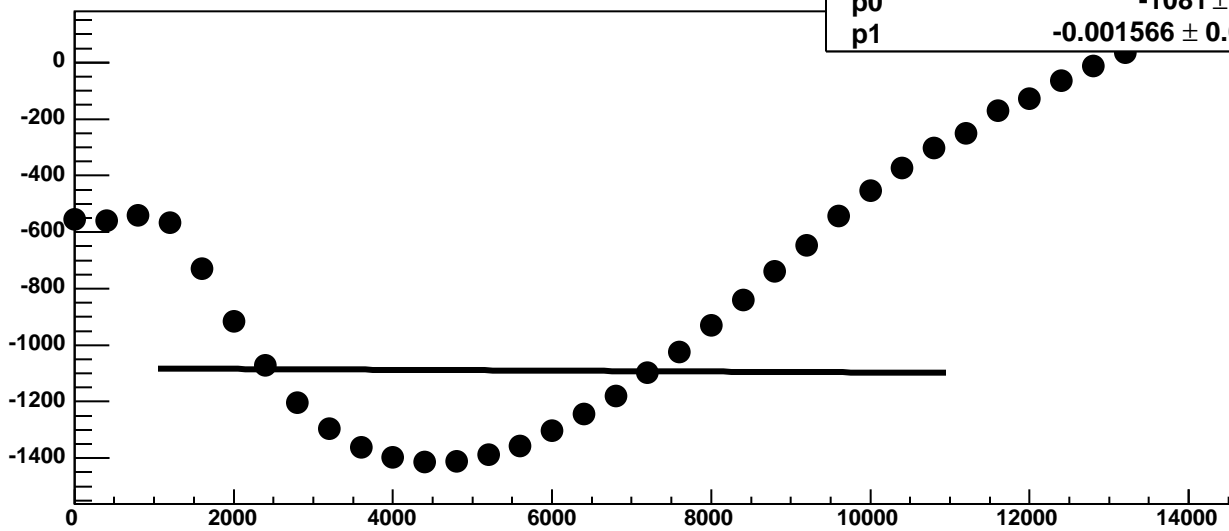
Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

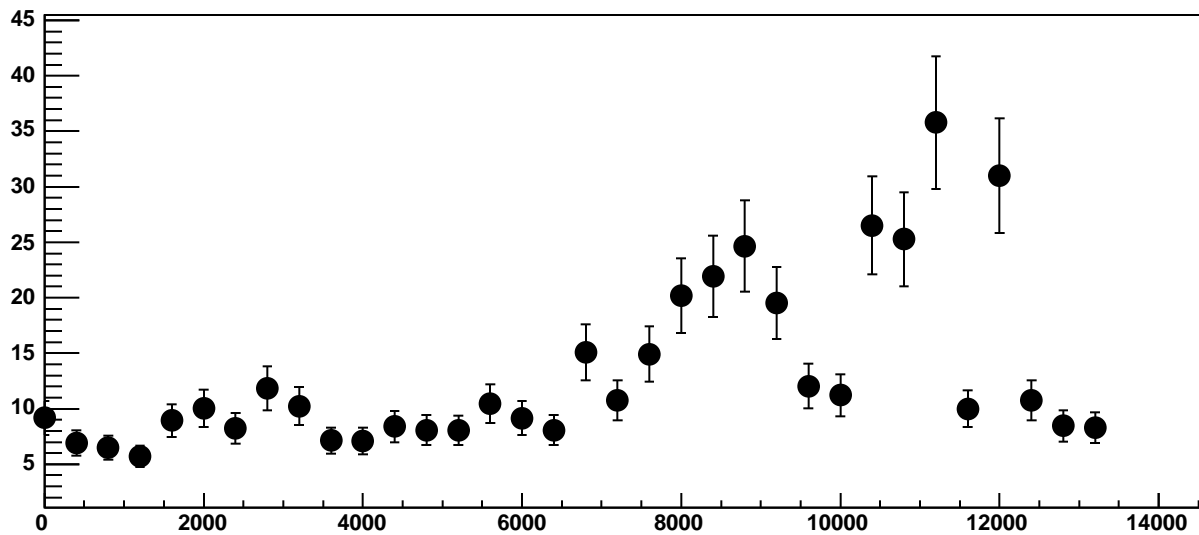


Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

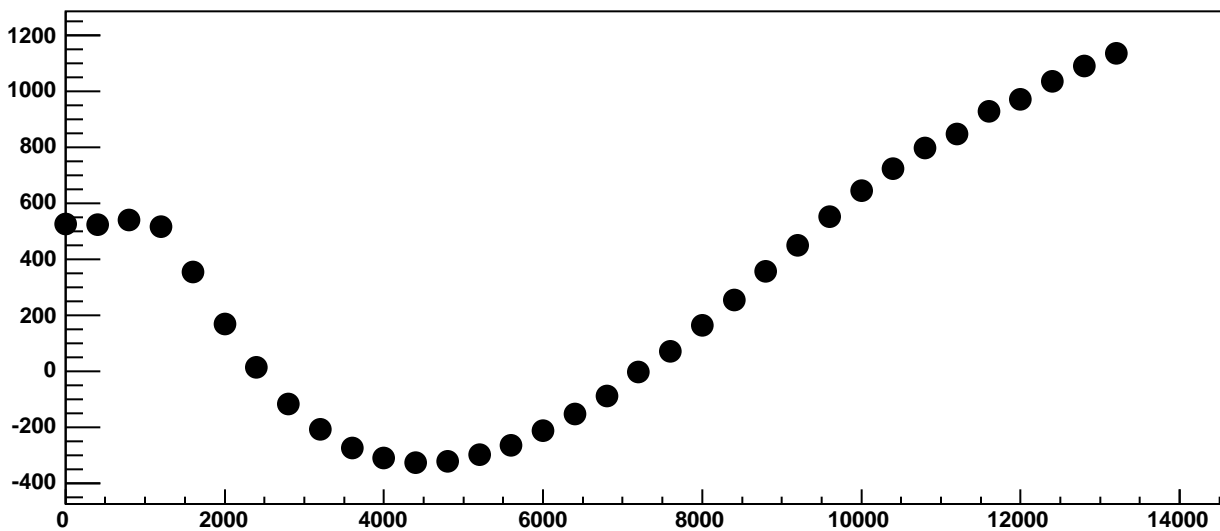


$\chi^2 / \text{ndf}$  5.352e+05 / 23  
p0 -1081 ± 0.9544  
p1 -0.001566 ± 0.000185

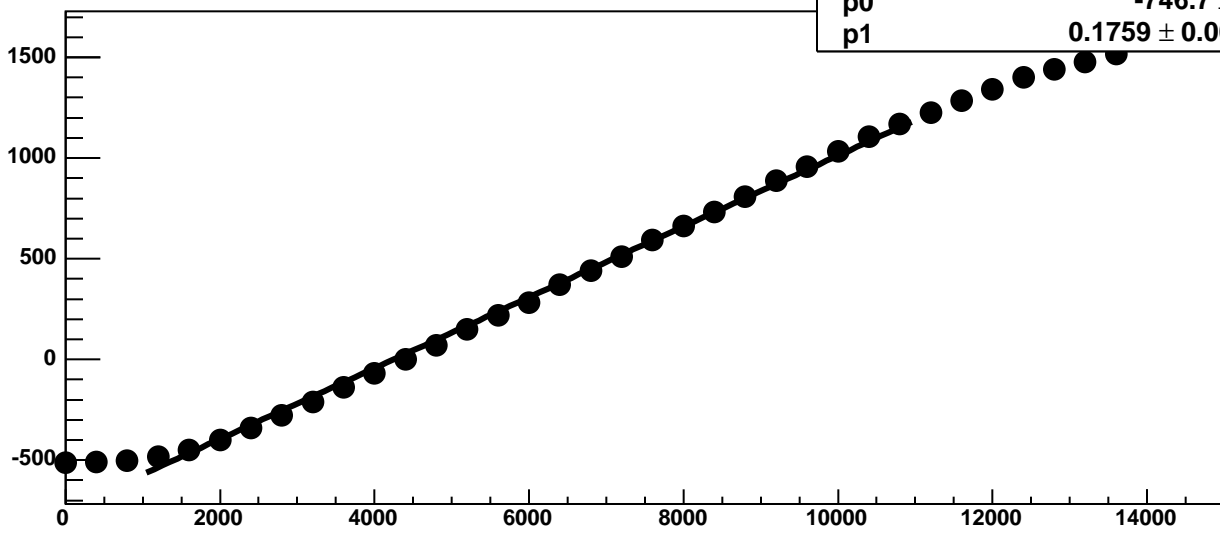
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



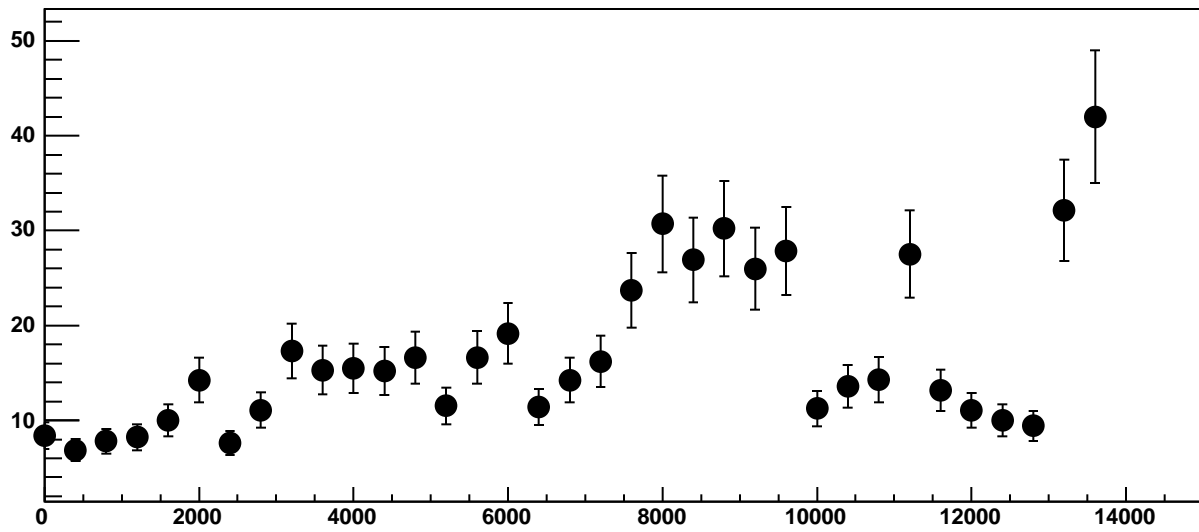
Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



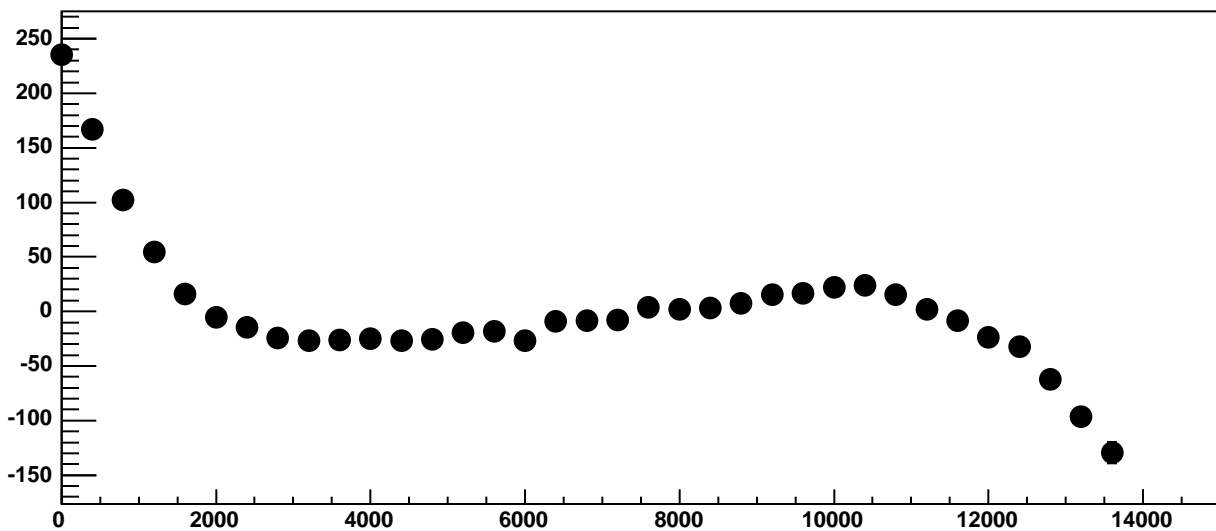
Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



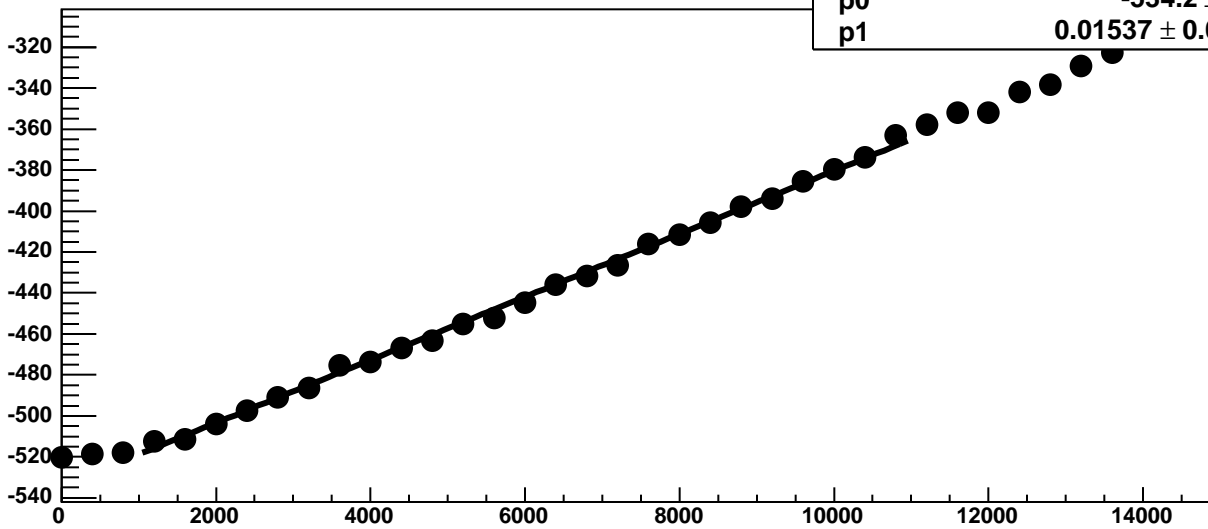
Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

39.27 / 23

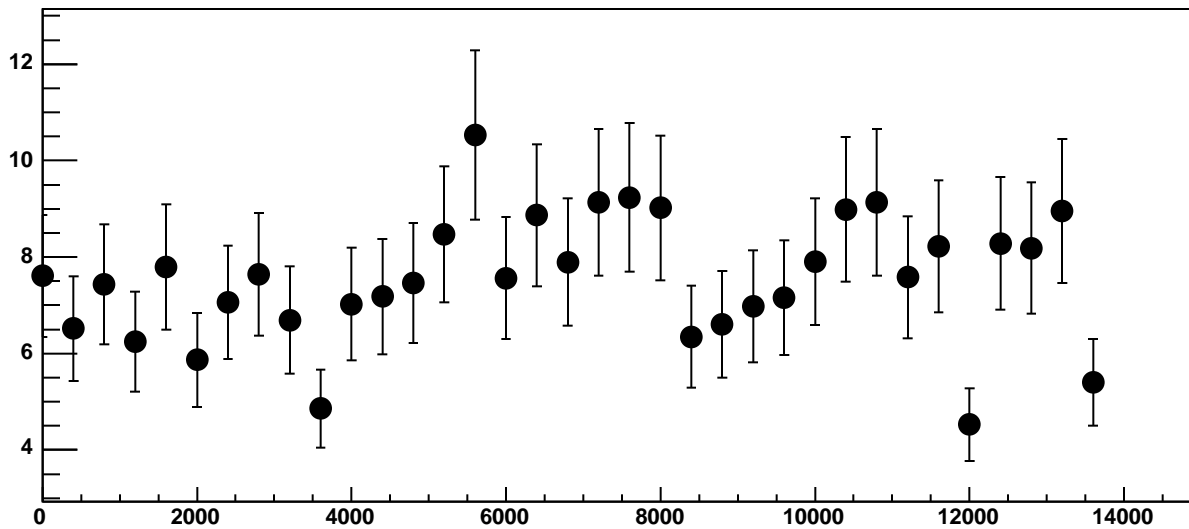
p0

$-534.2 \pm 0.7241$

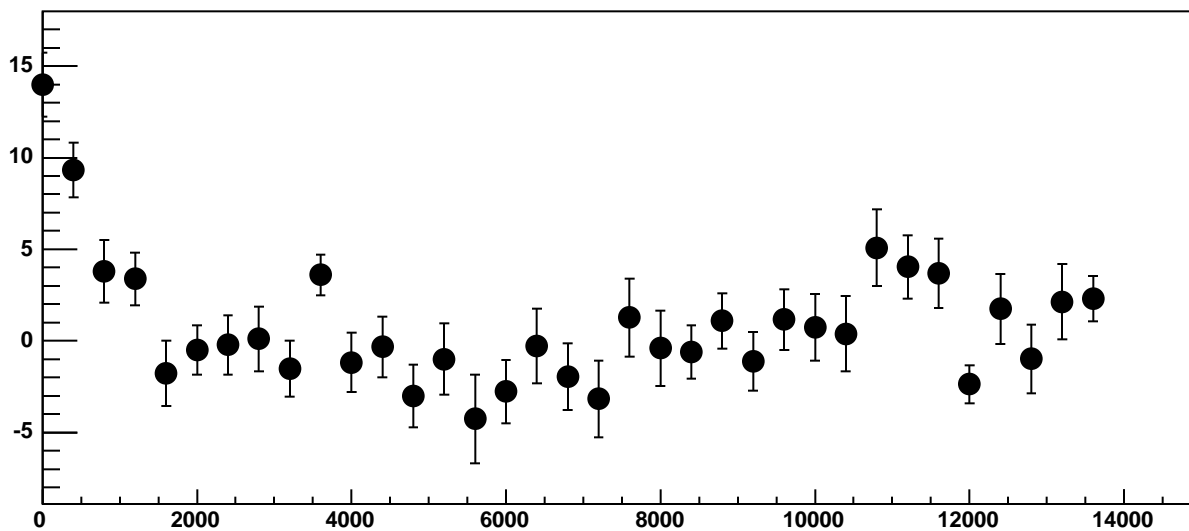
p1

$0.01537 \pm 0.0001151$

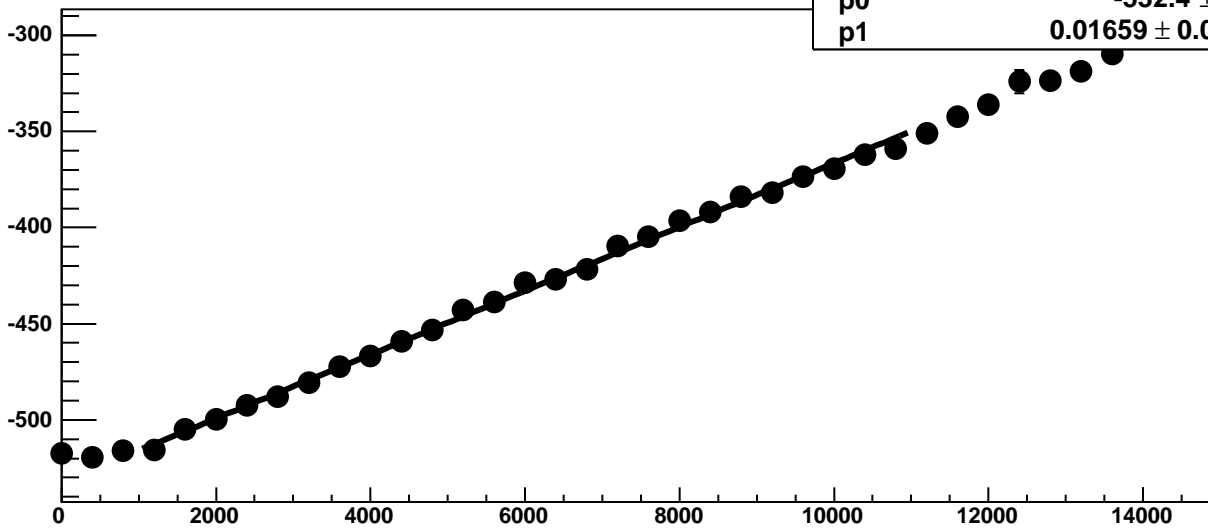
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

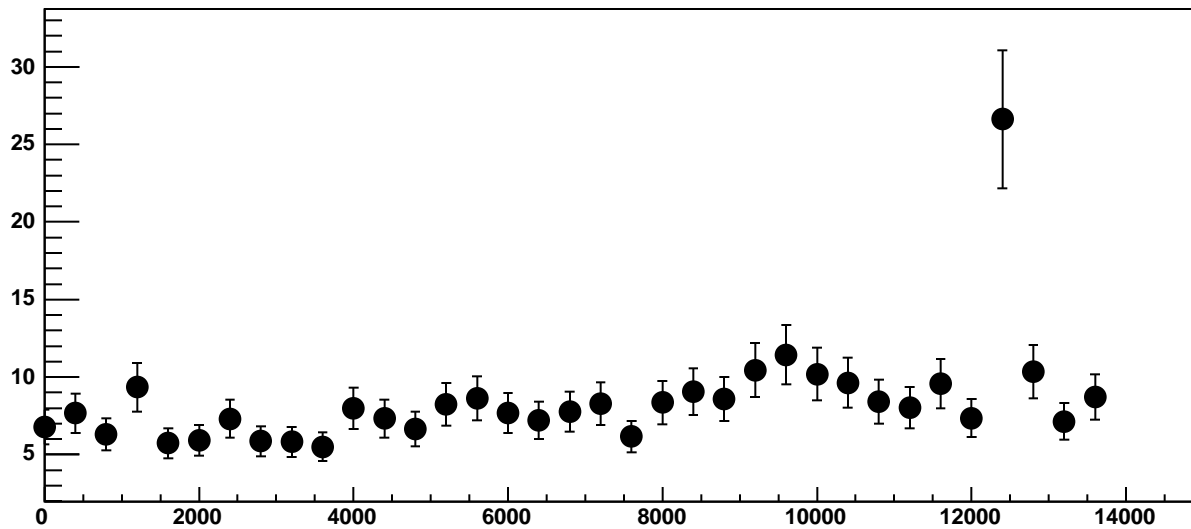


Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

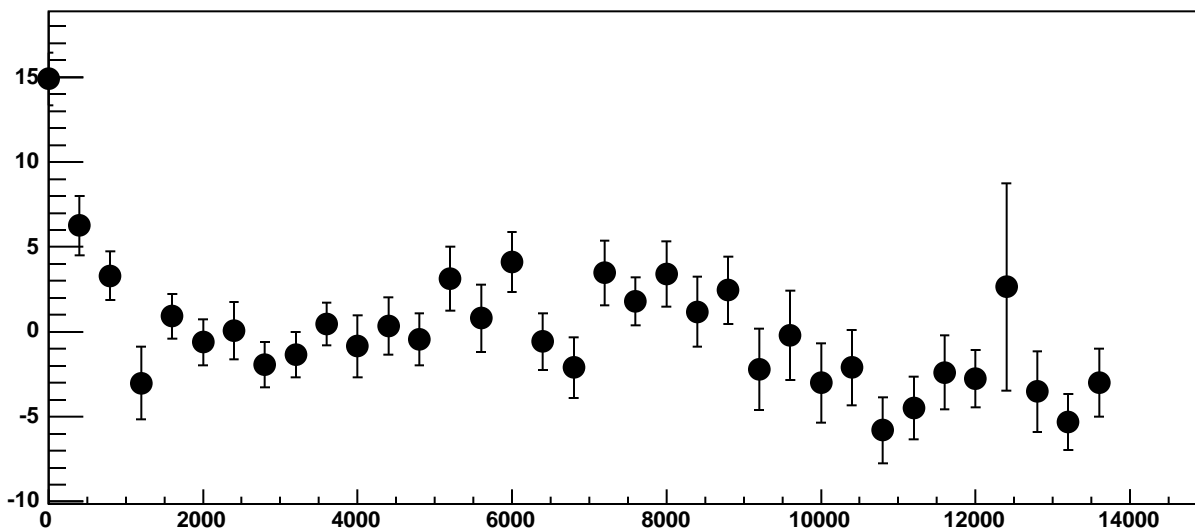


$\chi^2 / \text{ndf}$  38.49 / 23  
p0  $-532.4 \pm 0.7376$   
p1  $0.01659 \pm 0.0001248$

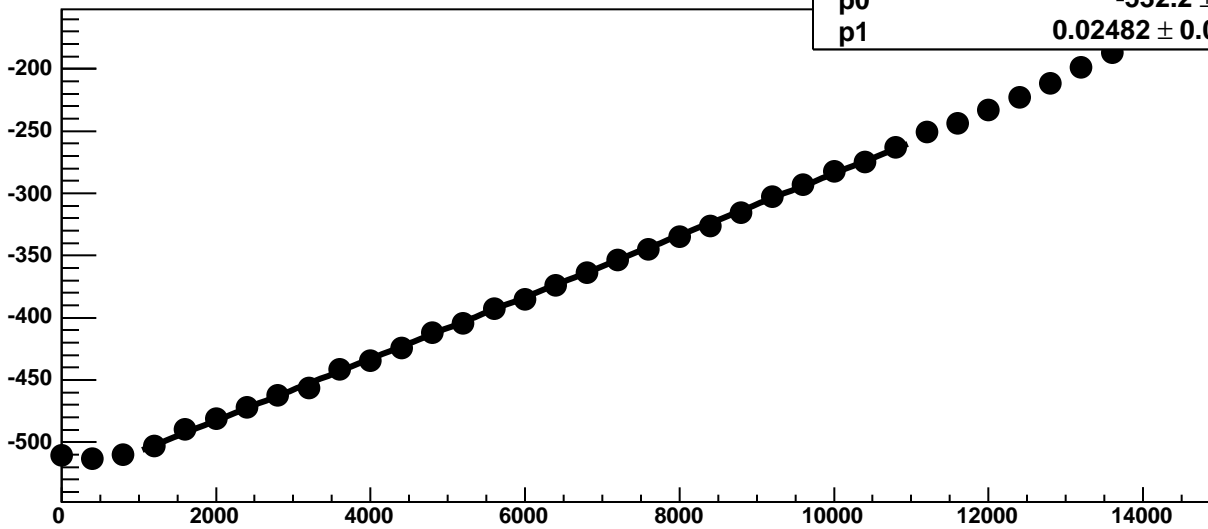
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

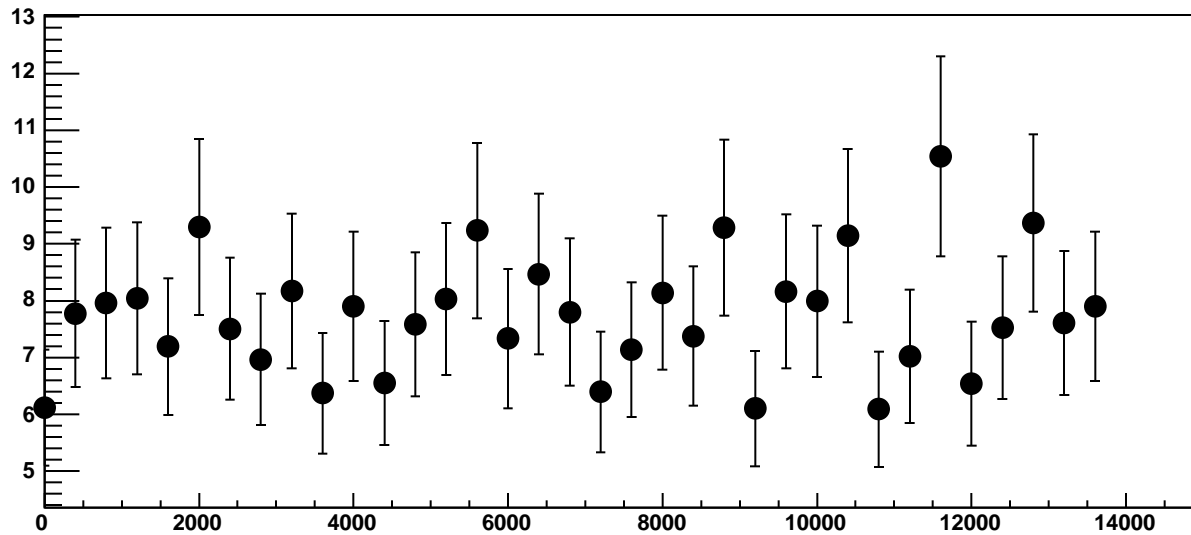


Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

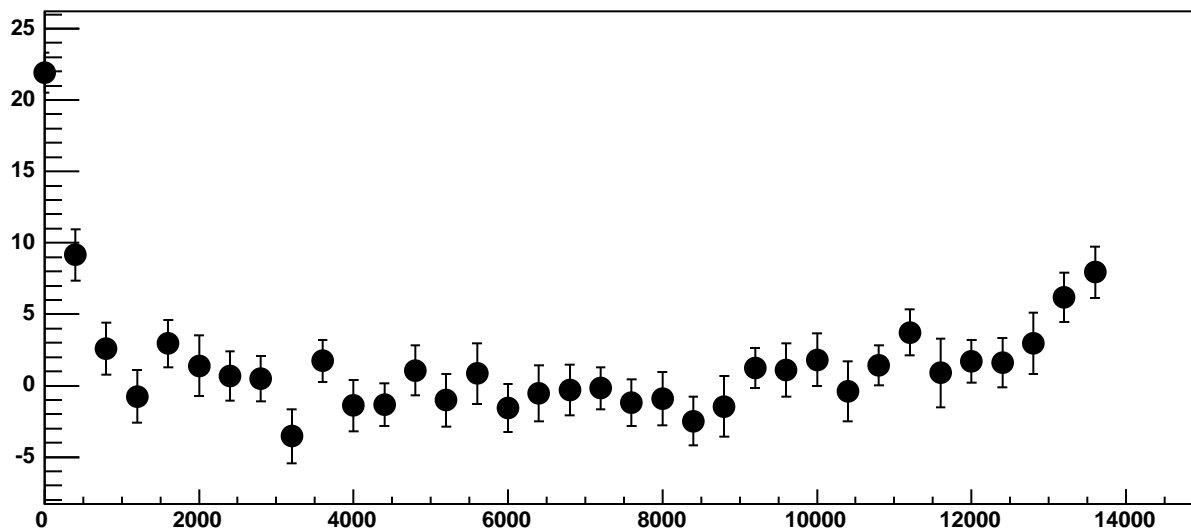


$\chi^2 / \text{ndf}$  18.83 / 23  
p0  $-532.2 \pm 0.8006$   
p1  $0.02482 \pm 0.0001192$

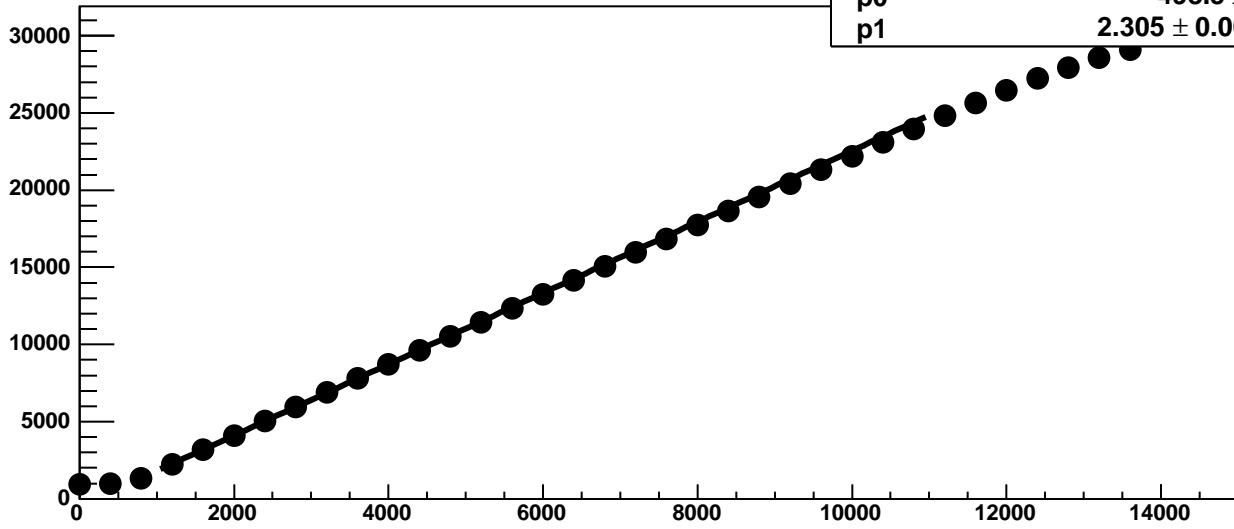
Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



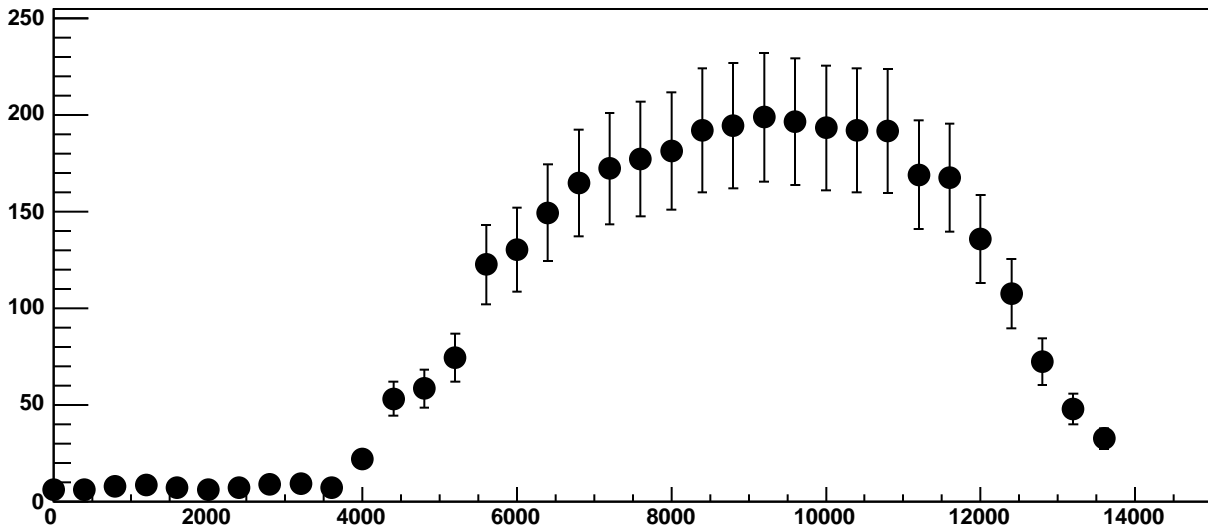
Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



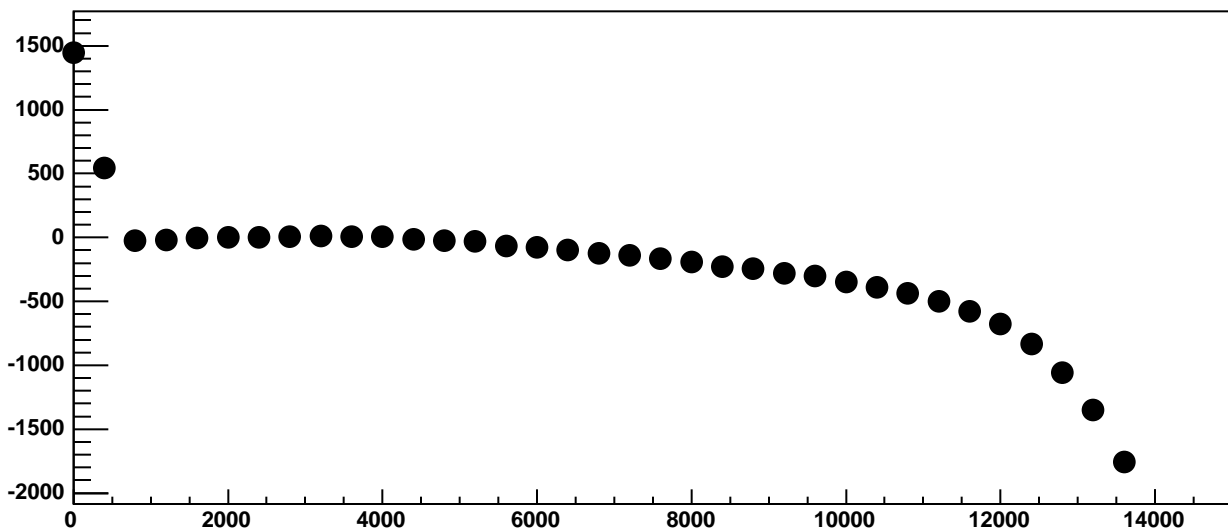
Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

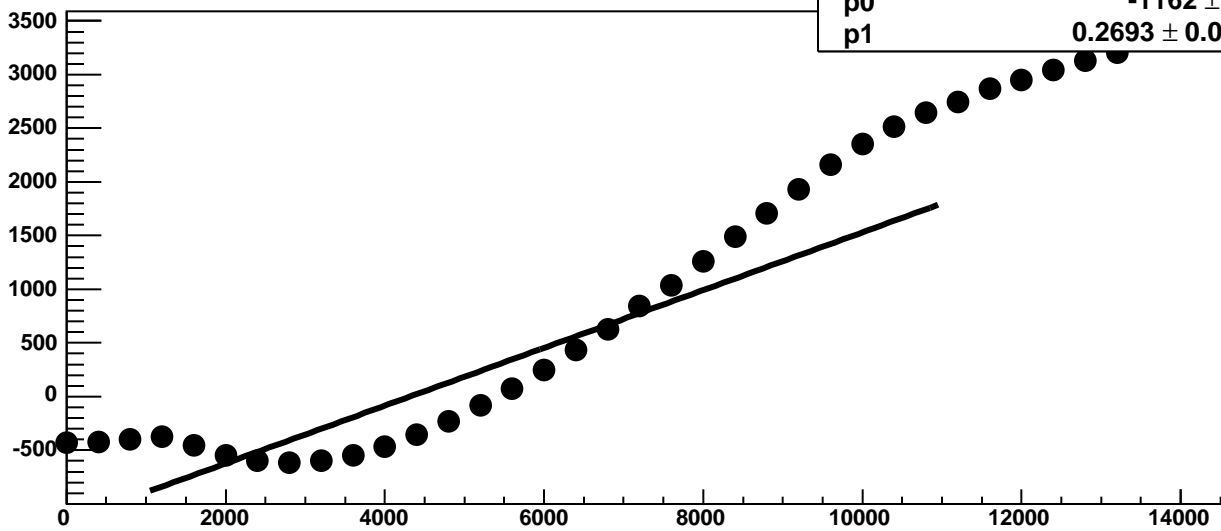


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



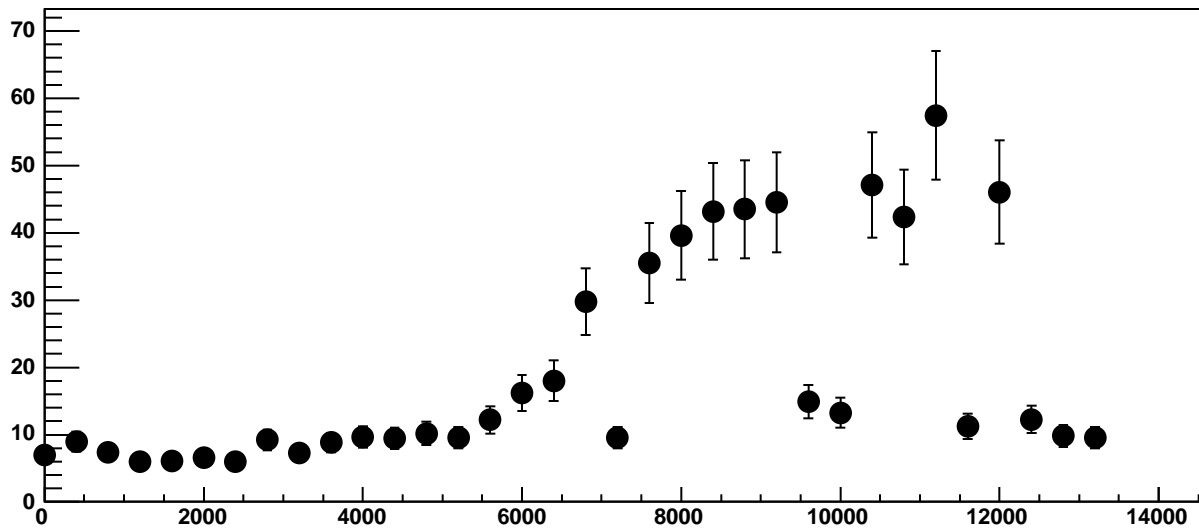


Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

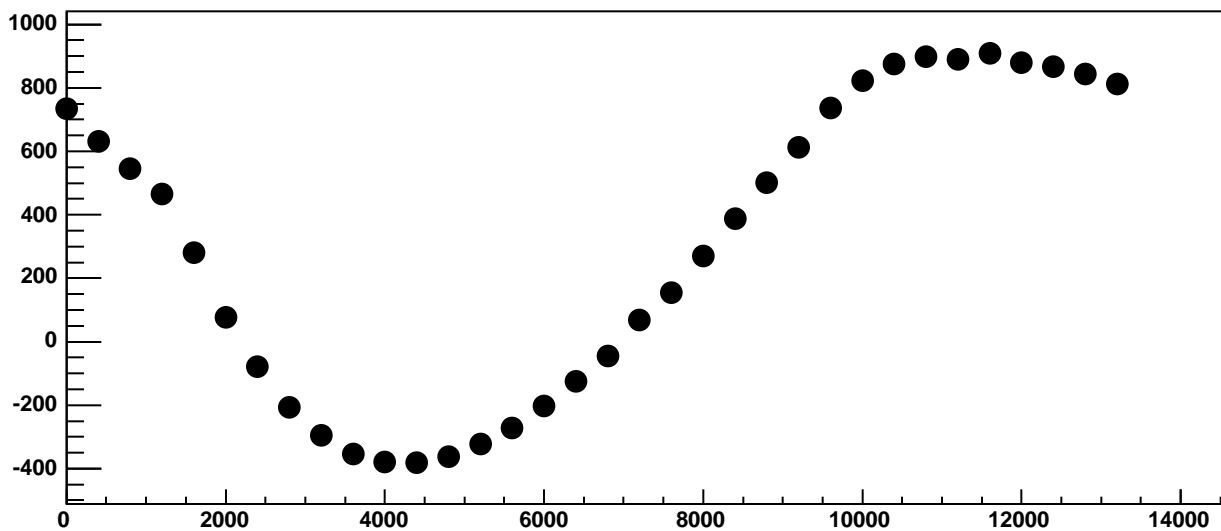


$\chi^2 / \text{ndf}$  4.969e+05 / 23  
p0 -1162 ± 0.8946  
p1 0.2693 ± 0.0002121

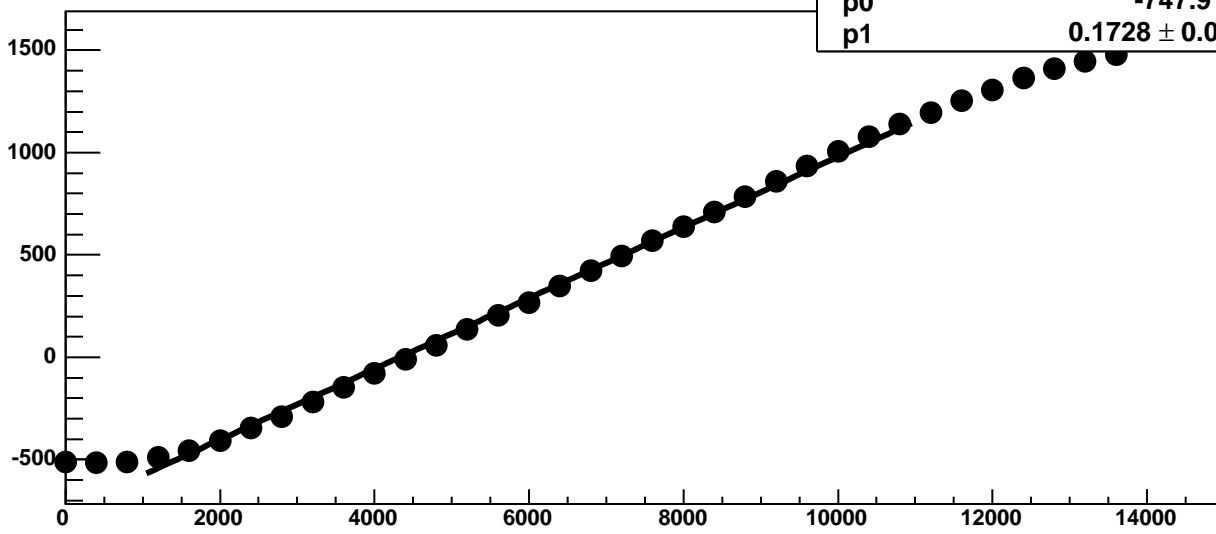
Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



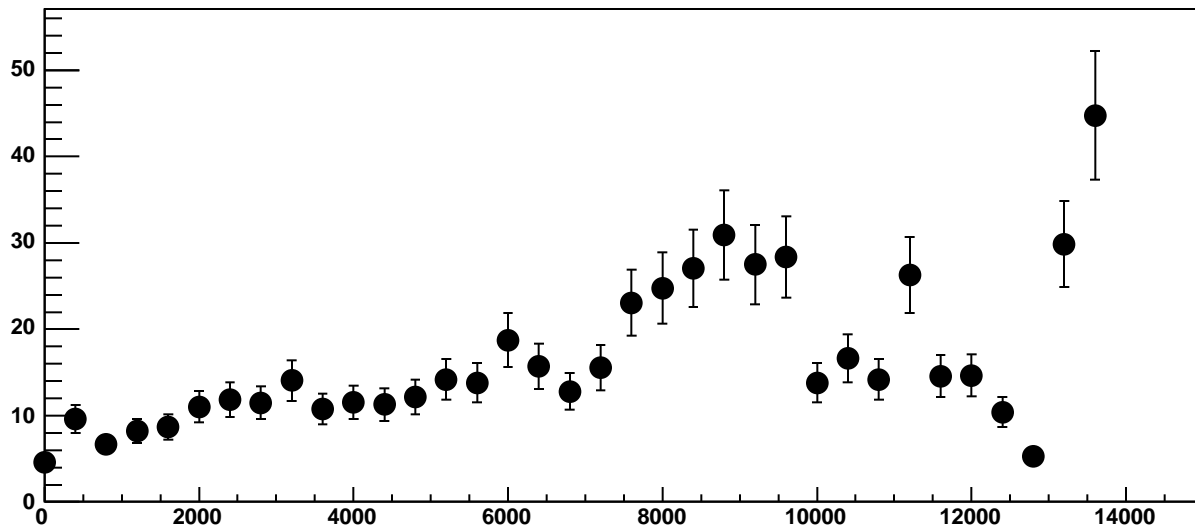
Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



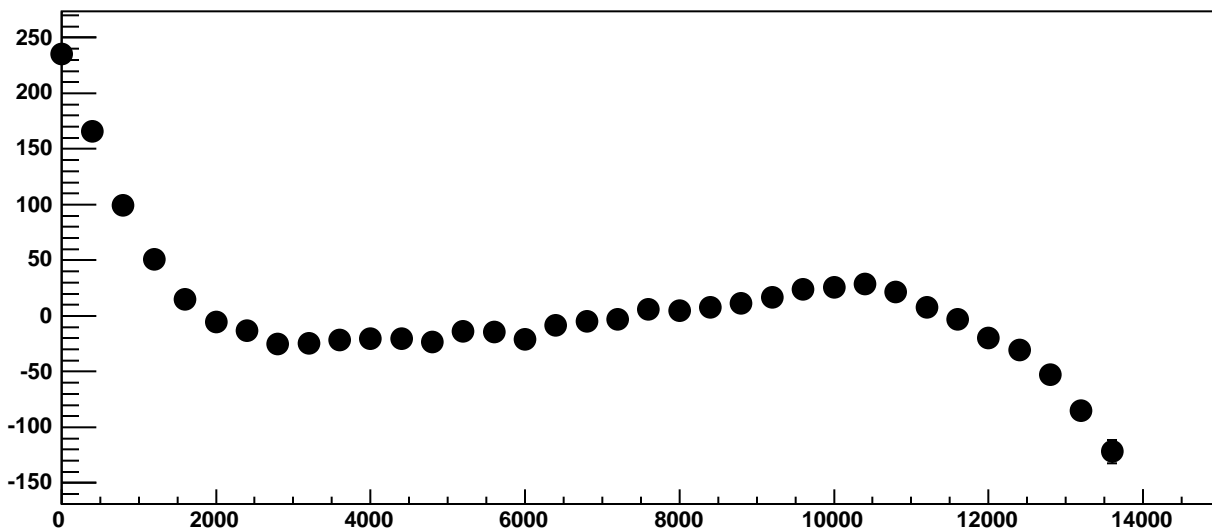
Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



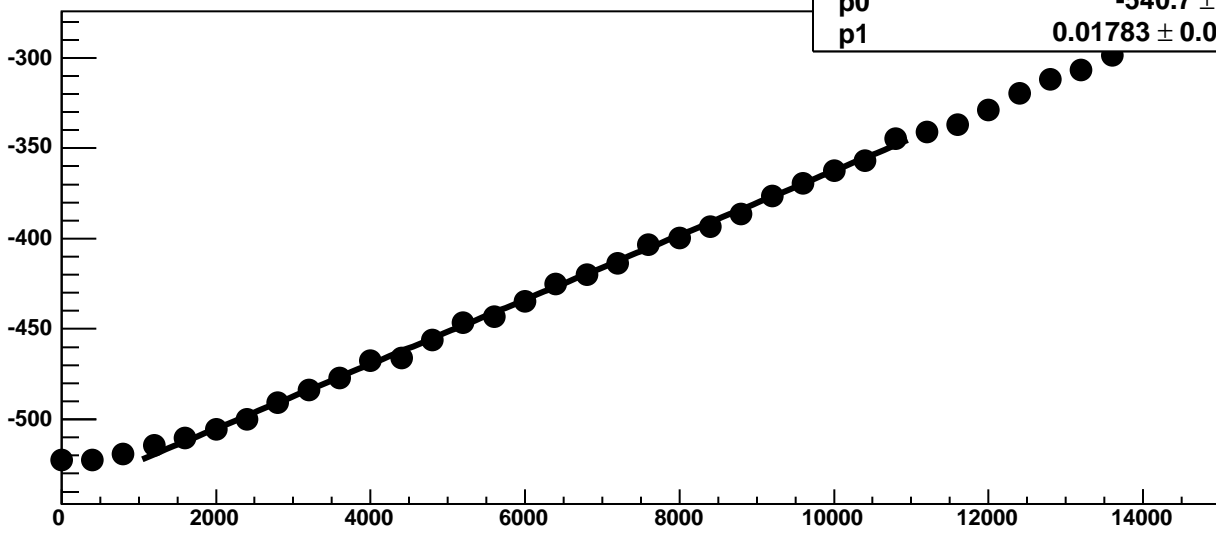
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

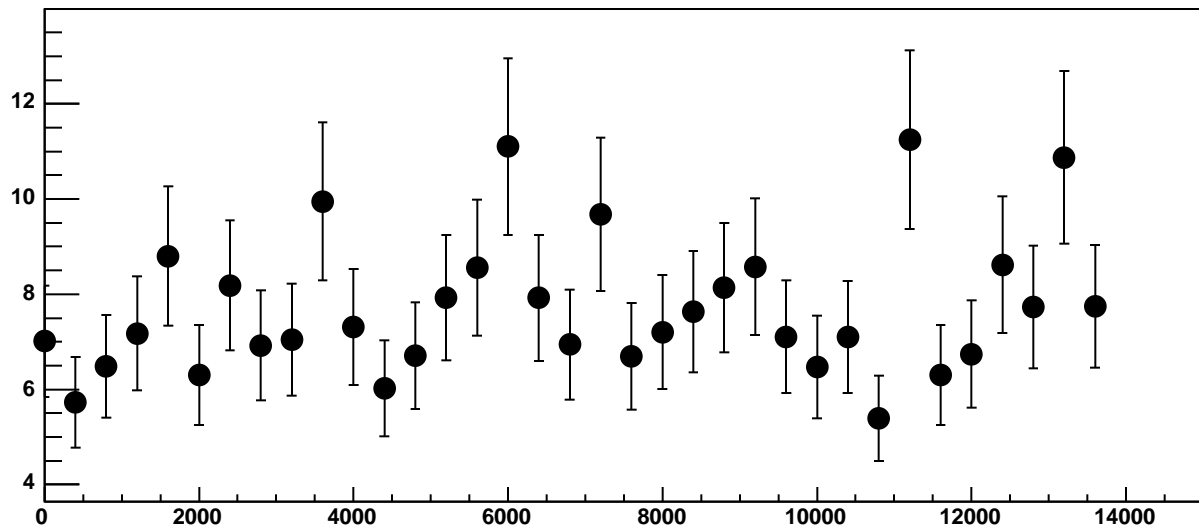


Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

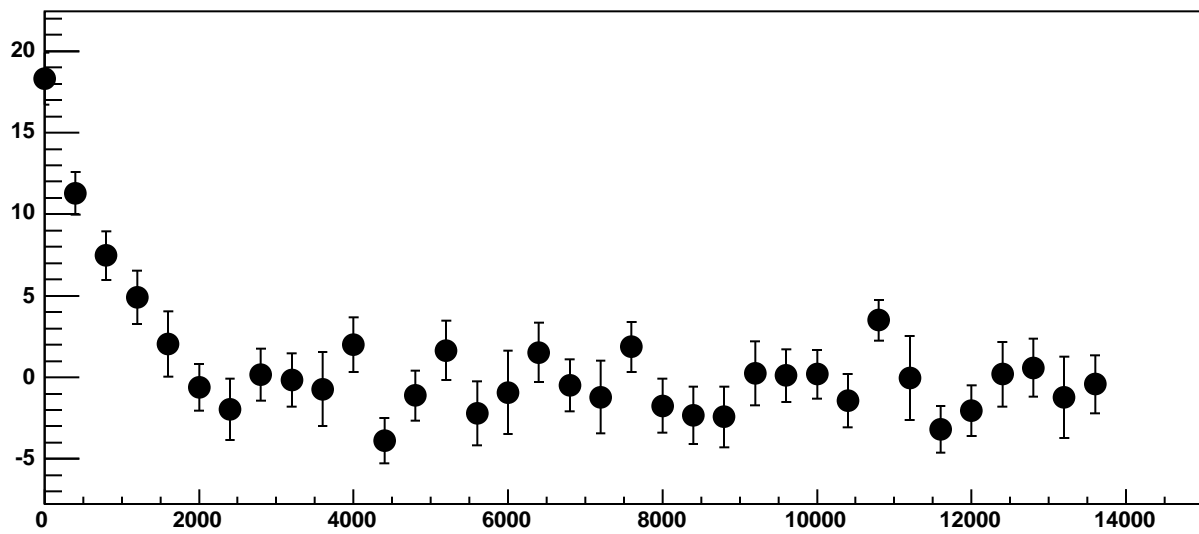


$\chi^2 / \text{ndf}$  39.3 / 23  
p0  $-540.7 \pm 0.7659$   
p1  $0.01783 \pm 0.0001115$

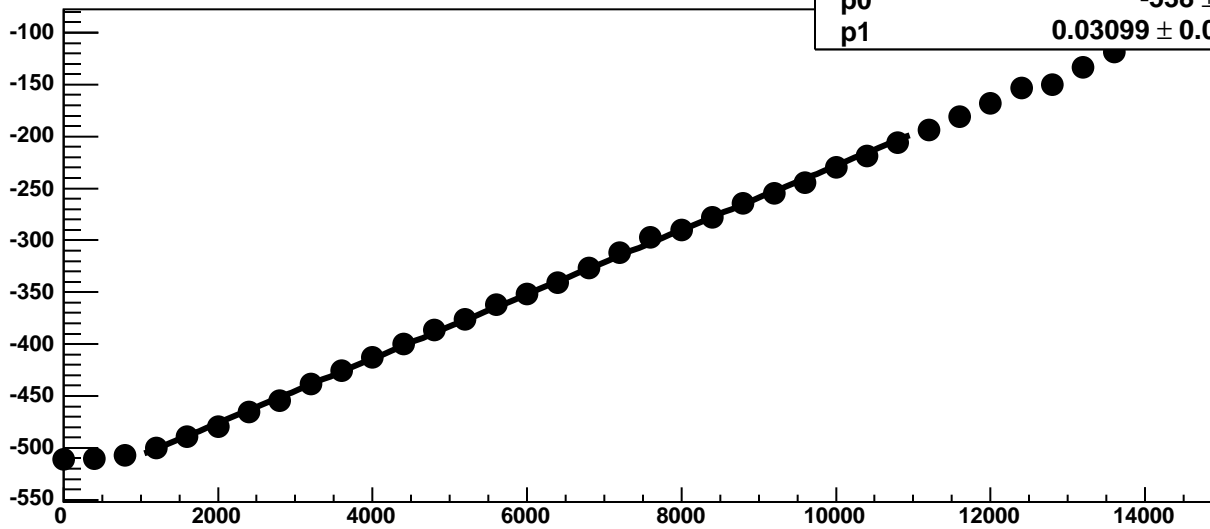
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



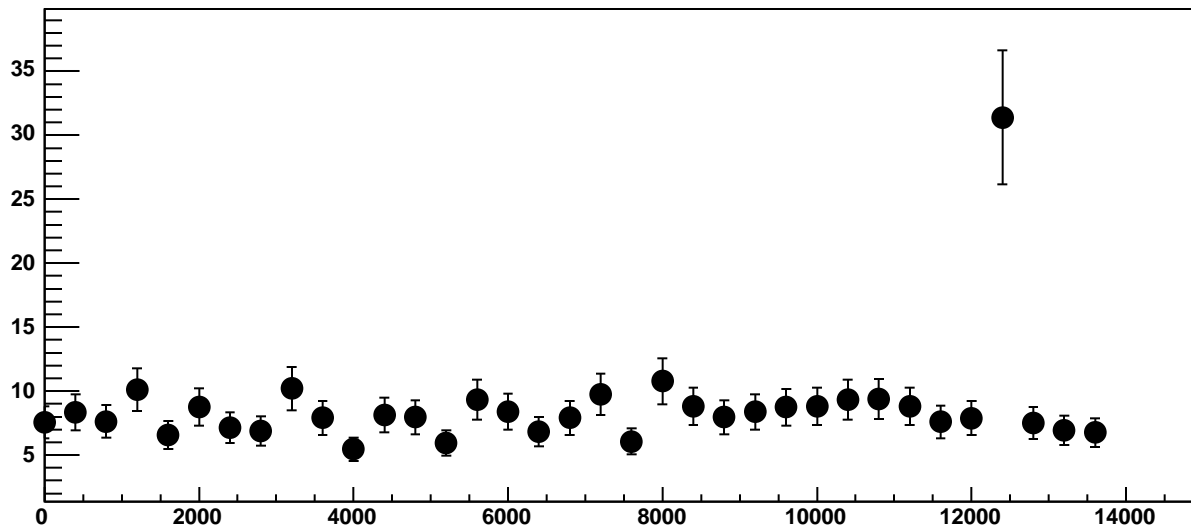
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



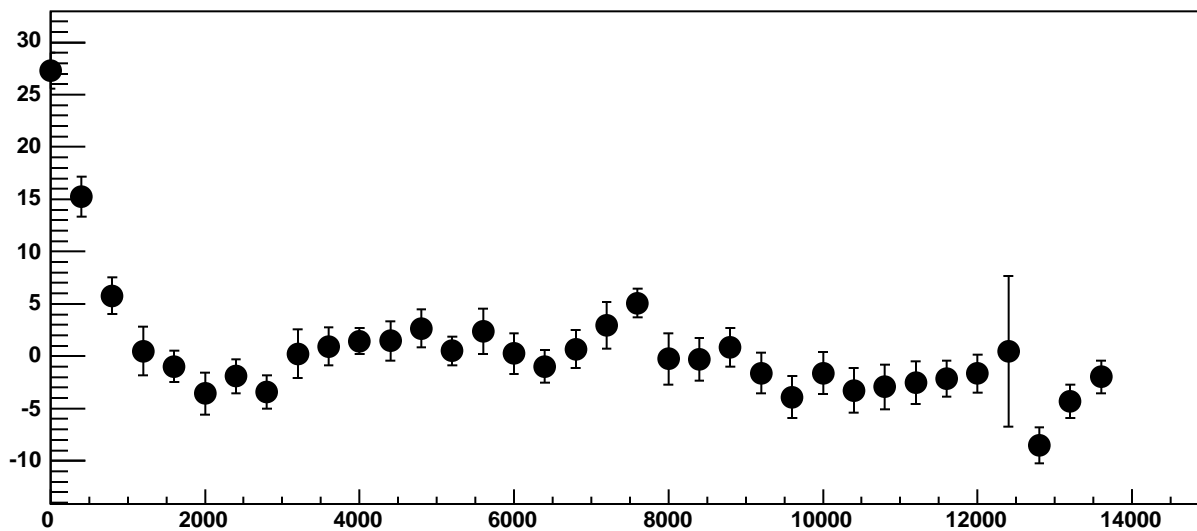
Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



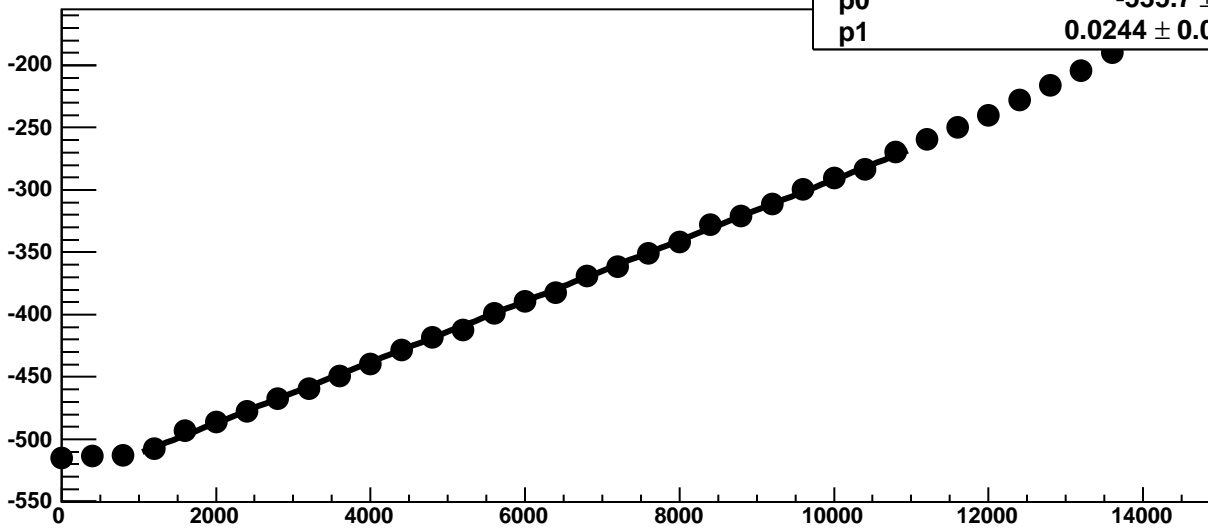
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



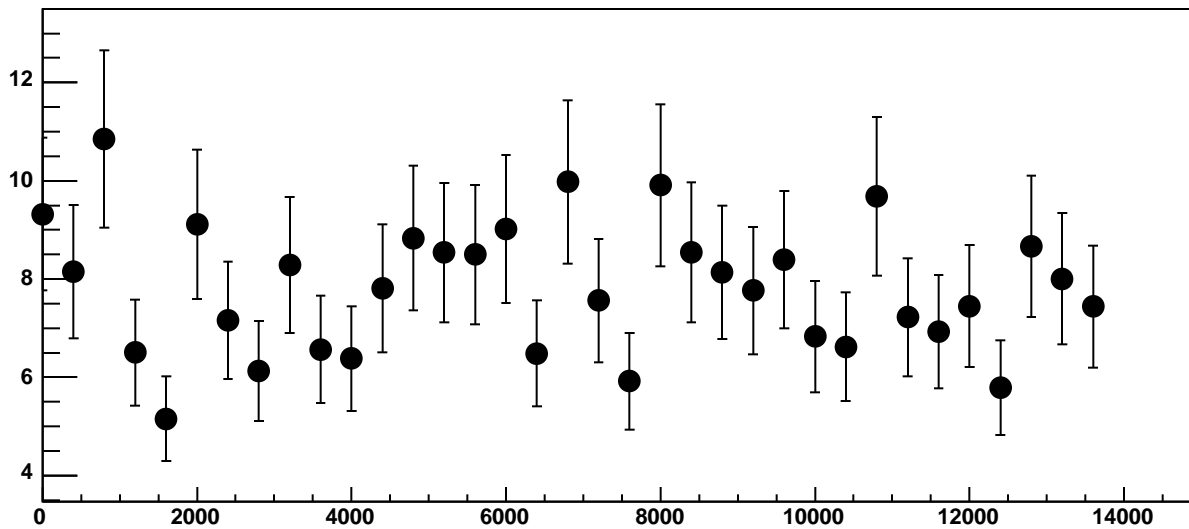
Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



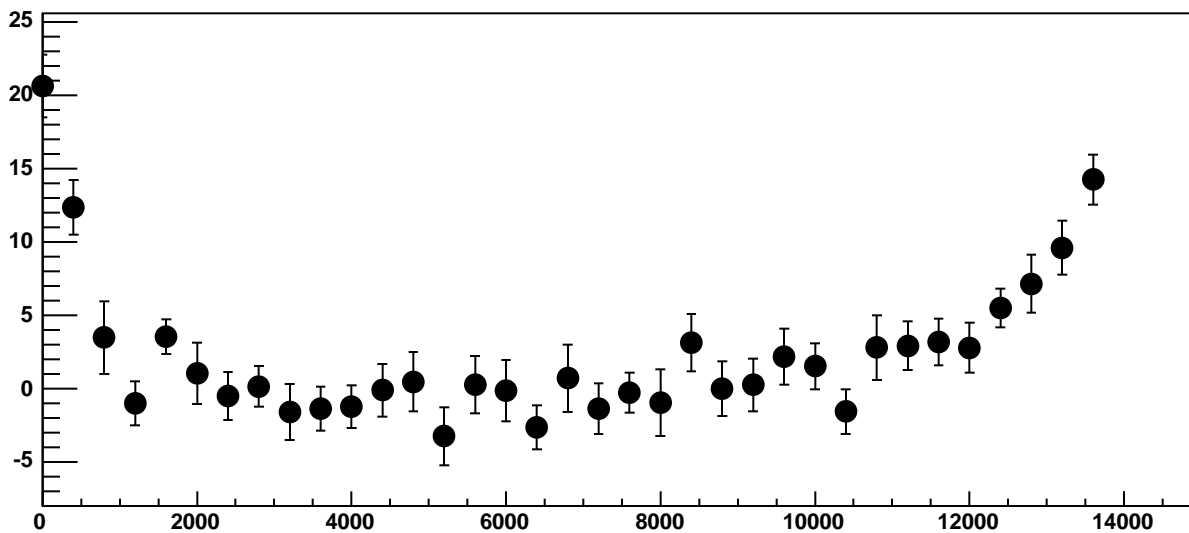
Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



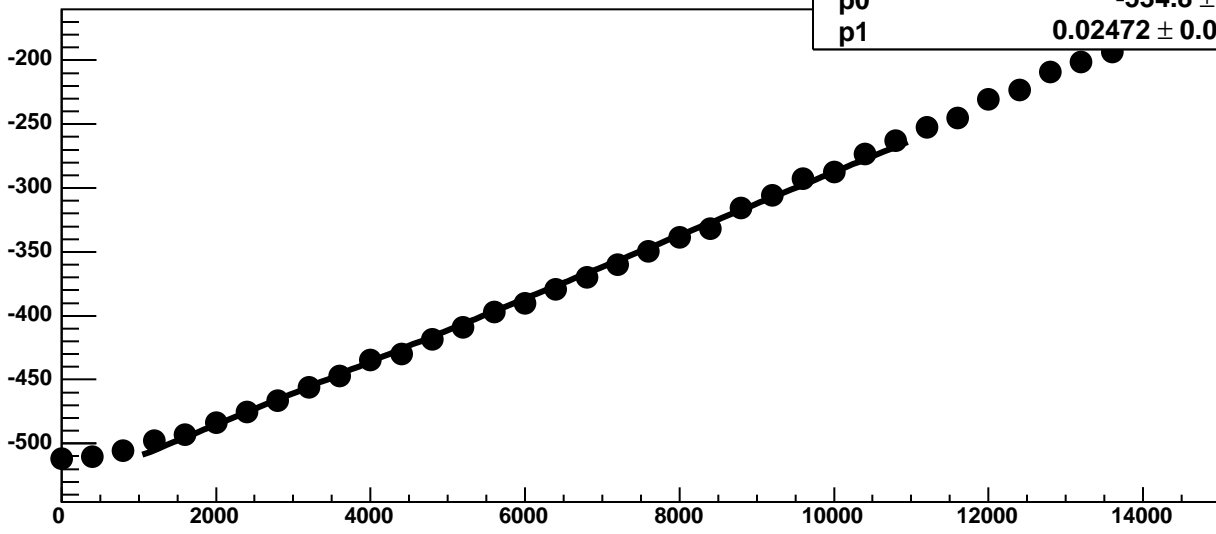
Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

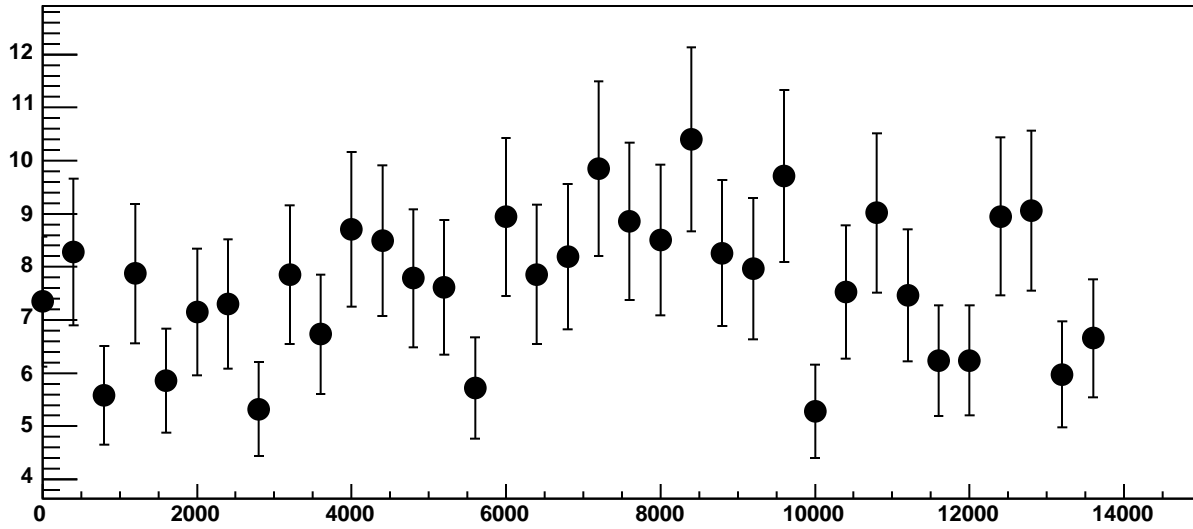


Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

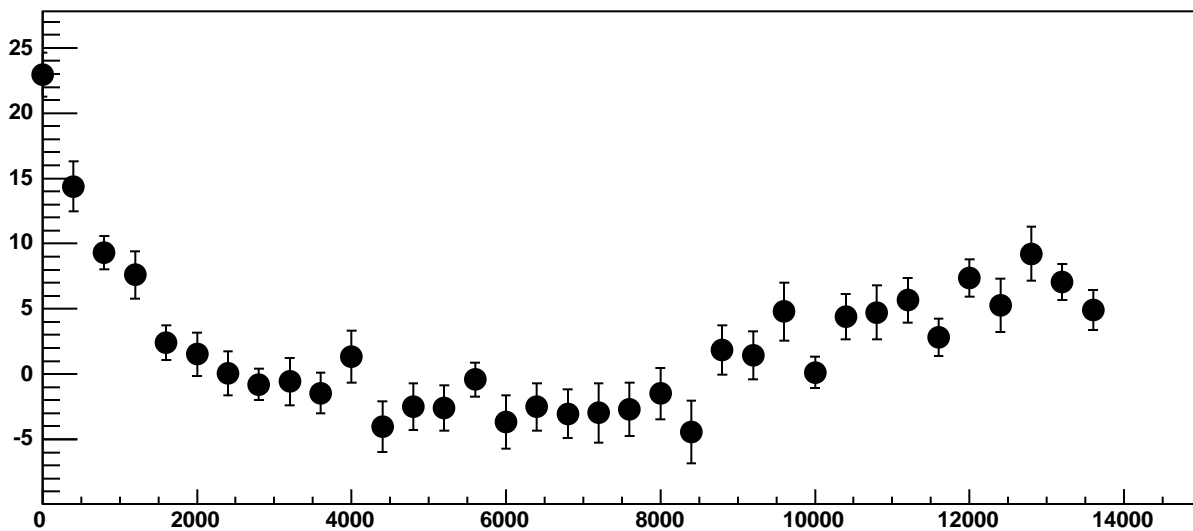


$\chi^2 / \text{ndf}$  65.31 / 23  
p0  $-534.8 \pm 0.7384$   
p1  $0.02472 \pm 0.0001152$

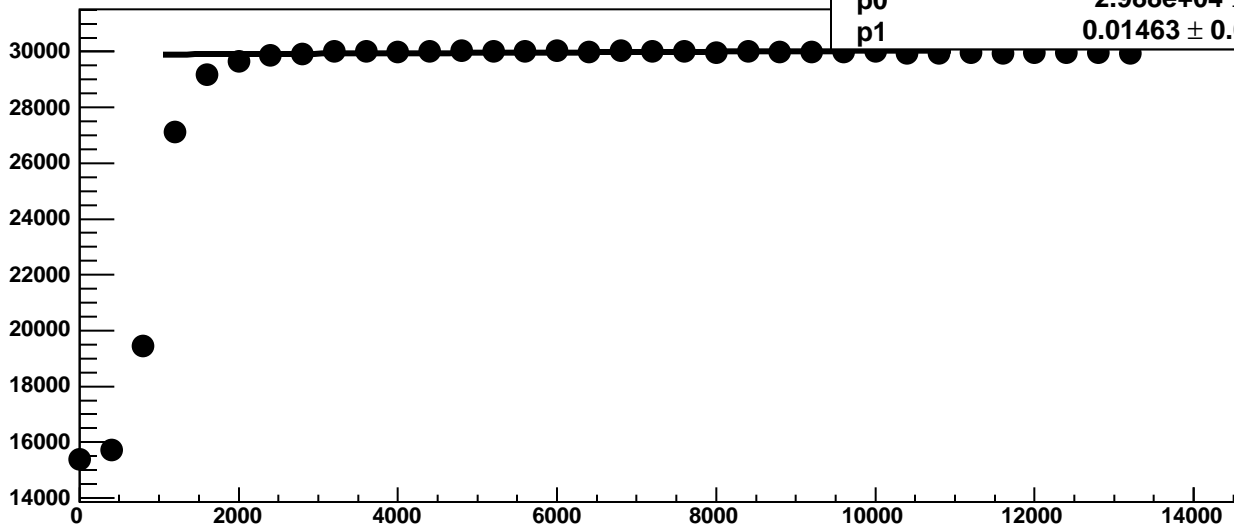
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



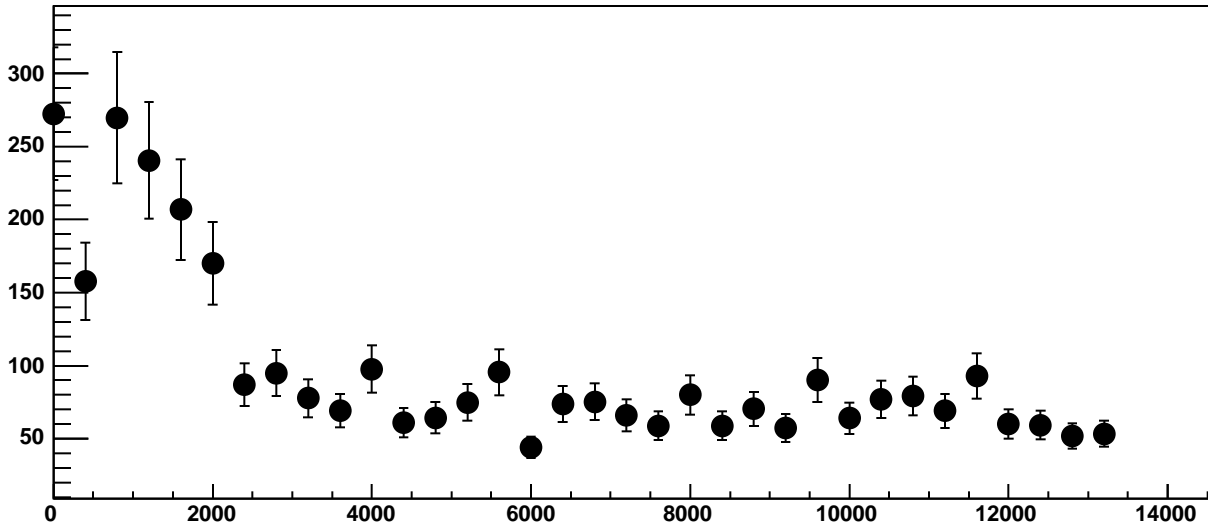
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



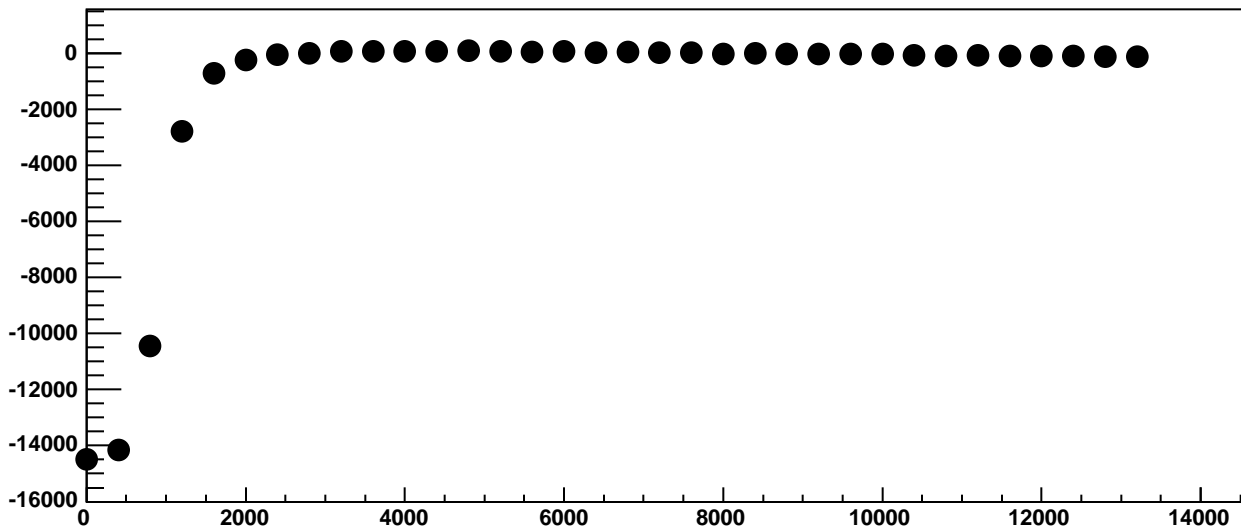
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



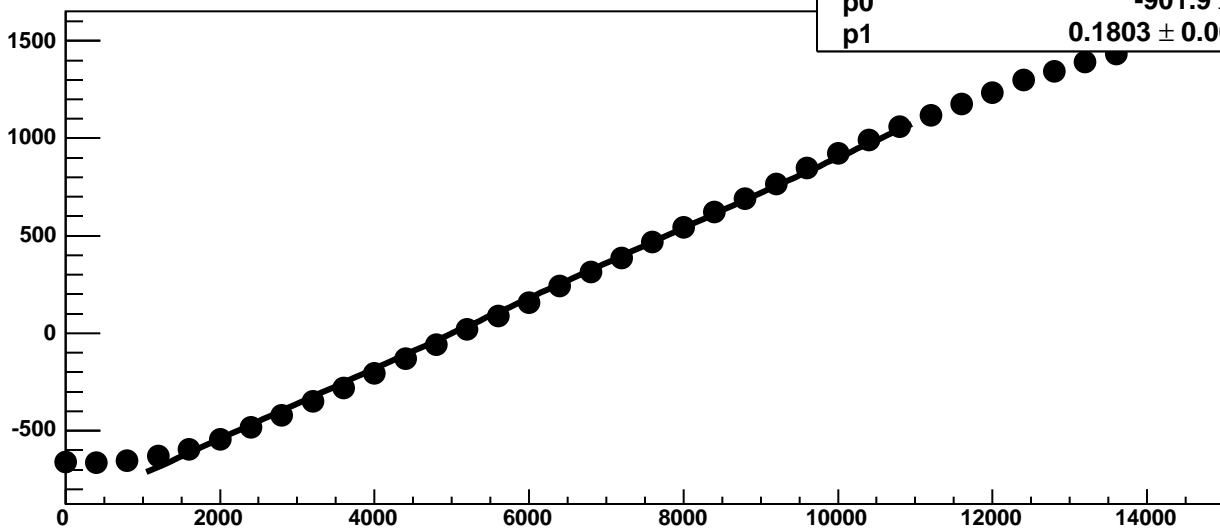
Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

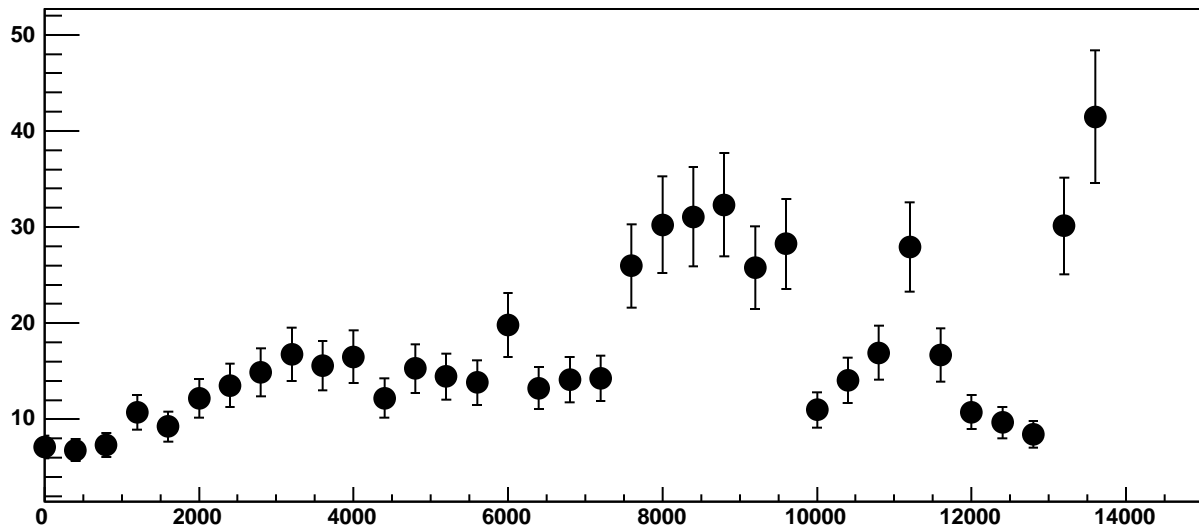


Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

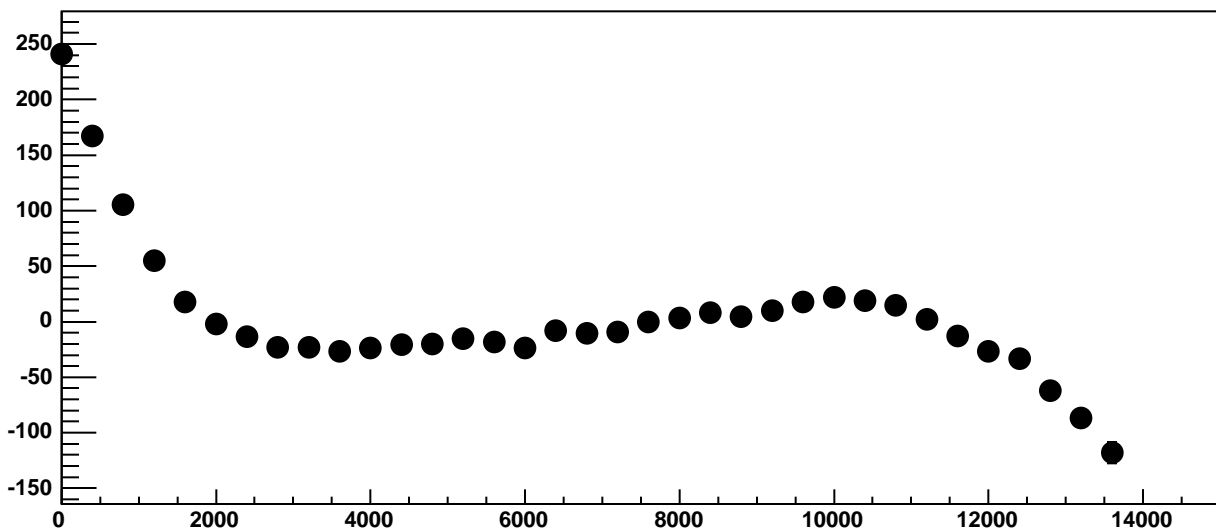


$\chi^2 / \text{ndf}$  1091 / 23  
p0  $-901.9 \pm 1.342$   
p1  $0.1803 \pm 0.0002259$

Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

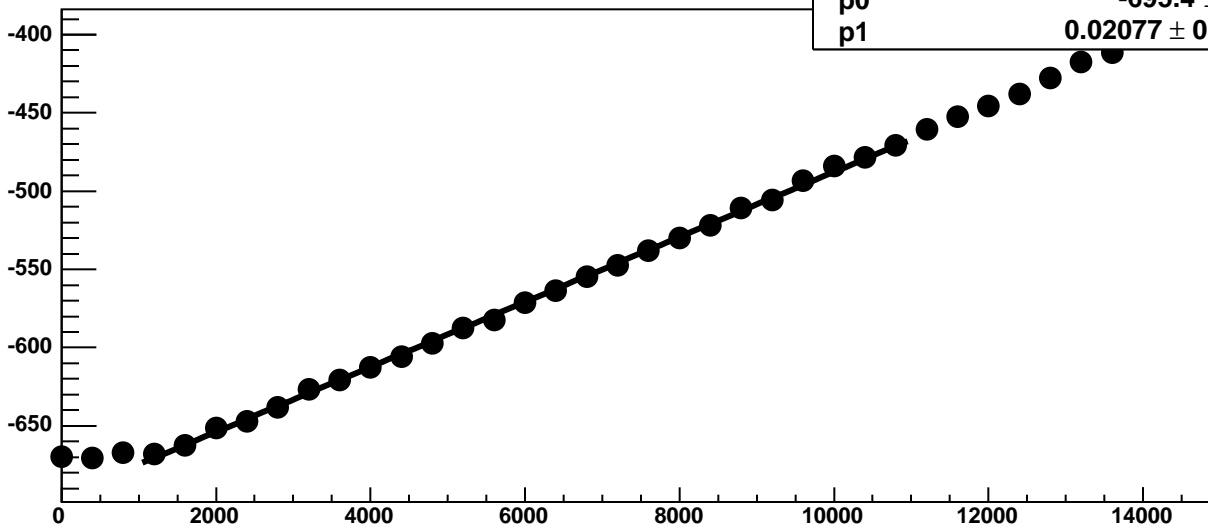


Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



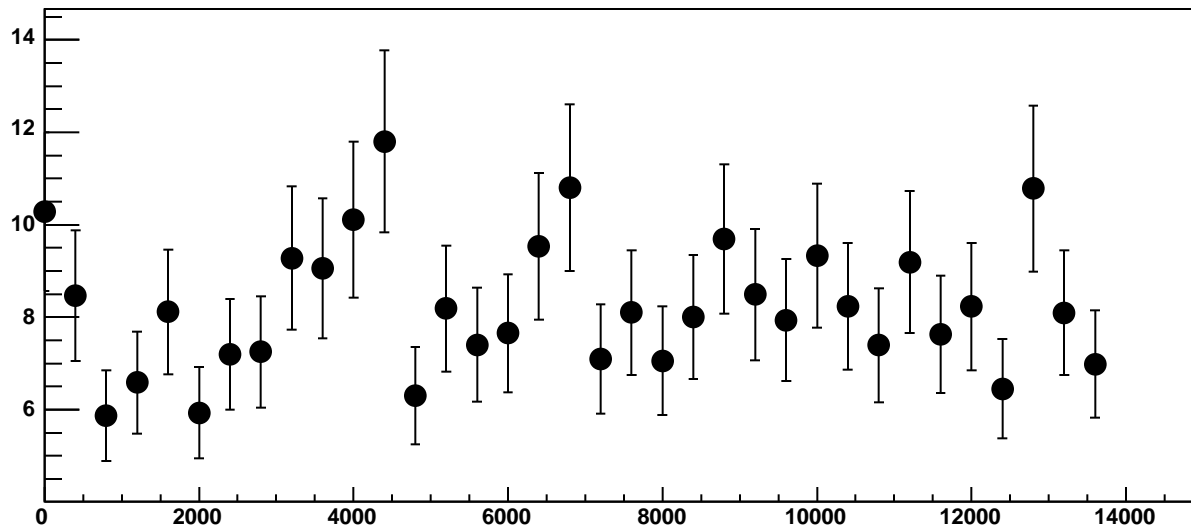


Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

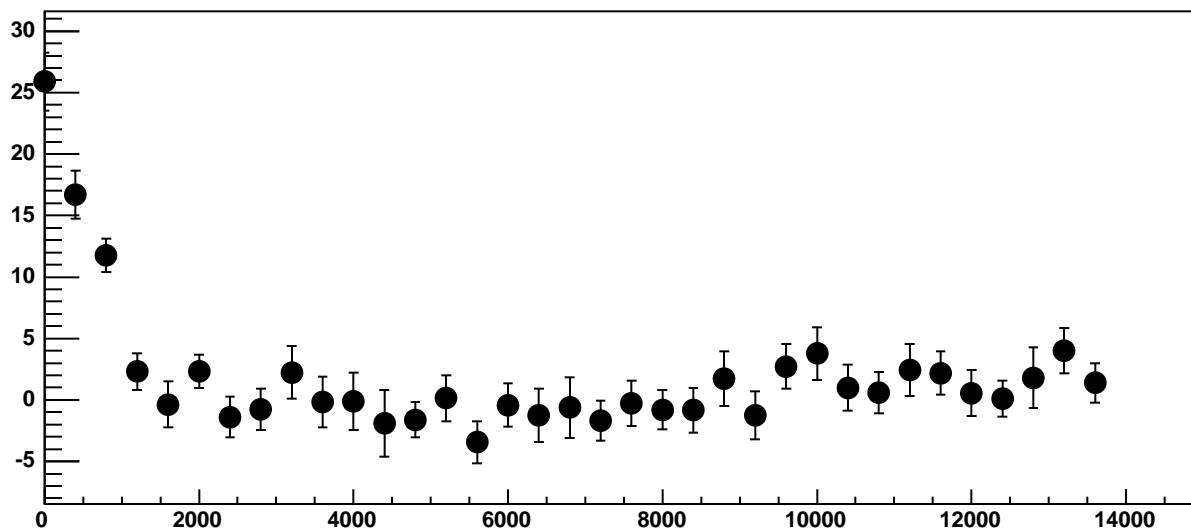


$\chi^2 / \text{ndf}$  21.87 / 23  
p0  $-695.4 \pm 0.7911$   
p1  $0.02077 \pm 0.000122$

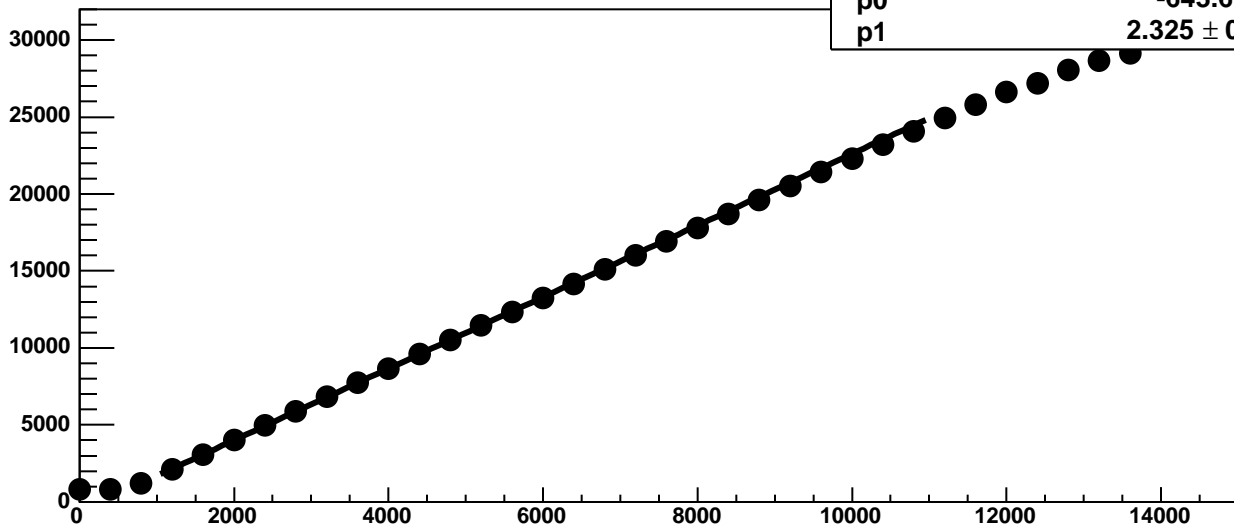
Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

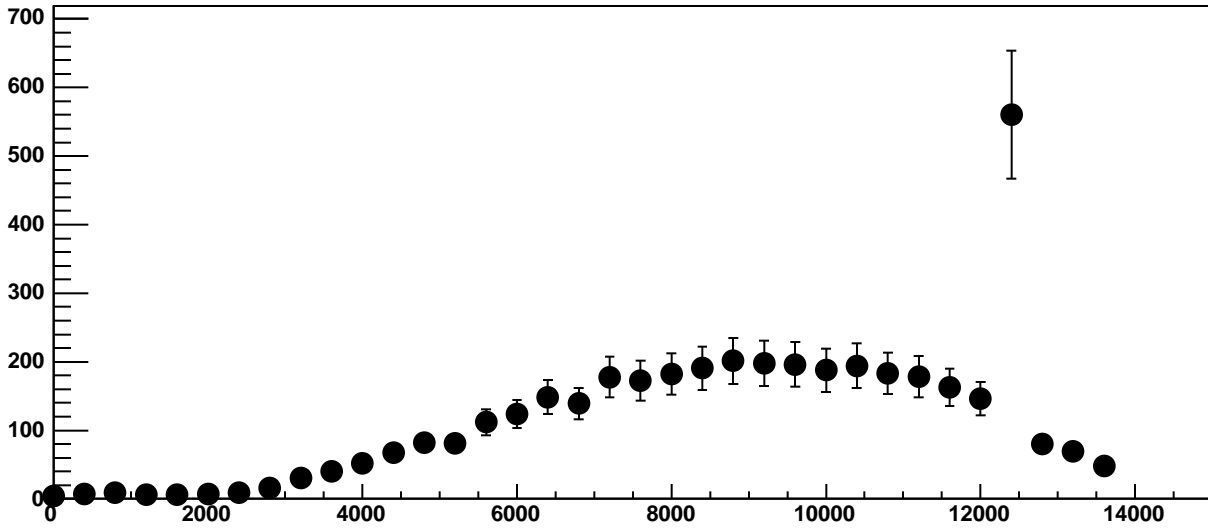


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

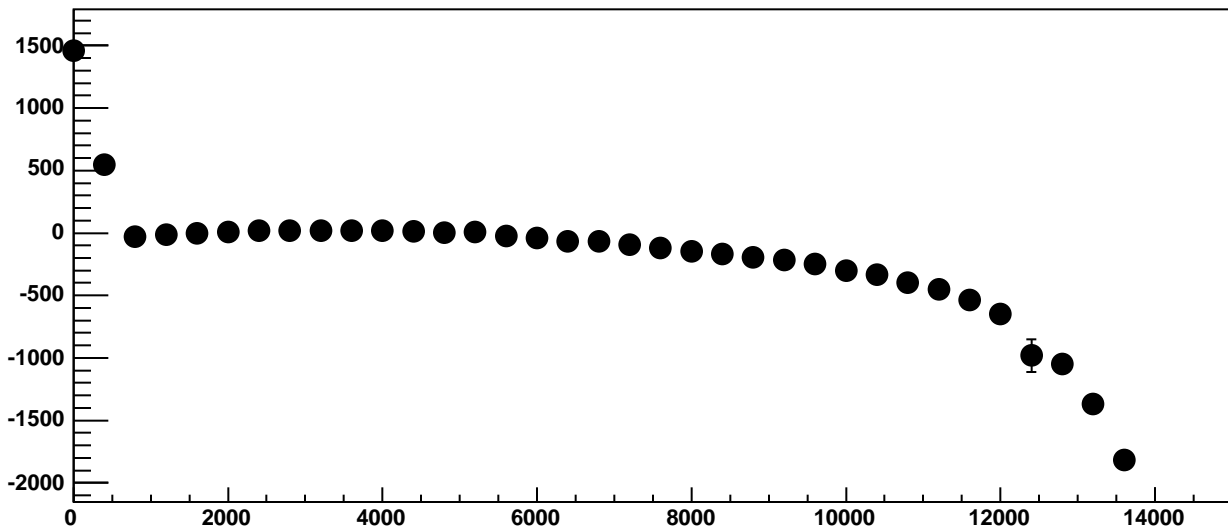


$\chi^2 / \text{ndf}$	544.8 / 23
p0	$-643.6 \pm 2.06$
p1	$2.325 \pm 0.00104$

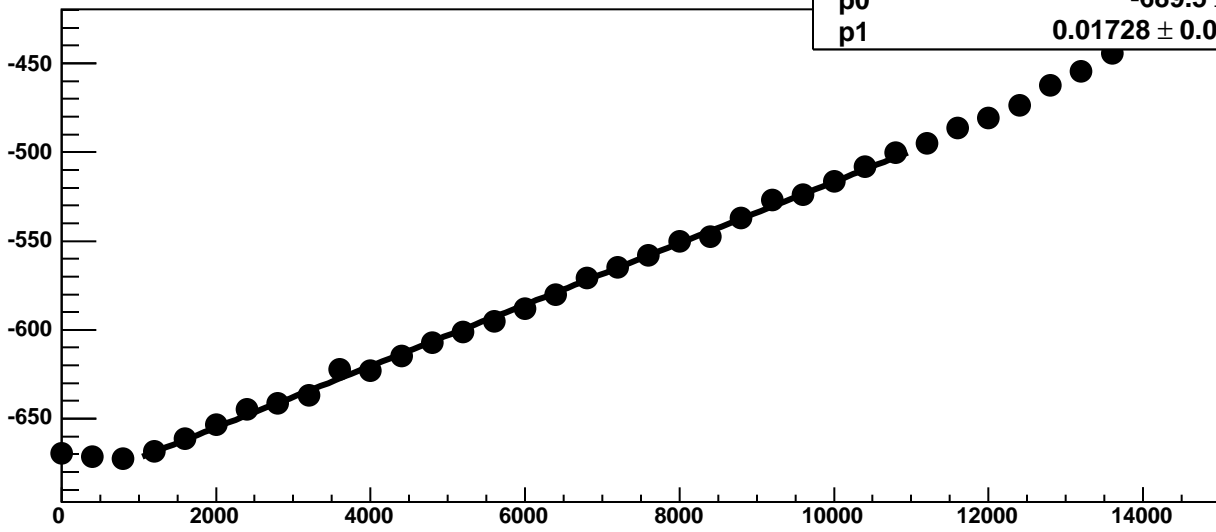
Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

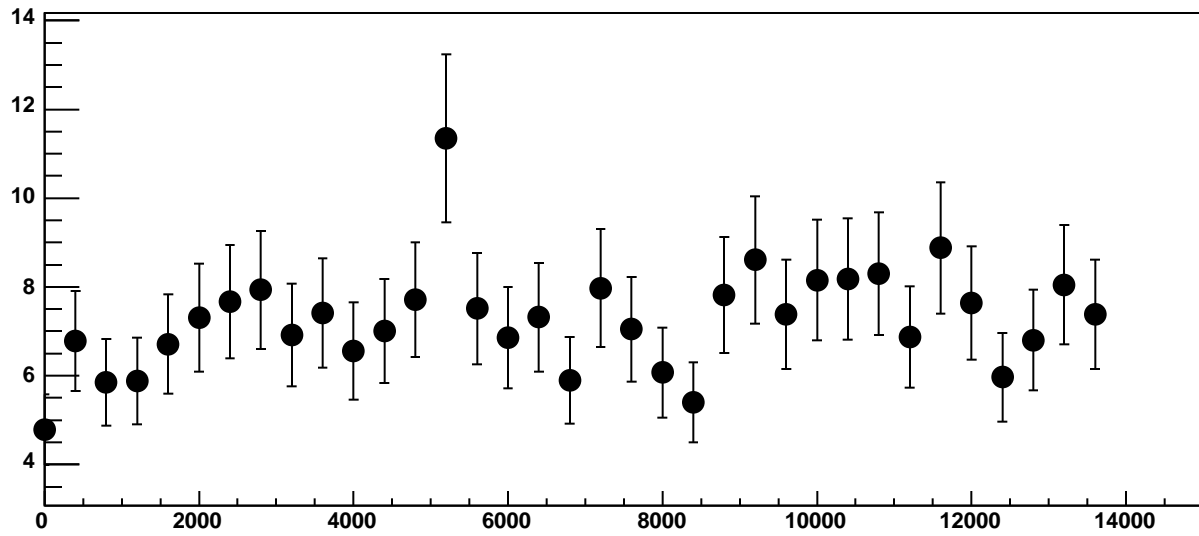


Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

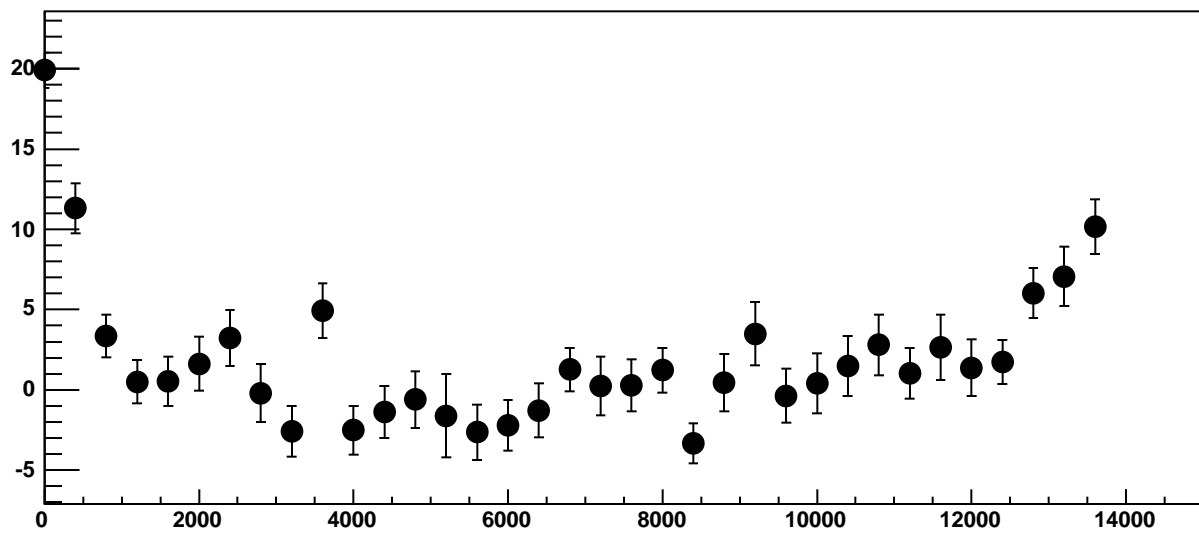


$\chi^2 / \text{ndf}$  39.53 / 23  
p0  $-689.5 \pm 0.749$   
p1  $0.01728 \pm 0.0001149$

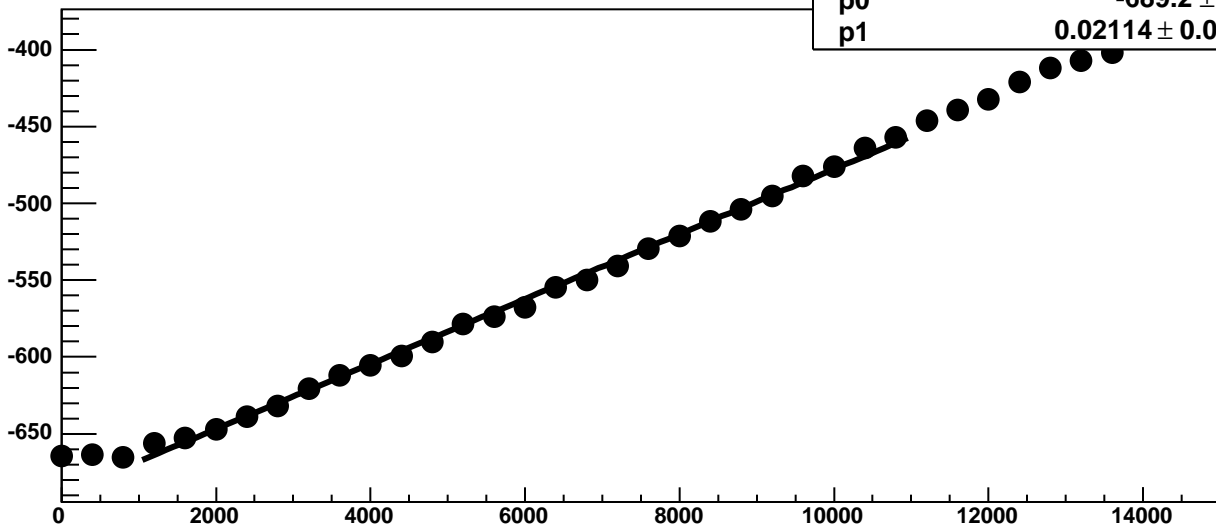
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

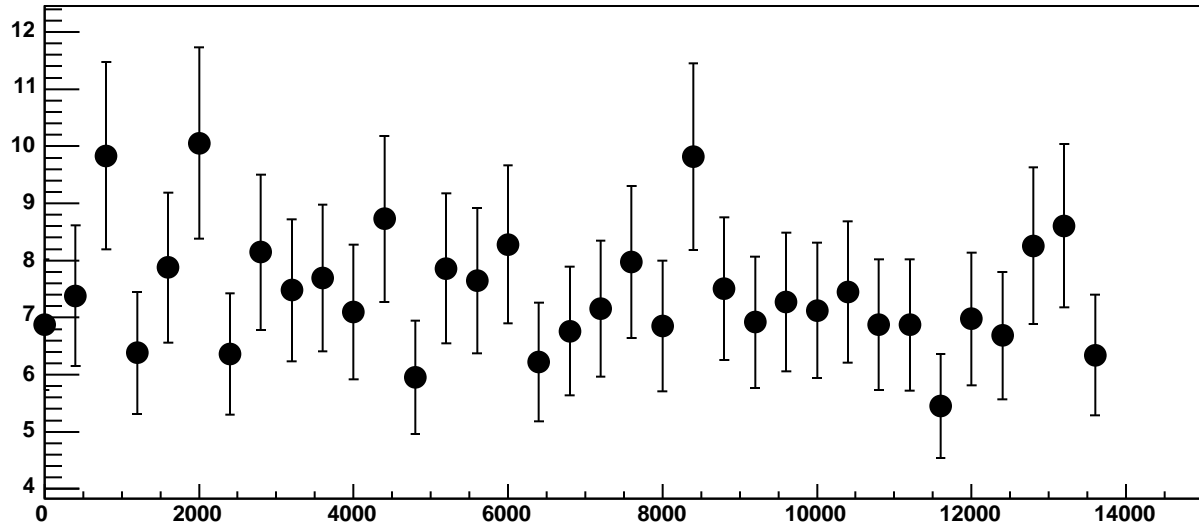


Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

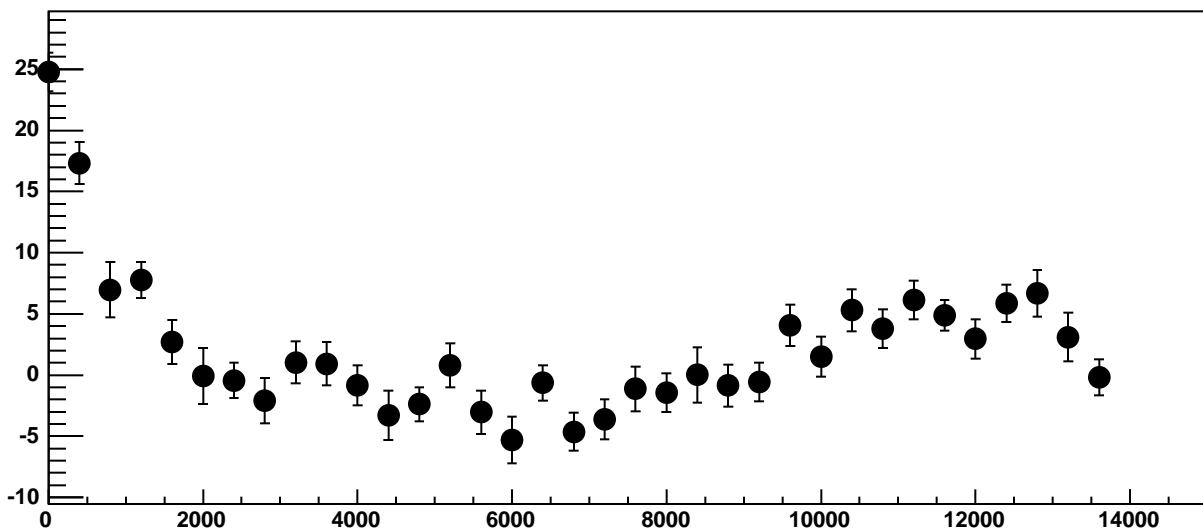


$\chi^2 / \text{ndf}$  87.19 / 23  
p0  $-689.2 \pm 0.7783$   
p1  $0.02114 \pm 0.0001162$

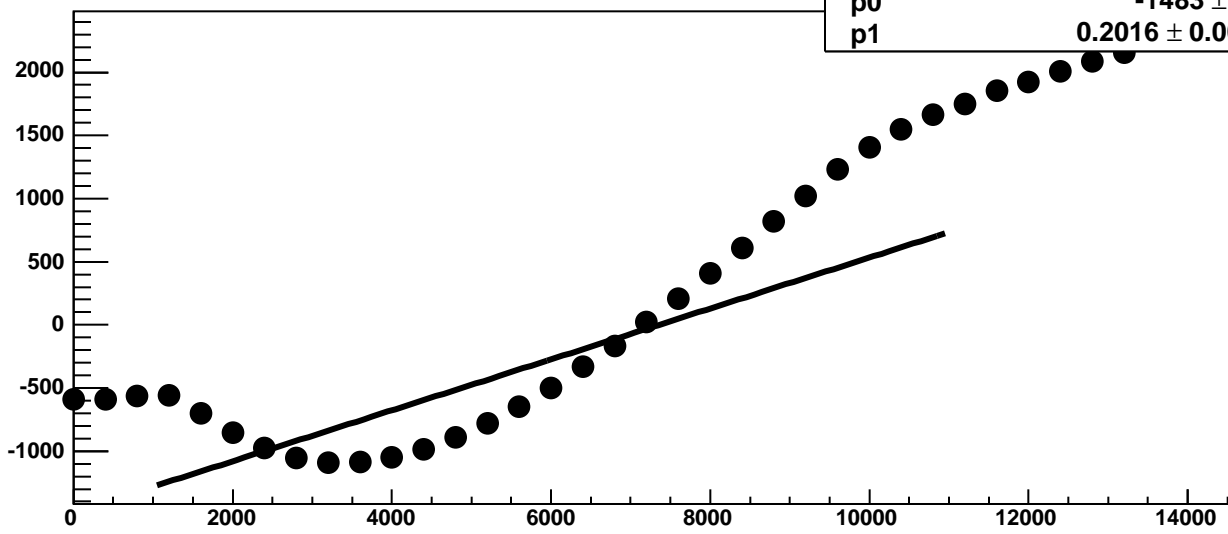
Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

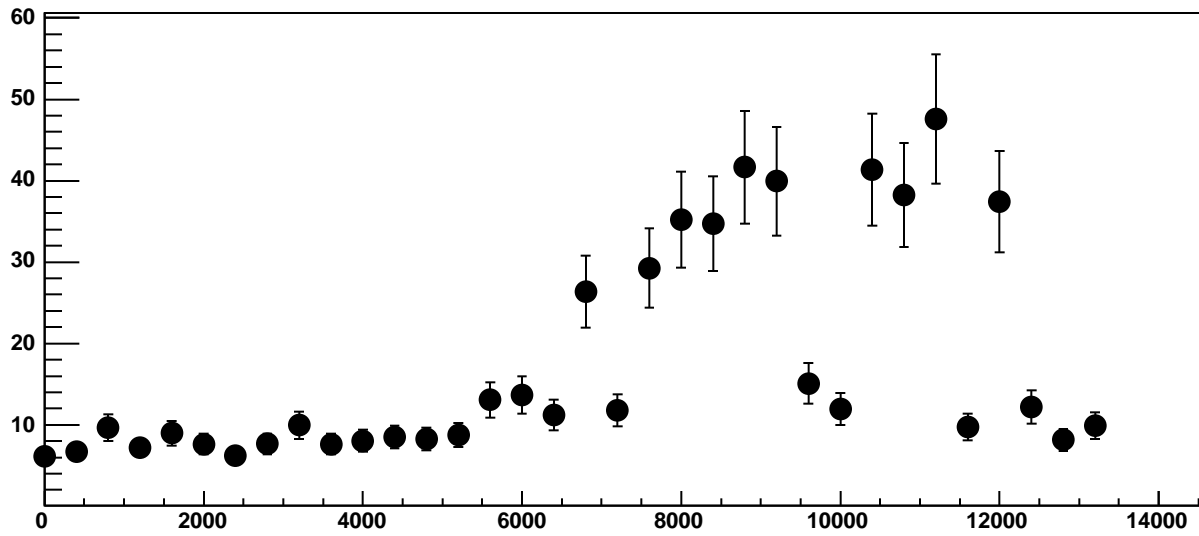


Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

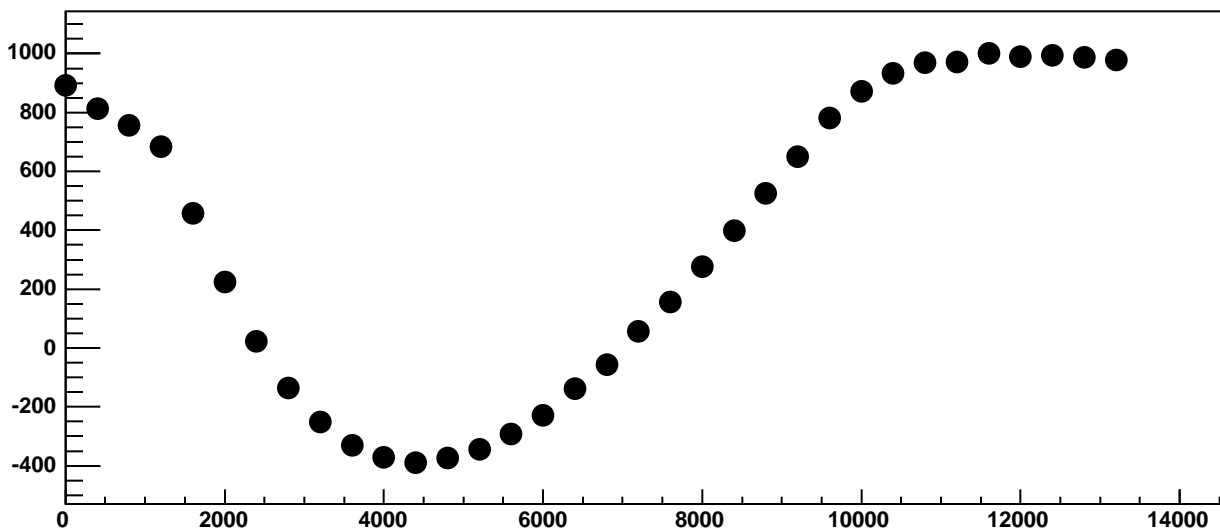


$\chi^2 / \text{ndf}$  6.43e+05 / 23  
p0 -1483 ± 0.9963  
p1 0.2016 ± 0.0002179

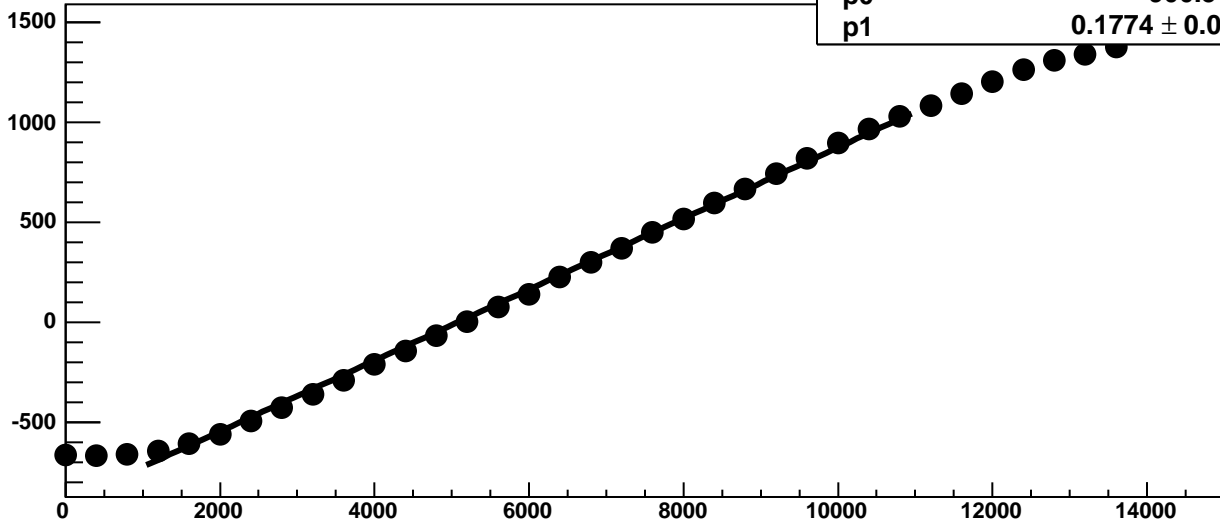
Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



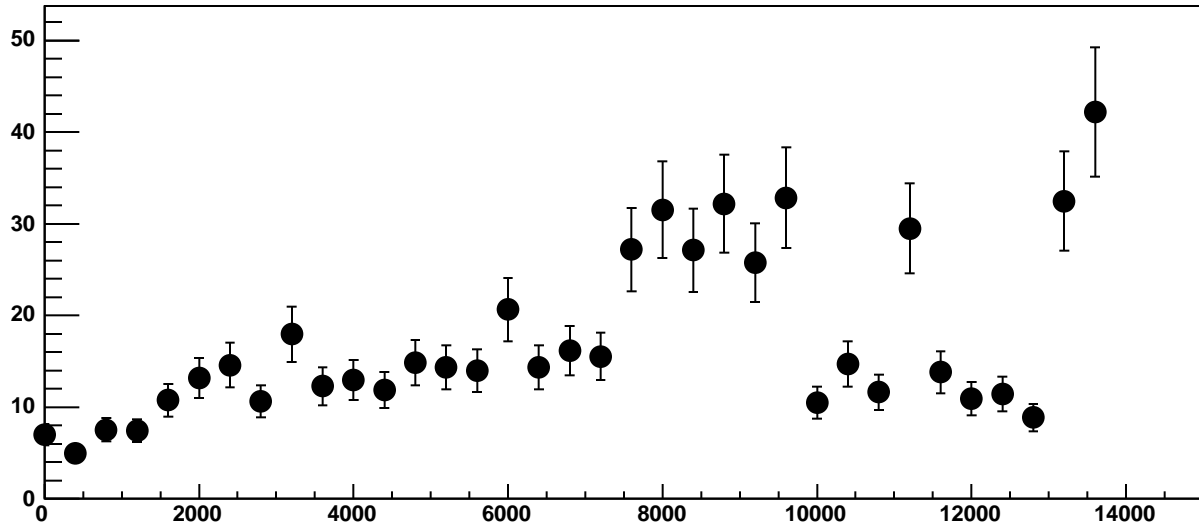
Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



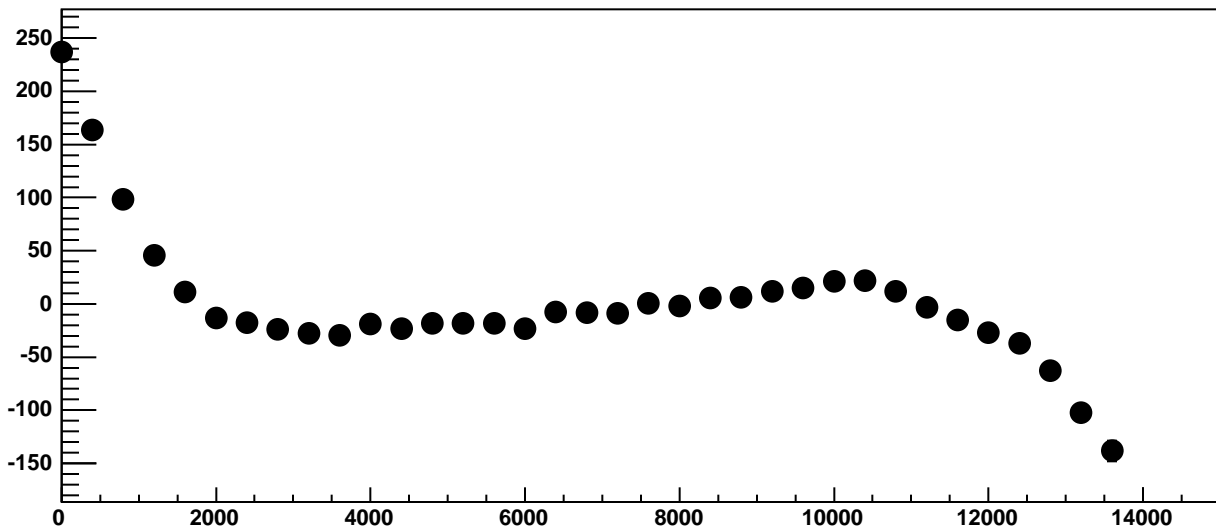
Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



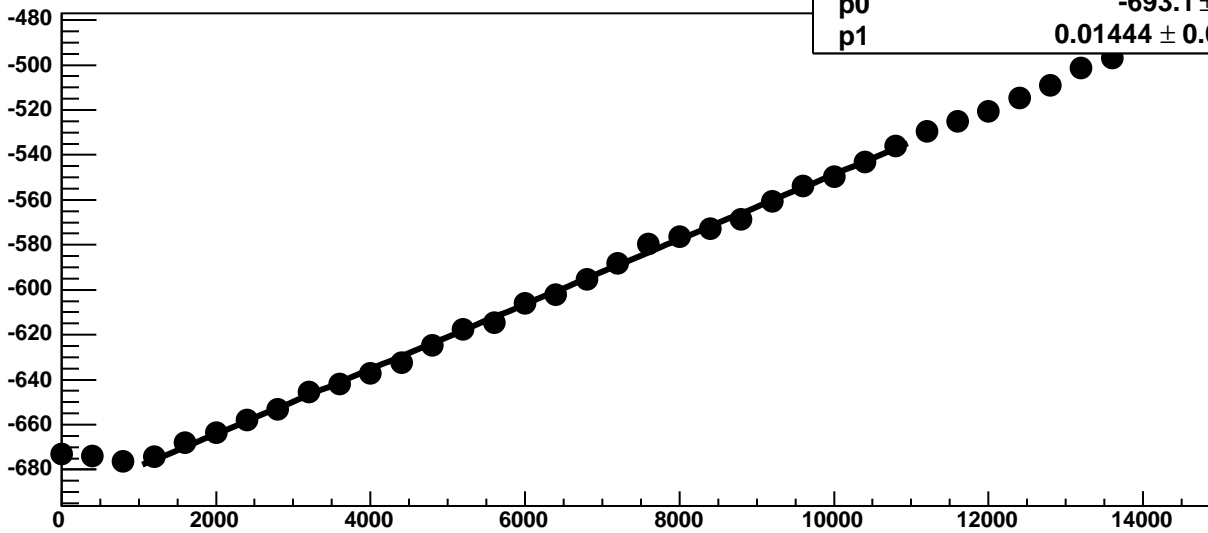
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

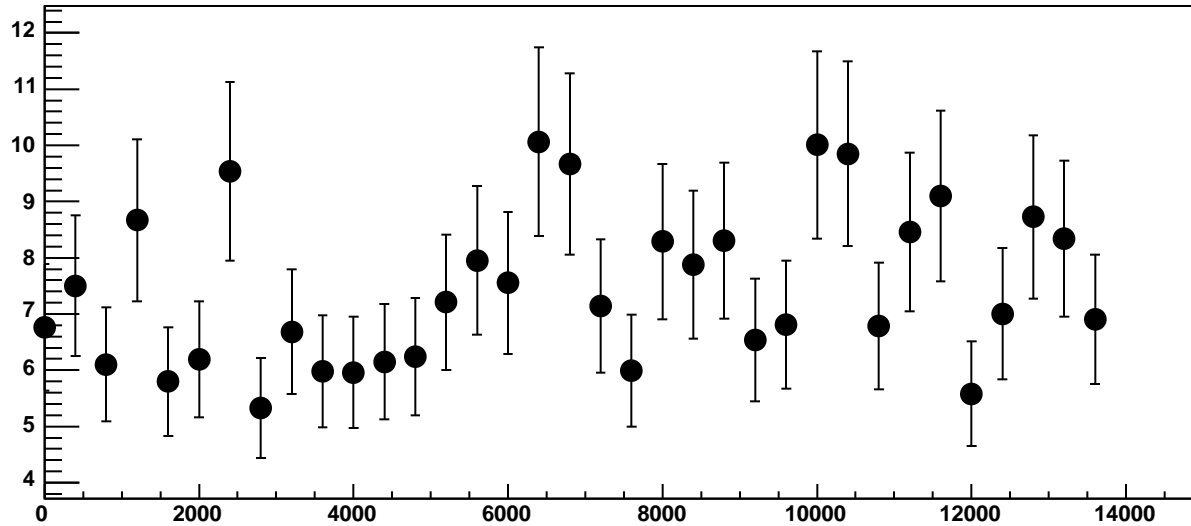


Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

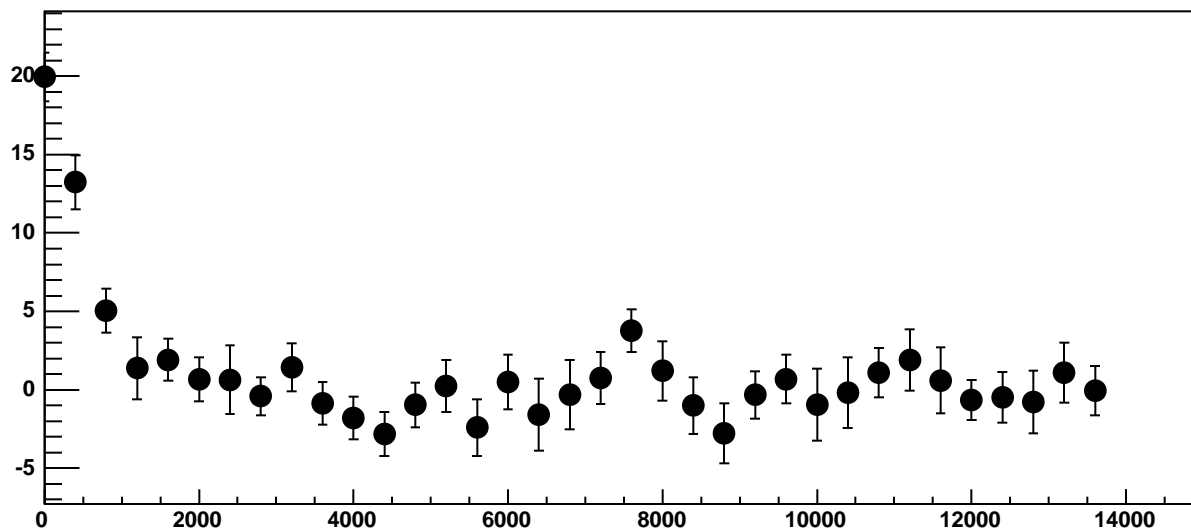


$\chi^2 / \text{ndf}$  24.2 / 23  
p0  $-693.1 \pm 0.7165$   
p1  $0.01444 \pm 0.0001141$

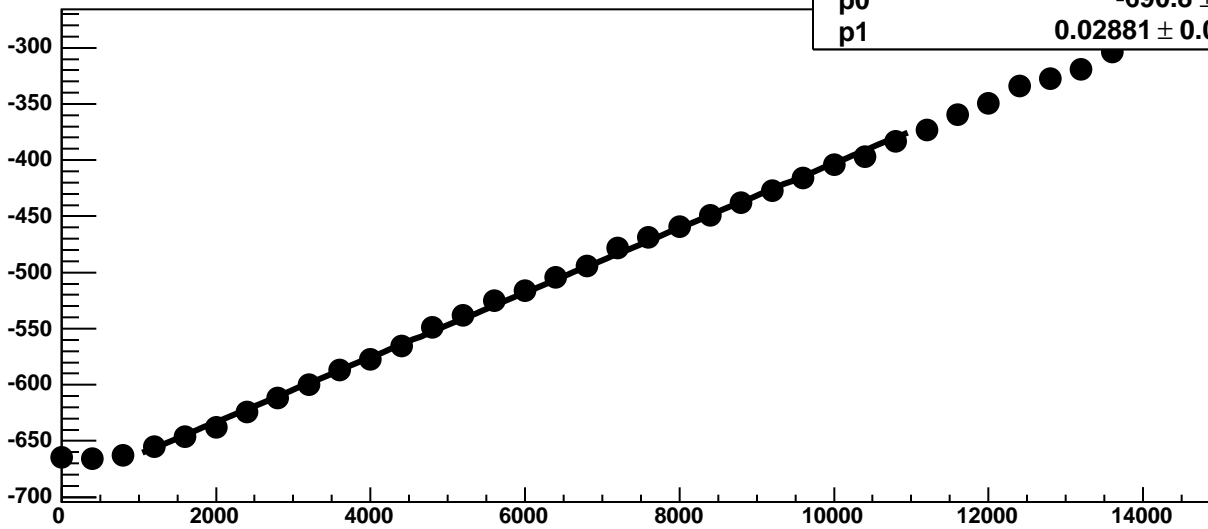
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



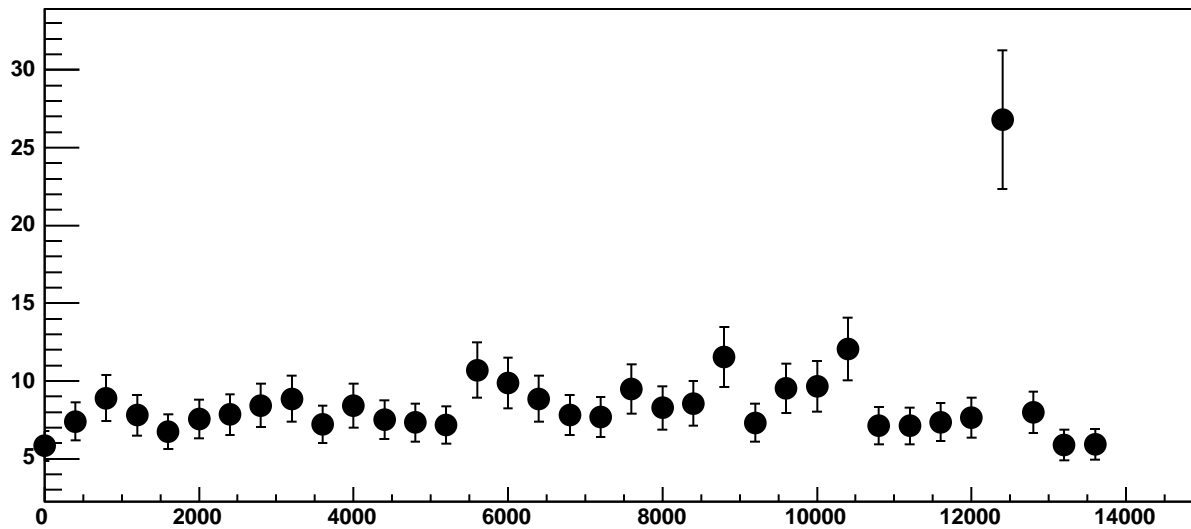
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



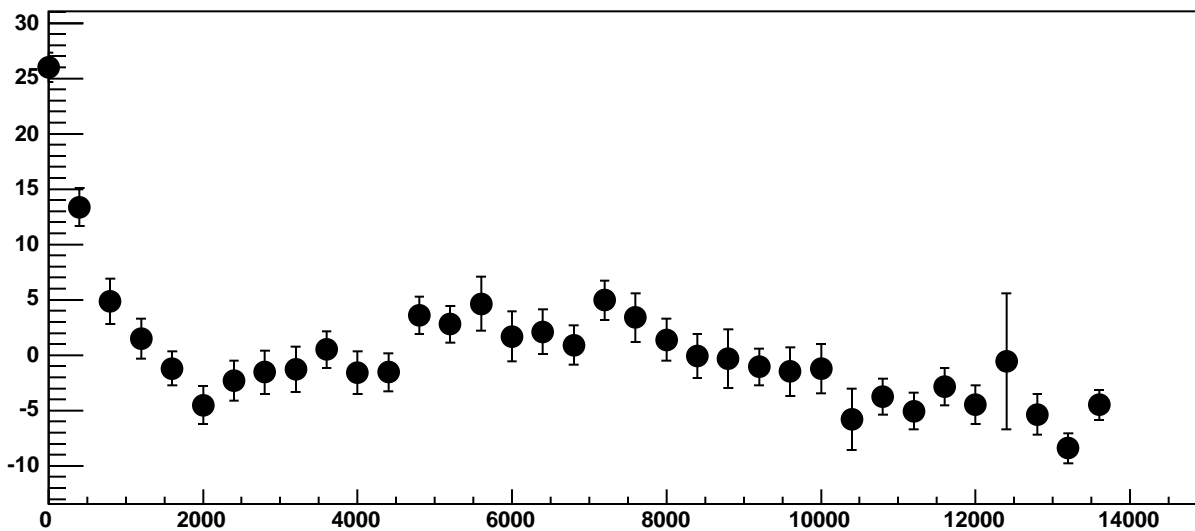
Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

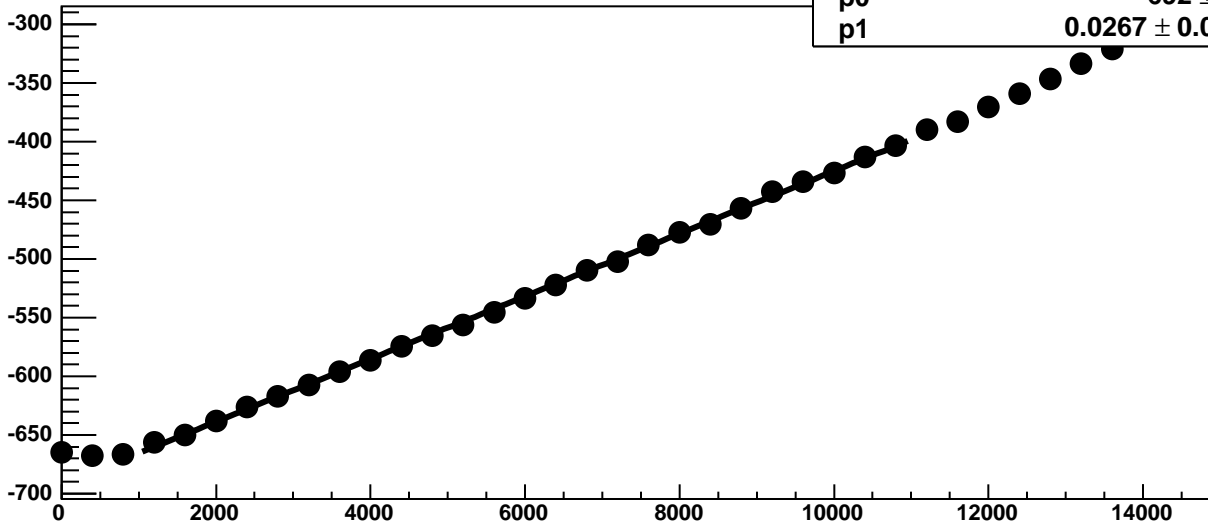


Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



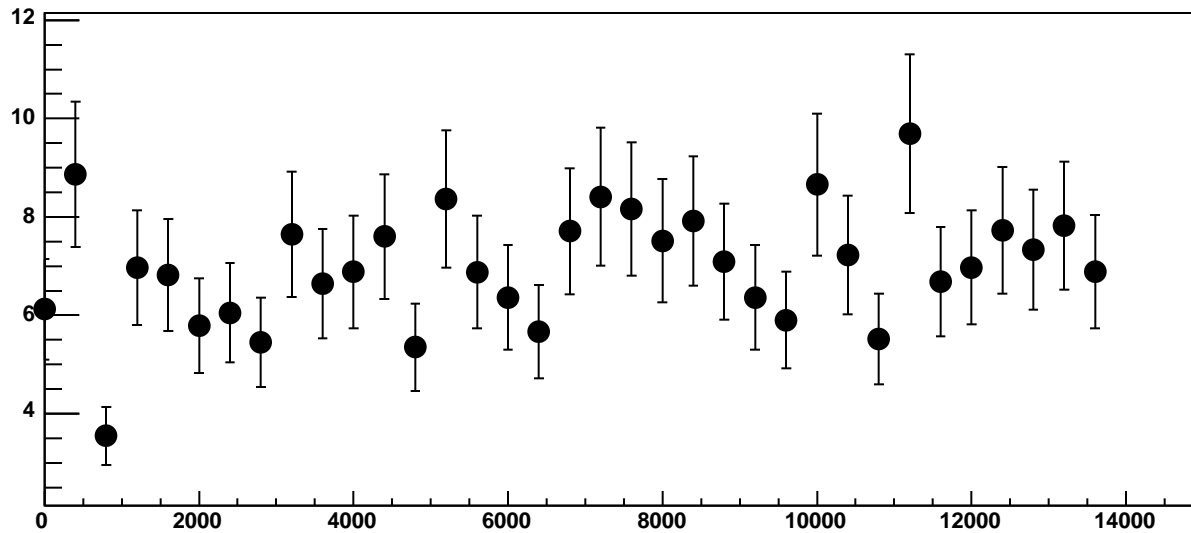


Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

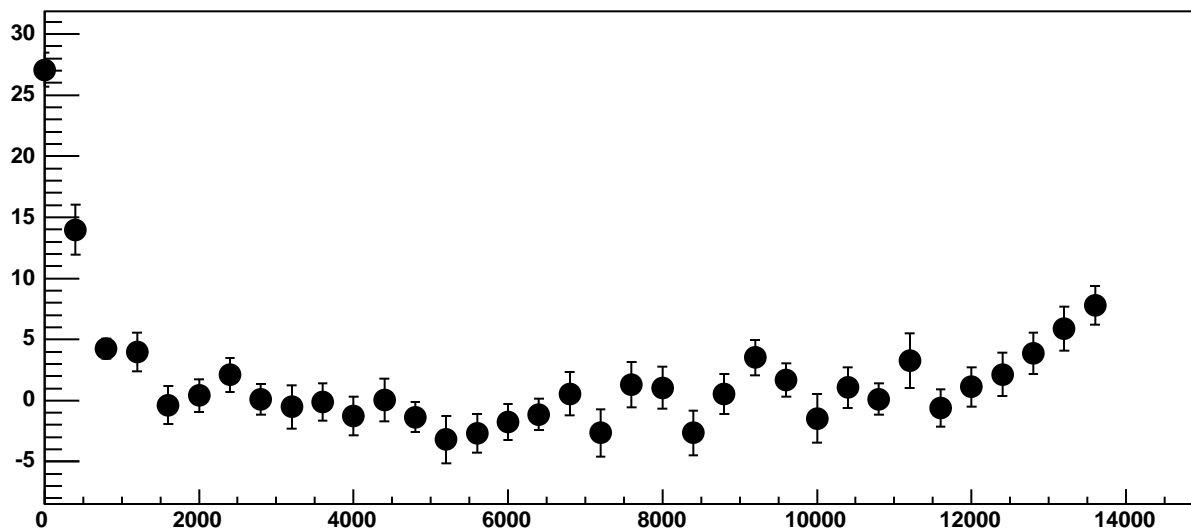


$\chi^2 / \text{ndf}$  31.84 / 23  
p0  $-692 \pm 0.6822$   
p1  $0.0267 \pm 0.0001039$

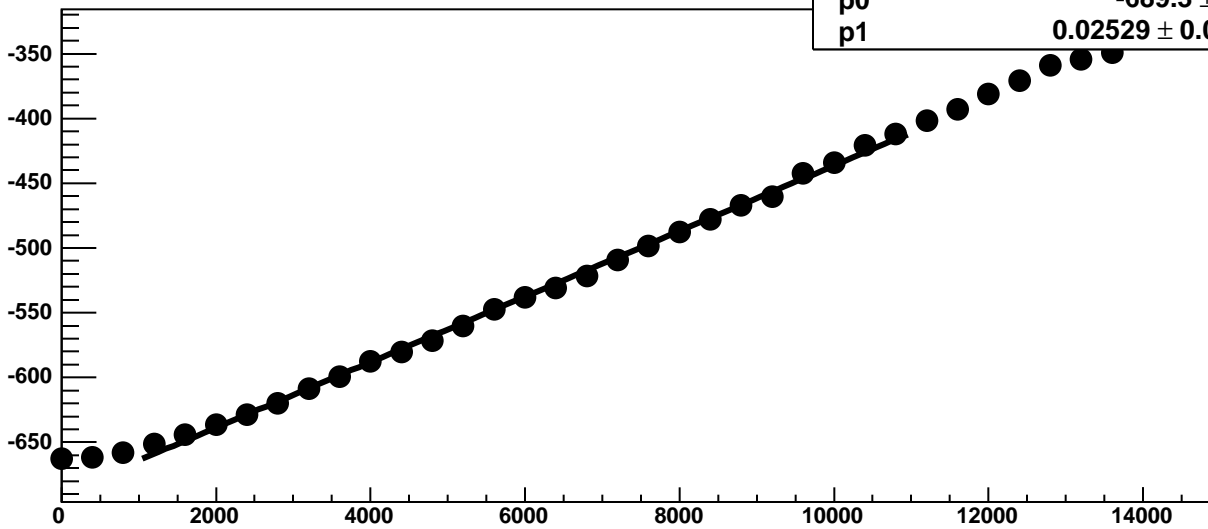
Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

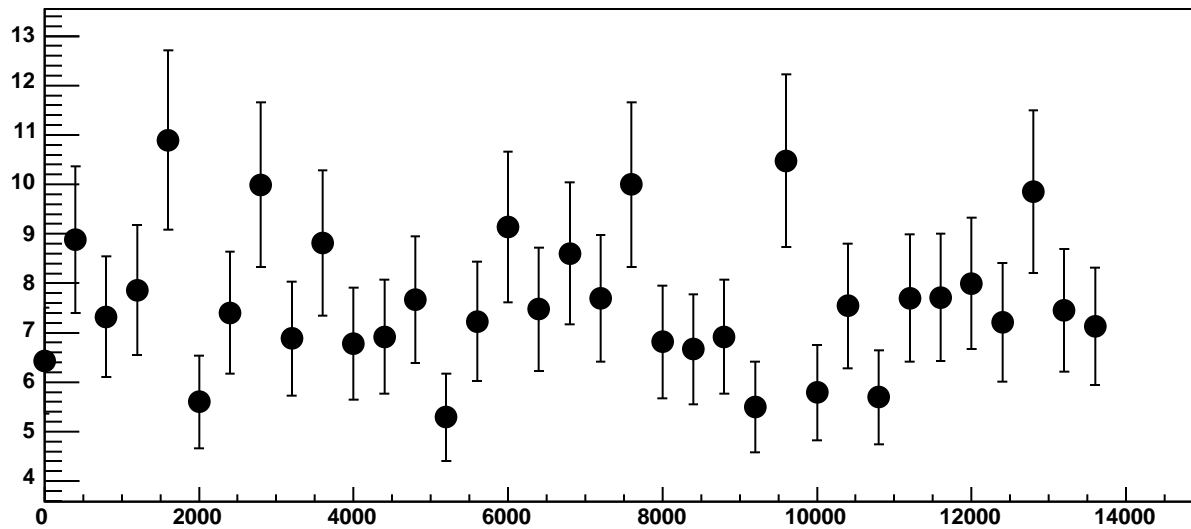


Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

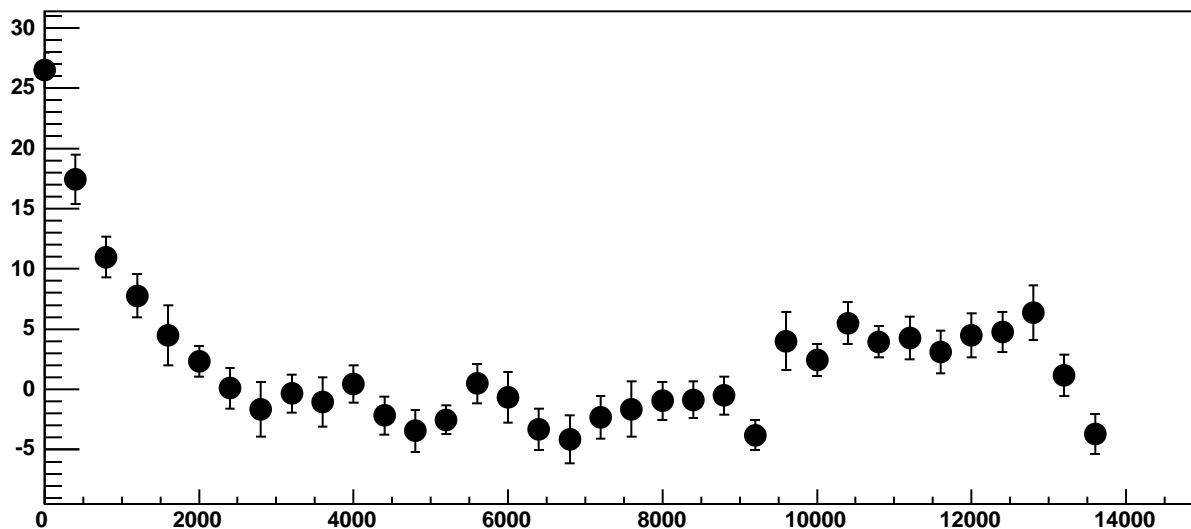


$\chi^2 / \text{ndf}$  81.73 / 23  
p0 -689.3 ± 0.7737  
p1 0.02529 ± 0.0001119

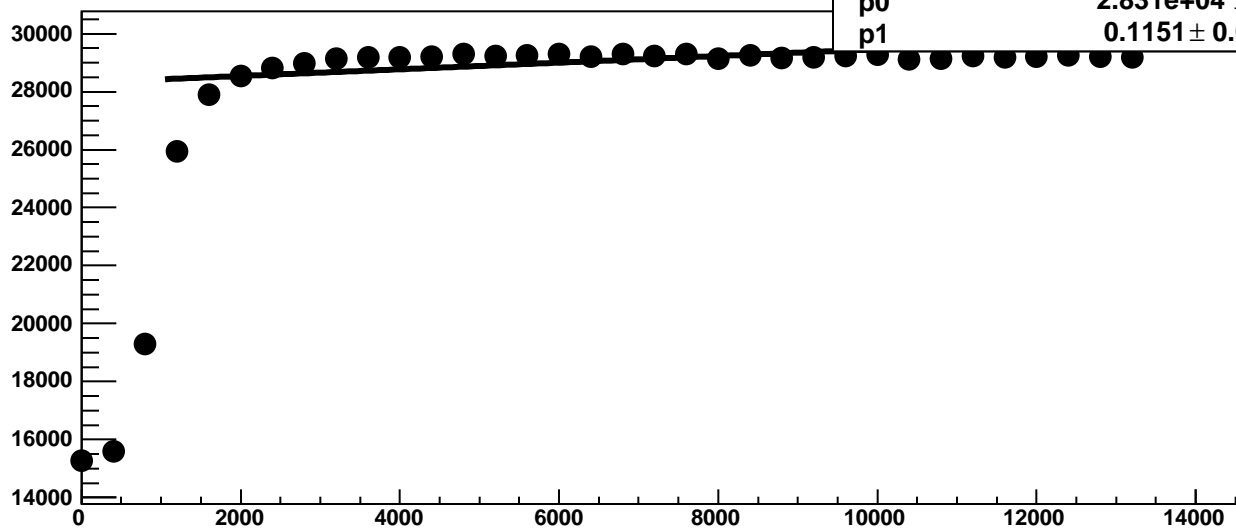
Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



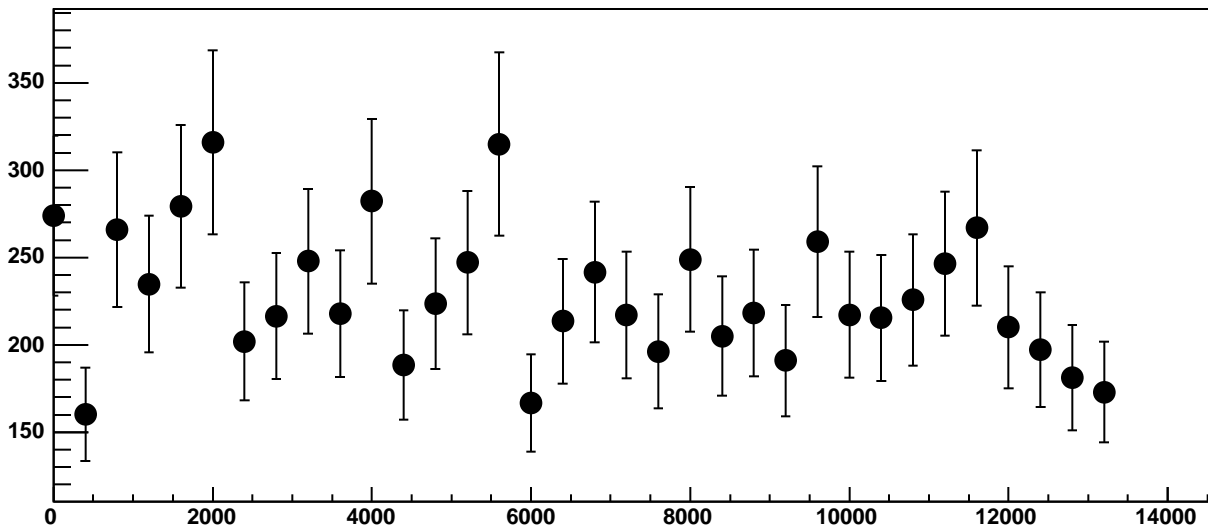
Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



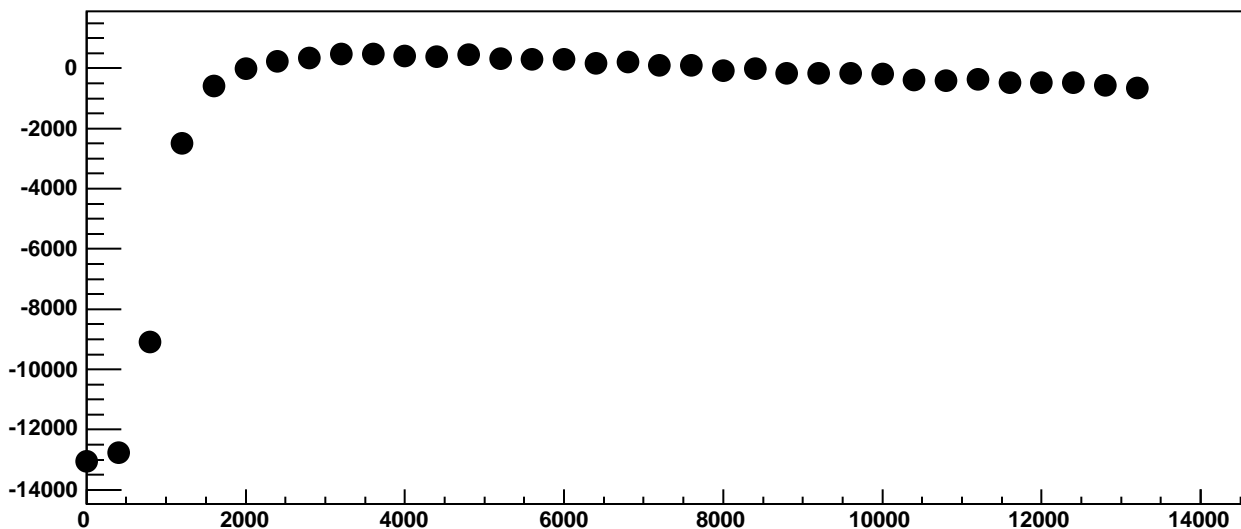
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



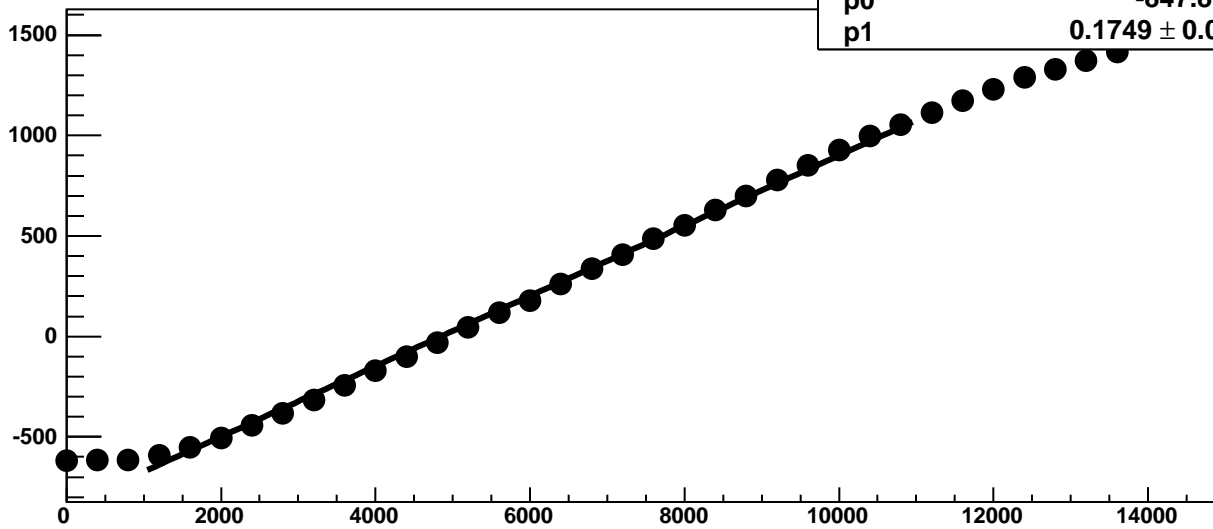
Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

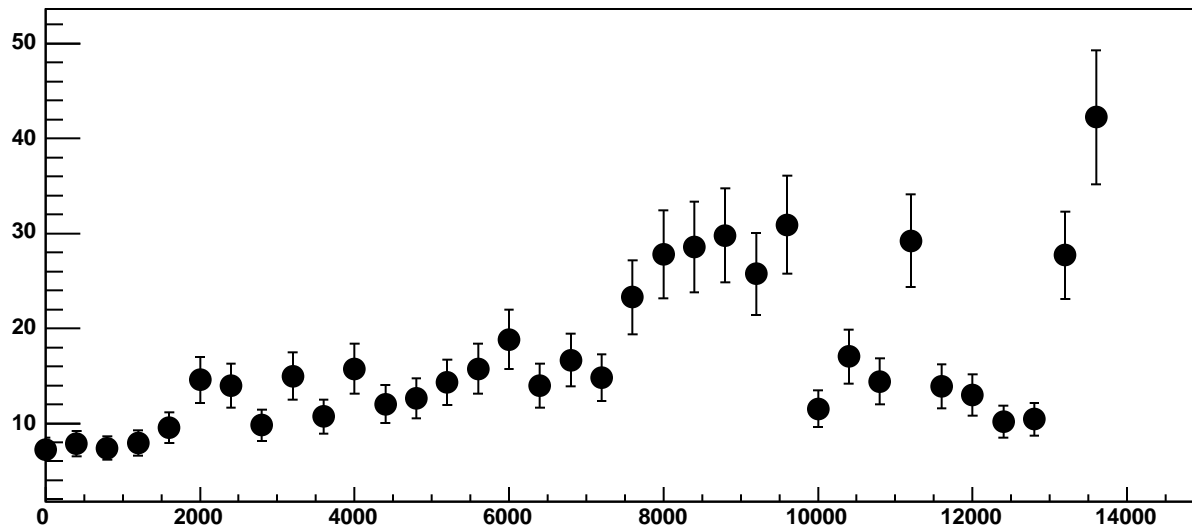


Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

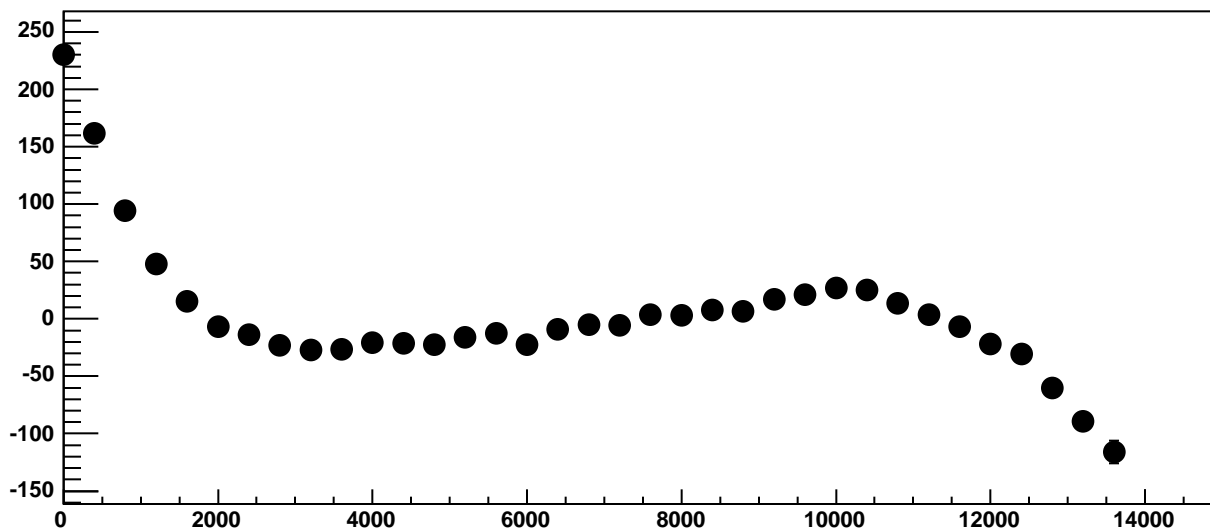


$\chi^2 / \text{ndf}$  1452 / 23  
p0  $-847.8 \pm 1.213$   
p1  $0.1749 \pm 0.0002174$

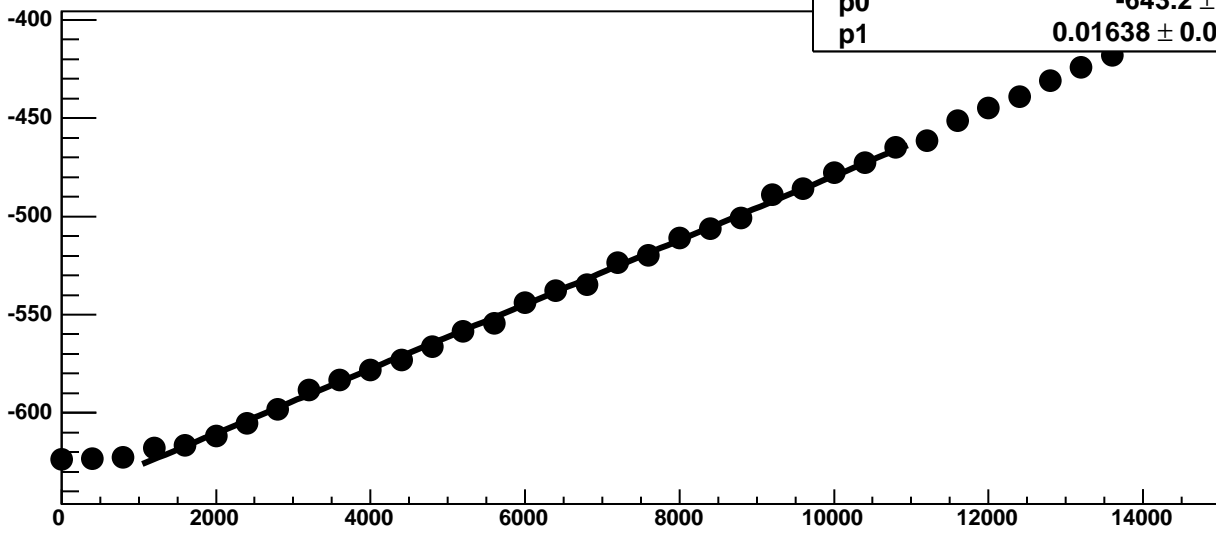
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



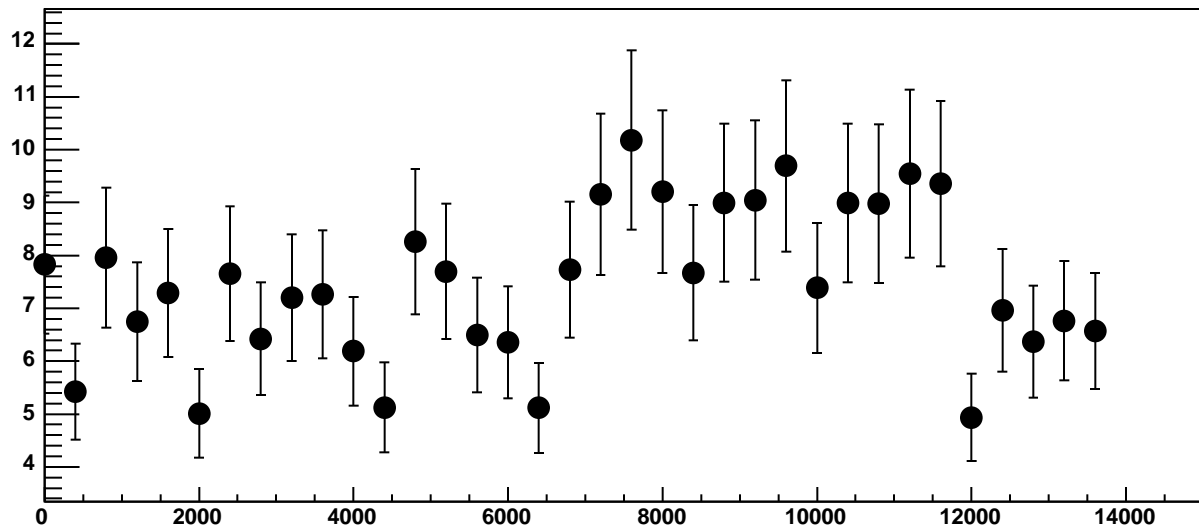
Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



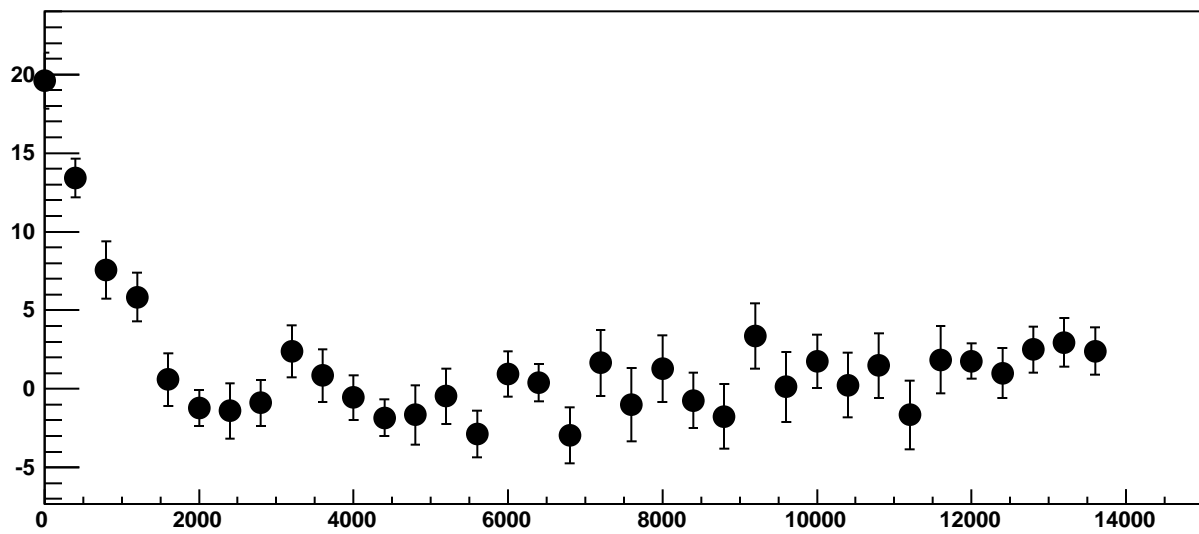
Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



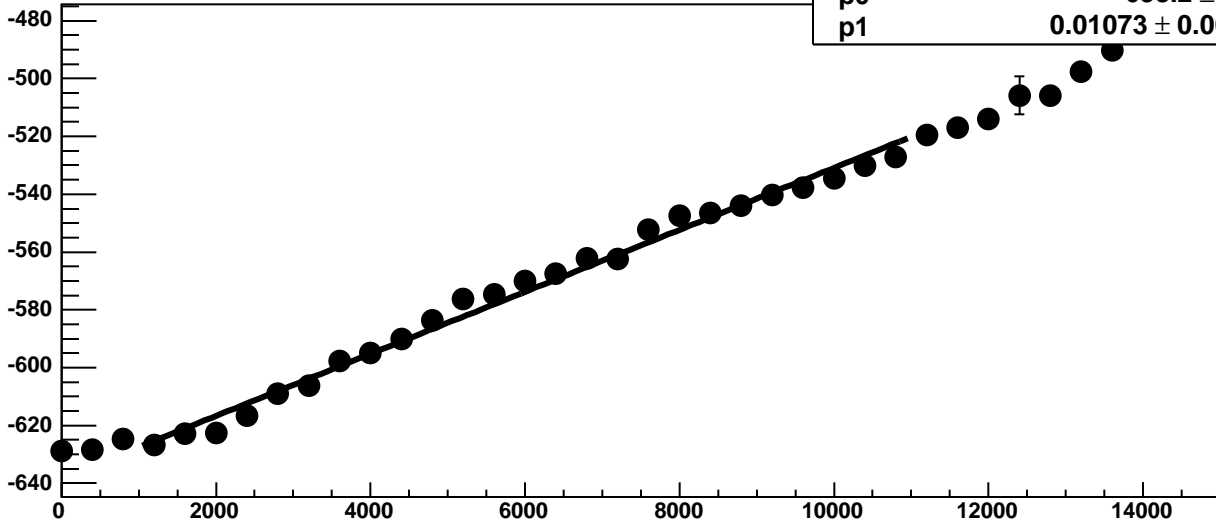
Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



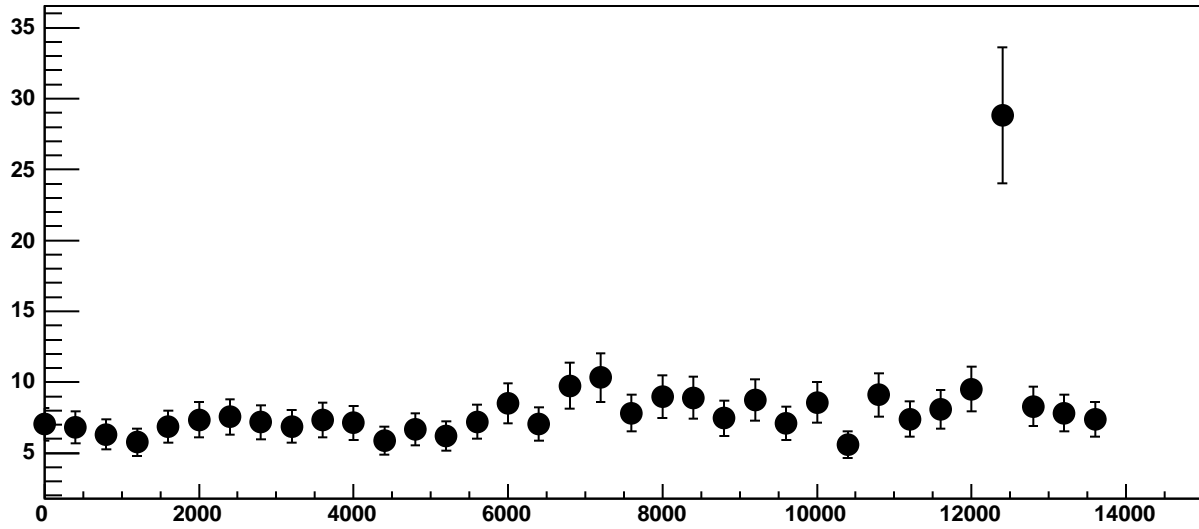
Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



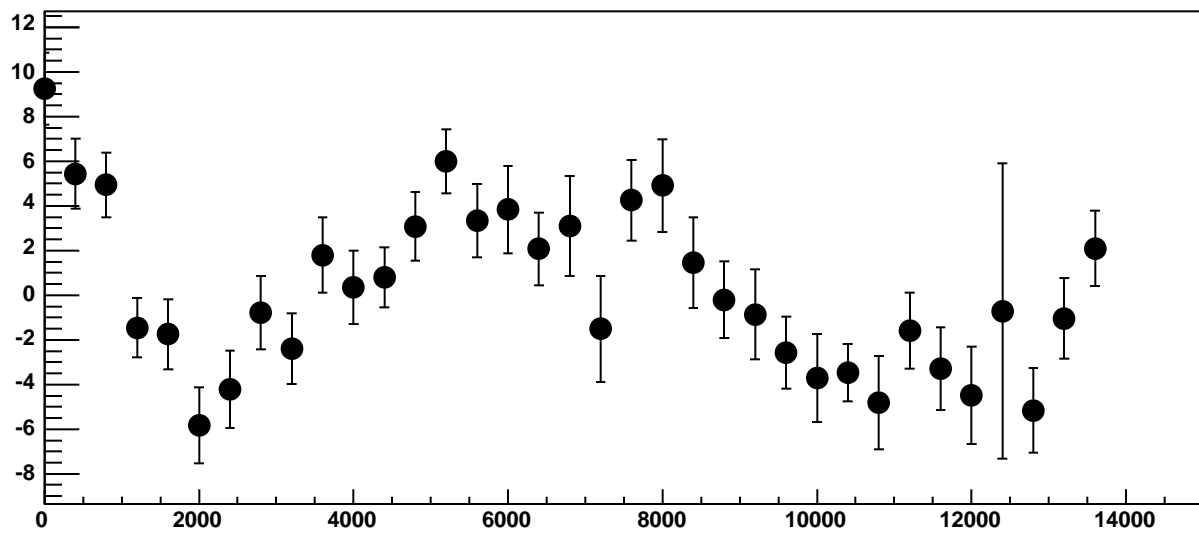
Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



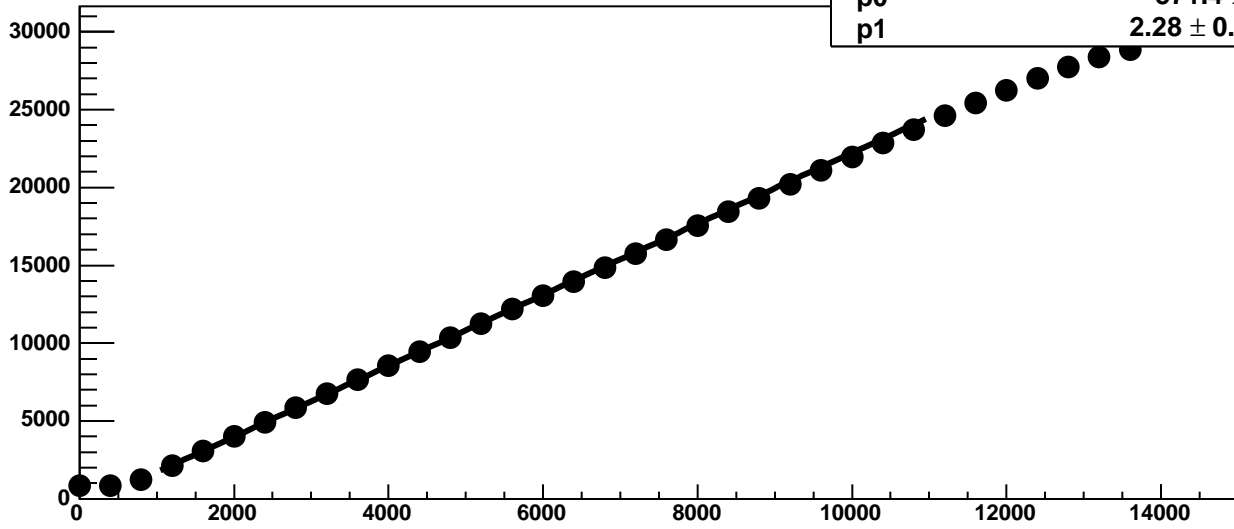
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

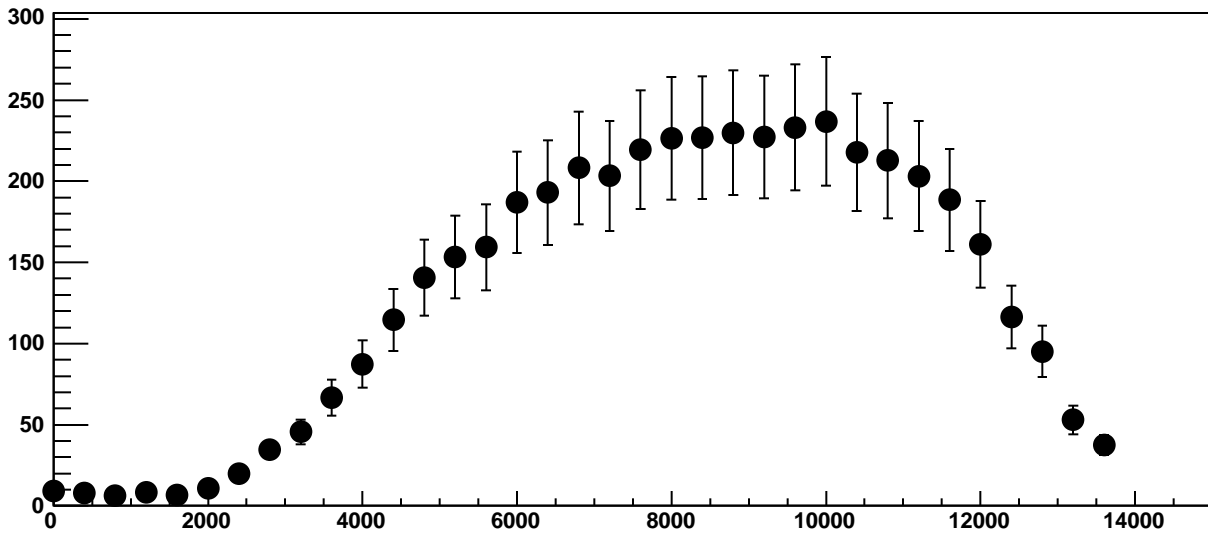


Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

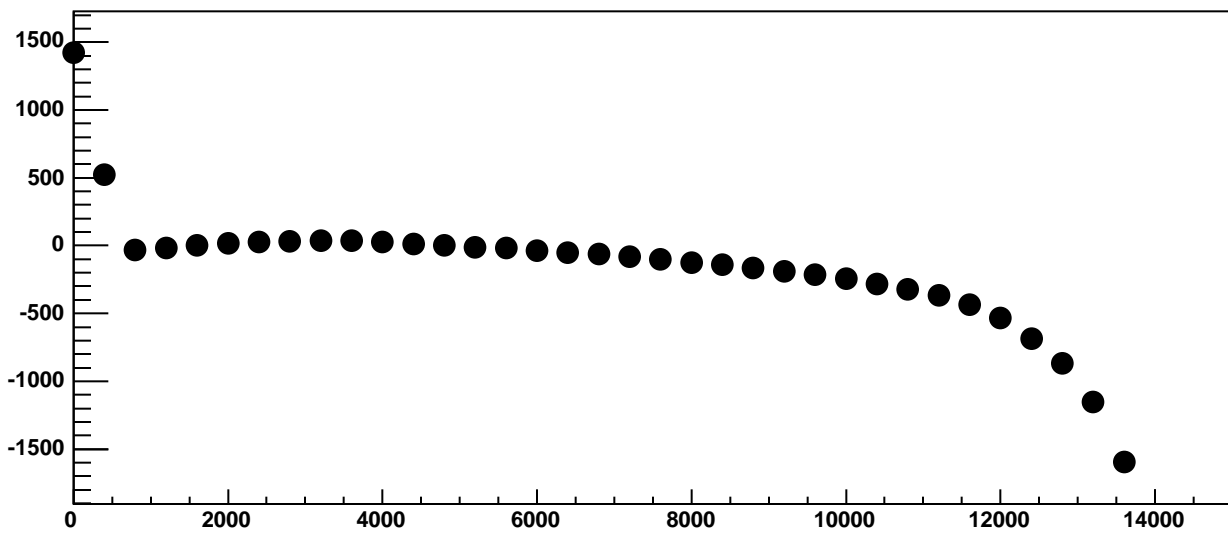


$\chi^2 / \text{ndf}$  393.1 / 23  
p0  $-571.4 \pm 2.692$   
p1  $2.28 \pm 0.001471$

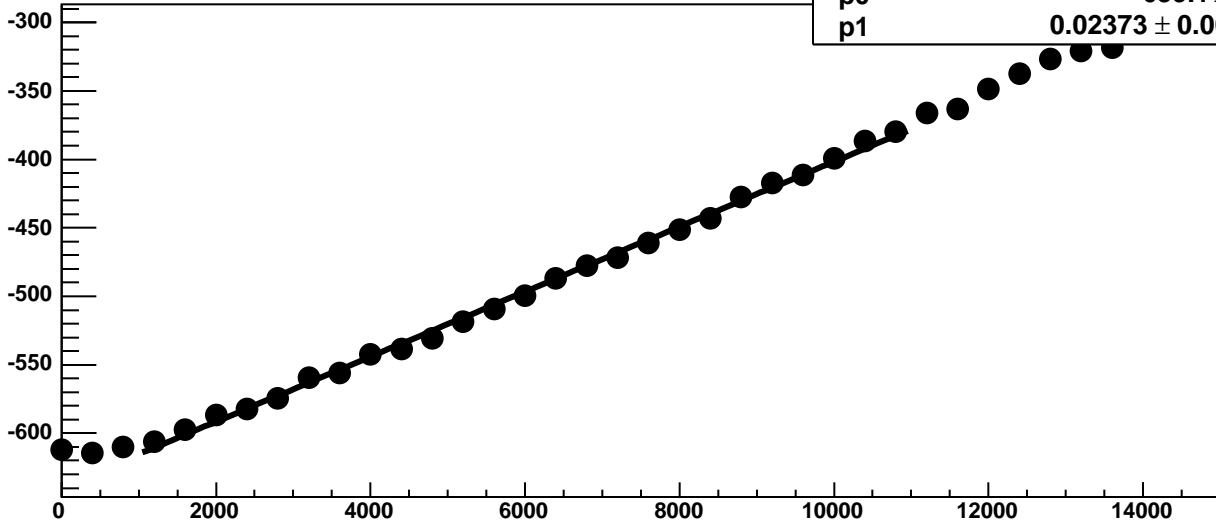
Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

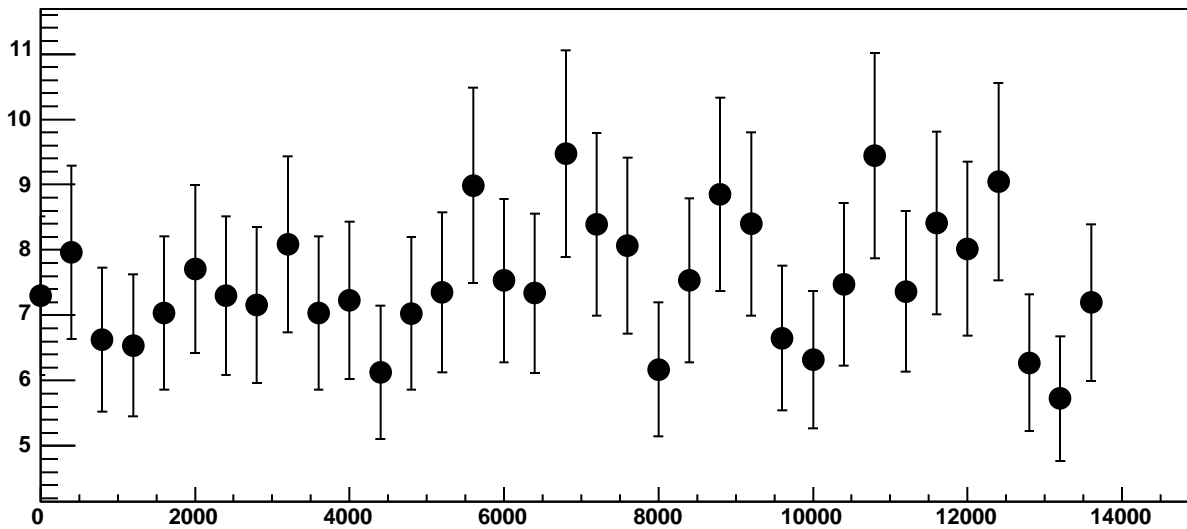


Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

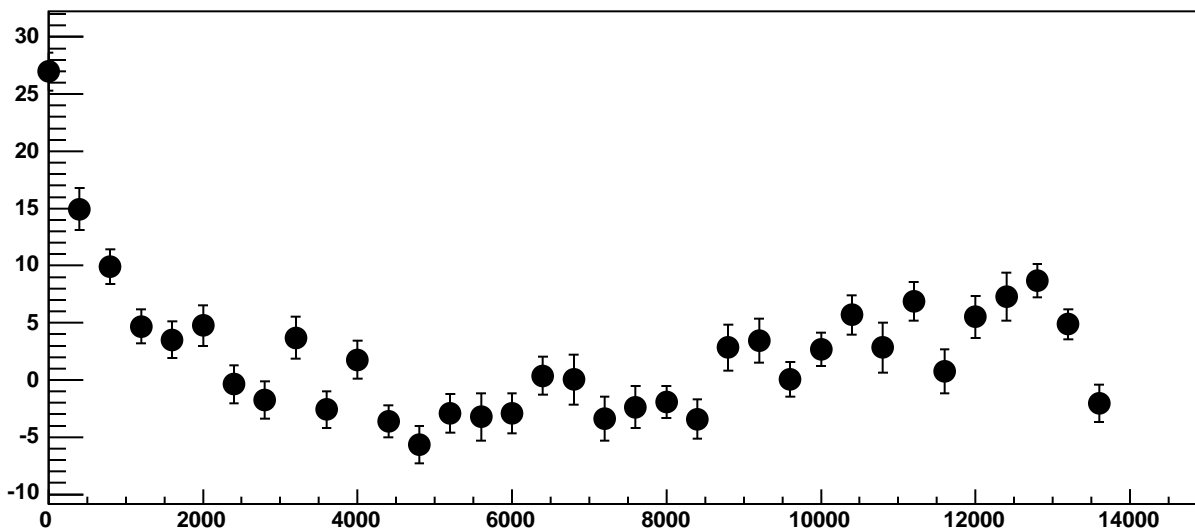


$\chi^2 / \text{ndf}$  89.68 / 23  
p0  $-639.1 \pm 0.762$   
p1  $0.02373 \pm 0.0001165$

Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

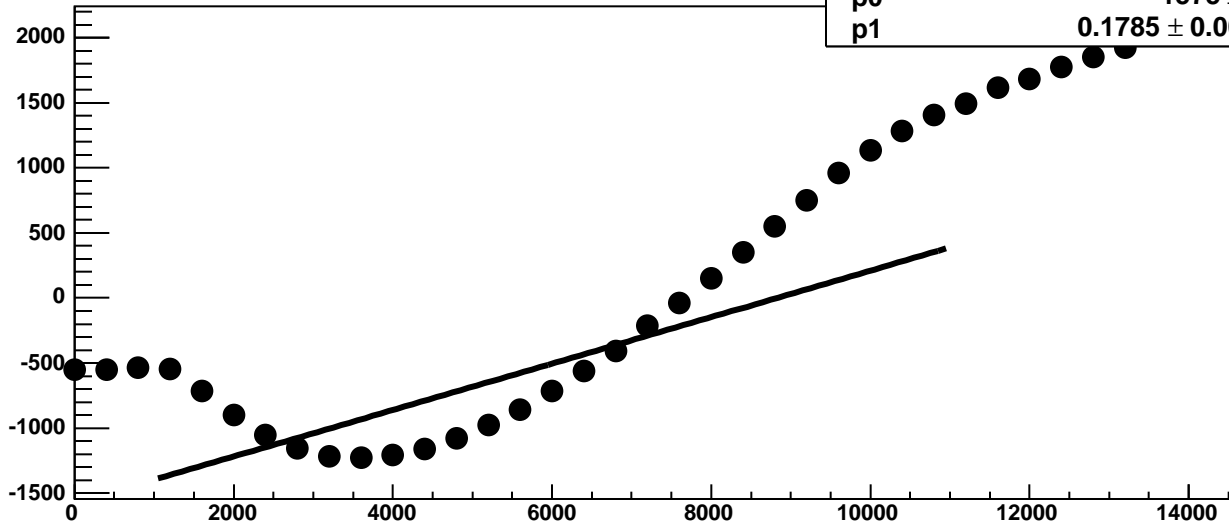


Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



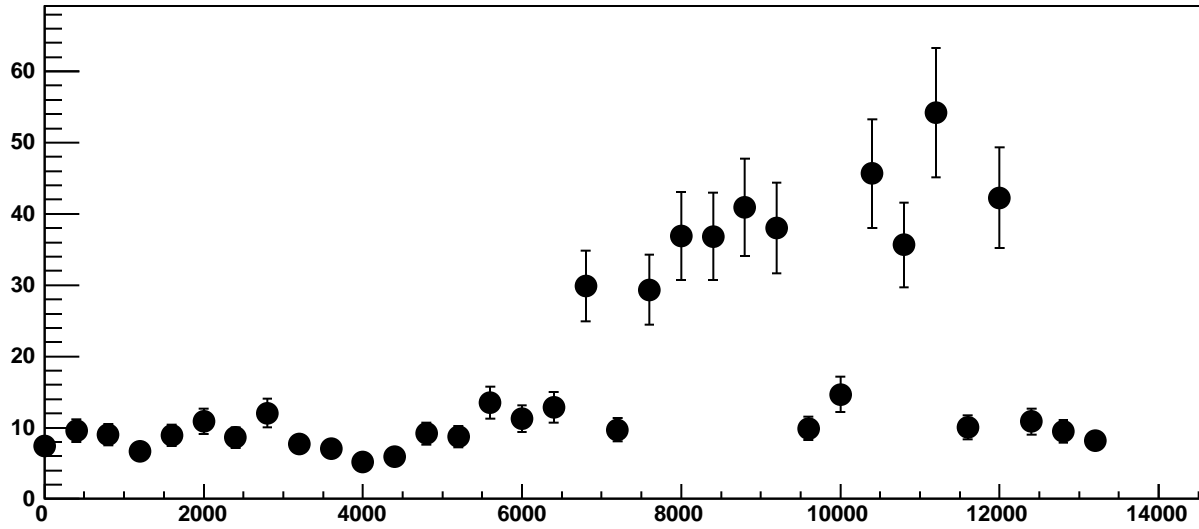


Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

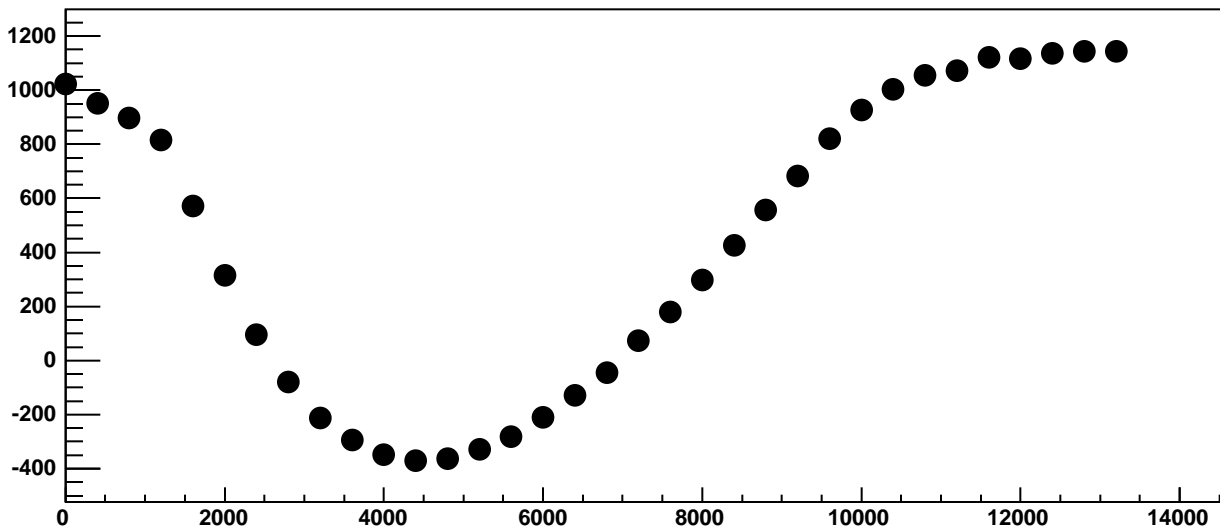


$\chi^2 / \text{ndf}$  9.108e+05 / 23  
p0 -1575 ± 1.014  
p1 0.1785 ± 0.0002118

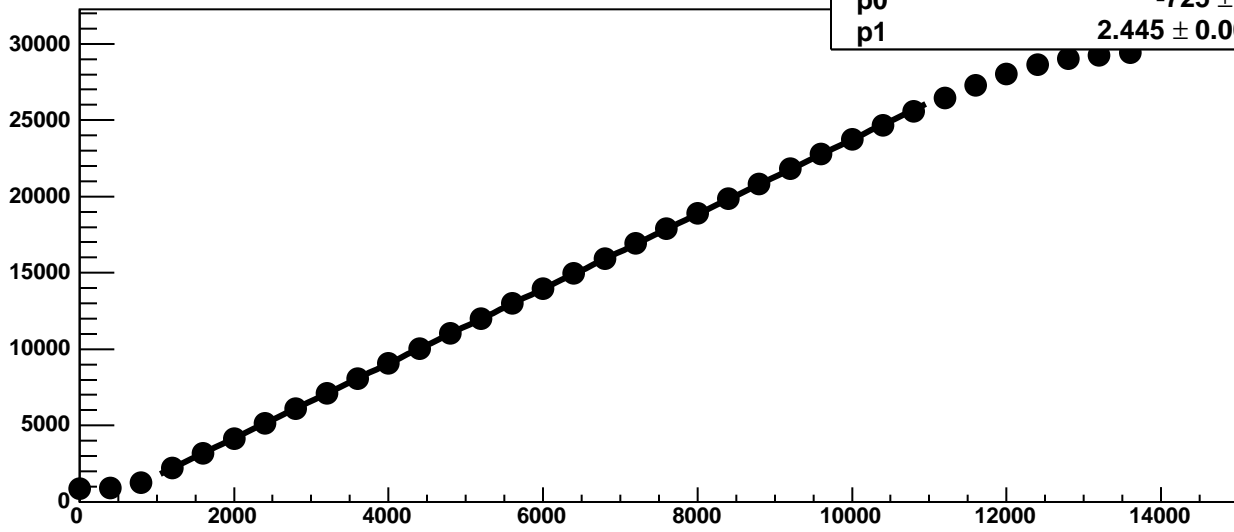
Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

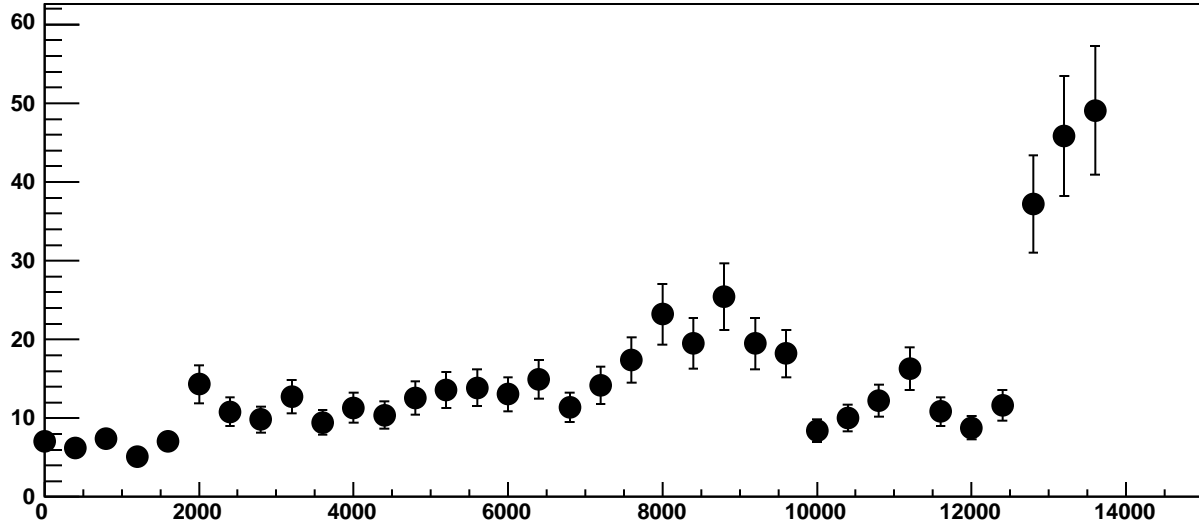


Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

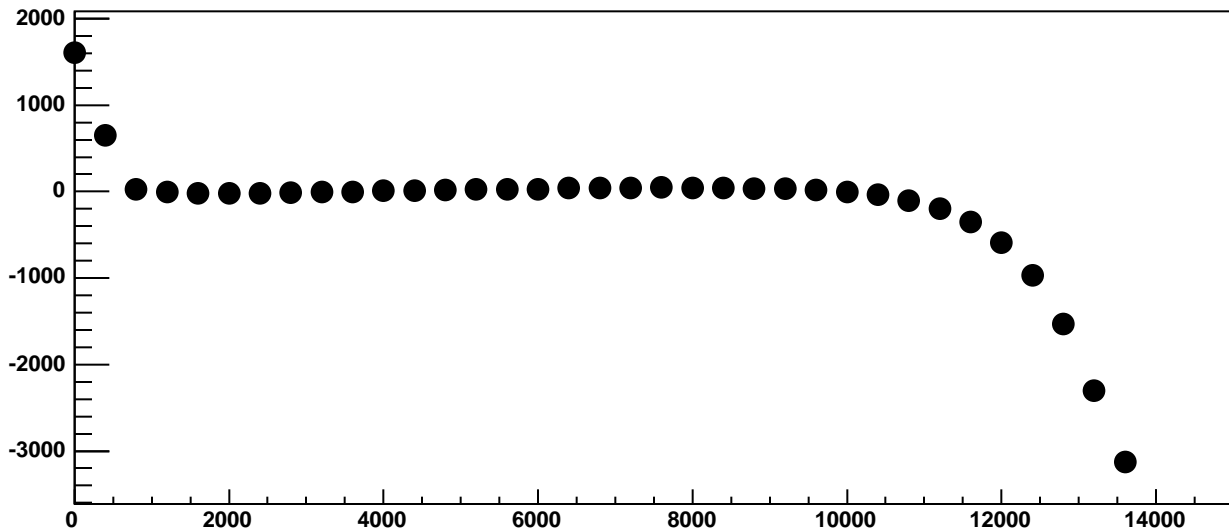


$\chi^2 / \text{ndf}$  3222 / 23  
p0  $-725 \pm 0.9014$   
p1  $2.445 \pm 0.0001578$

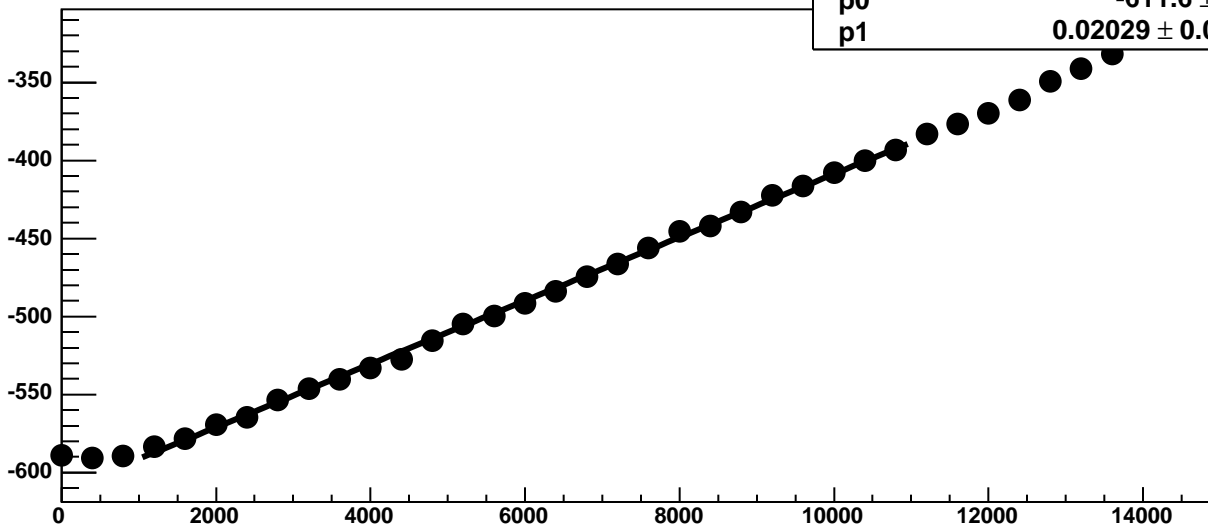
Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

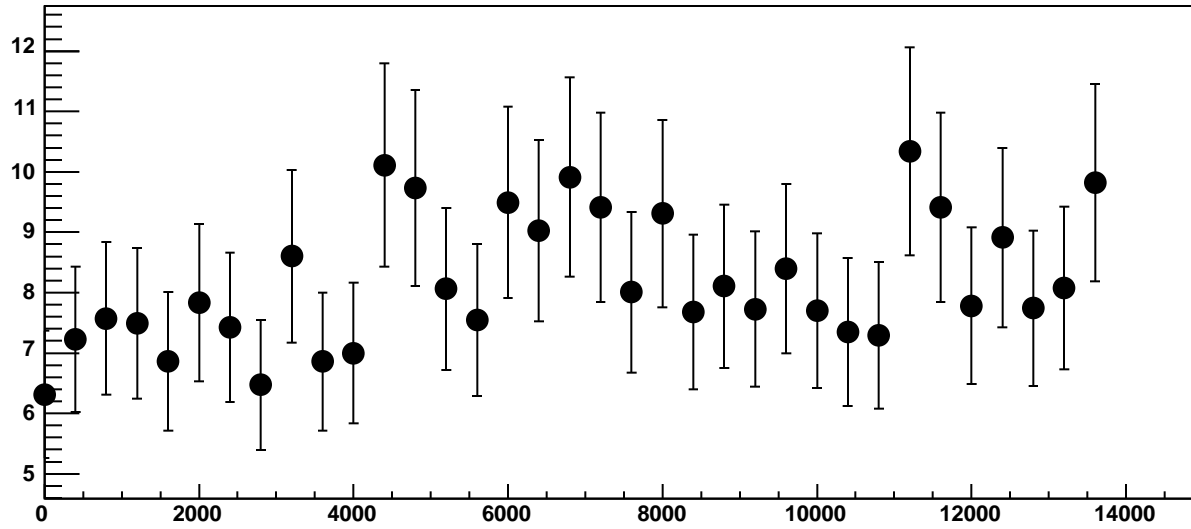


Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

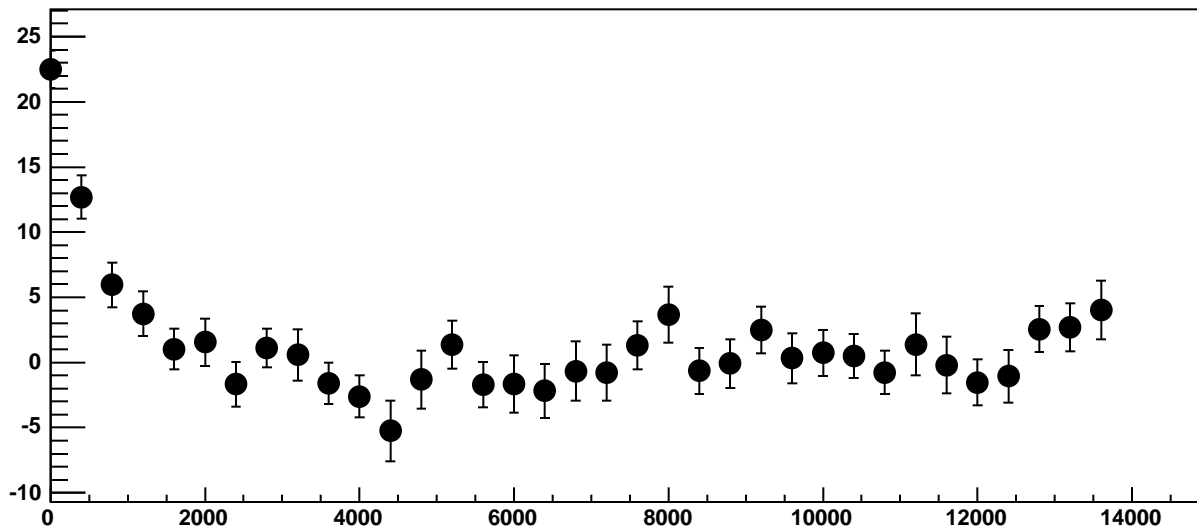


$\chi^2 / \text{ndf}$  26.06 / 23  
p0  $-611.6 \pm 0.7903$   
p1  $0.02029 \pm 0.0001199$

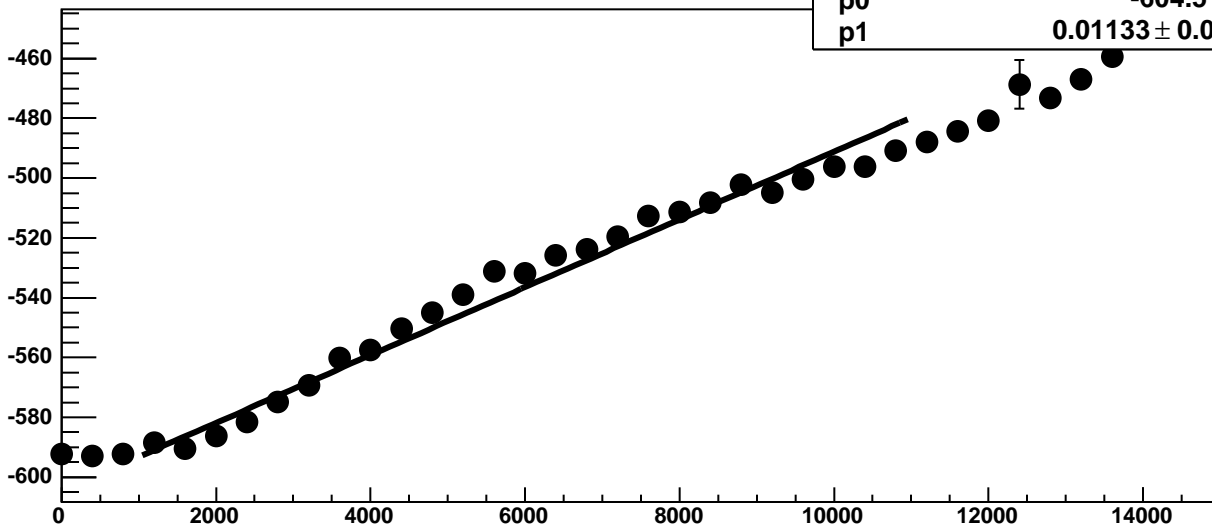
Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



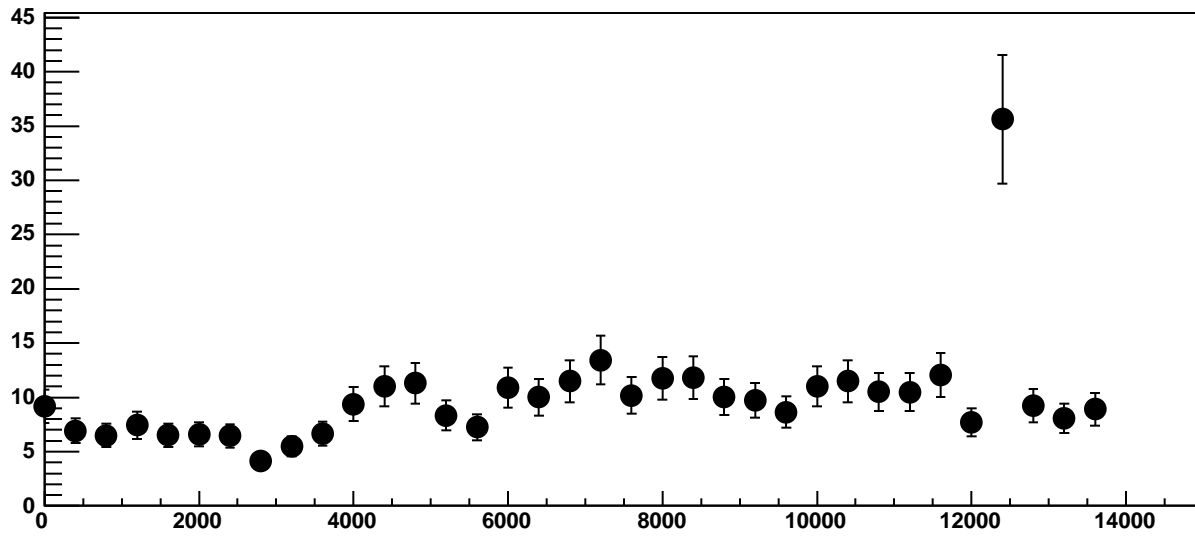
Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



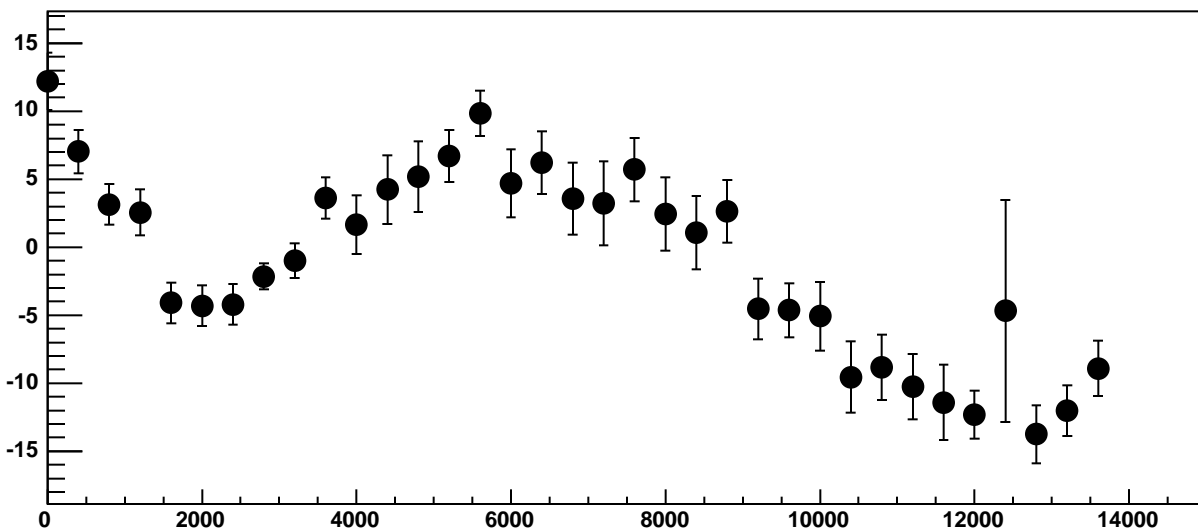
Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



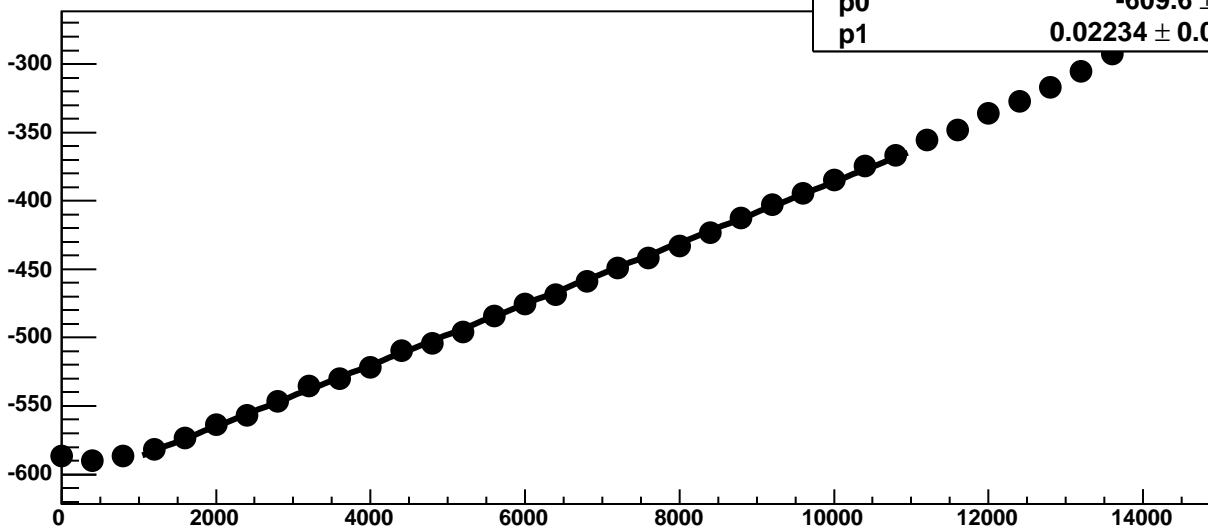
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

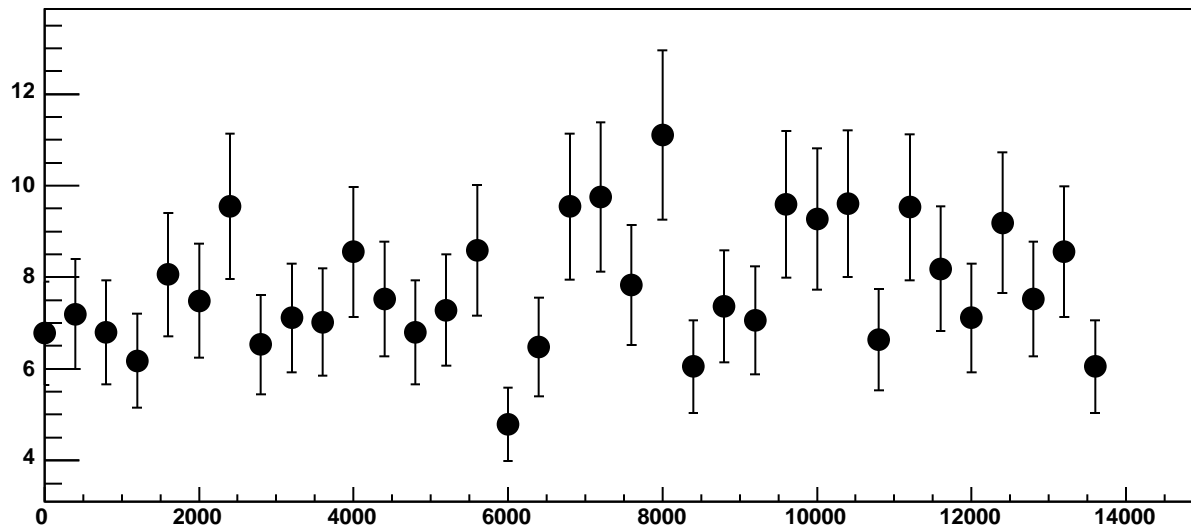


Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

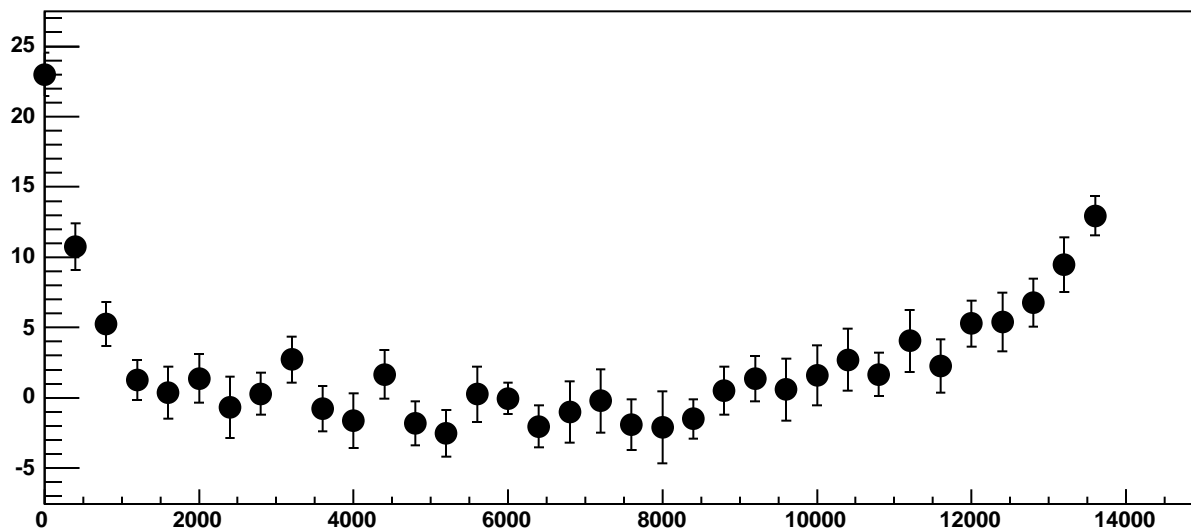


$\chi^2 / \text{ndf}$  19.08 / 23  
p0  $-609.6 \pm 0.7769$   
p1  $0.02234 \pm 0.0001203$

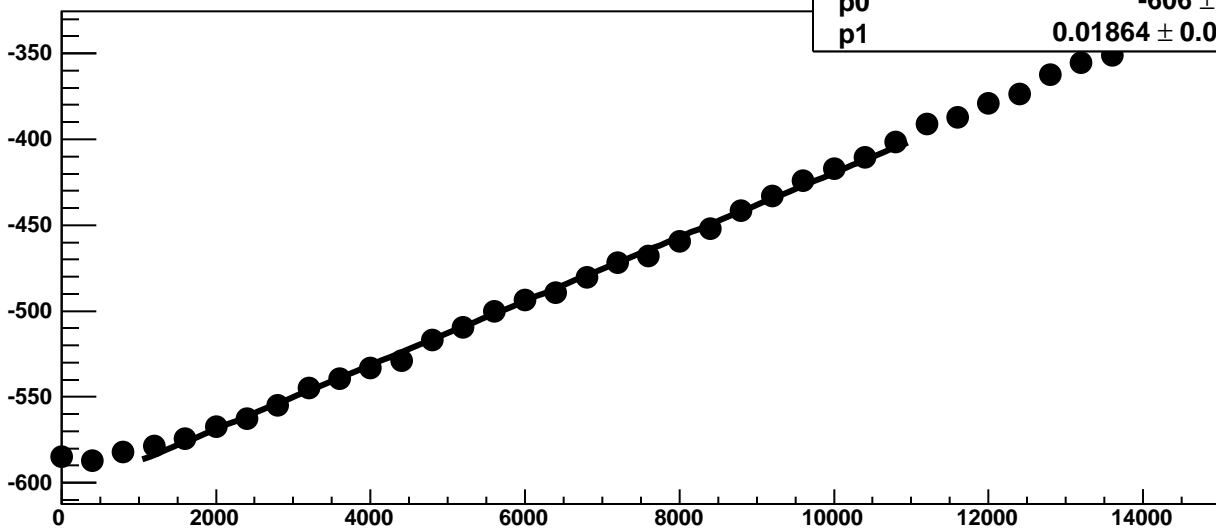
Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

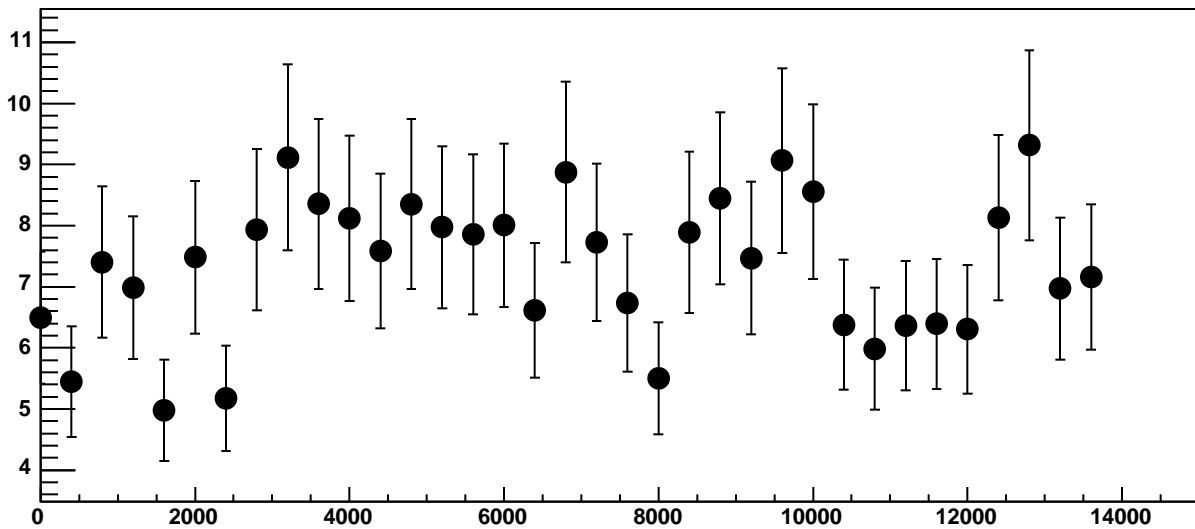


Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

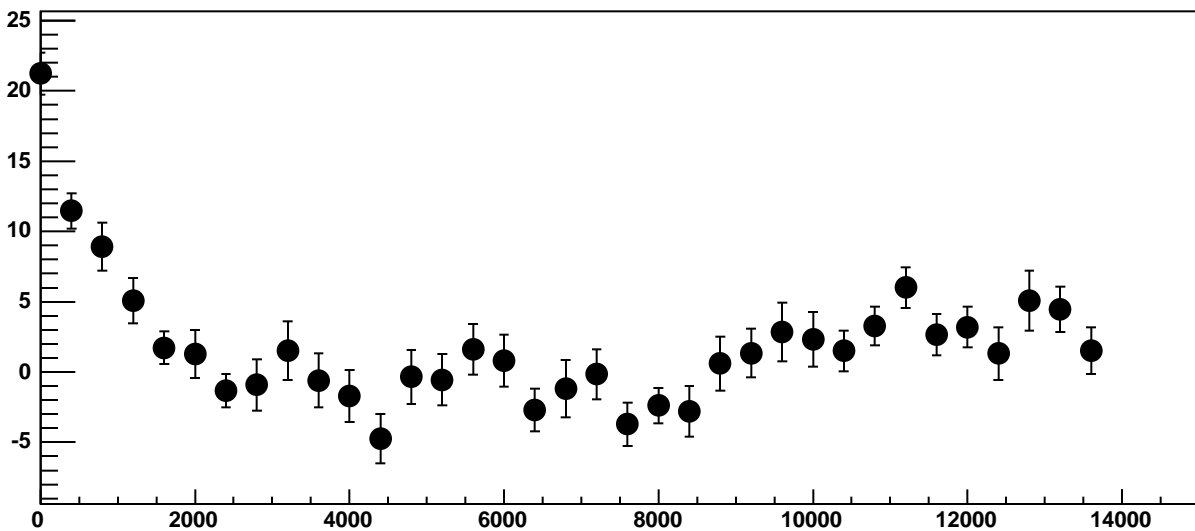


$\chi^2 / \text{ndf}$  50.33 / 23  
p0  $-606 \pm 0.7064$   
p1  $0.01864 \pm 0.0001071$

Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

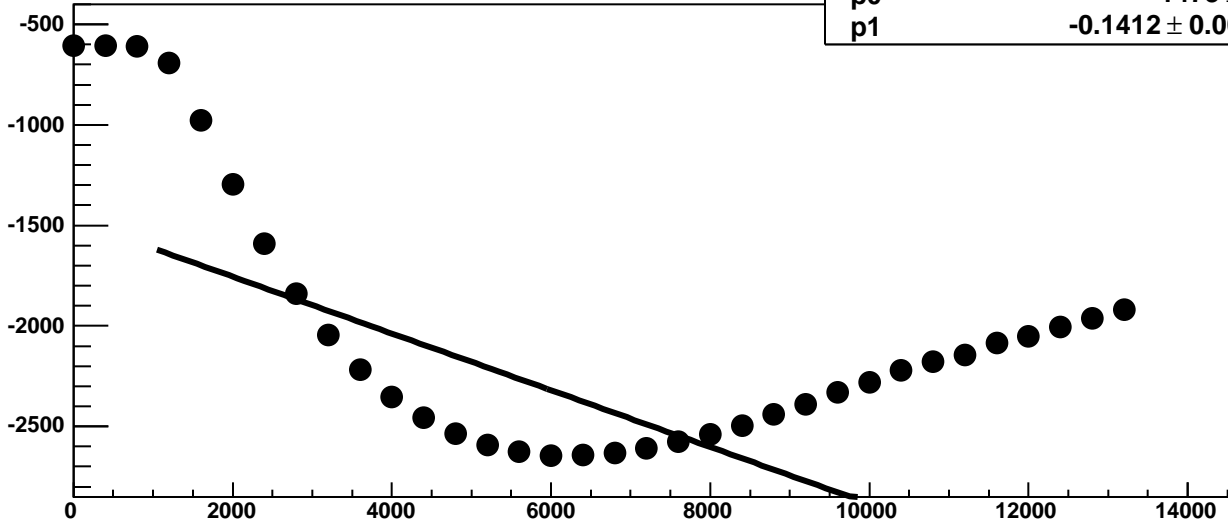


Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

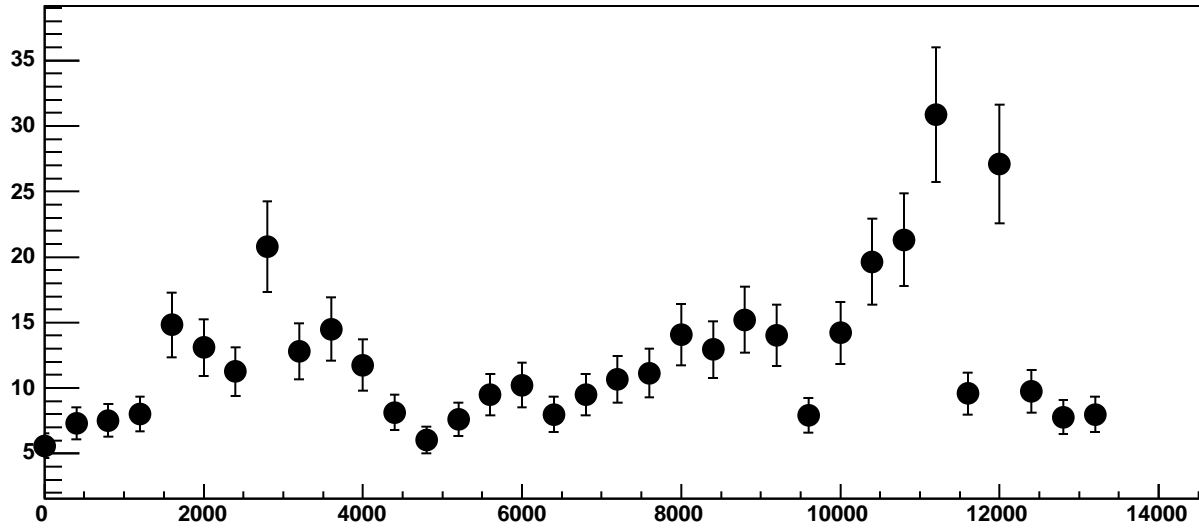


Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

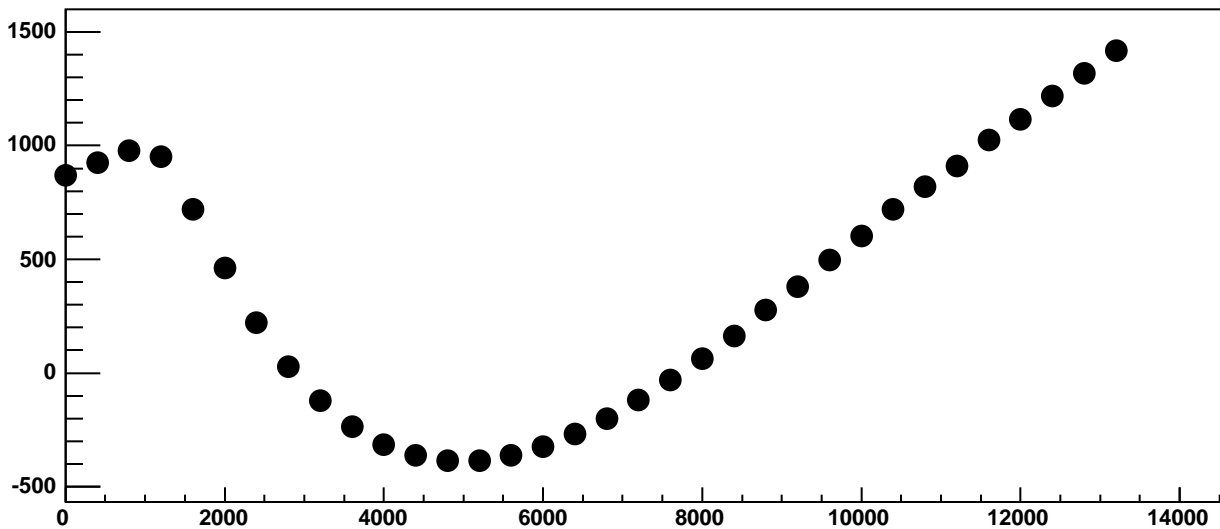
$\chi^2 / \text{ndf}$  7.922e+05 / 23  
p0 -1473 ± 1.195  
p1 -0.1412 ± 0.0001938



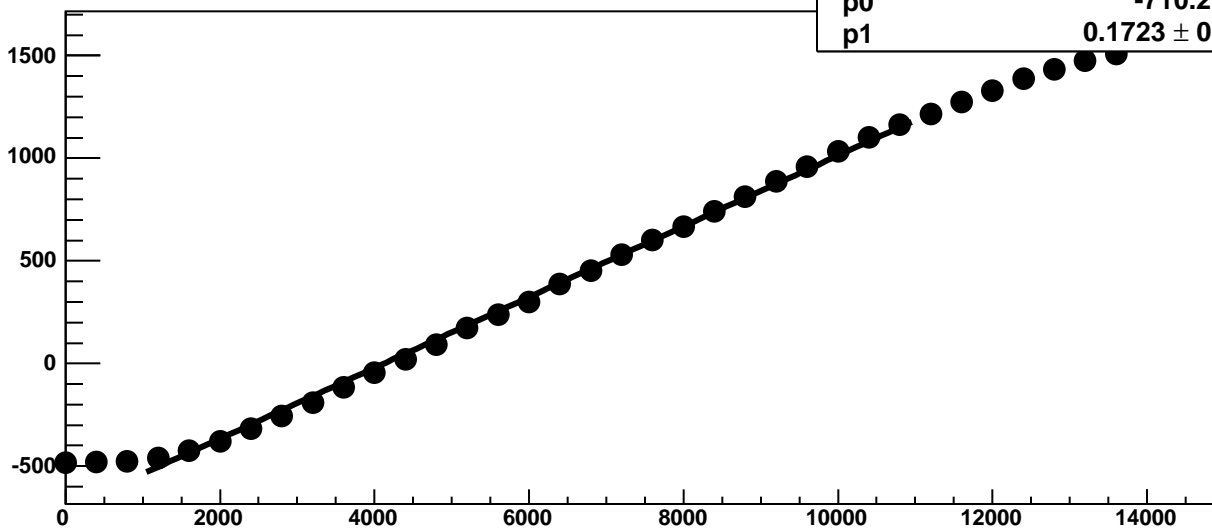
Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



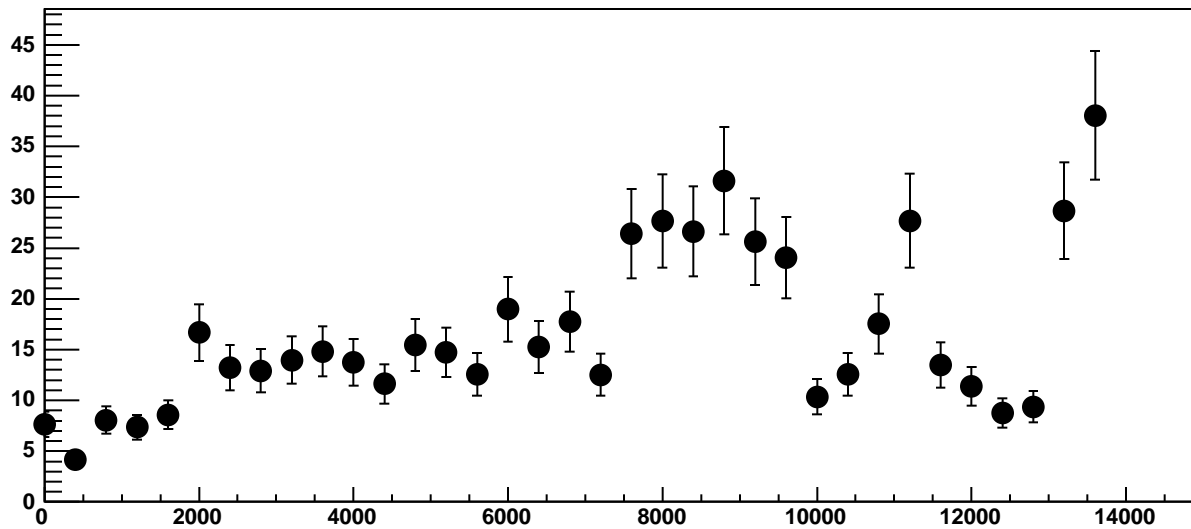
Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



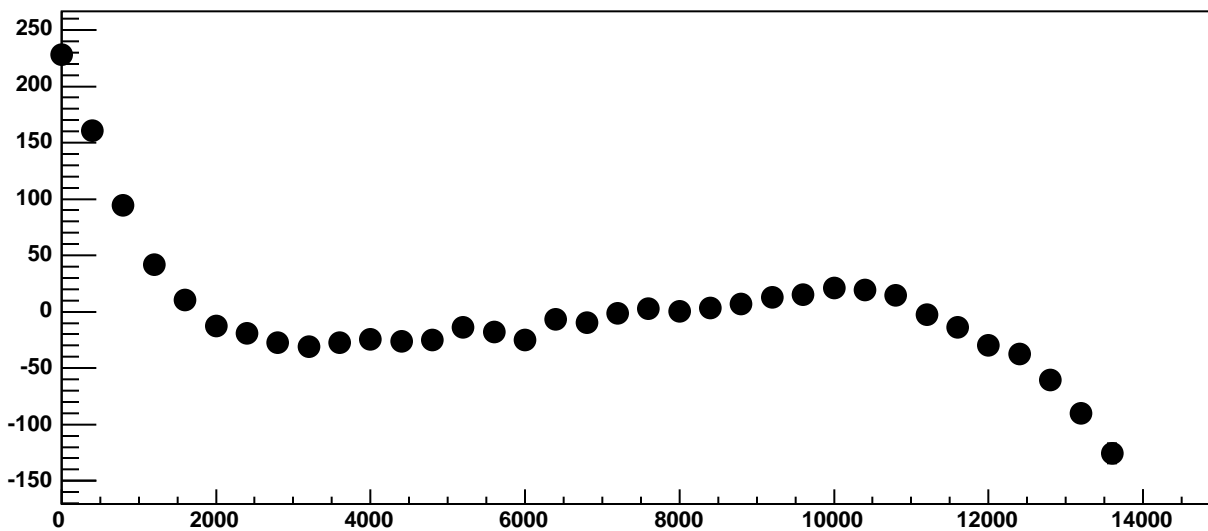
Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

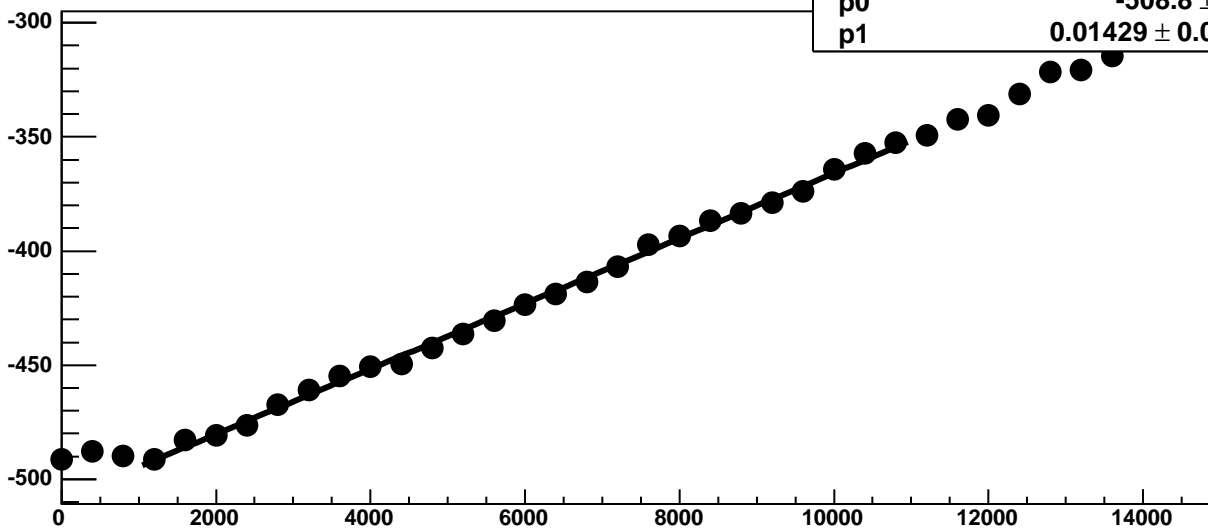


Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



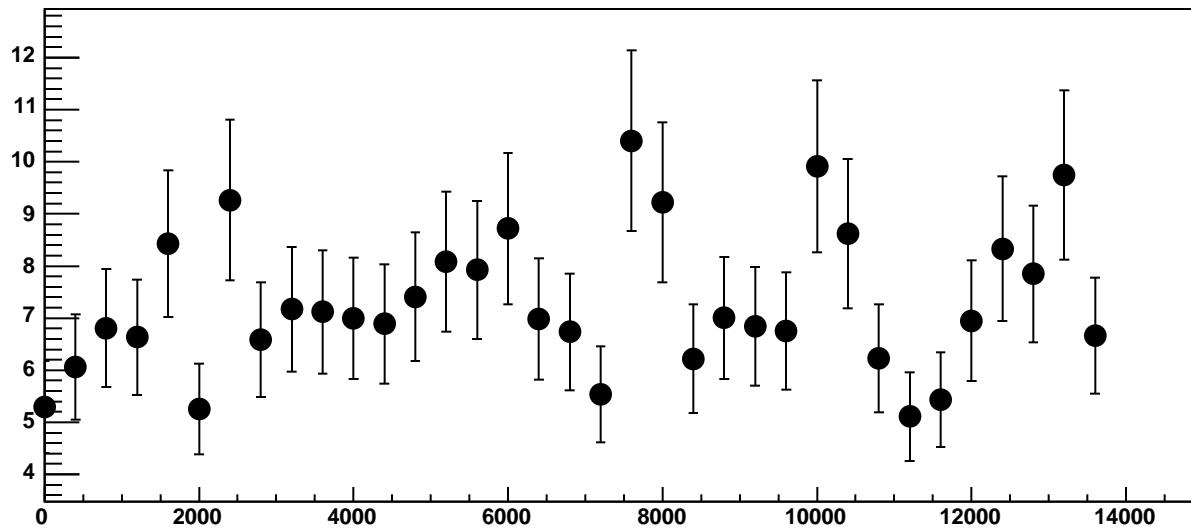


Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

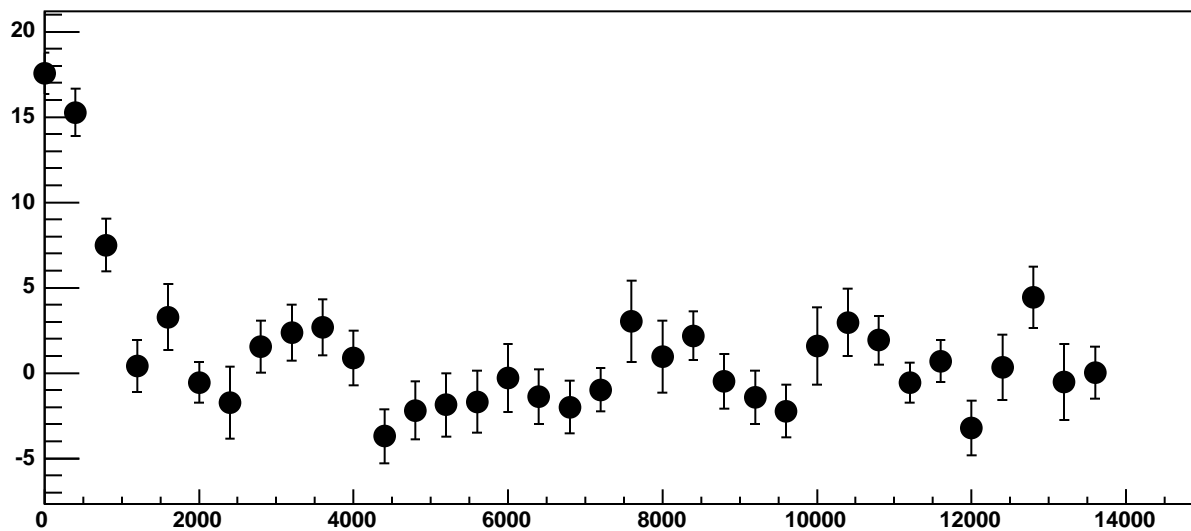


$\chi^2 / \text{ndf}$  33.65 / 23  
p0  $-508.8 \pm 0.7394$   
p1  $0.01429 \pm 0.0001125$

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

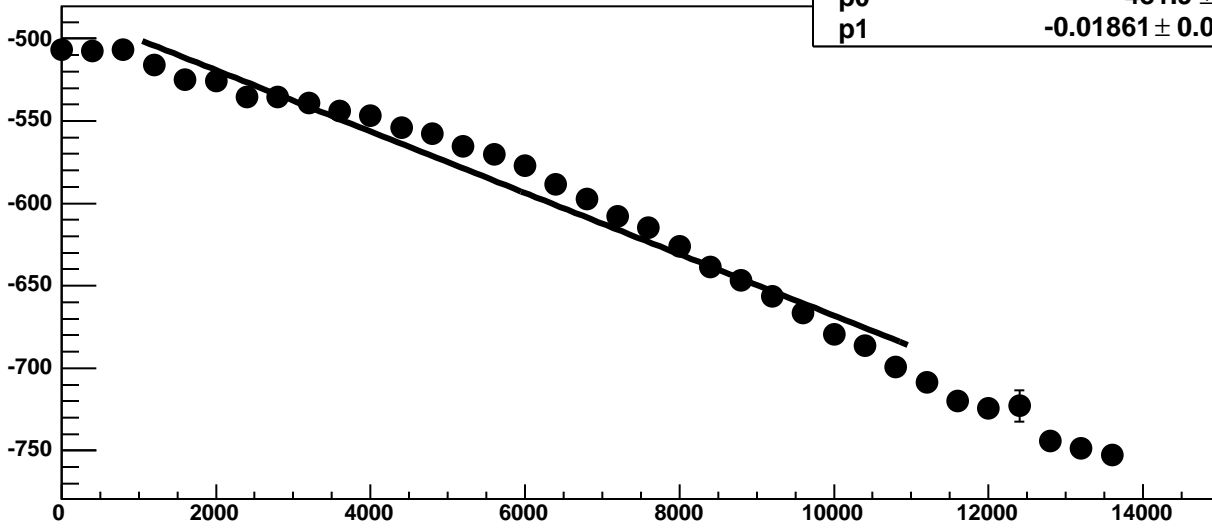


Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

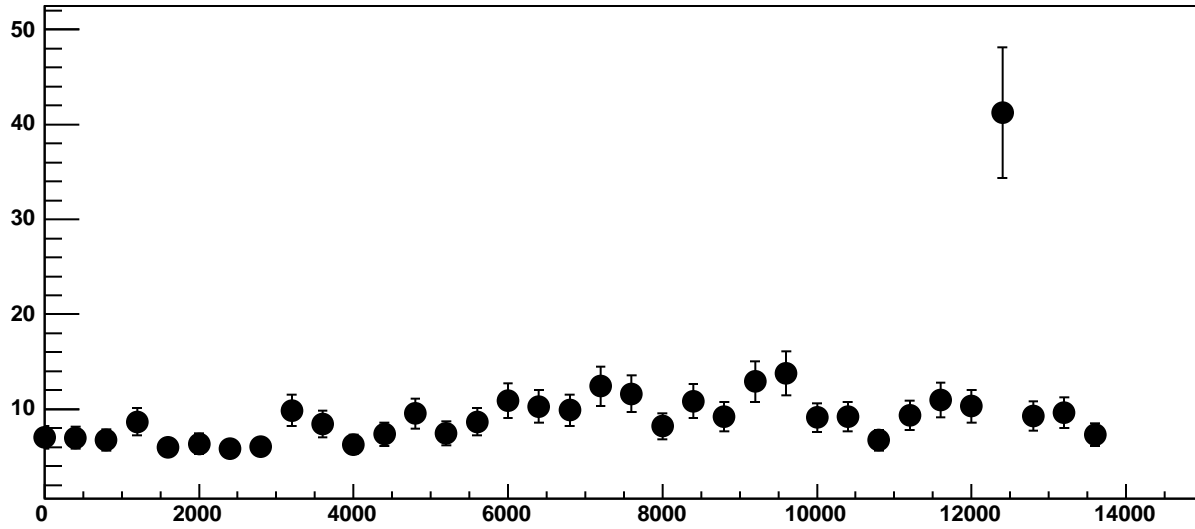


Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

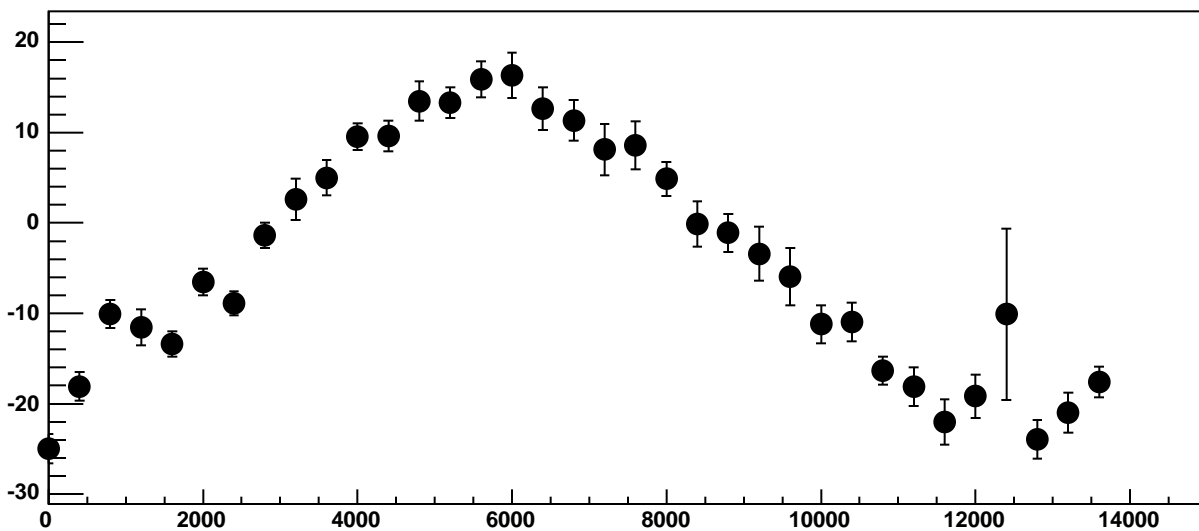
$\chi^2 / \text{ndf}$  733.6 / 23  
p0  $-481.9 \pm 0.7587$   
p1  $-0.01861 \pm 0.0001264$



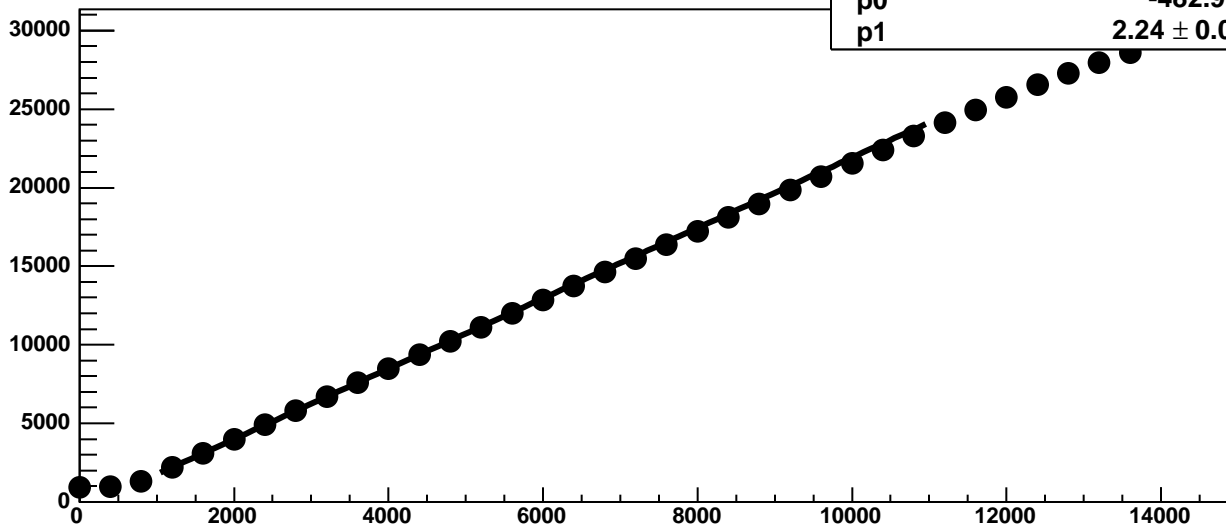
Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



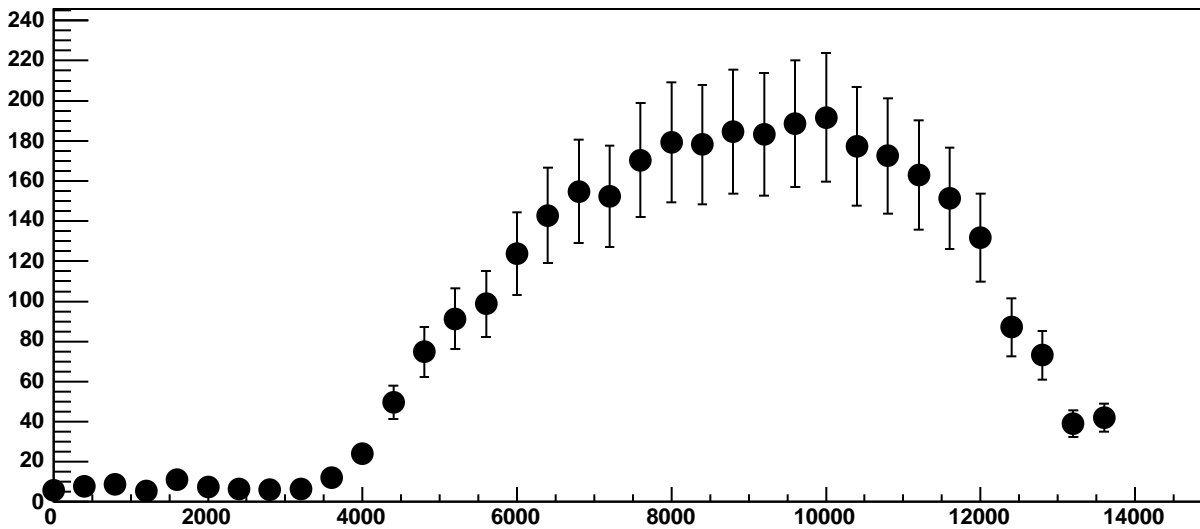
Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



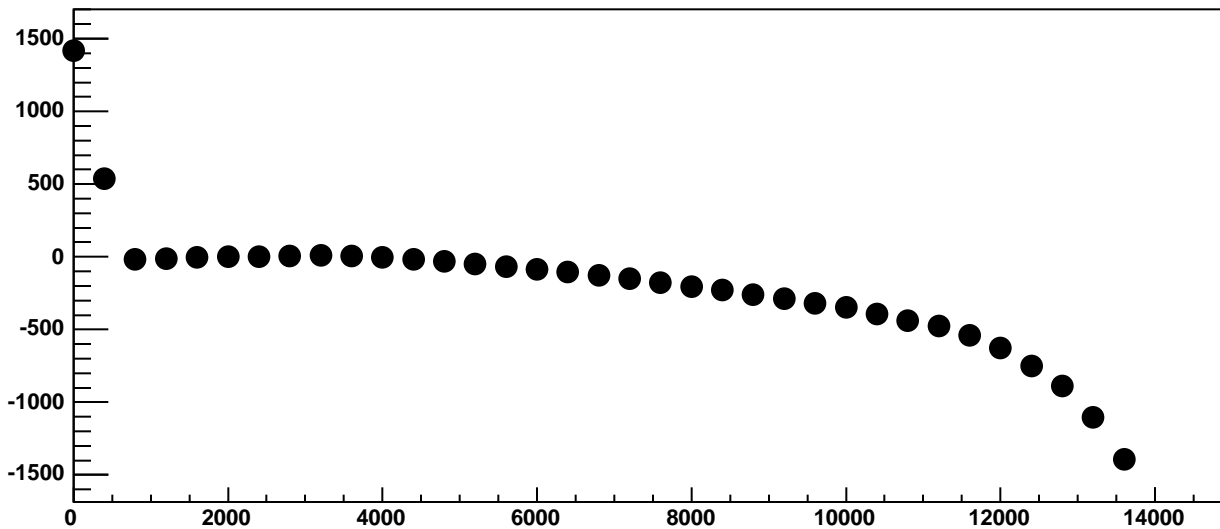
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



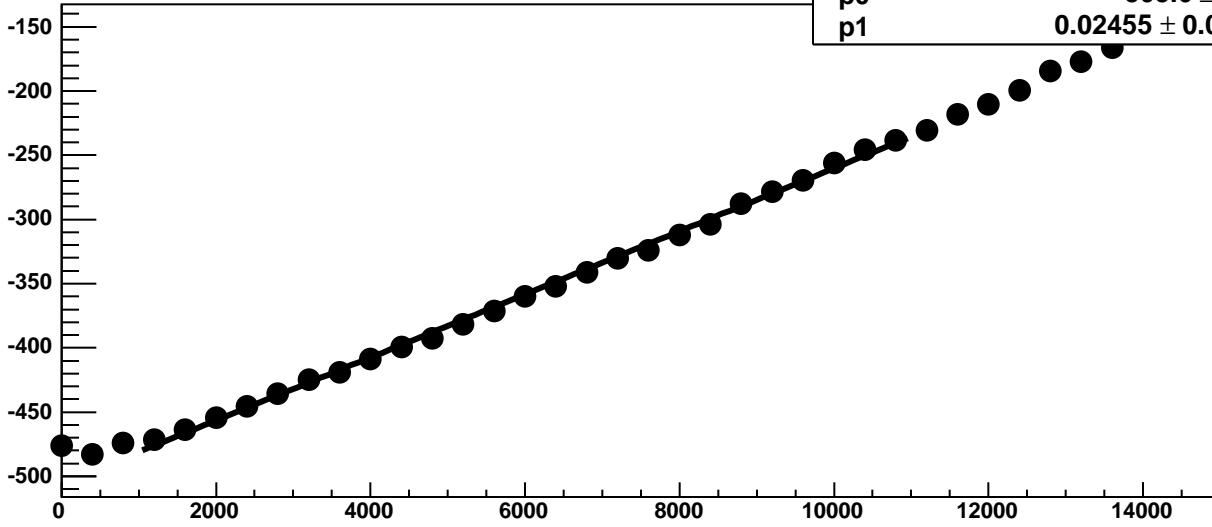
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



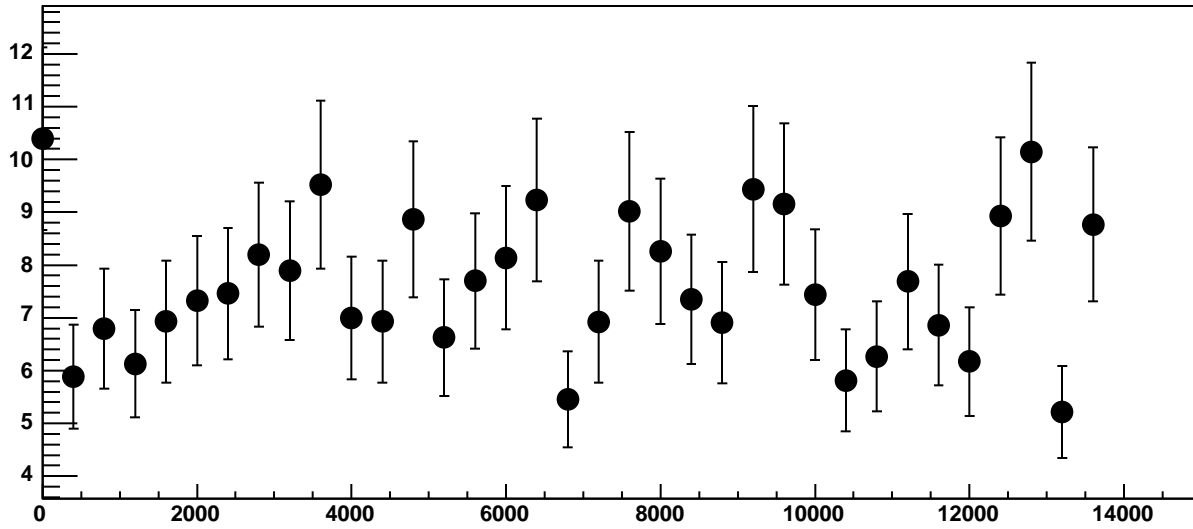
Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC



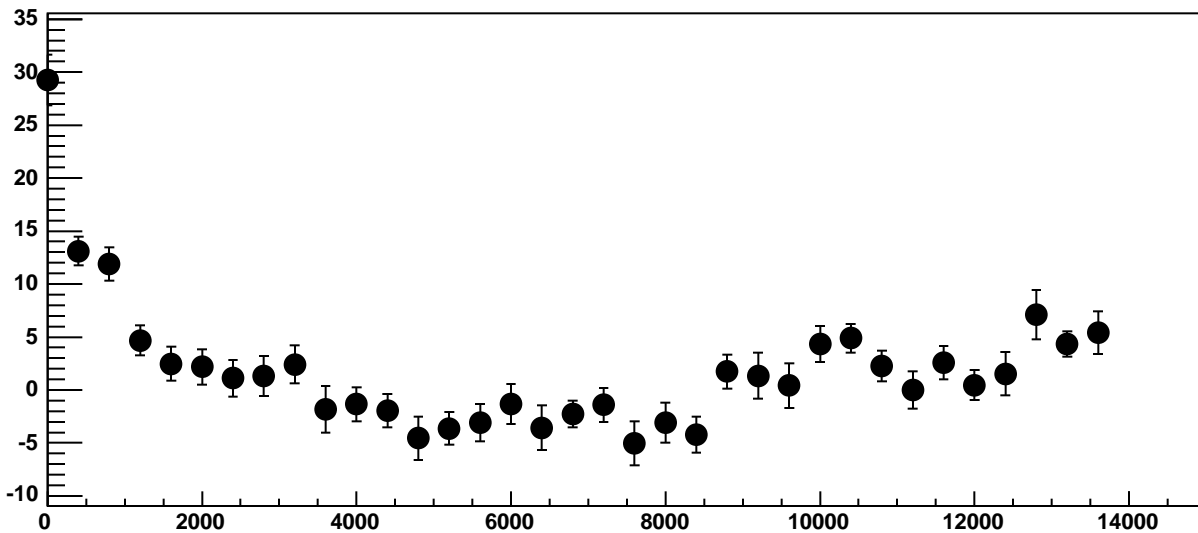
Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



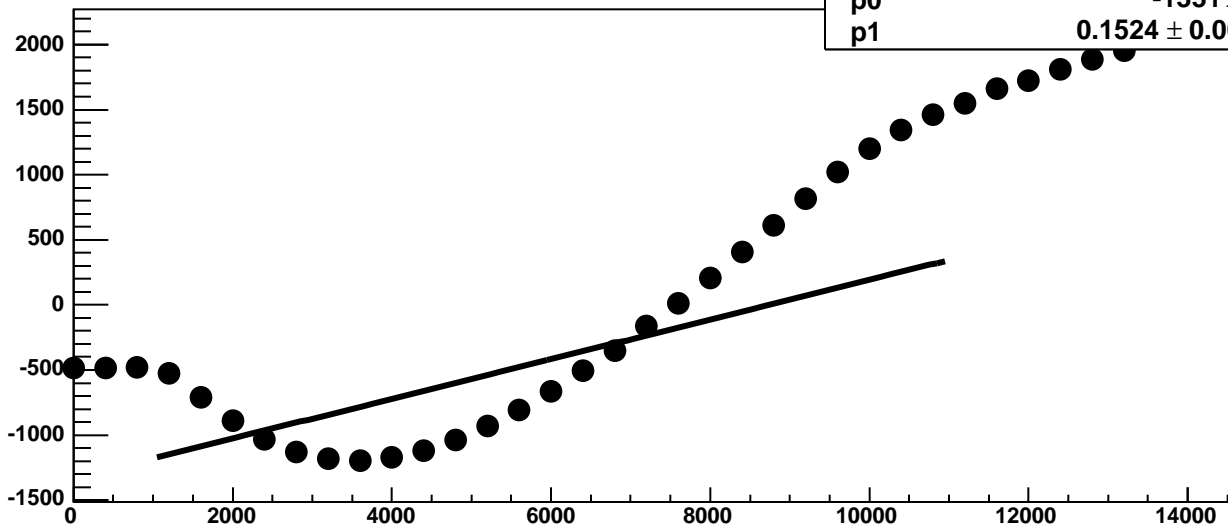
Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

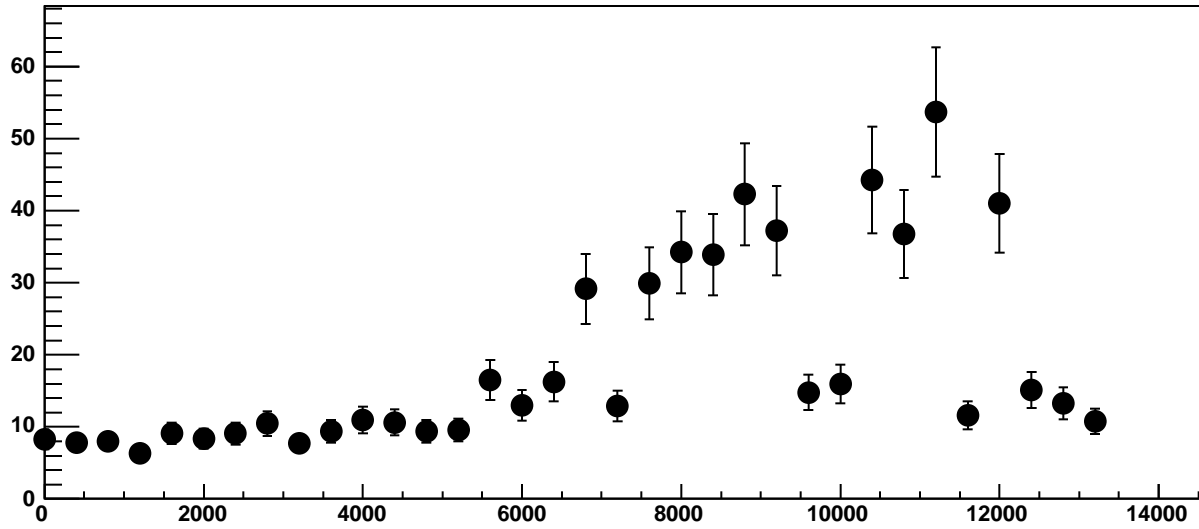


Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

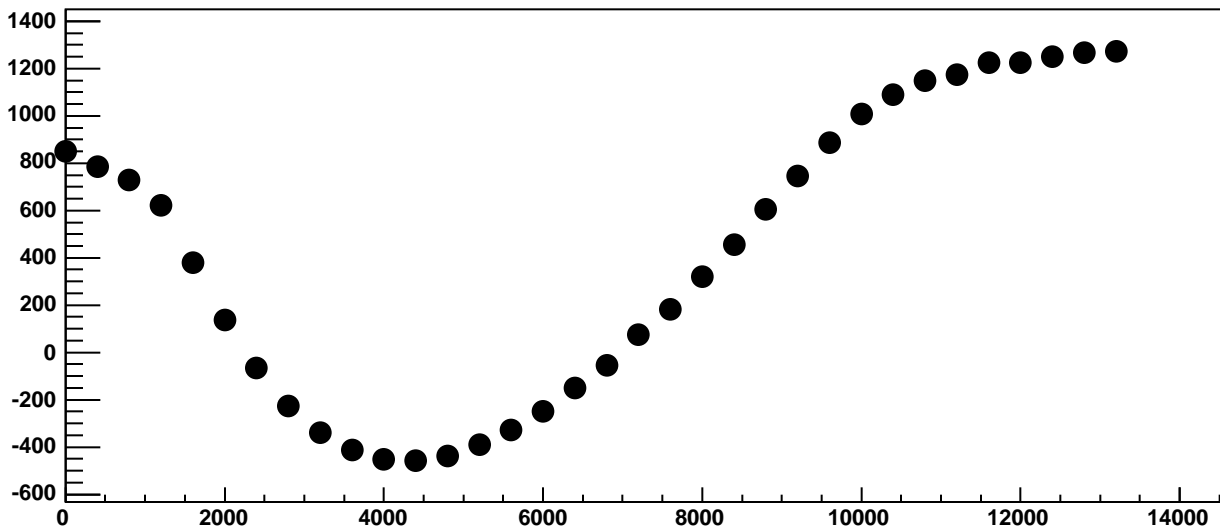


$\chi^2 / \text{ndf}$  6.528e+05 / 23  
p0 -1331 ± 1.065  
p1 0.1524 ± 0.0002365

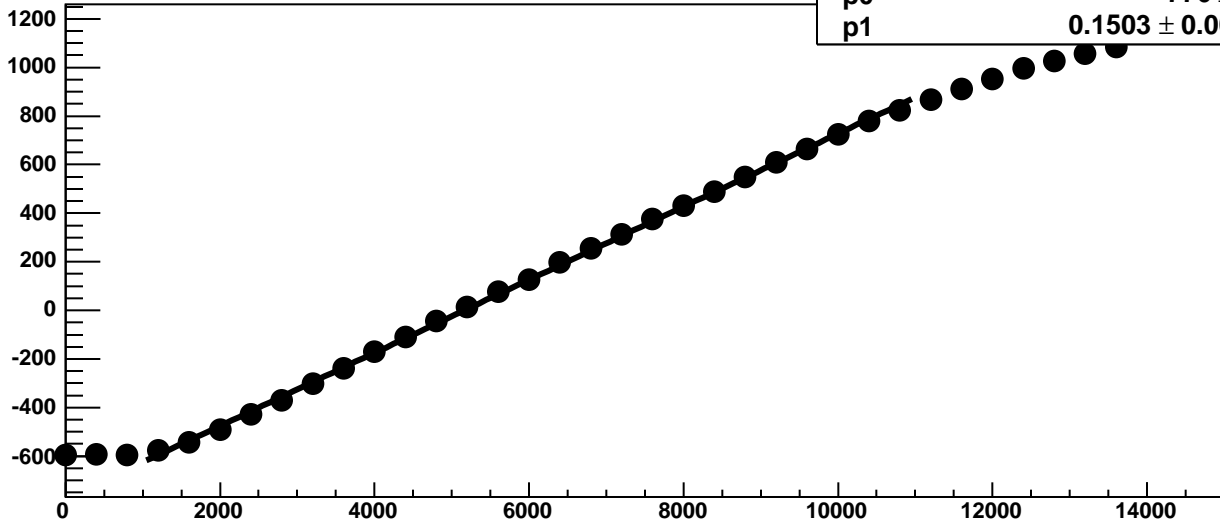
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



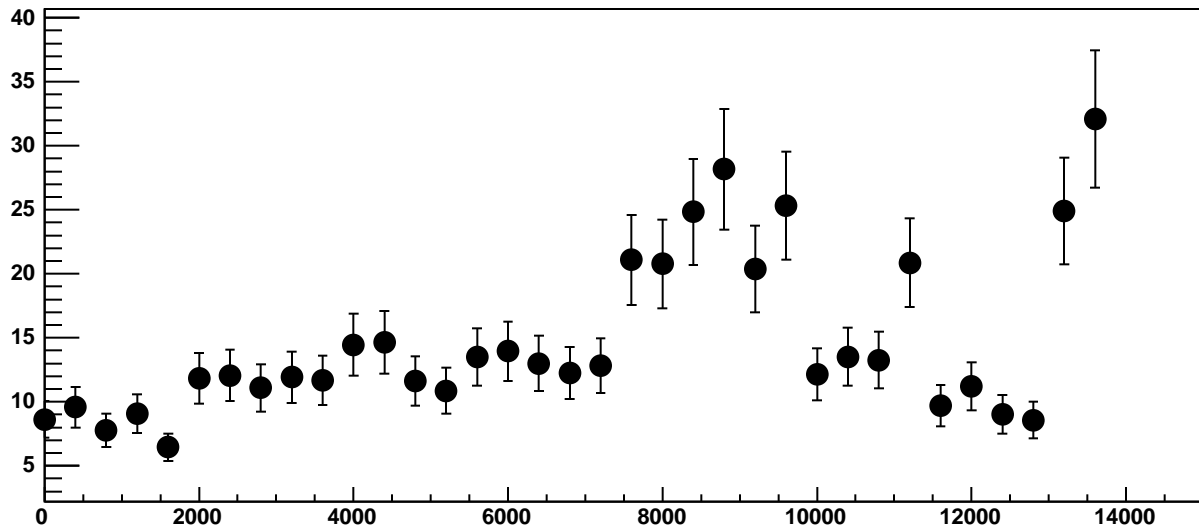
Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



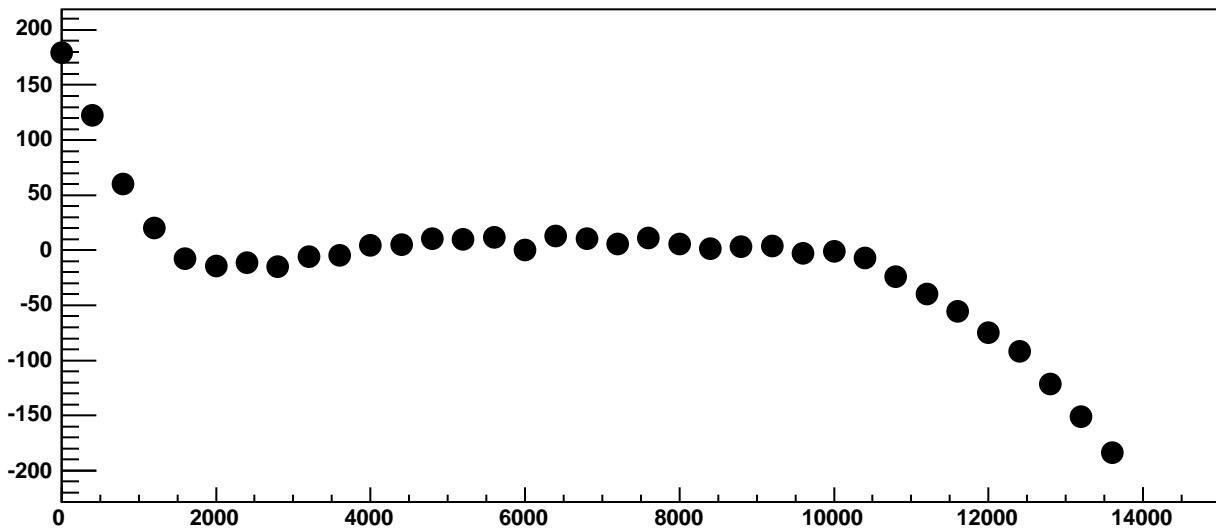
Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



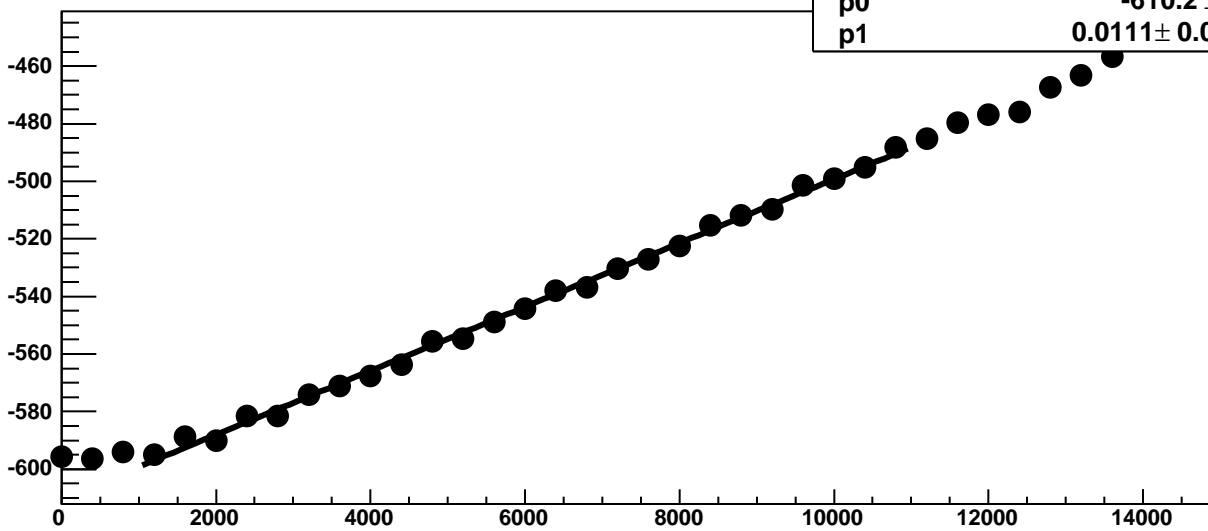
Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

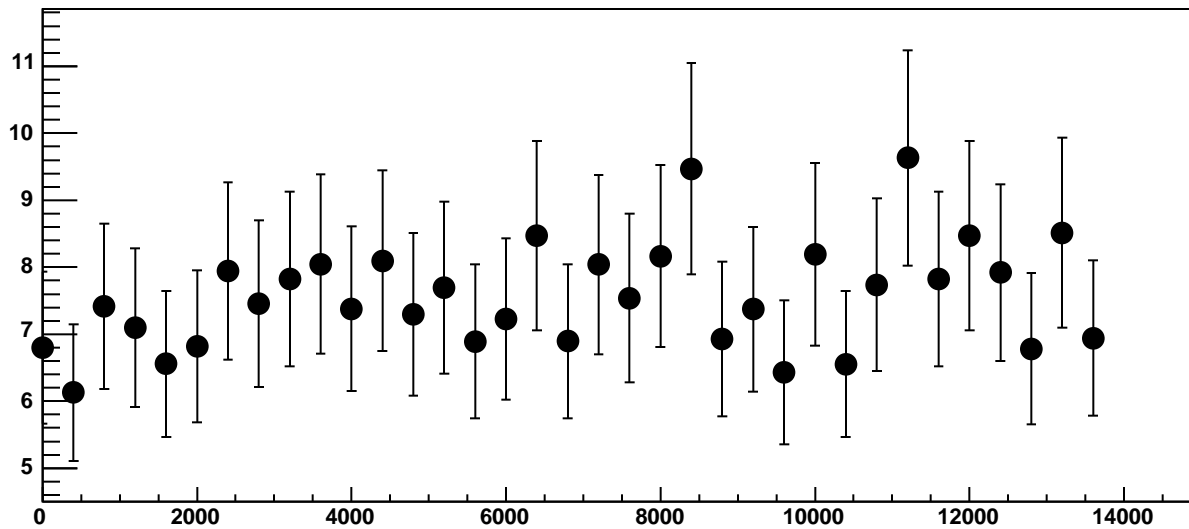


Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

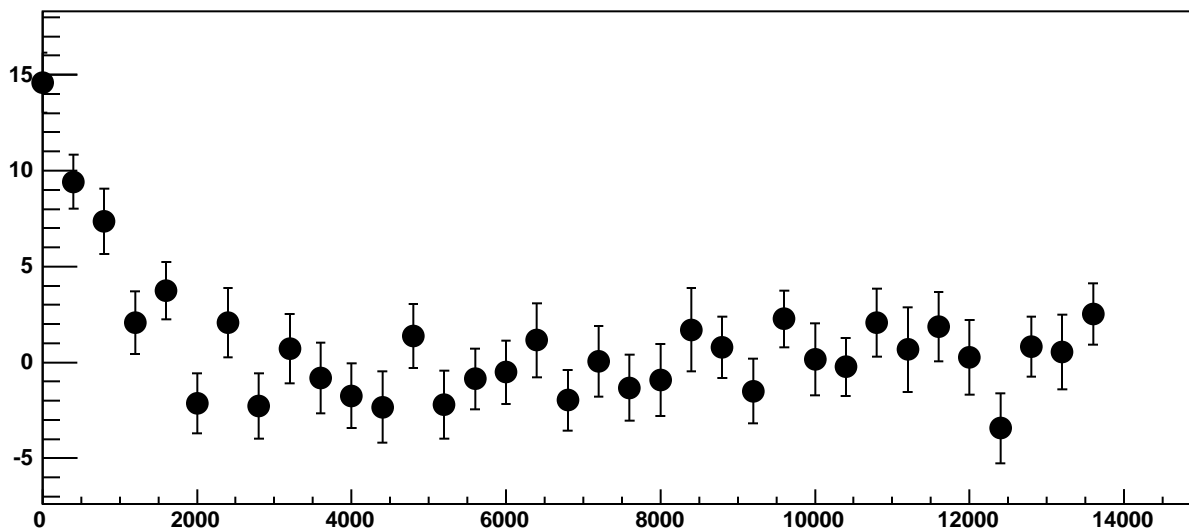


$\chi^2 / \text{ndf}$  26.44 / 23  
p0  $-610.2 \pm 0.7681$   
p1  $0.0111 \pm 0.0001153$

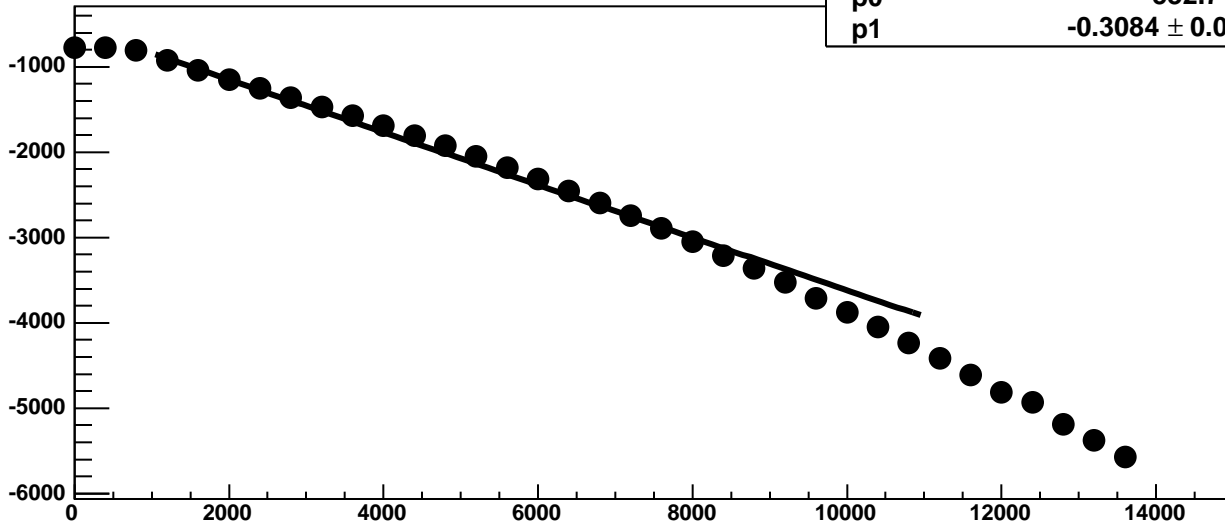
Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4006 / 23

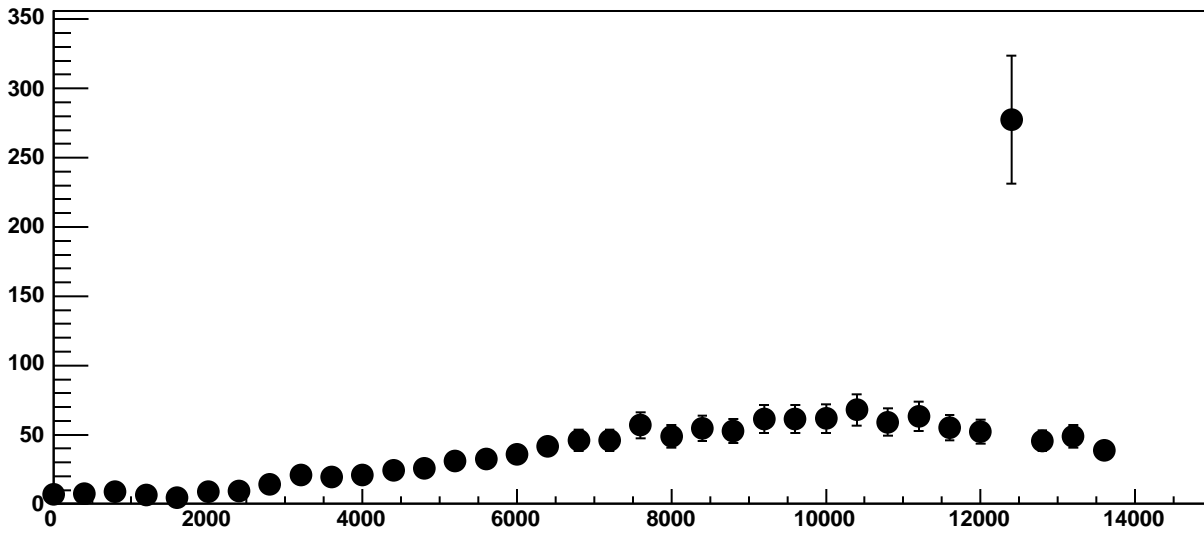
p0

$-532.7 \pm 1.187$

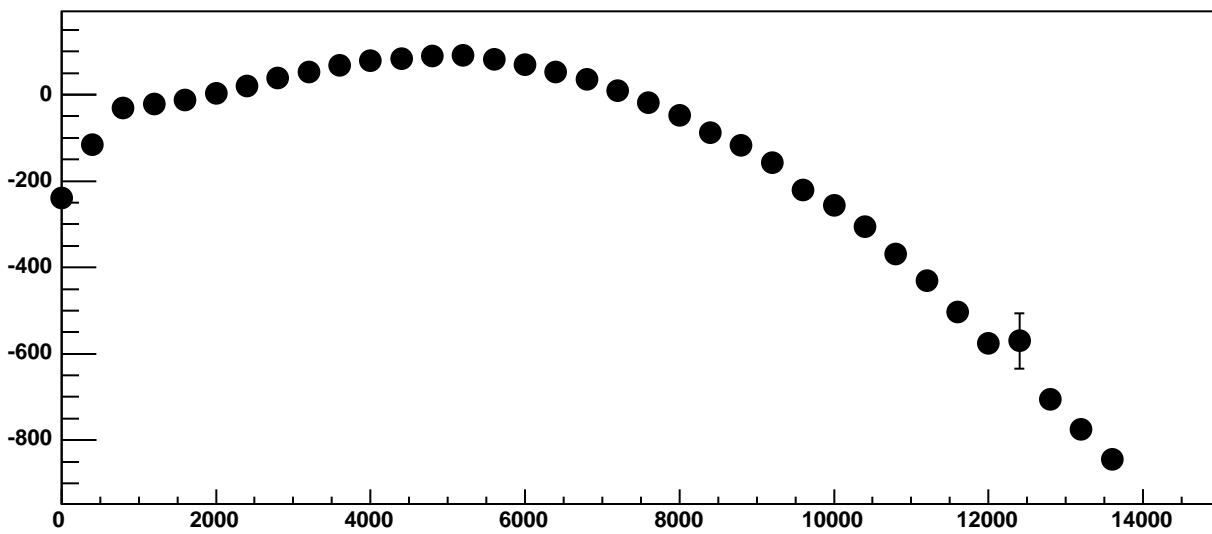
p1

$-0.3084 \pm 0.0004407$

Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

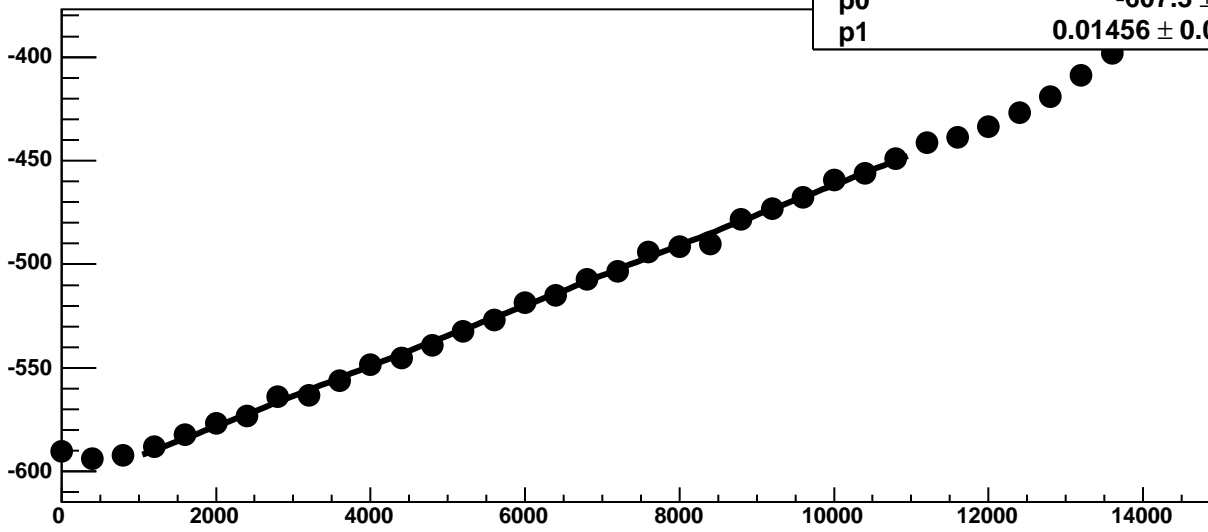


Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

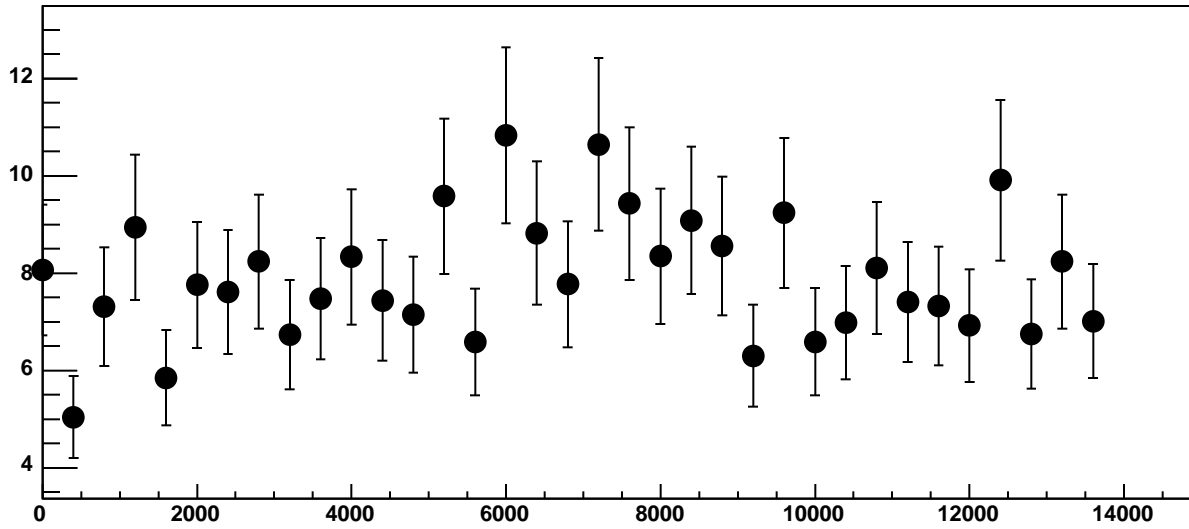




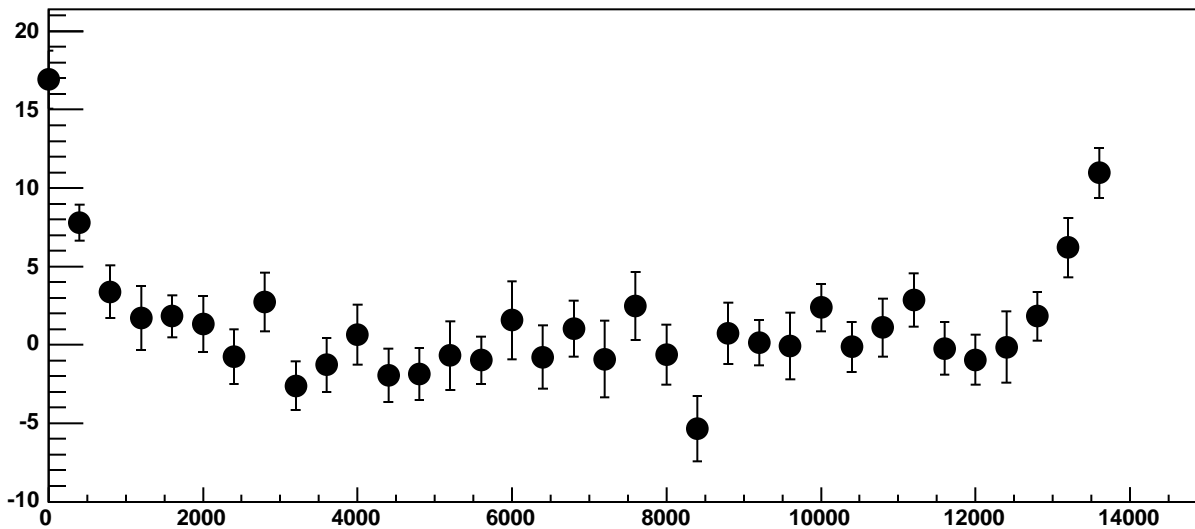
Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



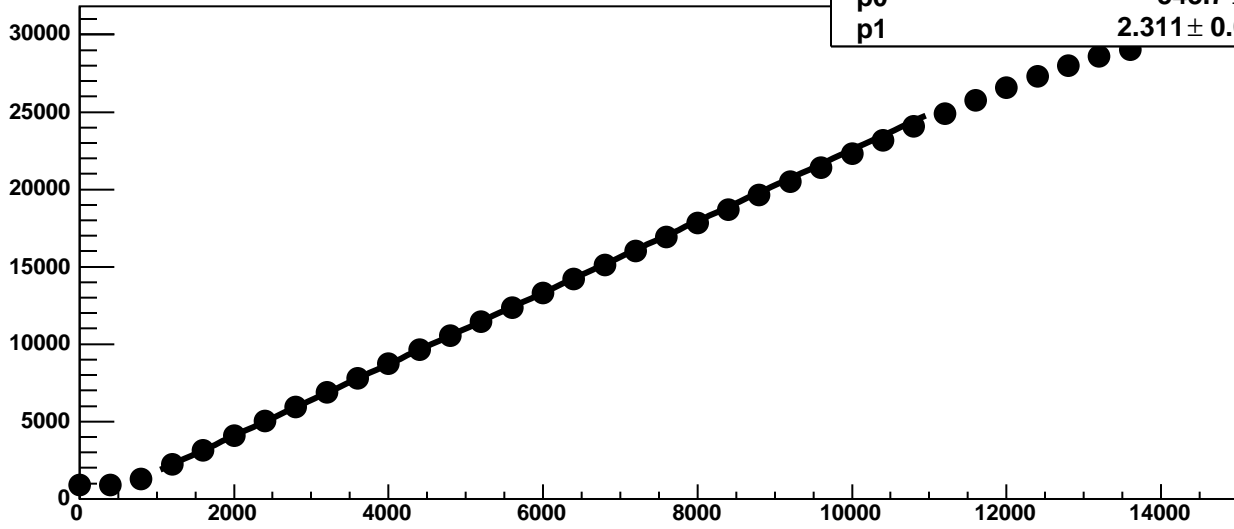
Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

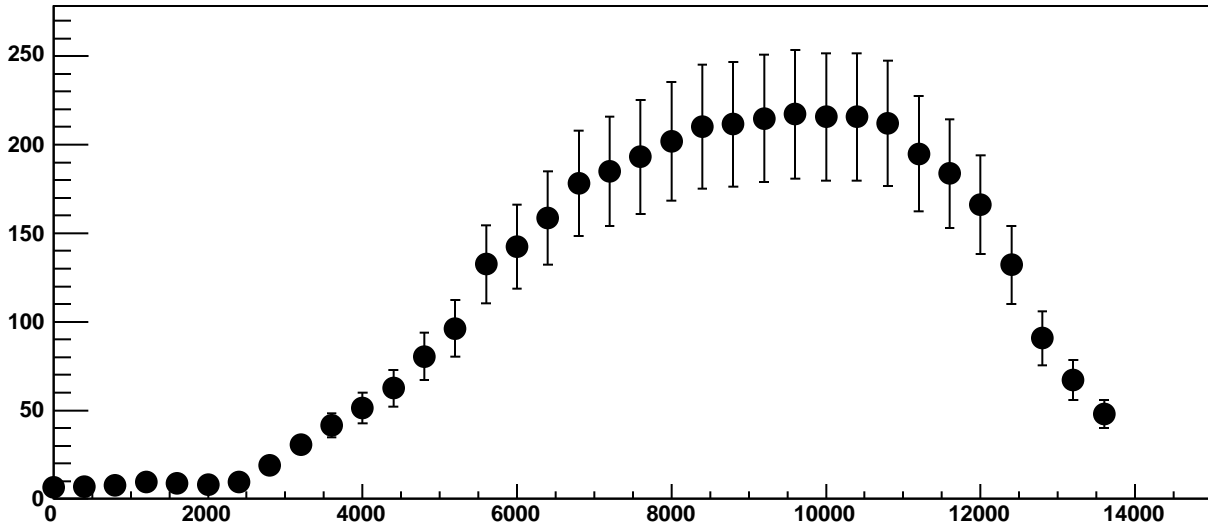


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

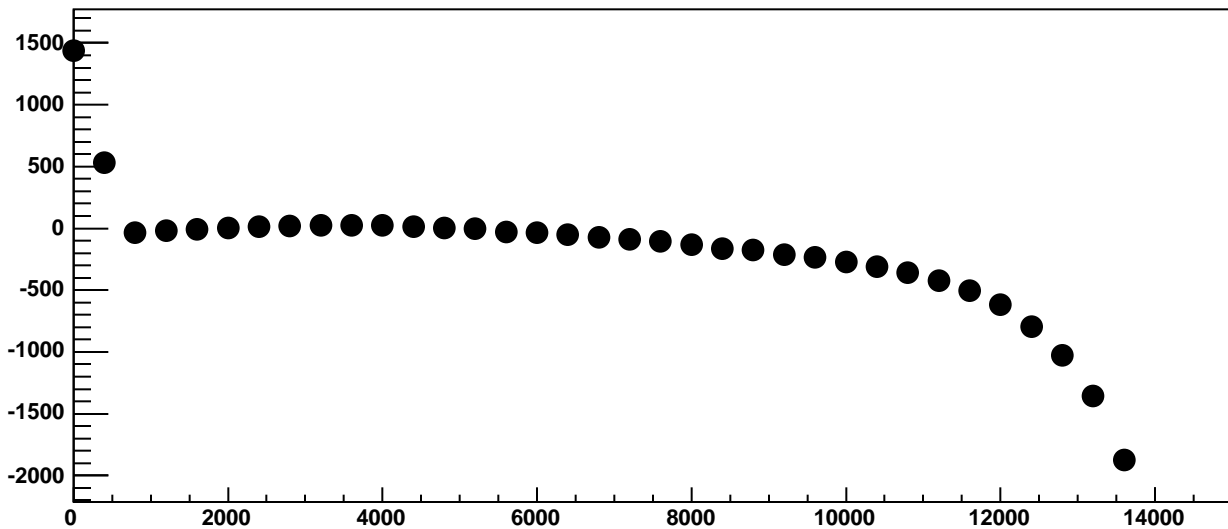


$\chi^2 / \text{ndf}$  387.8 / 23  
p0  $-543.7 \pm 2.625$   
p1  $2.311 \pm 0.001222$

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

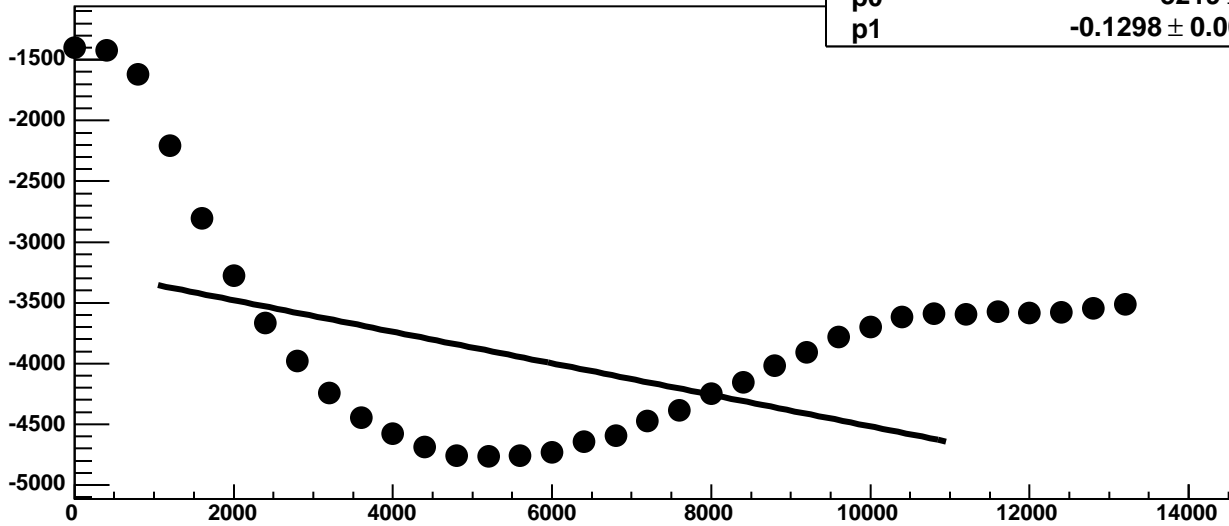


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

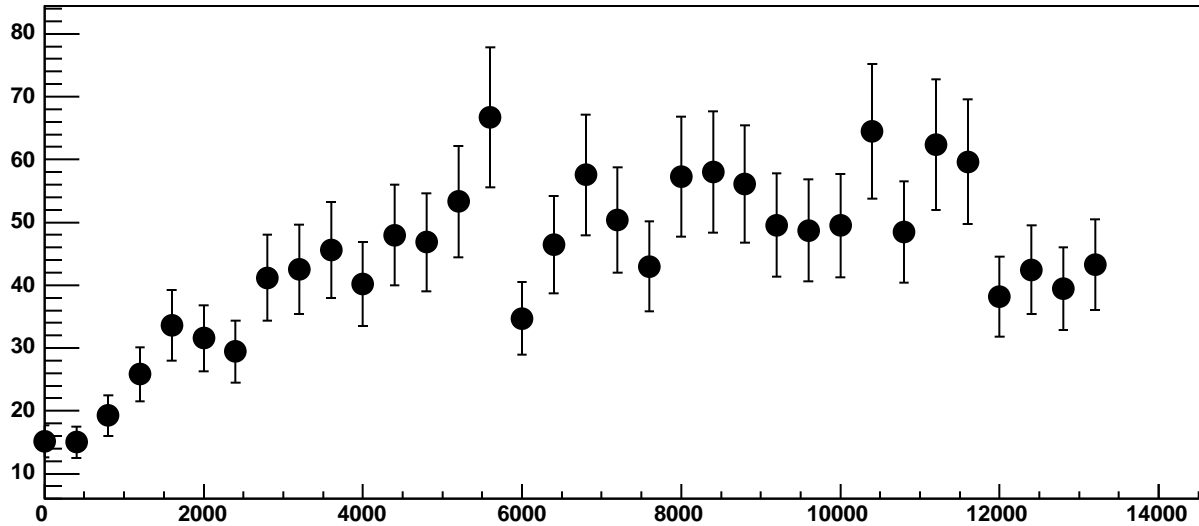


Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

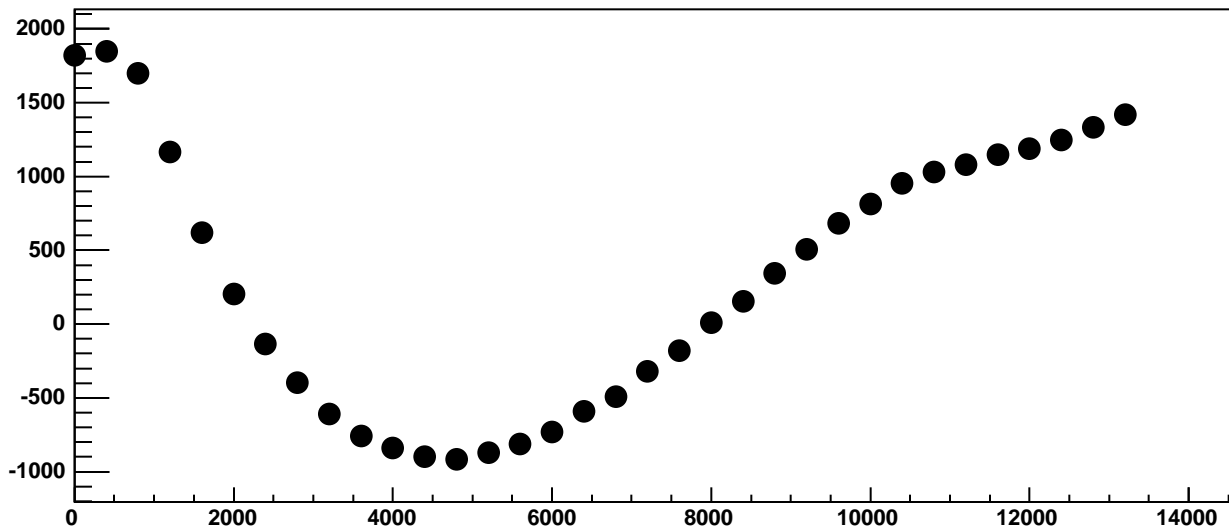
$\chi^2 / \text{ndf}$  1.259e+05 / 23  
p0 -3219 ± 3.768  
p1 -0.1298 ± 0.0006577



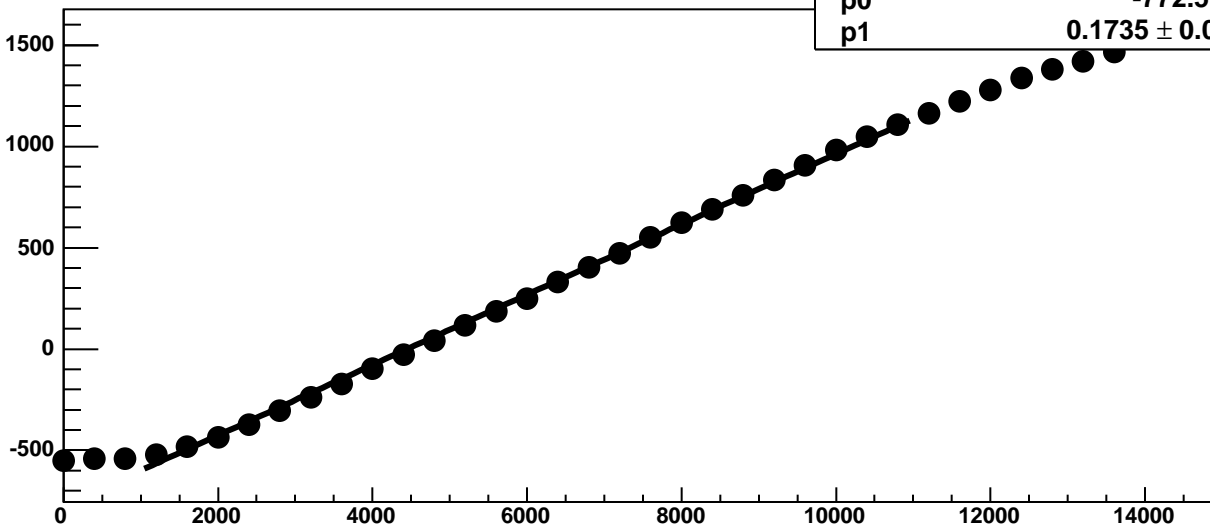
Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



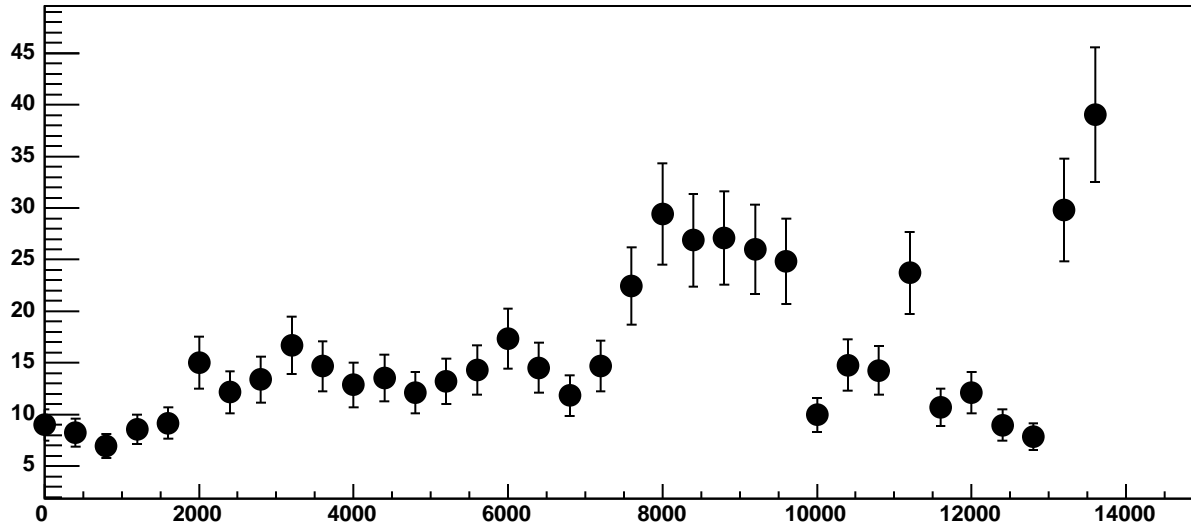
Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



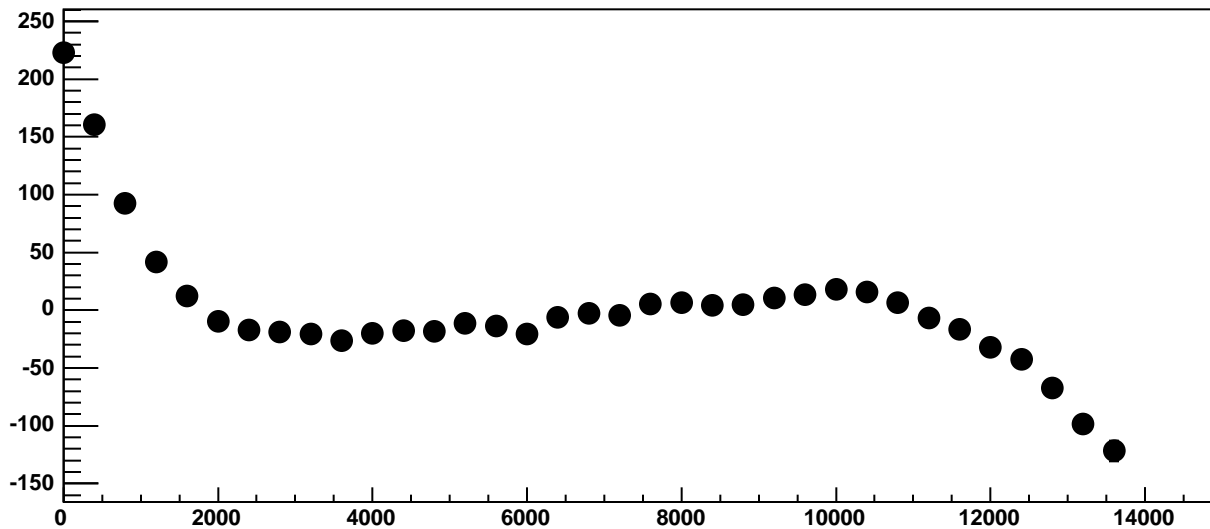
Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



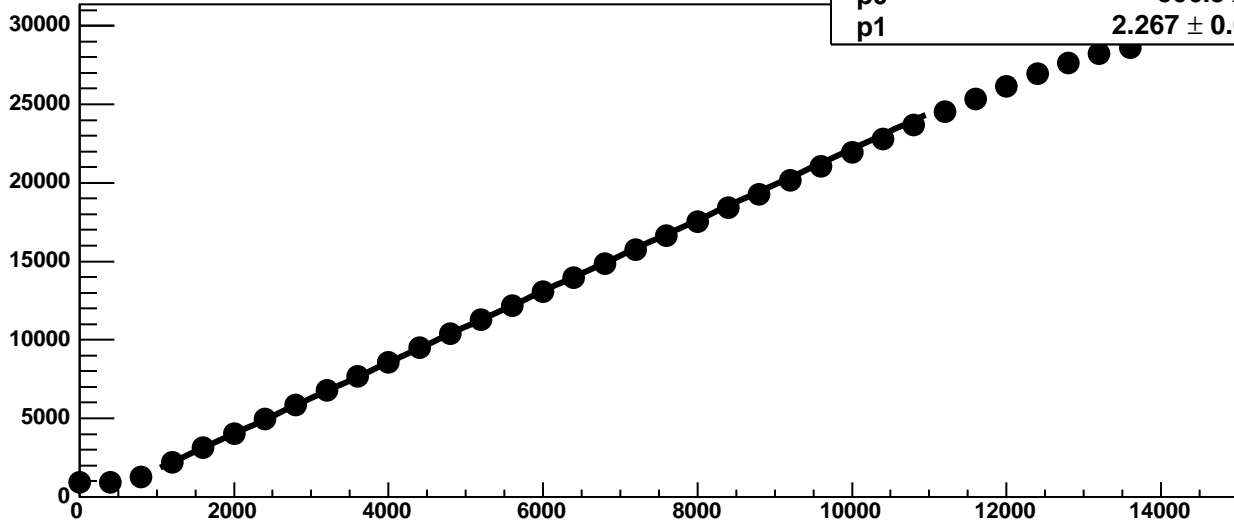
Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



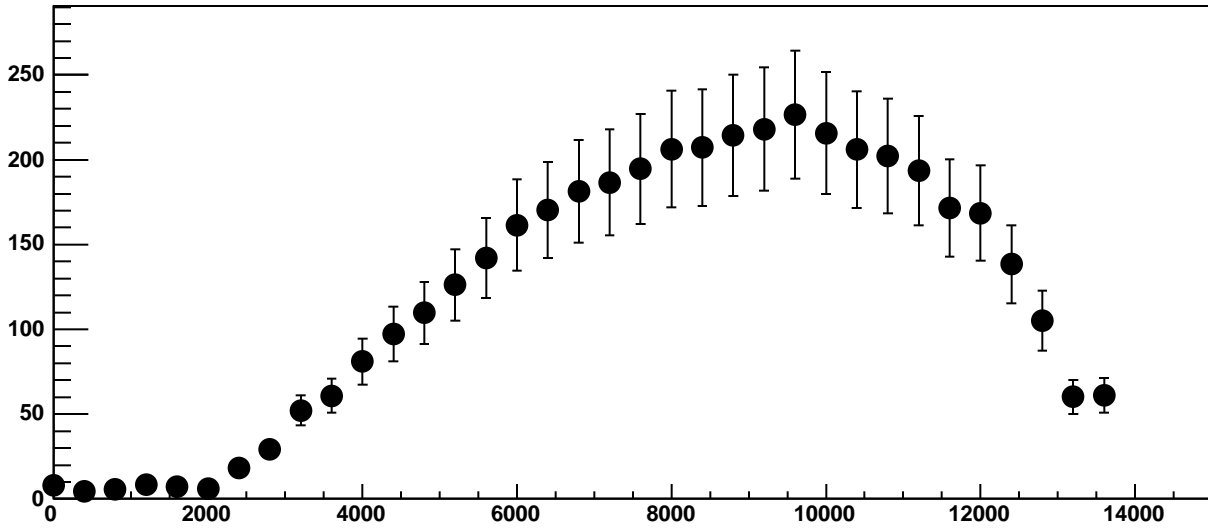
Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



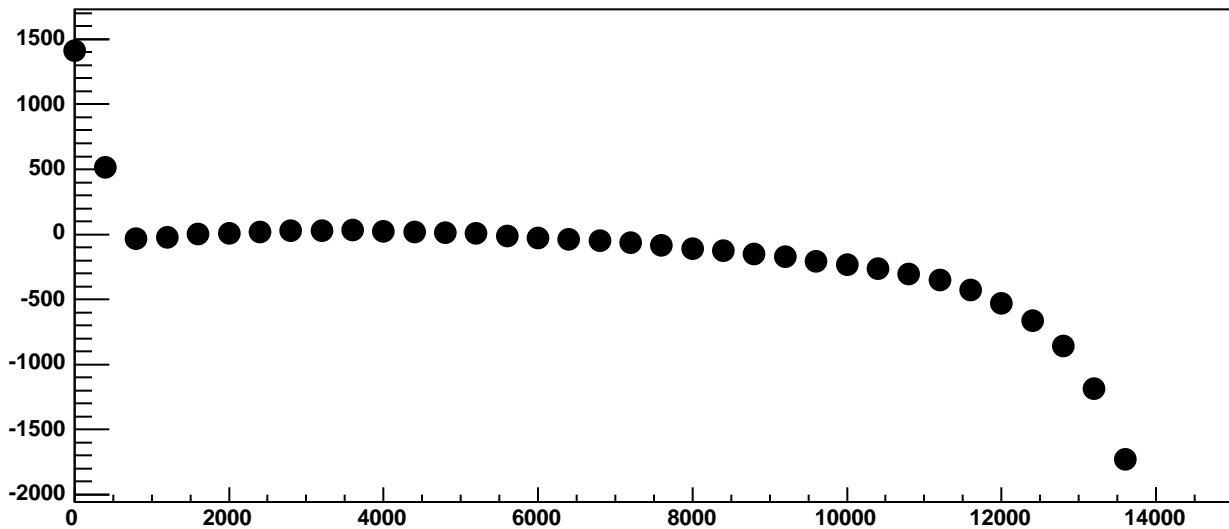
Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

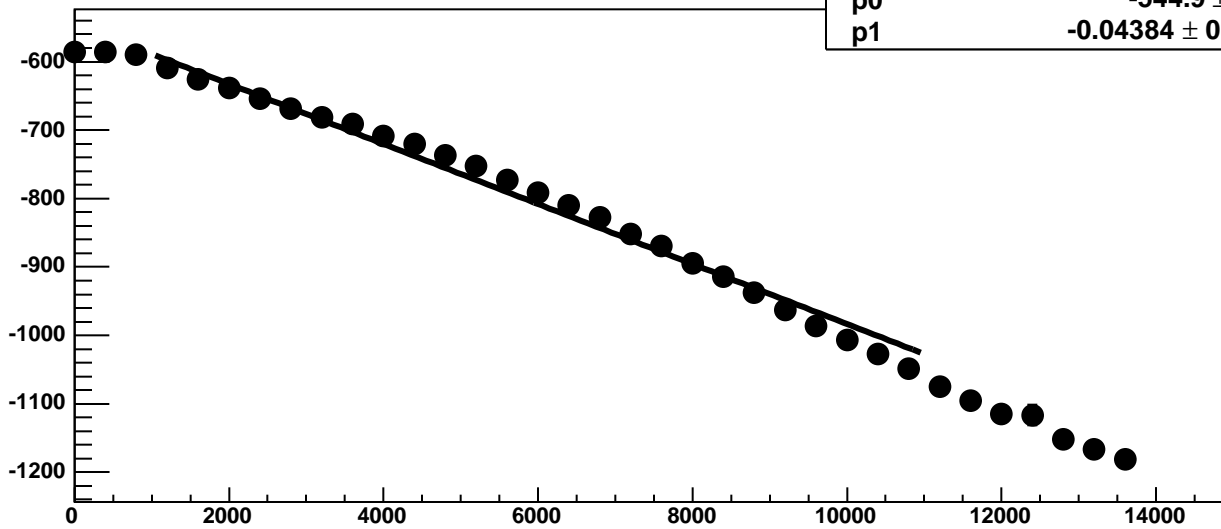
682 / 23

p0

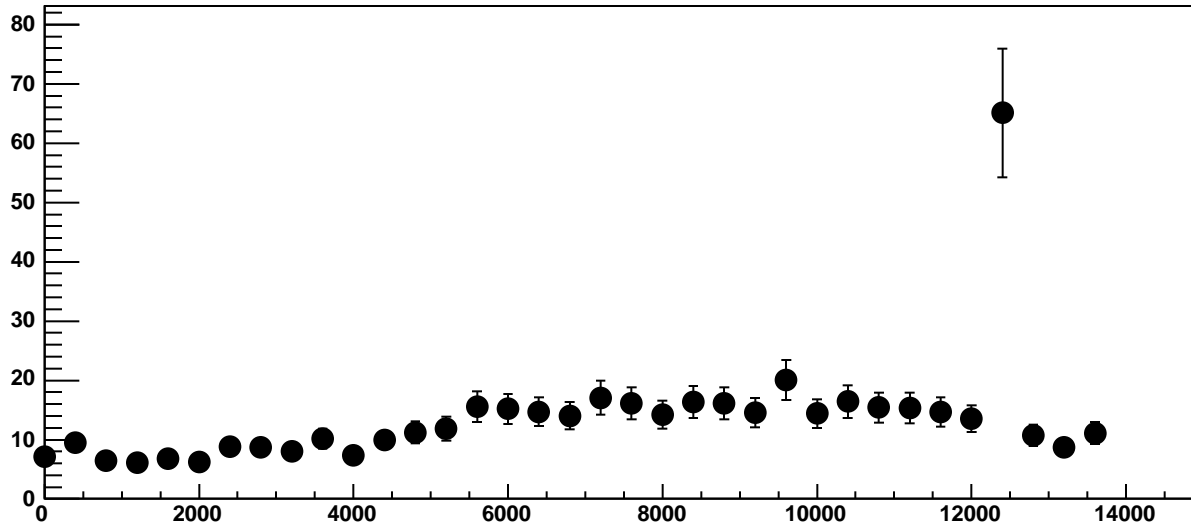
$-544.9 \pm 0.8775$

p1

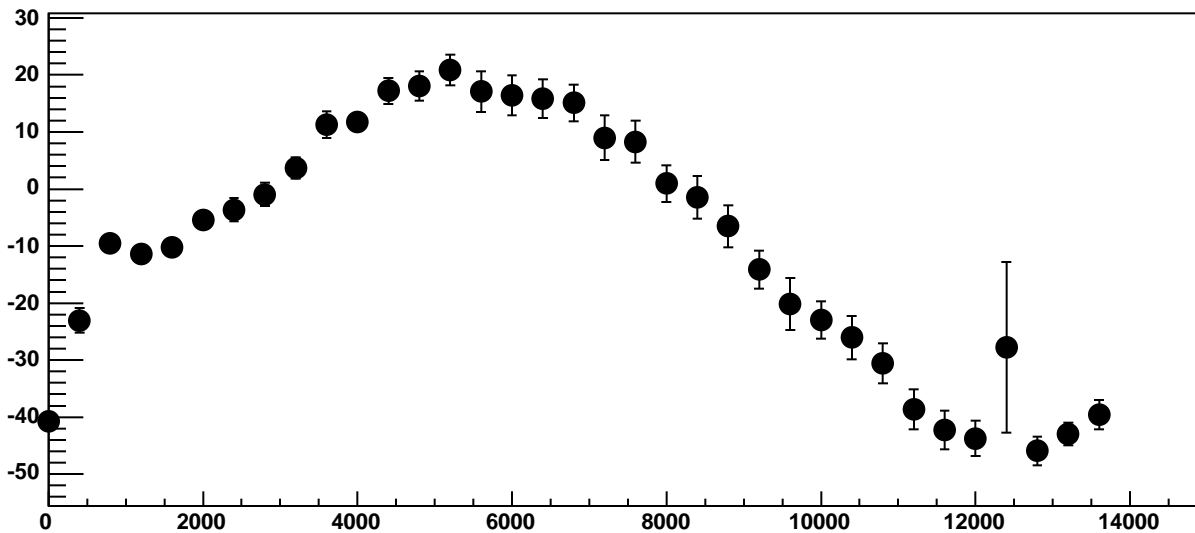
$-0.04384 \pm 0.000178$



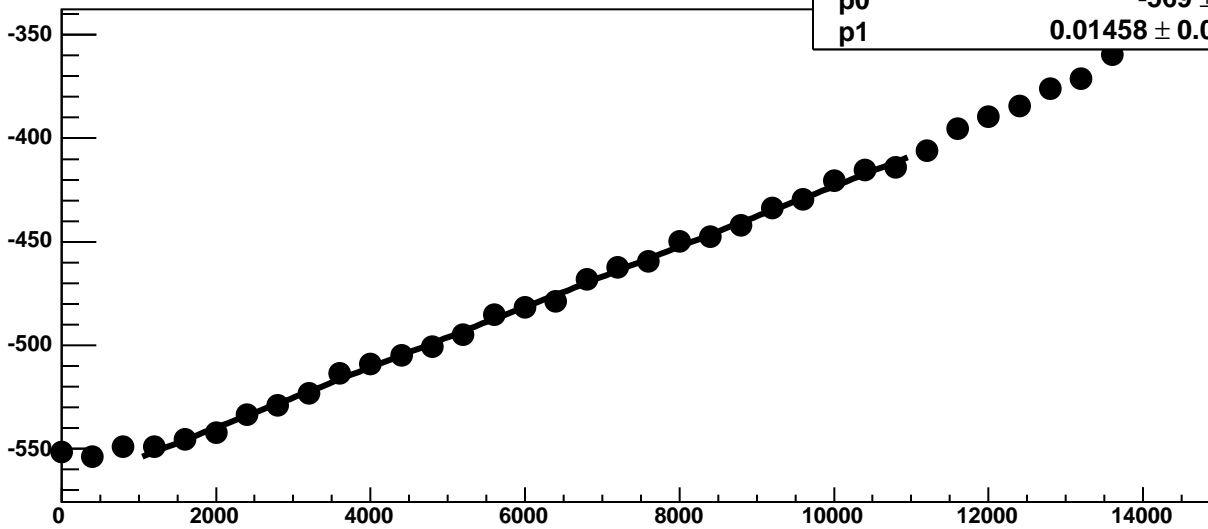
Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

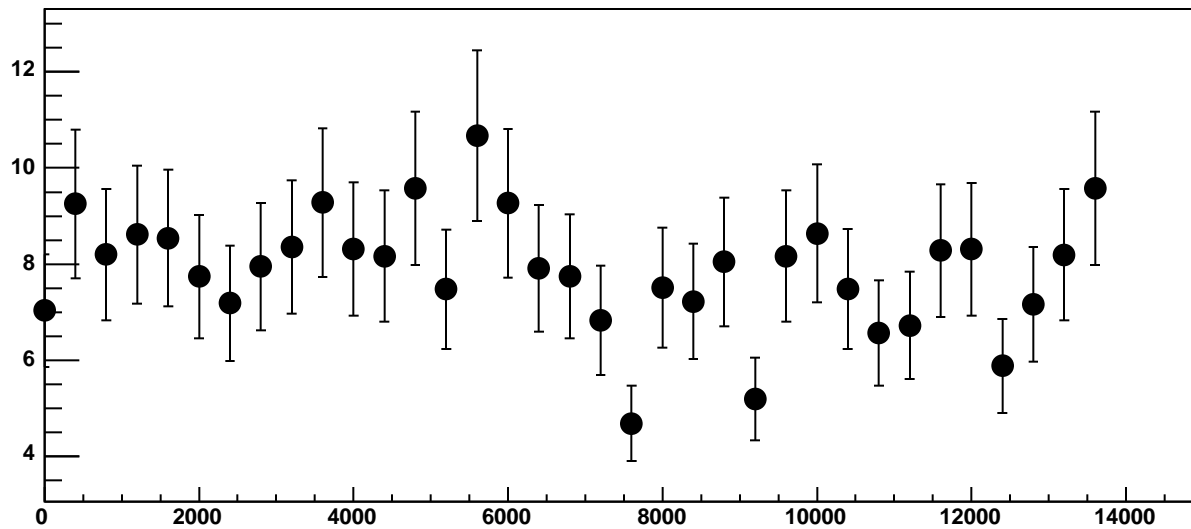


Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

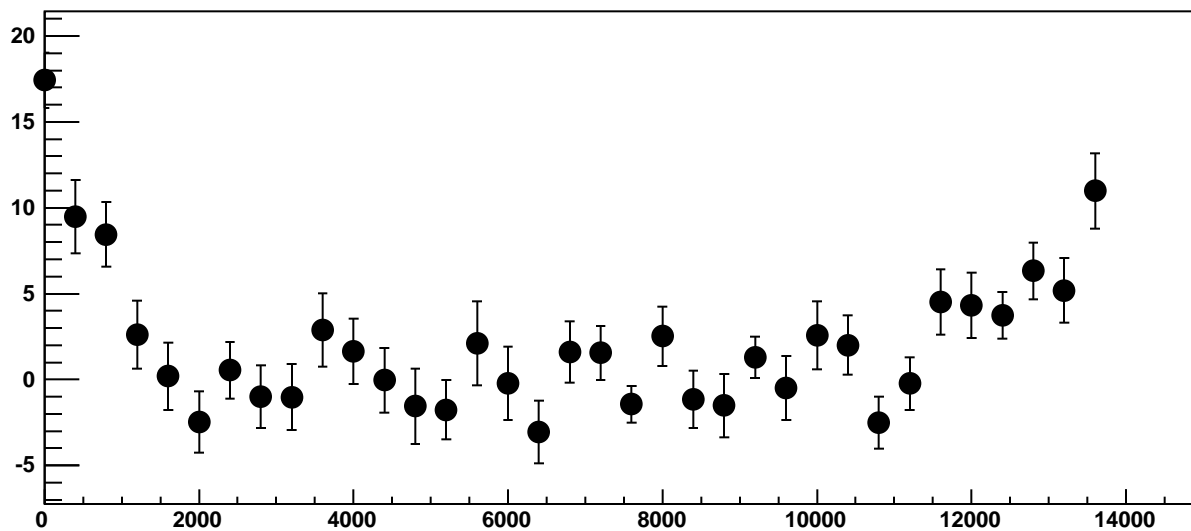


$\chi^2 / \text{ndf}$  25.99 / 23  
p0  $-569 \pm 0.8512$   
p1  $0.01458 \pm 0.0001208$

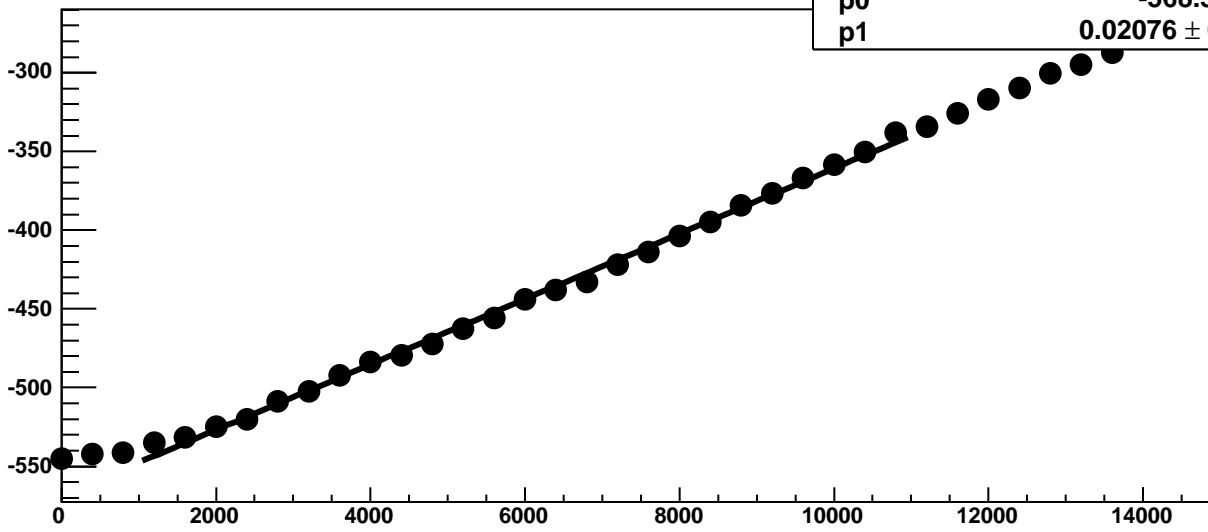
Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

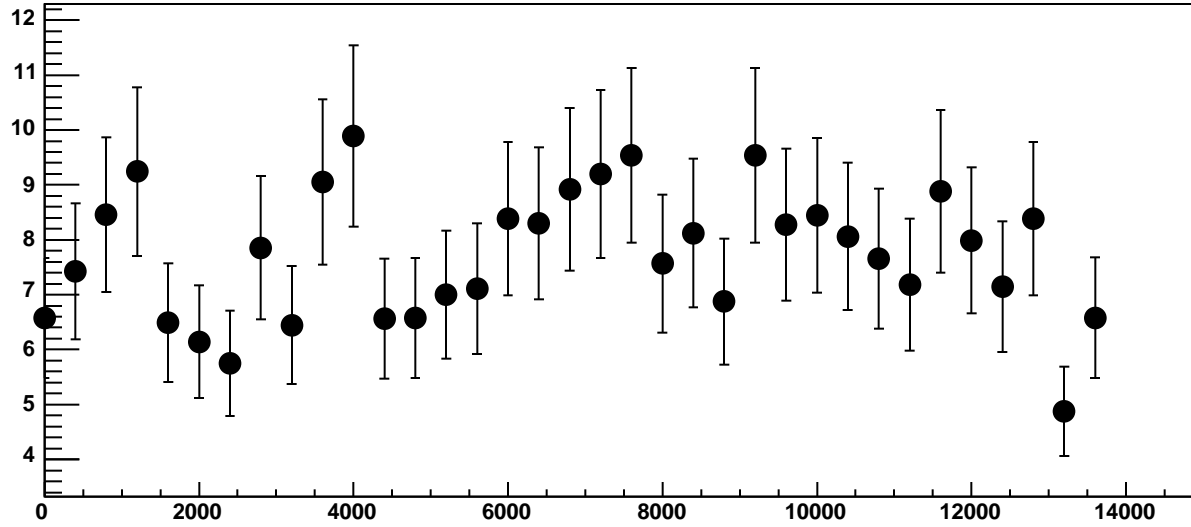


Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

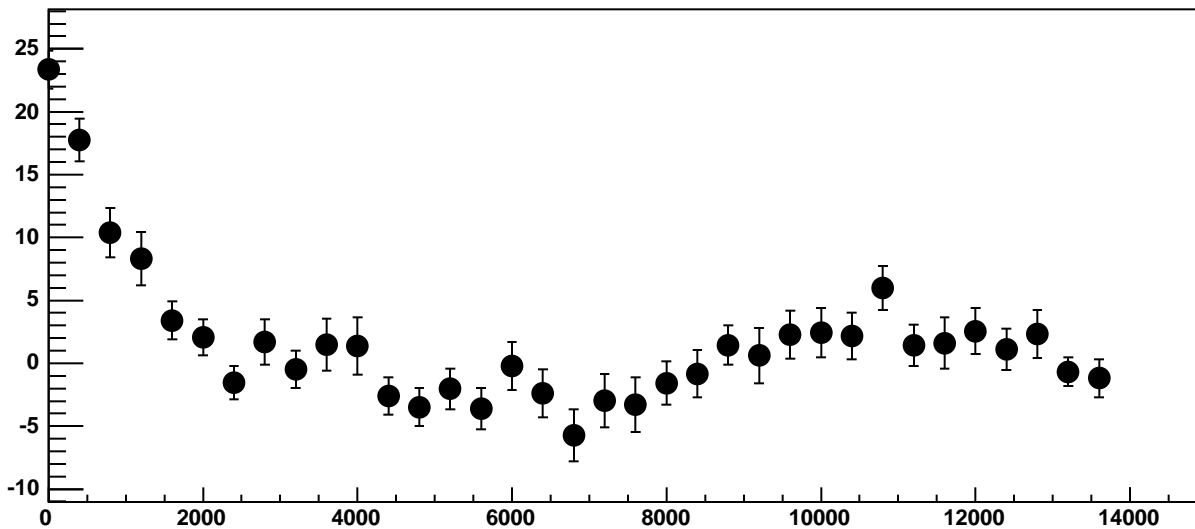


$\chi^2 / \text{ndf}$  72.39 / 23  
p0  $-568.3 \pm 0.76$   
p1  $0.02076 \pm 0.00012$

Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

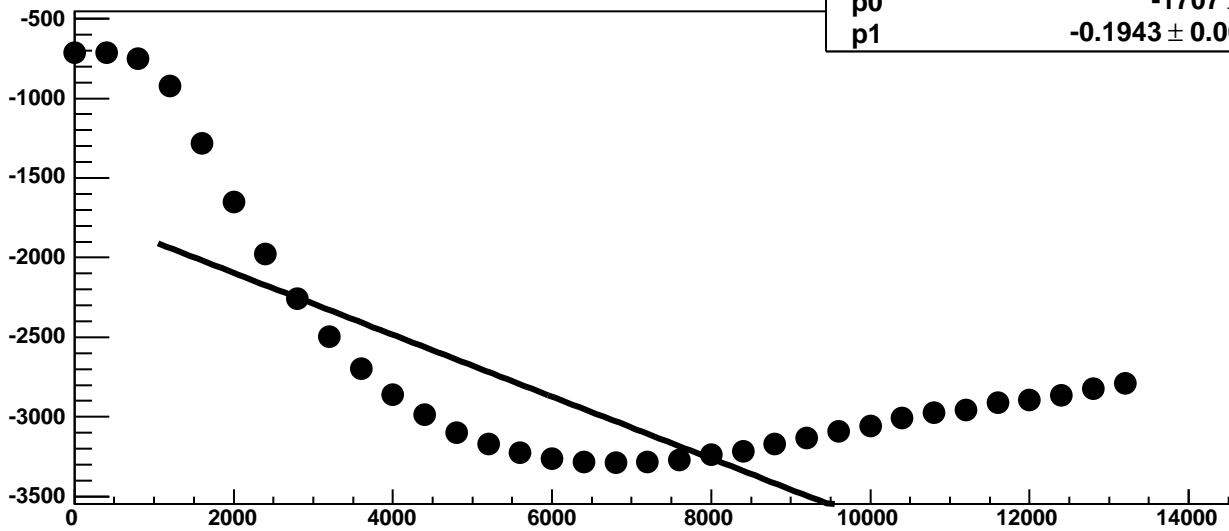


Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

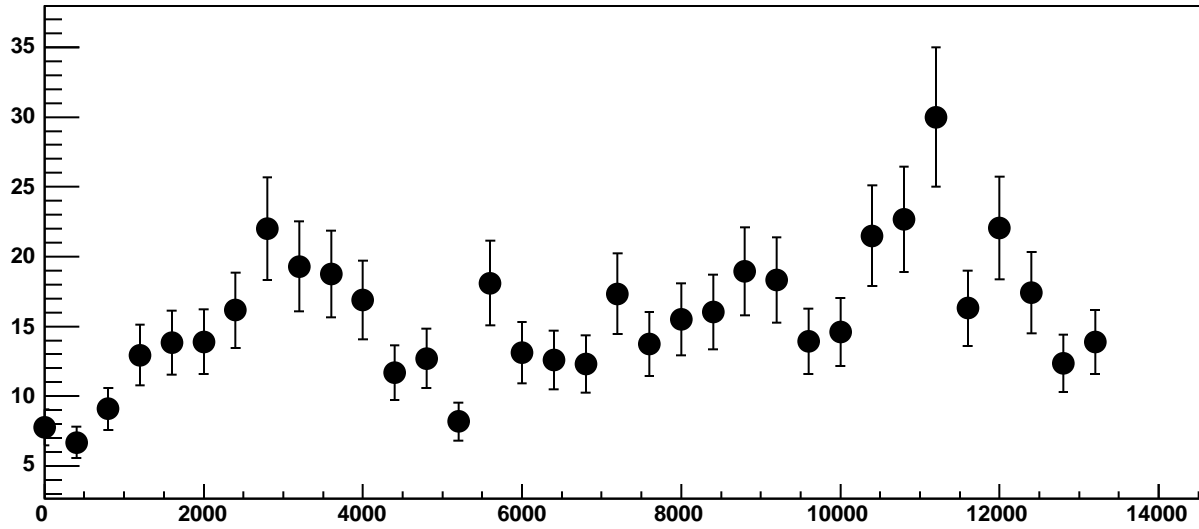




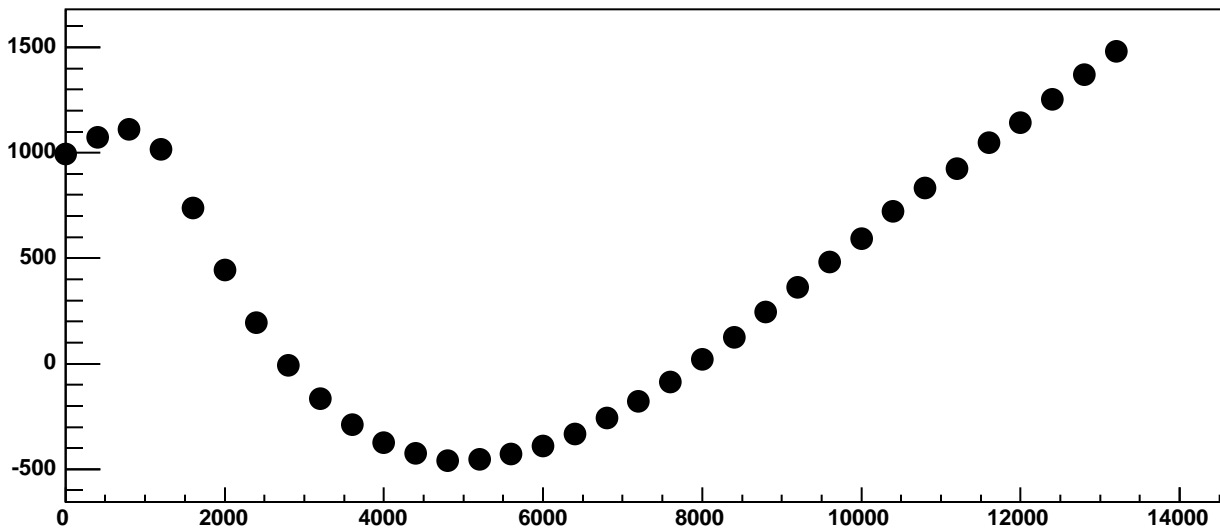
Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



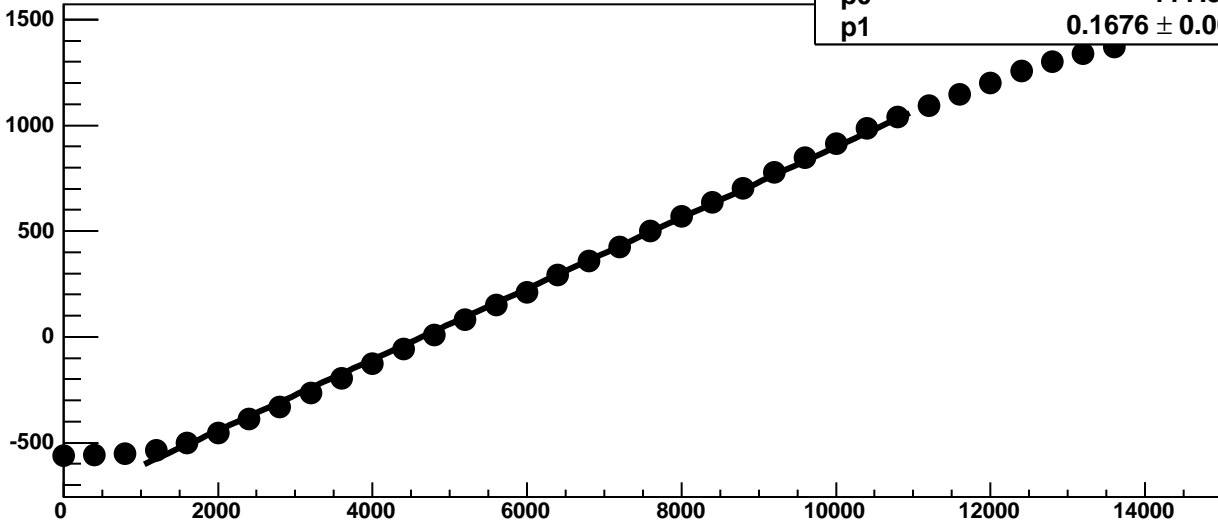
Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



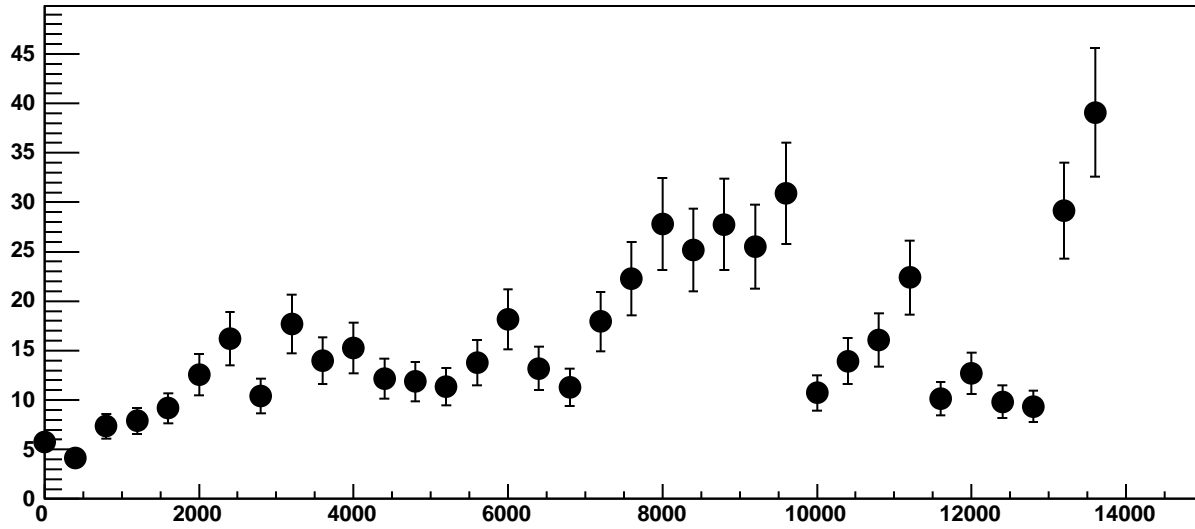
Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



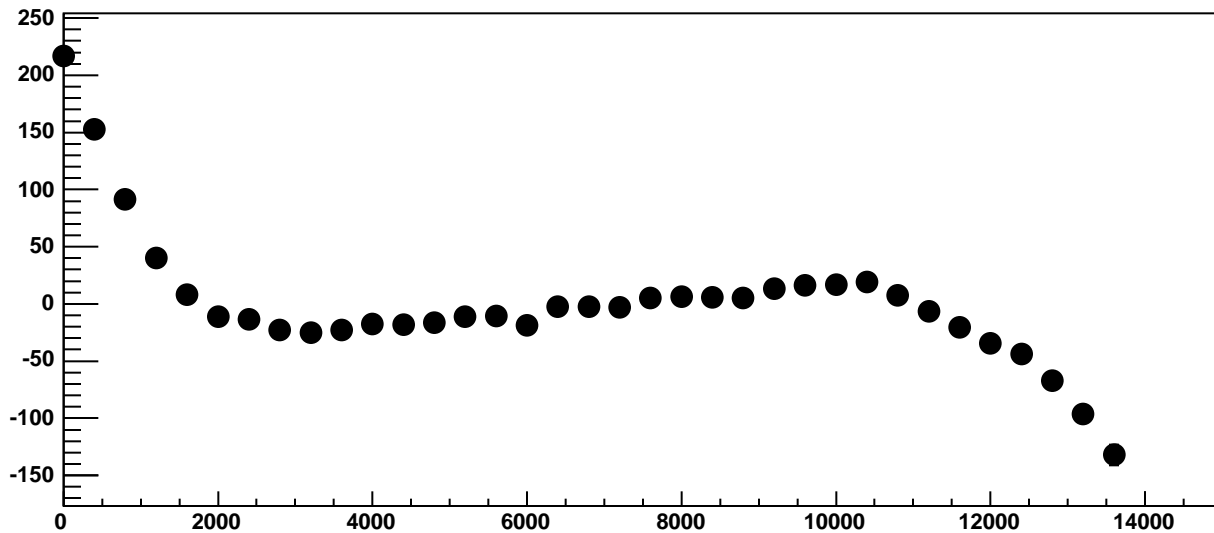
Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



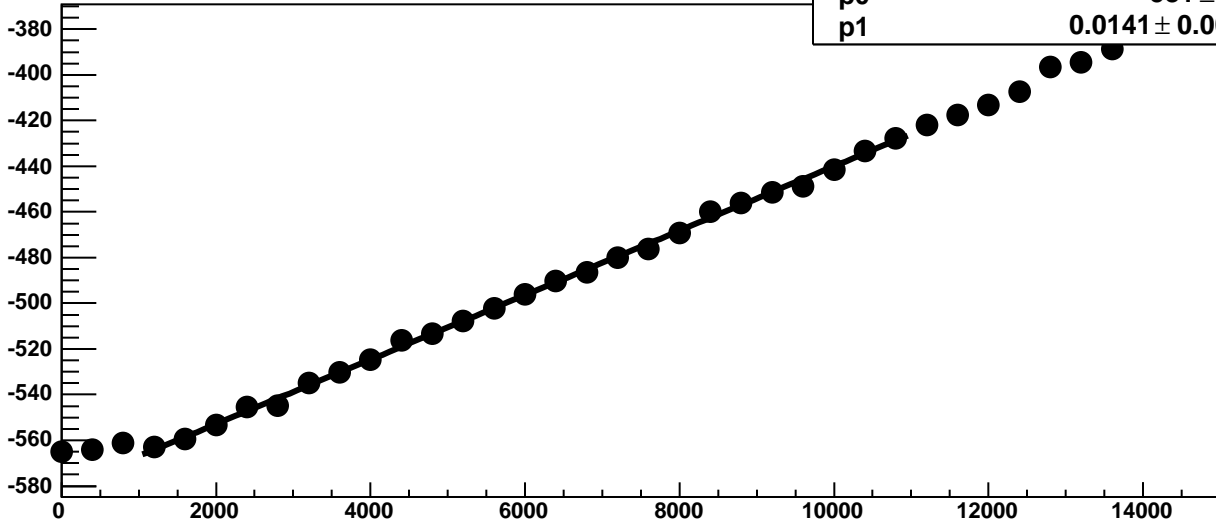
Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



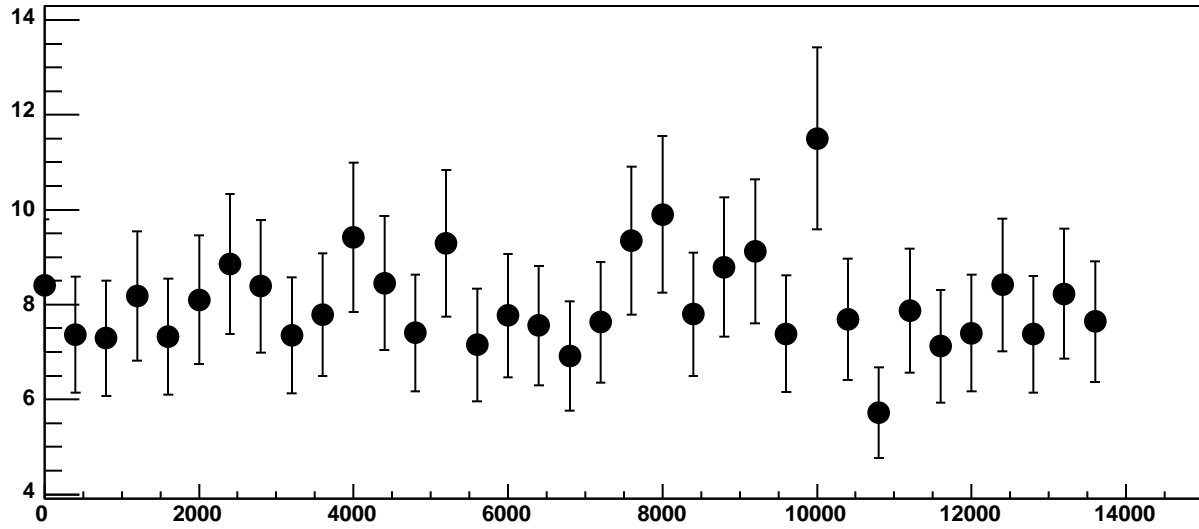
Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



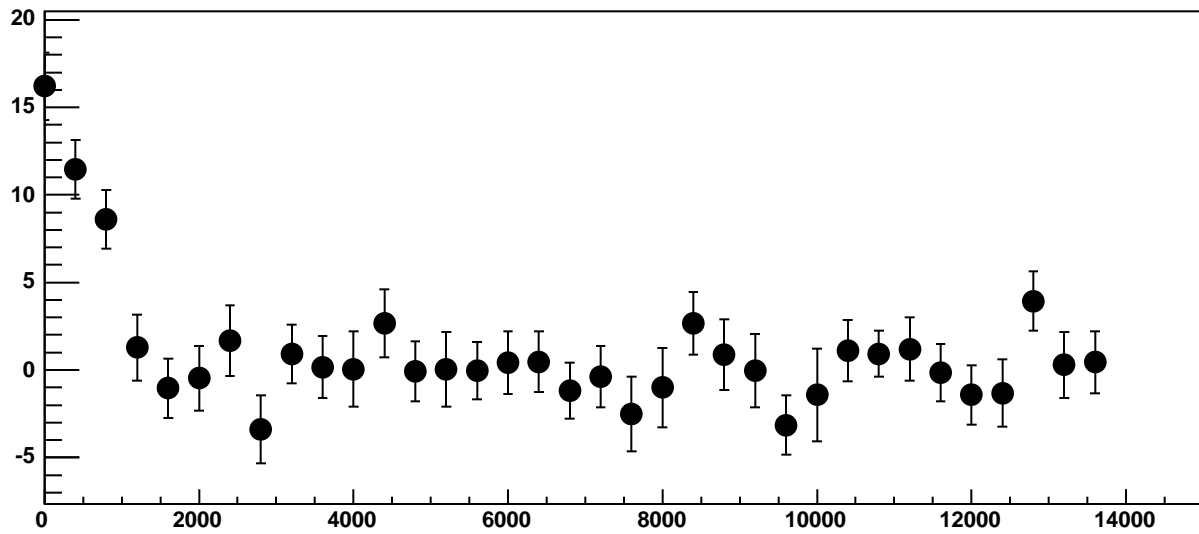
Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC

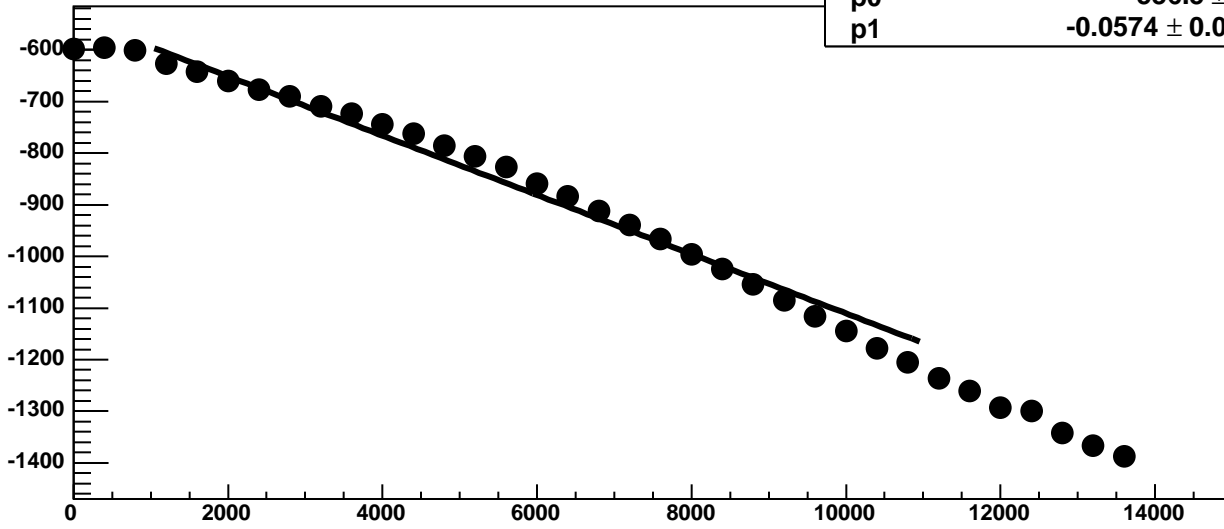


Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

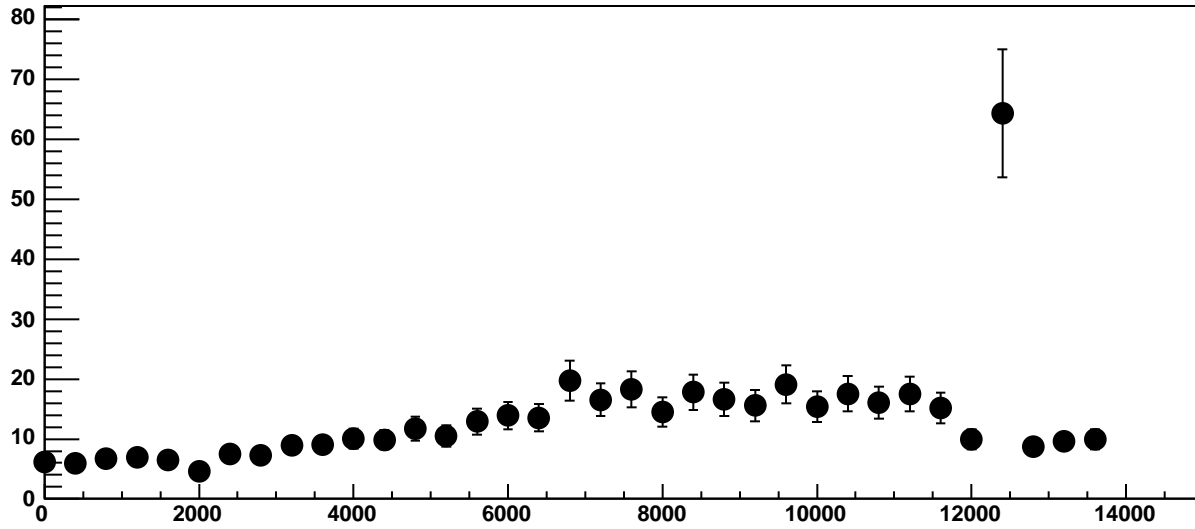


Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

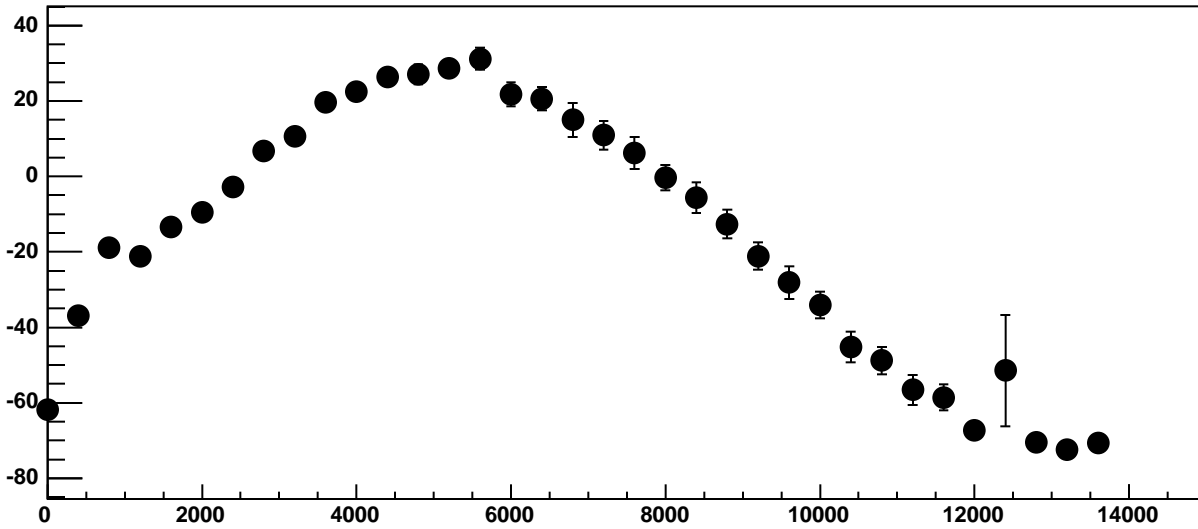
$\chi^2 / \text{ndf}$  1655 / 23  
p0  $-536.5 \pm 0.8358$   
p1  $-0.0574 \pm 0.0001797$



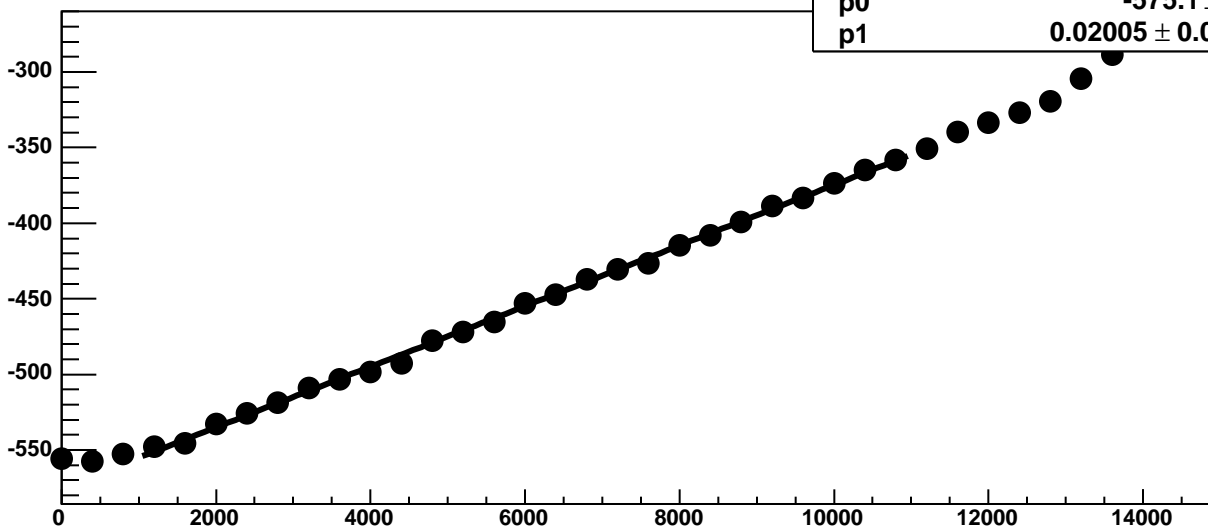
Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

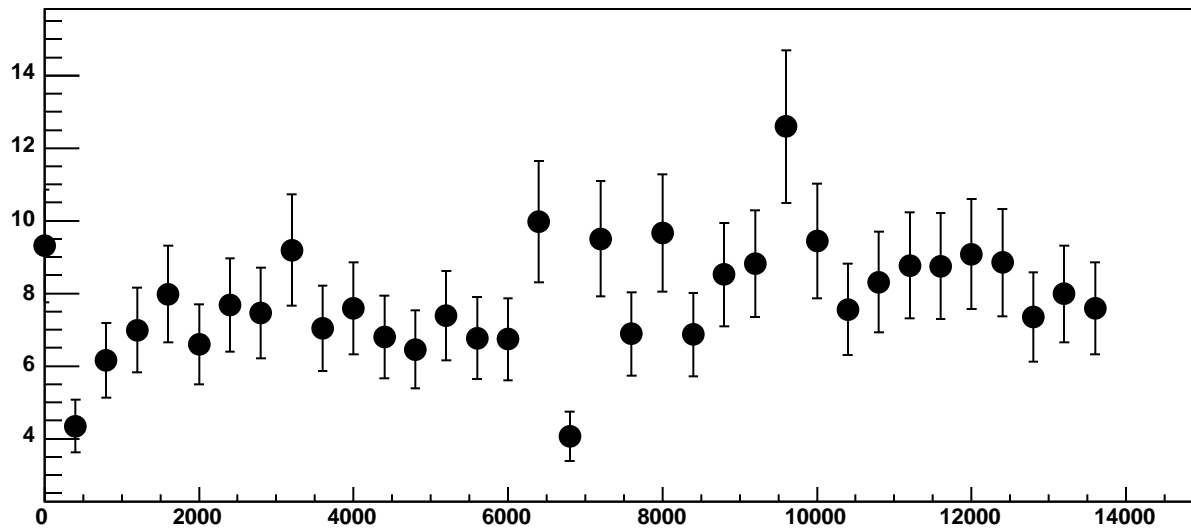


Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

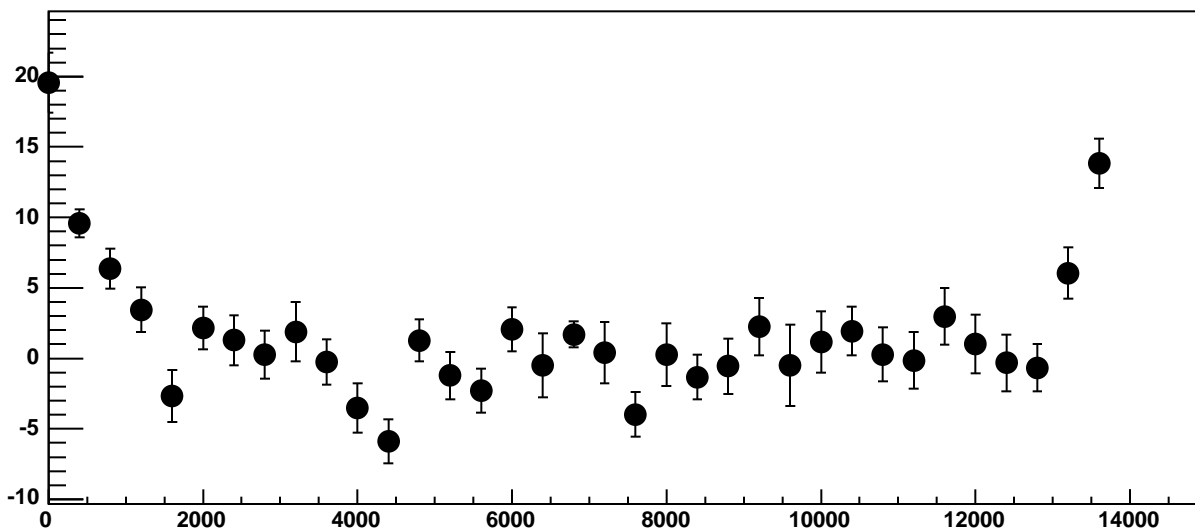


$\chi^2 / \text{ndf}$  46.95 / 23  
p0  $-575.1 \pm 0.7961$   
p1  $0.02005 \pm 0.0001254$

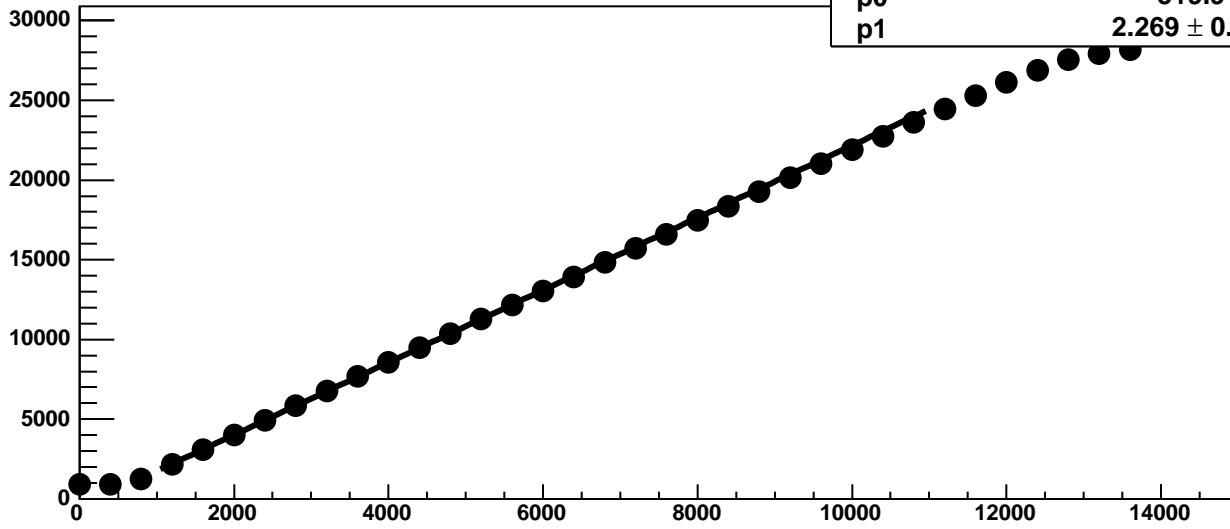
Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



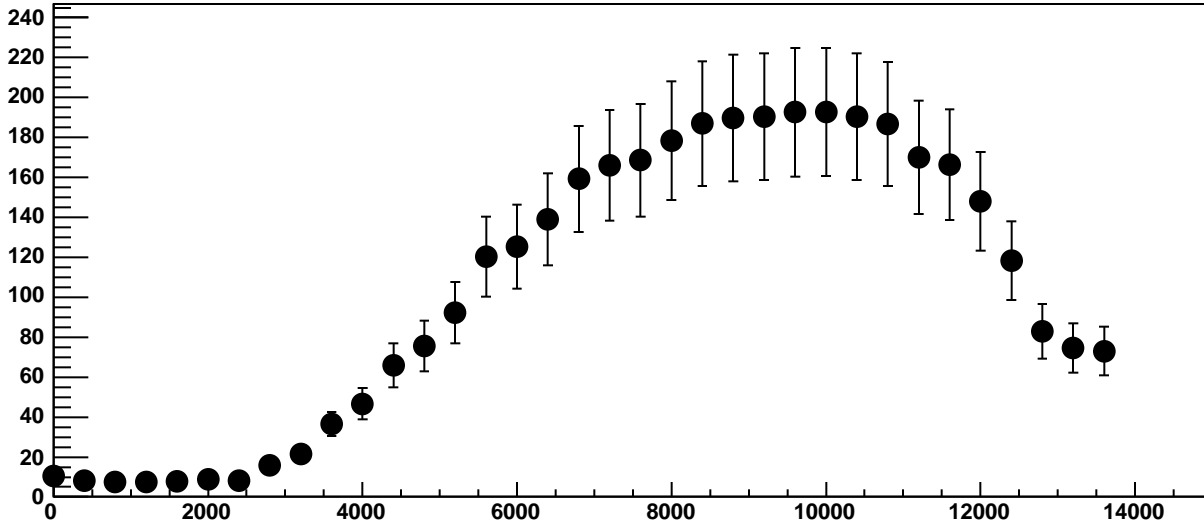
Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



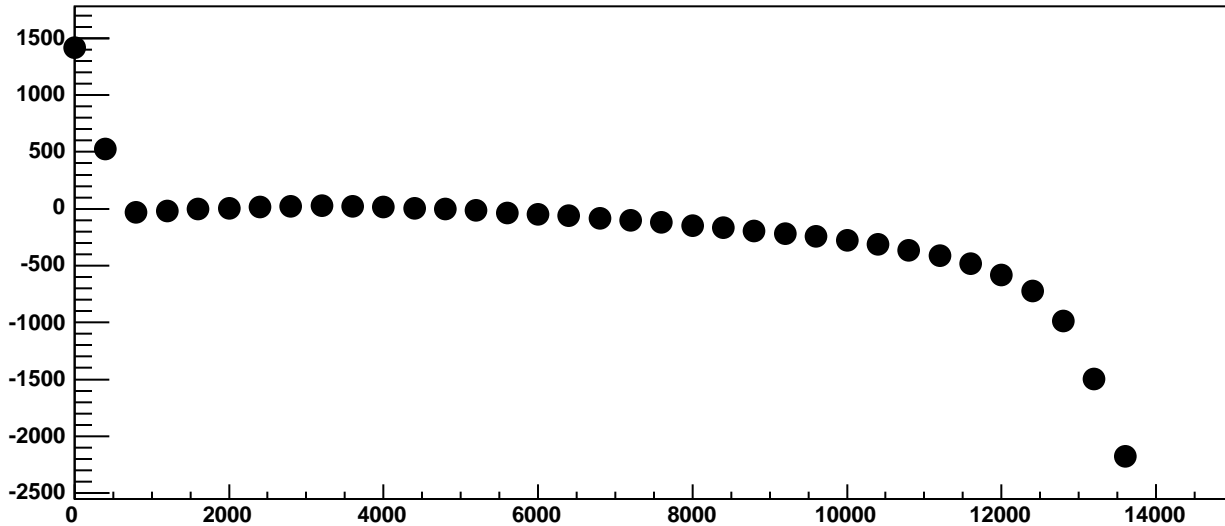
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



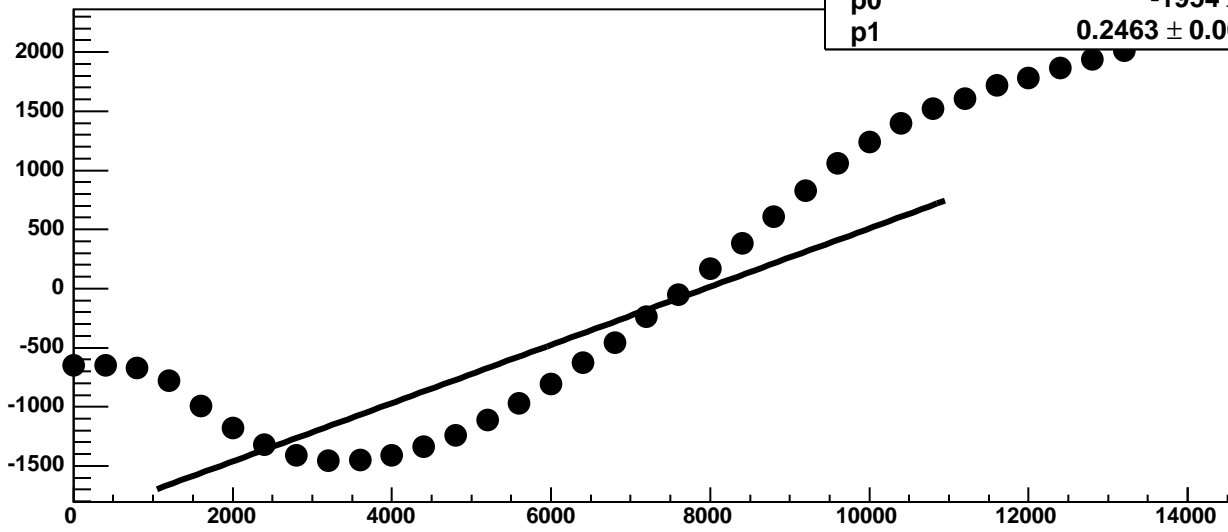
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



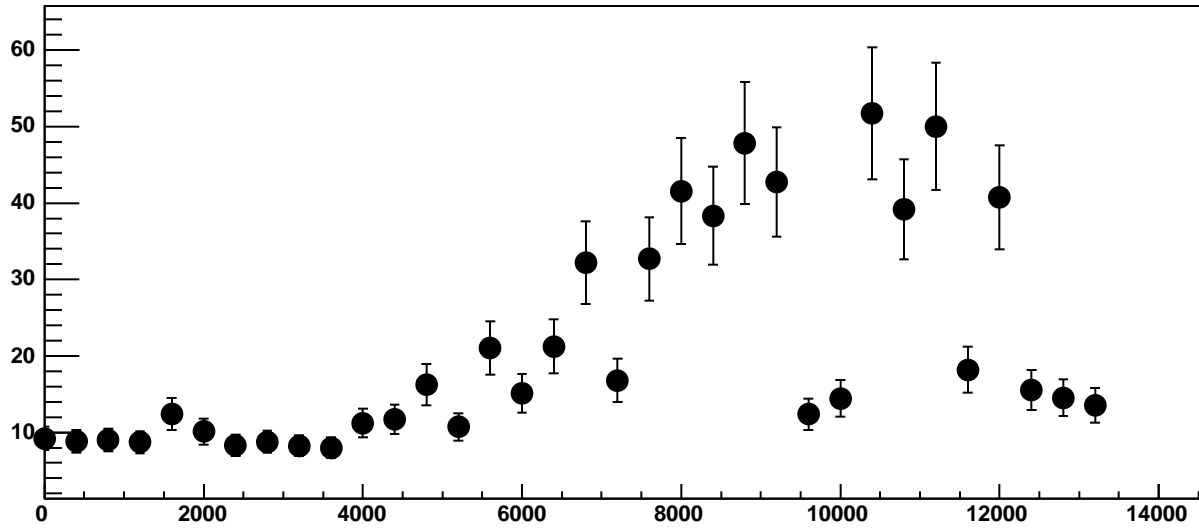
Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



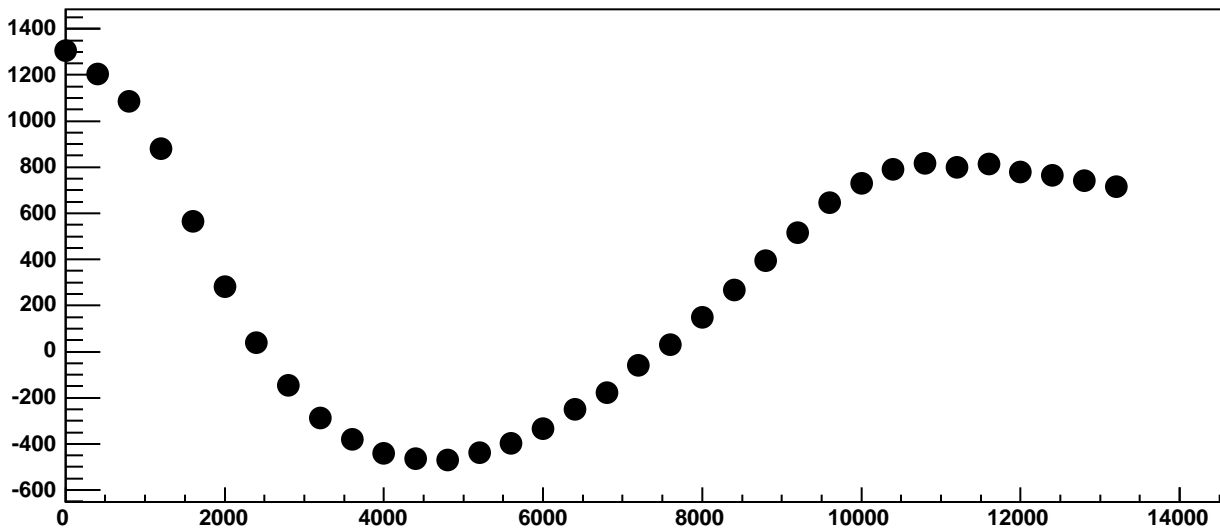
Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



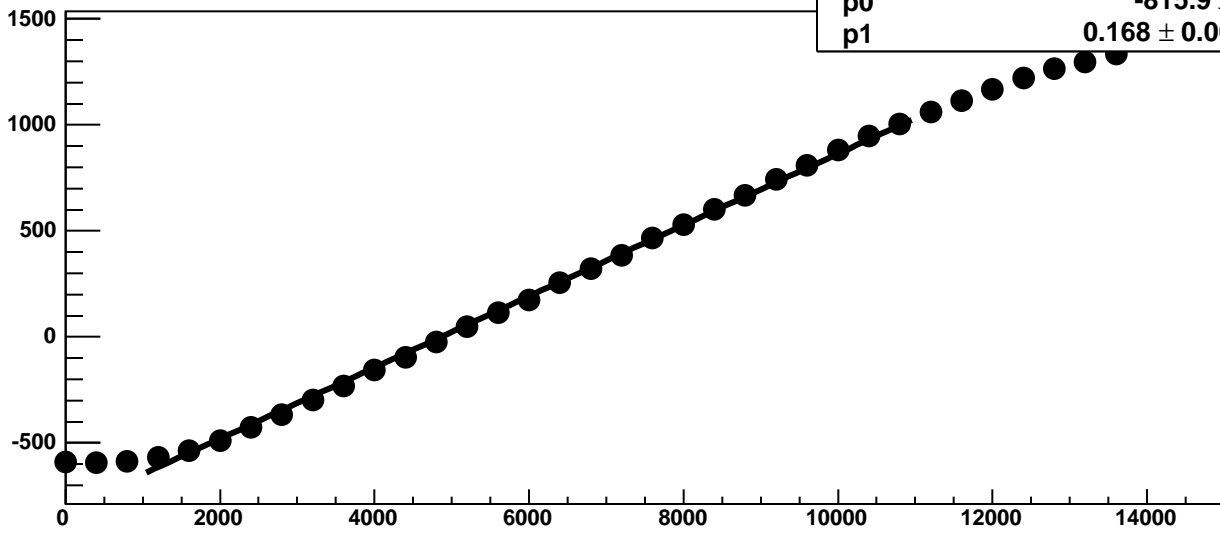
Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

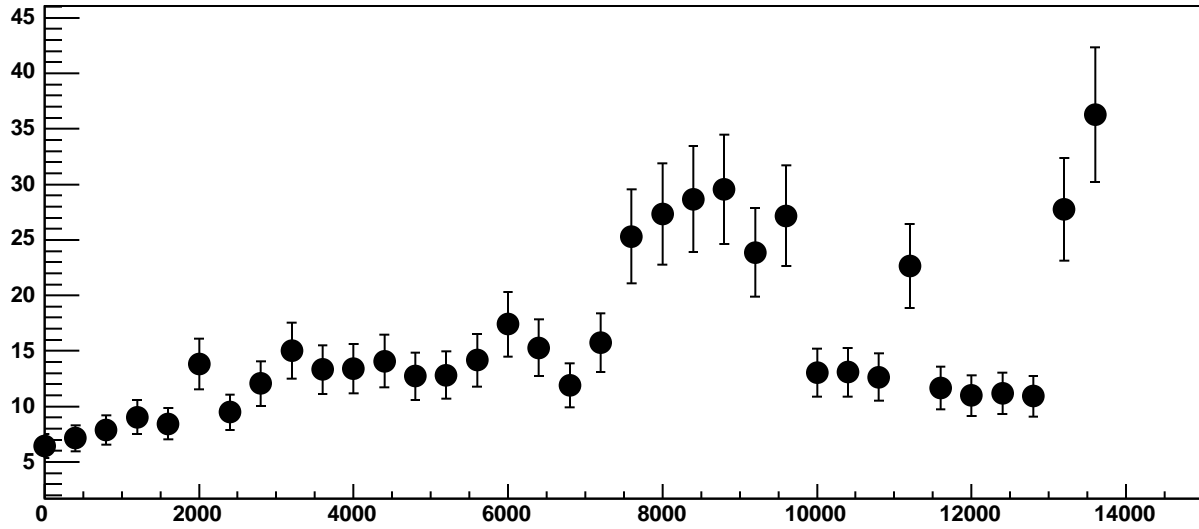


Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

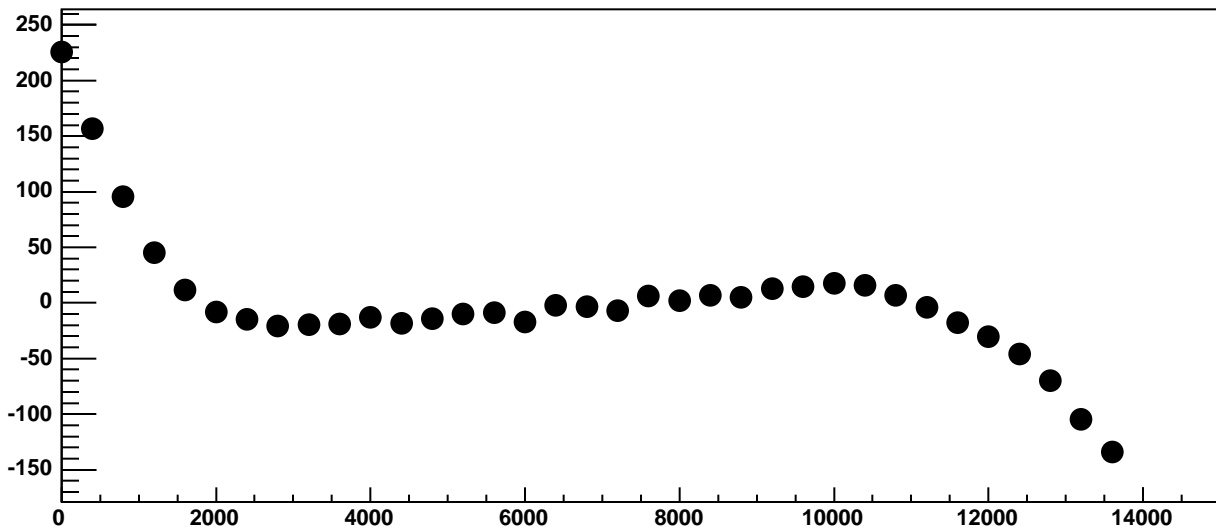


$\chi^2 / \text{ndf}$  887.2 / 23  
p0  $-815.9 \pm 1.192$   
p1  $0.168 \pm 0.0002076$

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

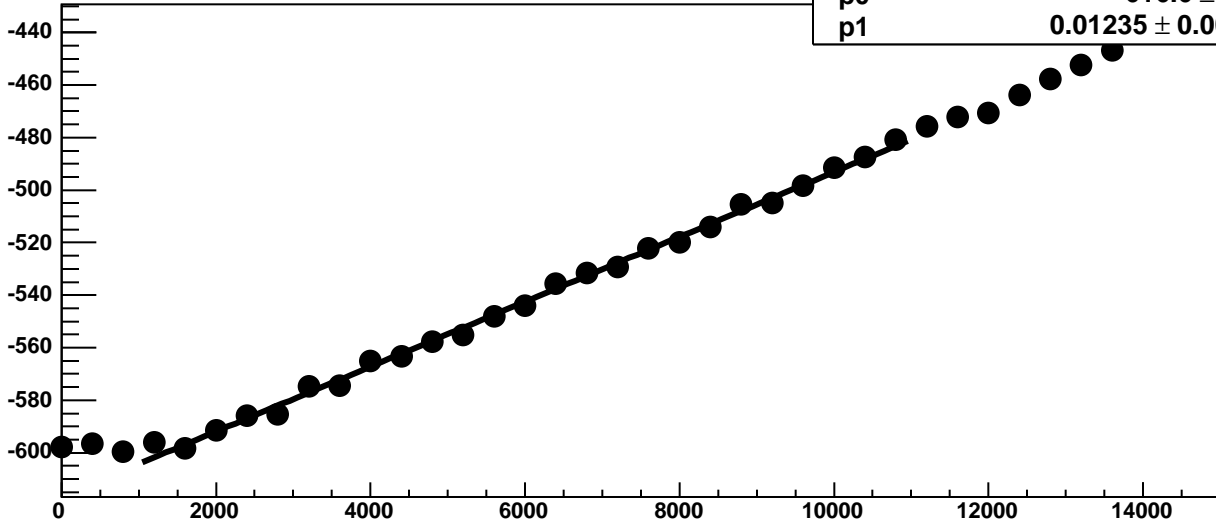


Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



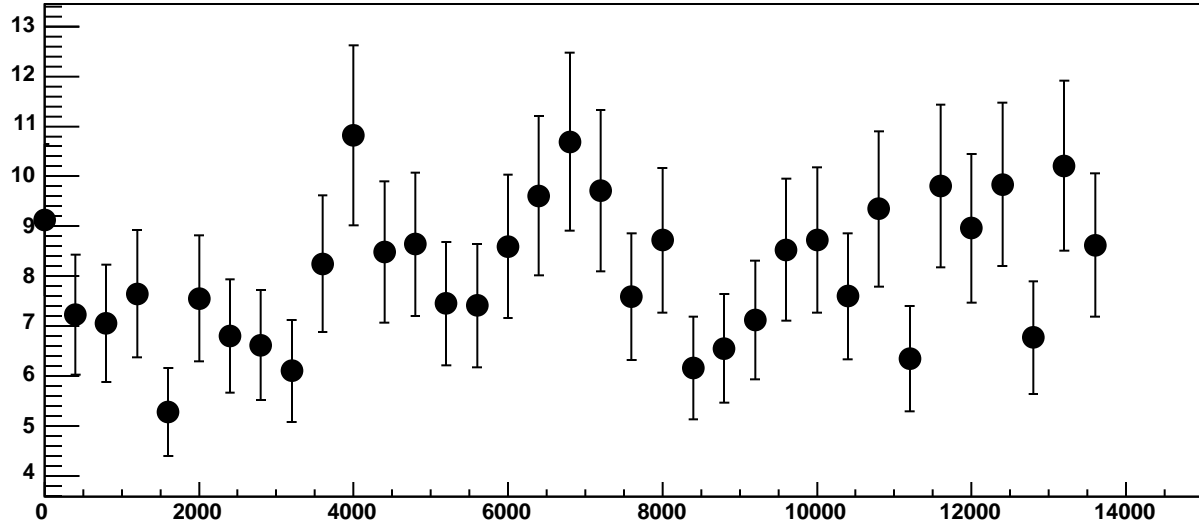


Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

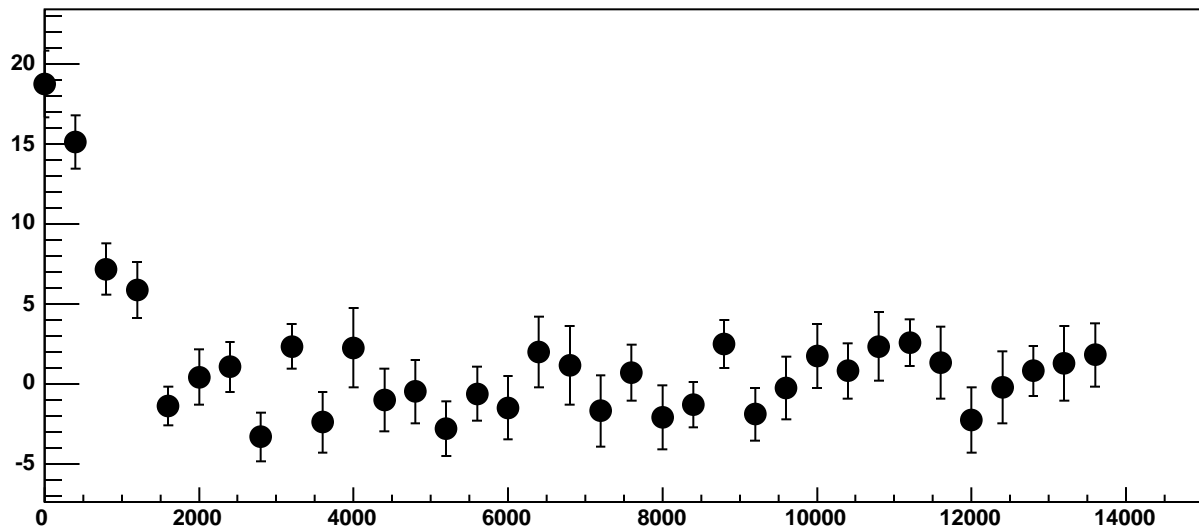


$\chi^2 / \text{ndf}$  36.66 / 23  
p0  $-616.6 \pm 0.7468$   
p1  $0.01235 \pm 0.0001165$

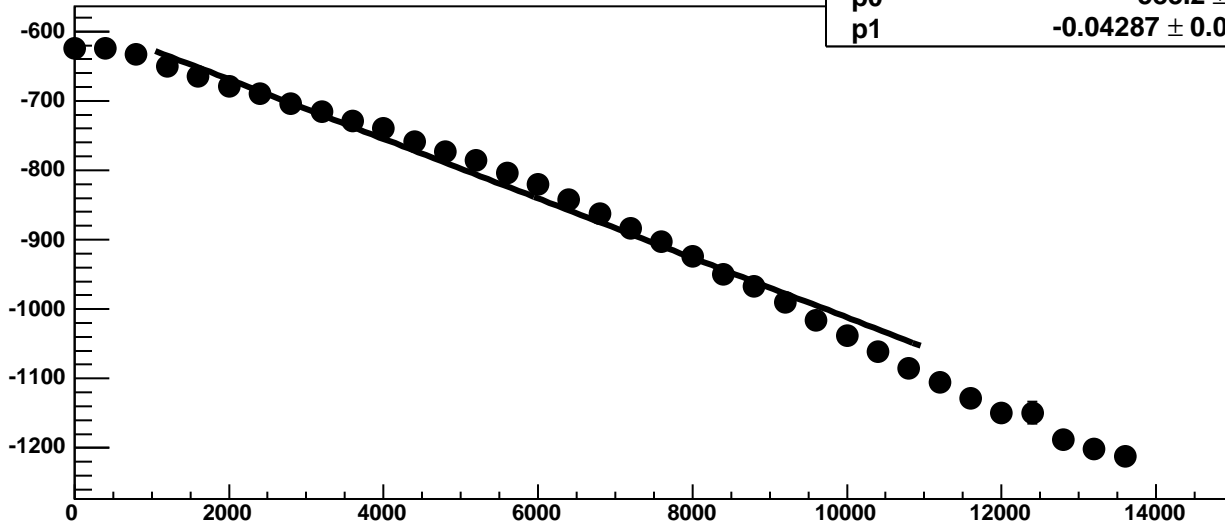
Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

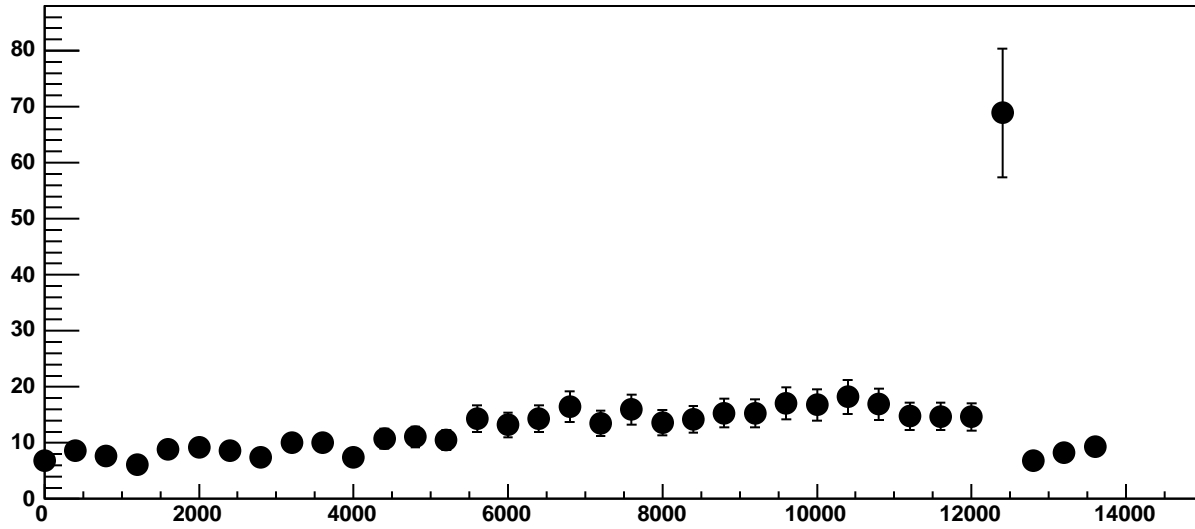


Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

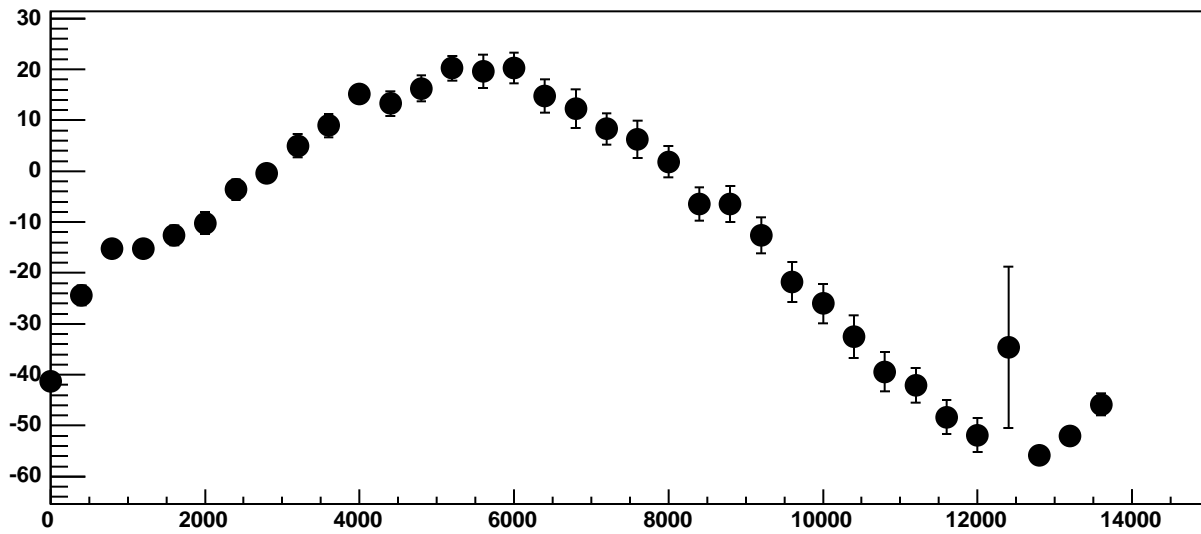


$\chi^2 / \text{ndf}$  803.4 / 23  
p0 -583.2 ± 0.9629  
p1 -0.04287 ± 0.0001873

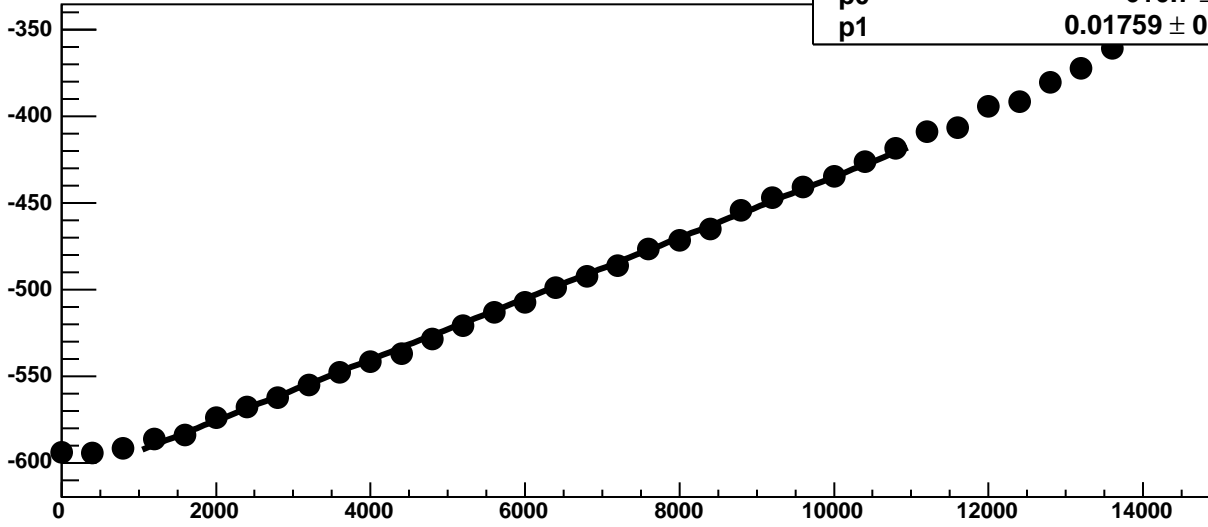
Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

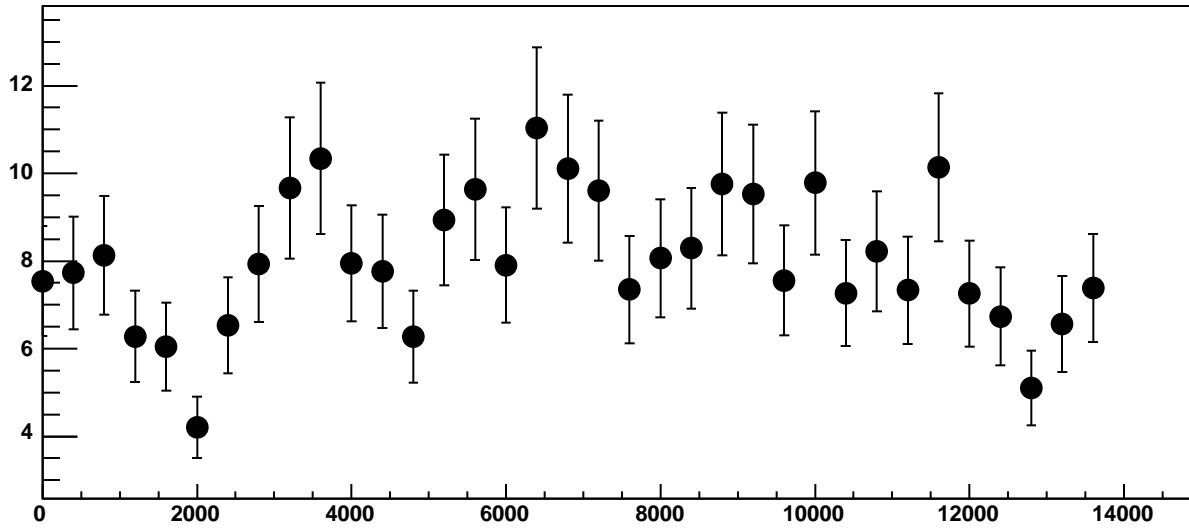


Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

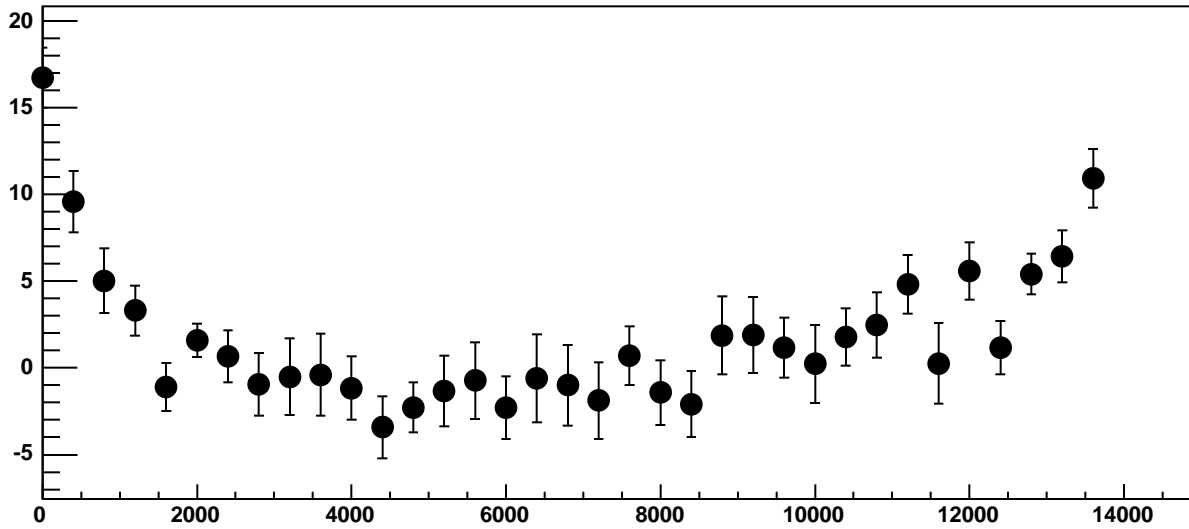


$\chi^2 / \text{ndf}$  25.63 / 23  
p0  $-610.7 \pm 0.6949$   
p1  $0.01759 \pm 0.000114$

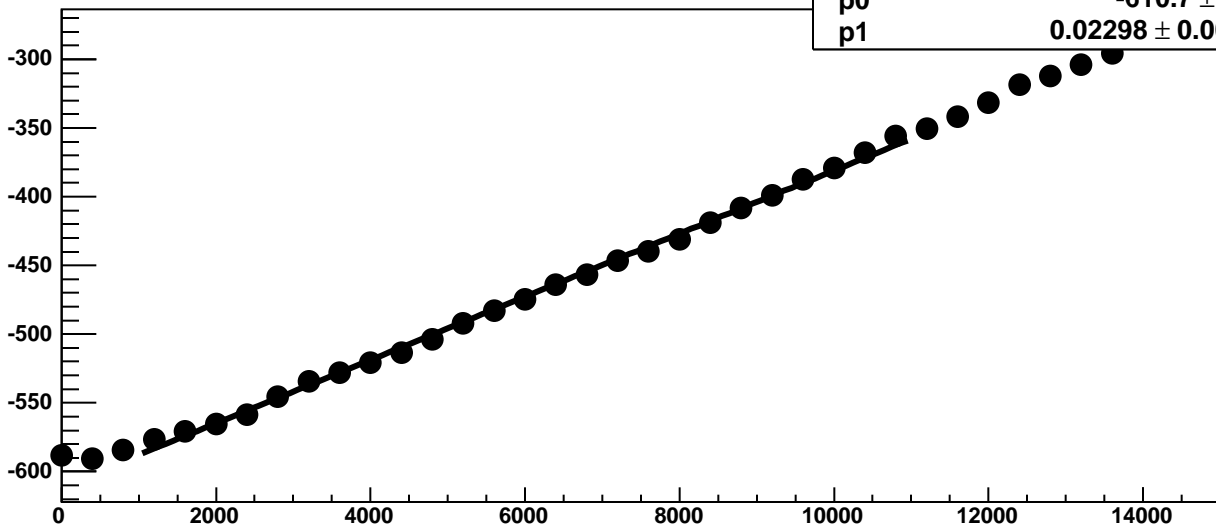
Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

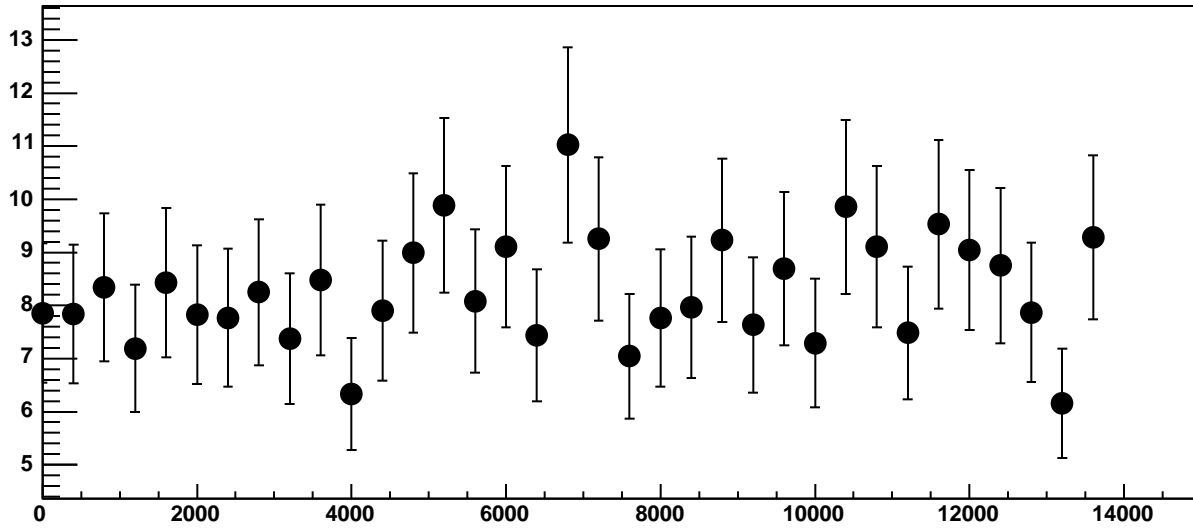


Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

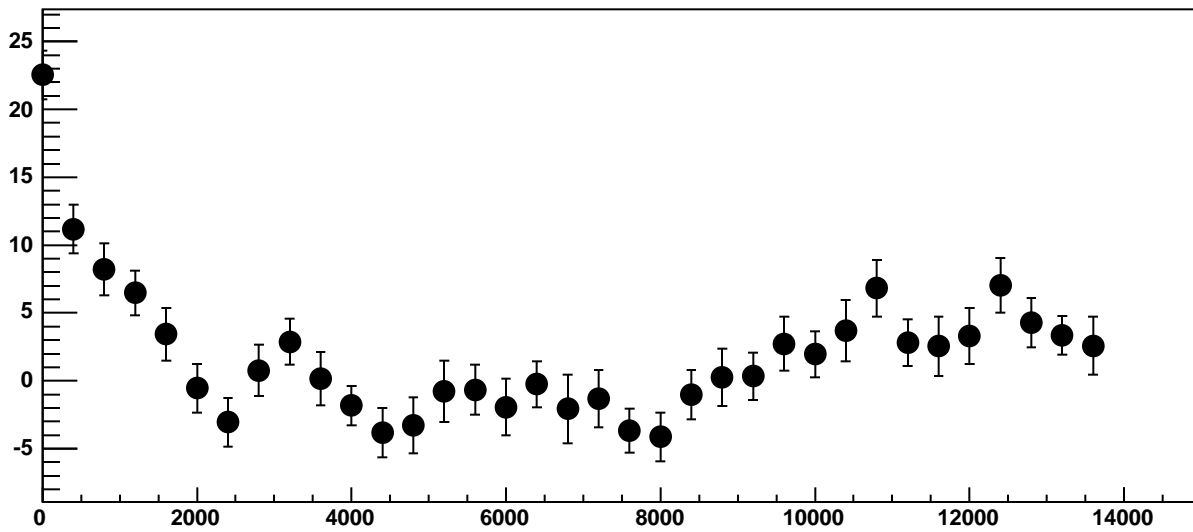


$\chi^2 / \text{ndf}$  62.88 / 23  
p0  $-610.7 \pm 0.8363$   
p1  $0.02298 \pm 0.0001288$

Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

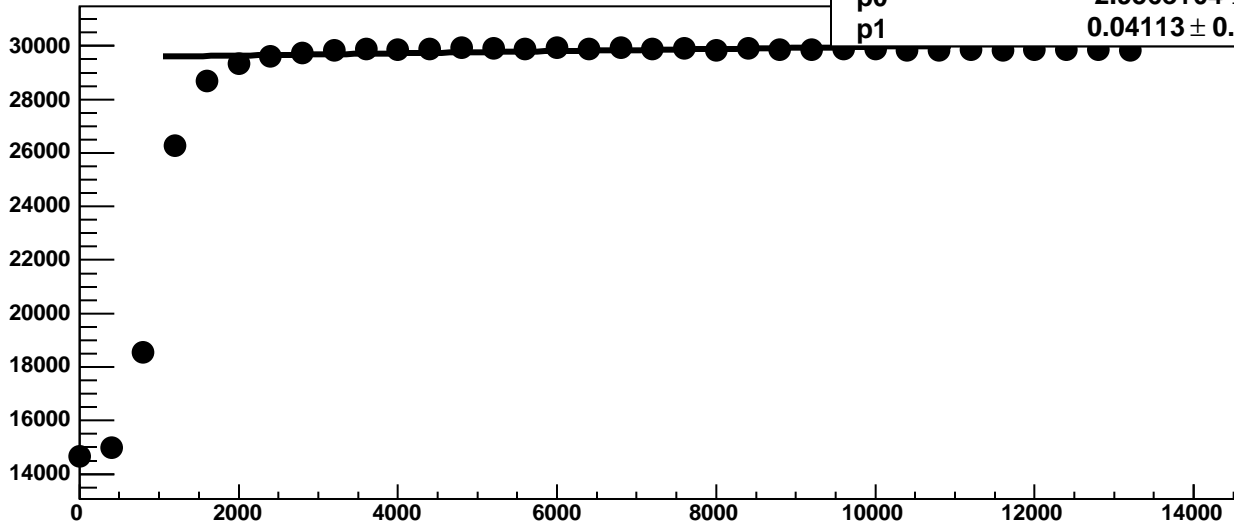
4370 / 23

p0

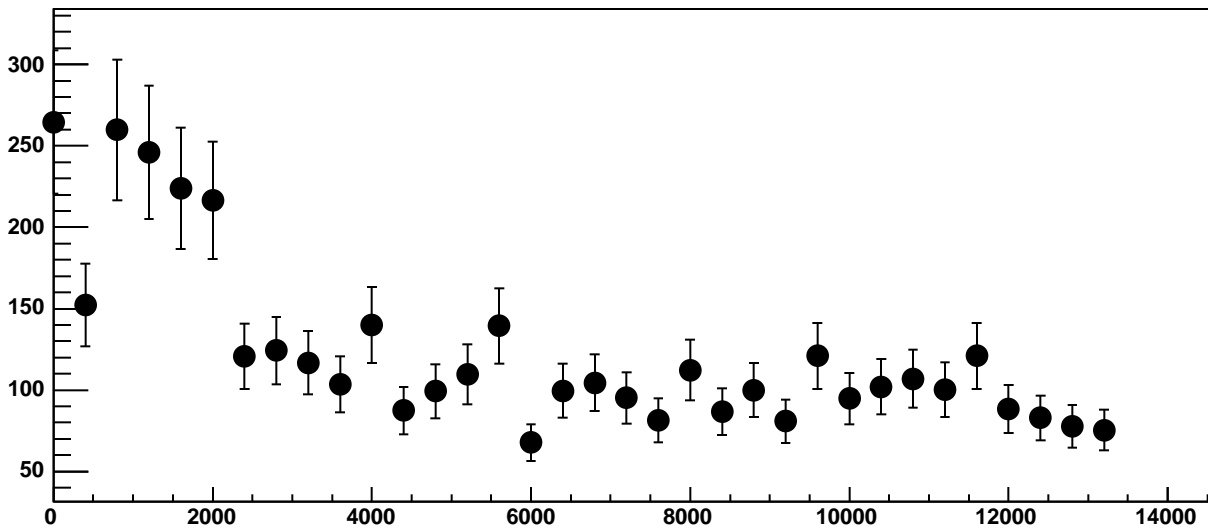
$2.956e+04 \pm 13.88$

p1

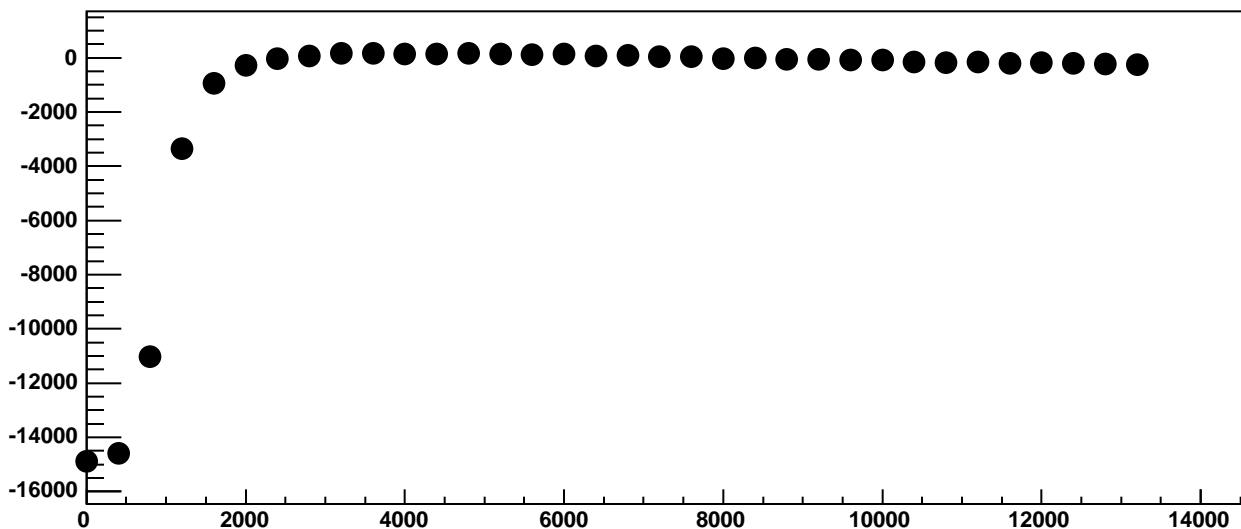
$0.04113 \pm 0.001941$



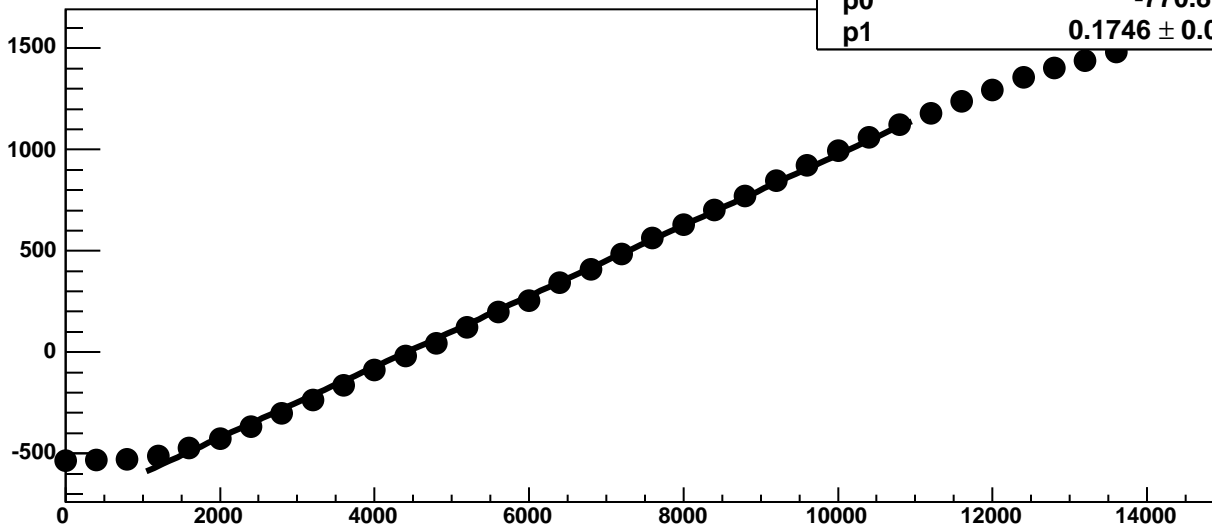
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



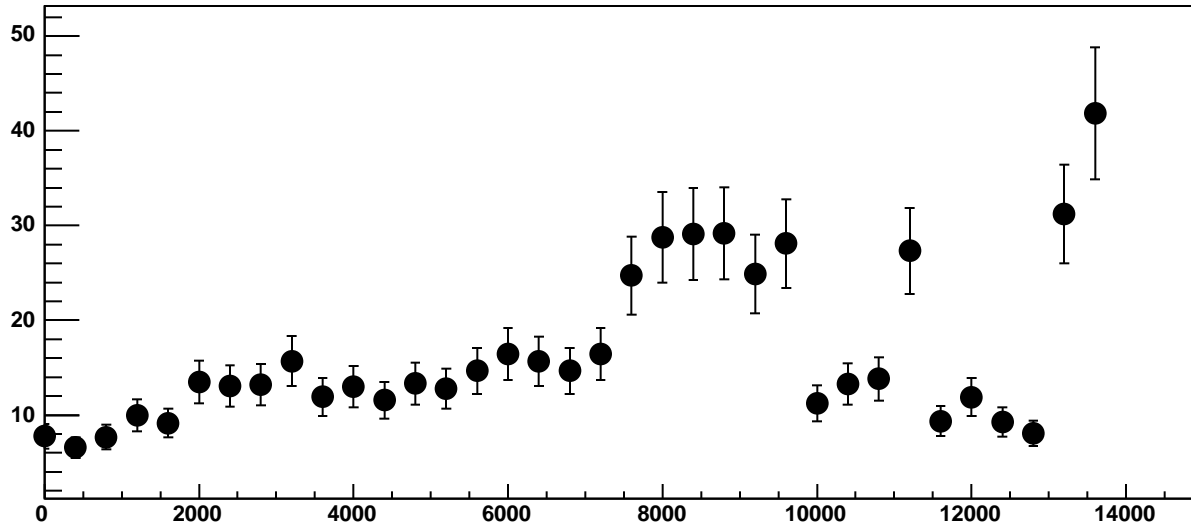
Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



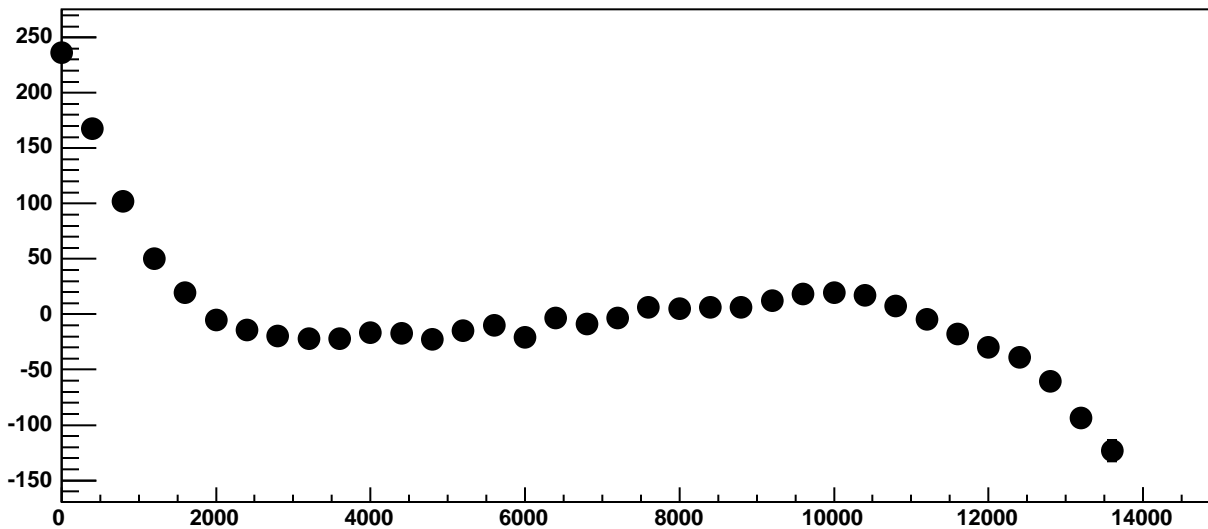
Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



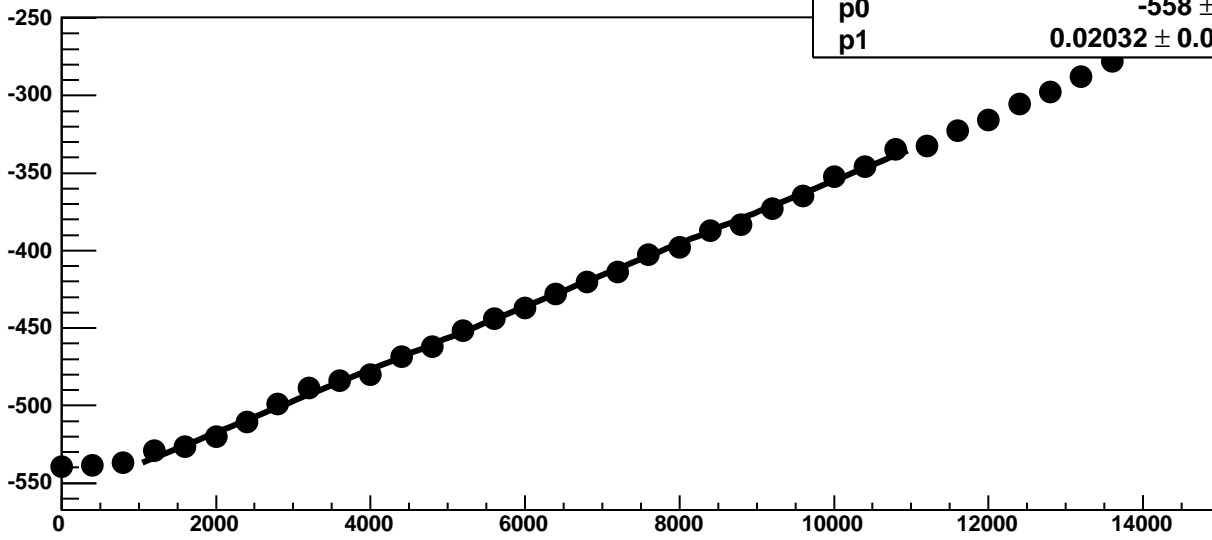
Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



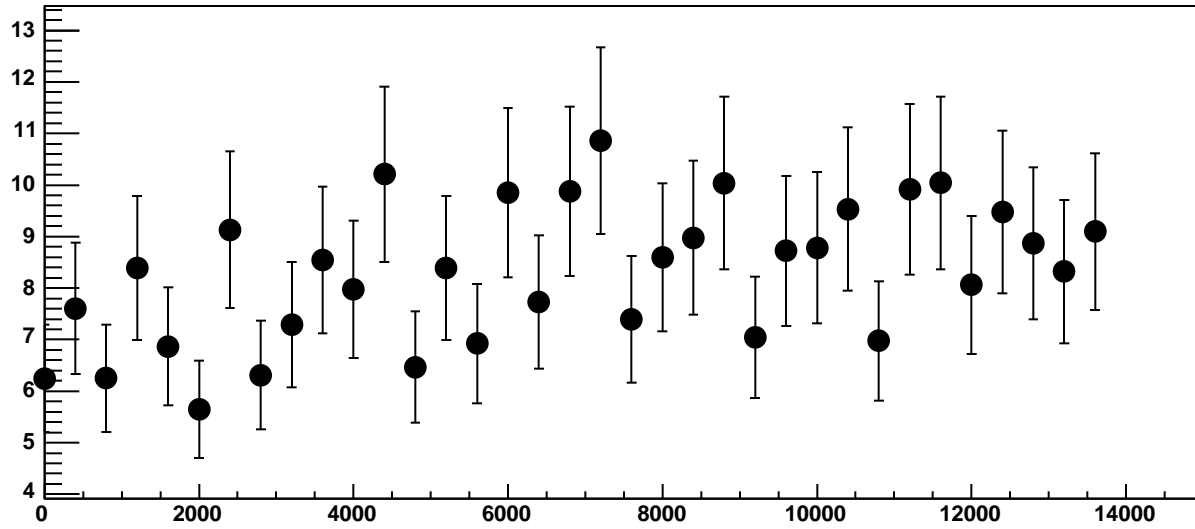
Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



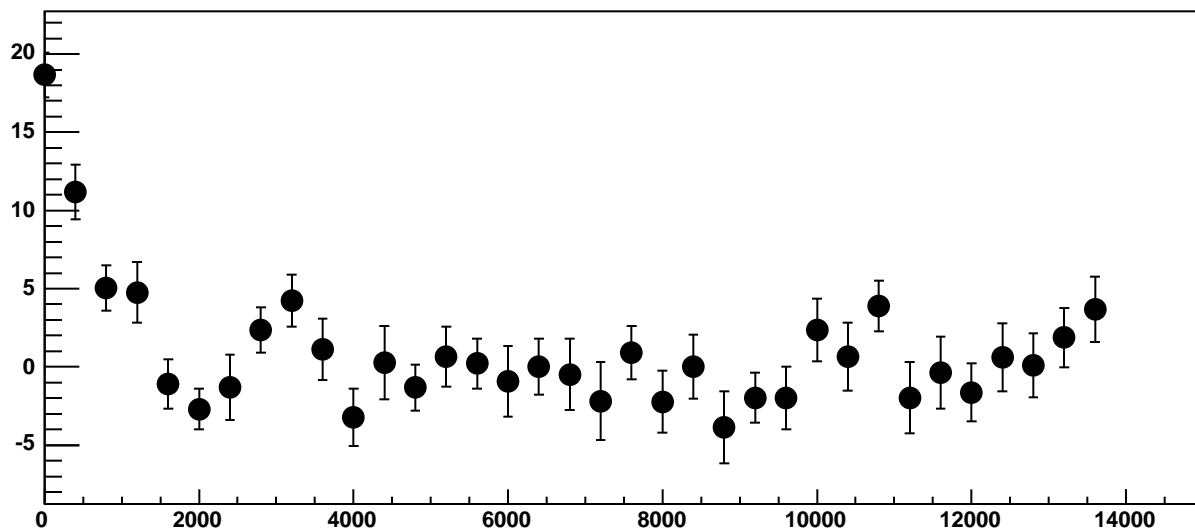
Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



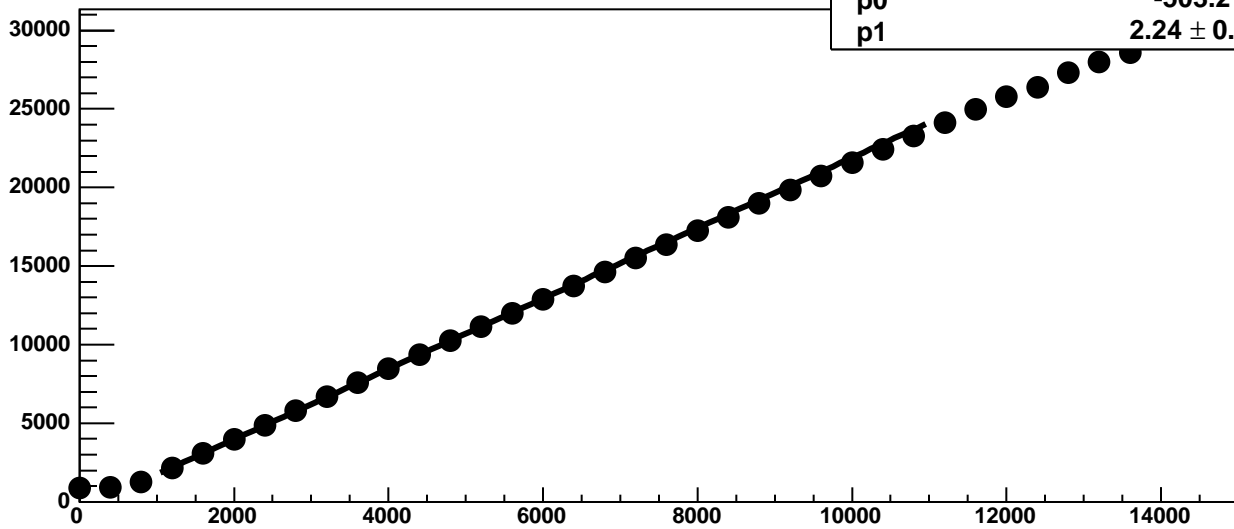
Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



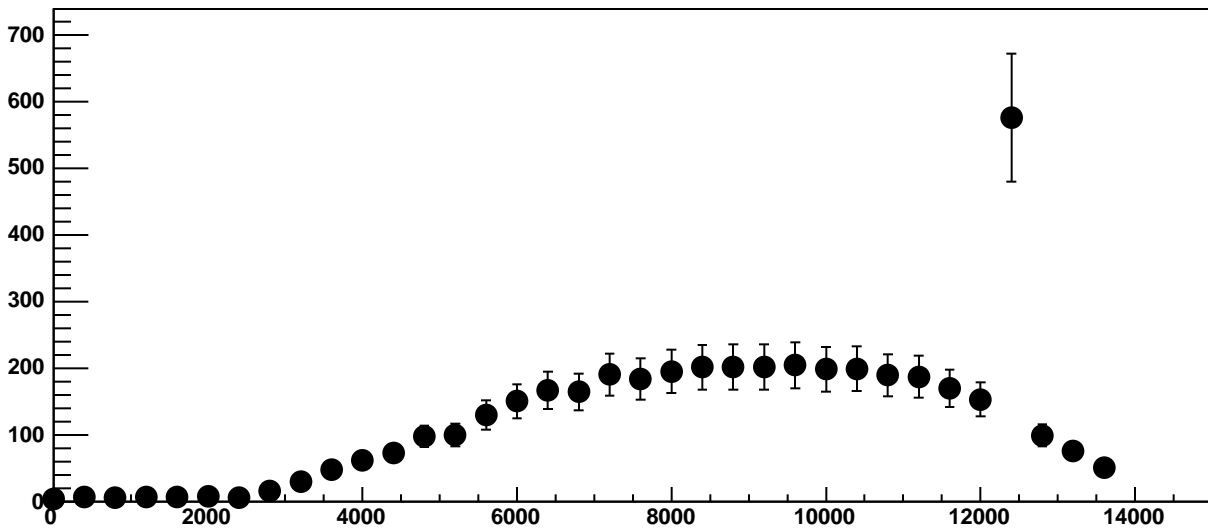
Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



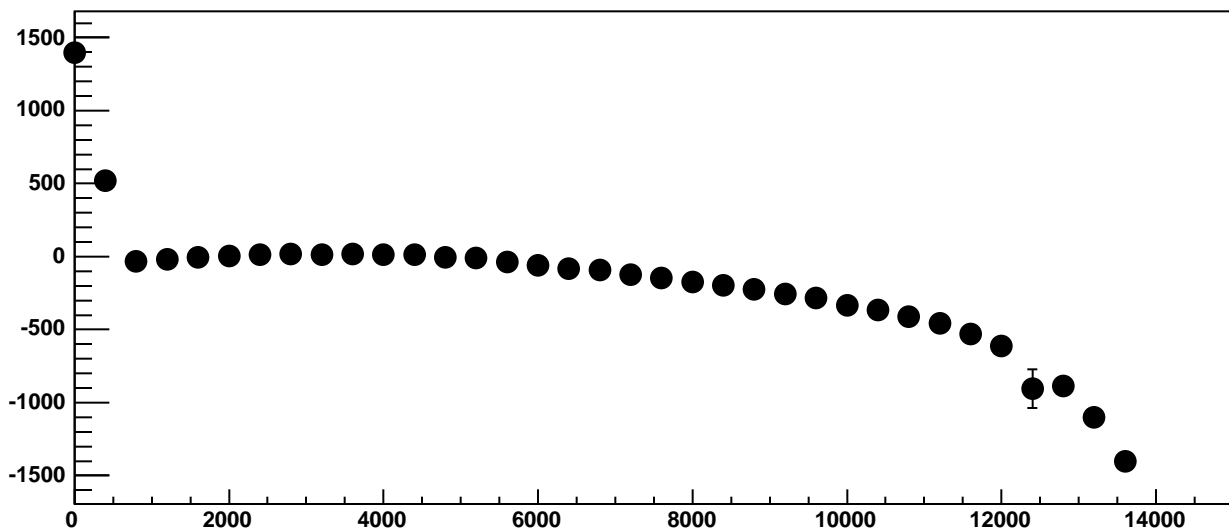
Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

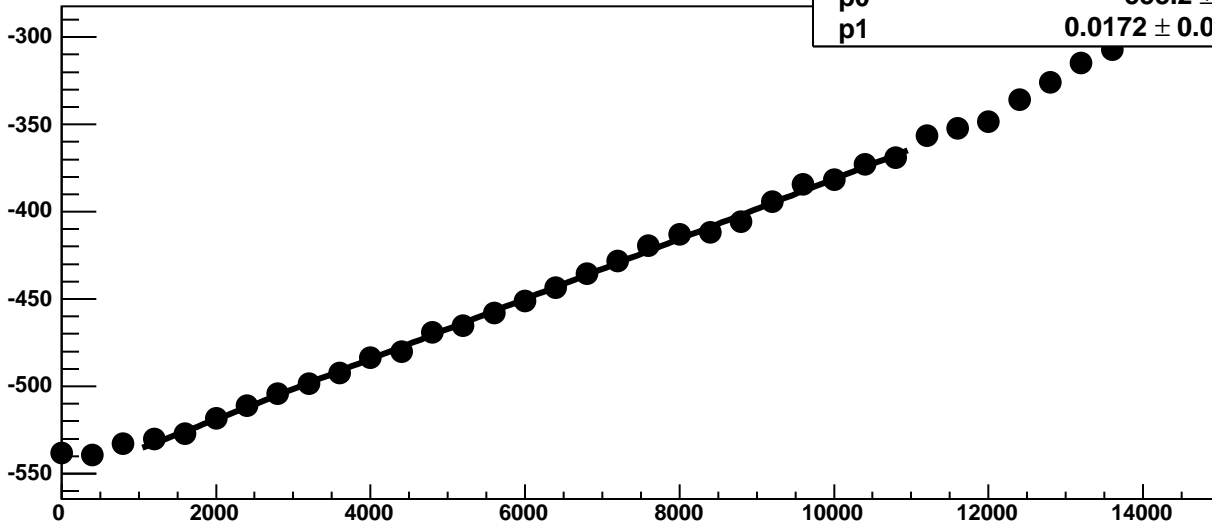


Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

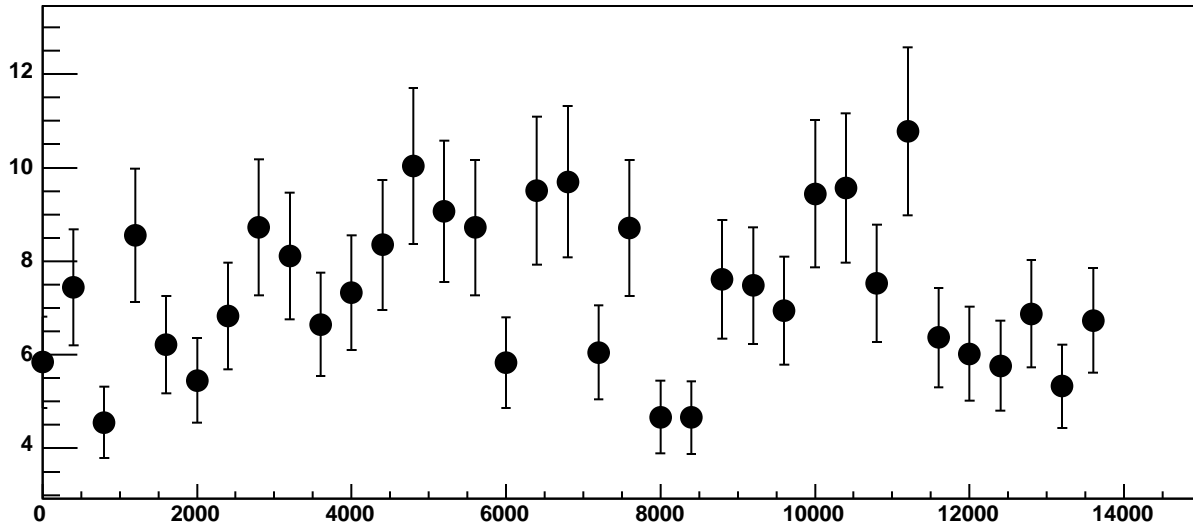




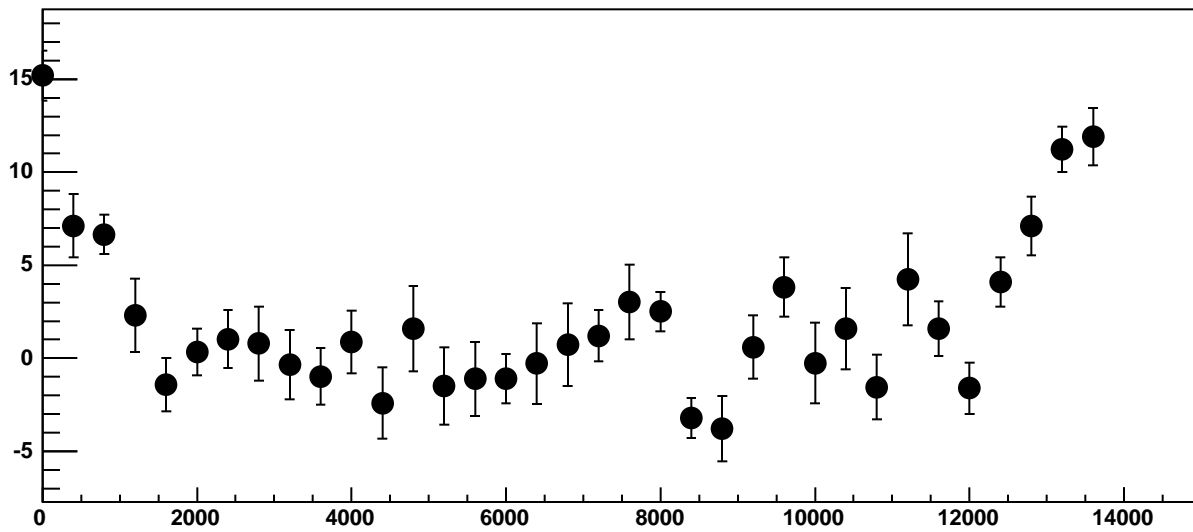
Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



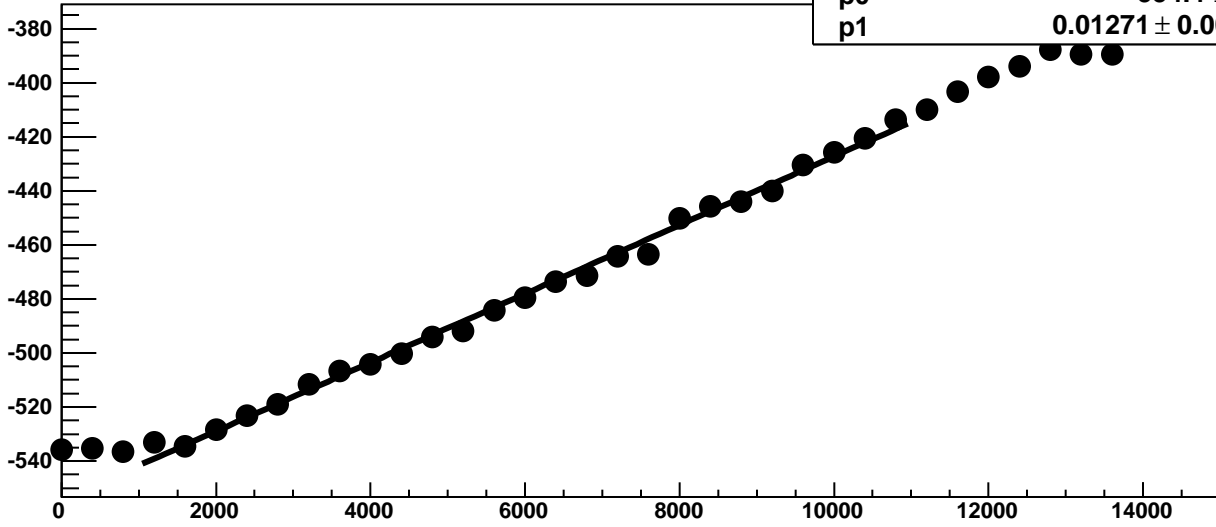
Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



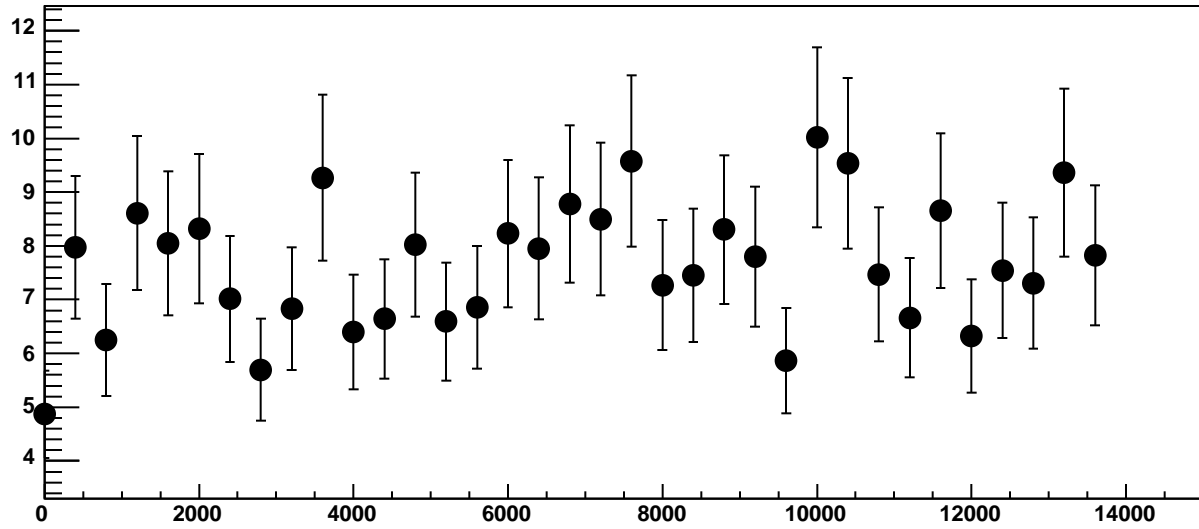
Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



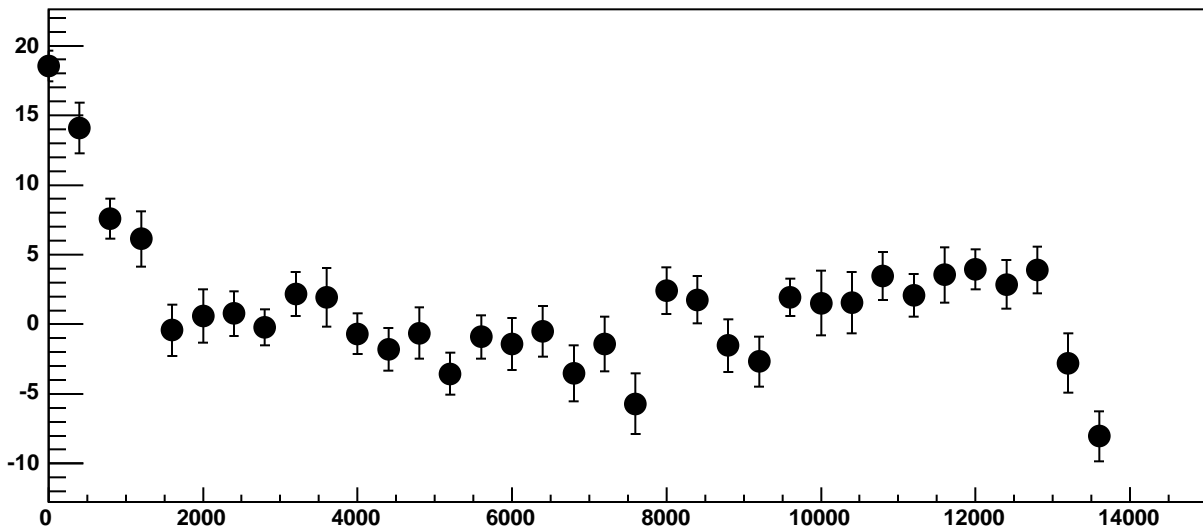
Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



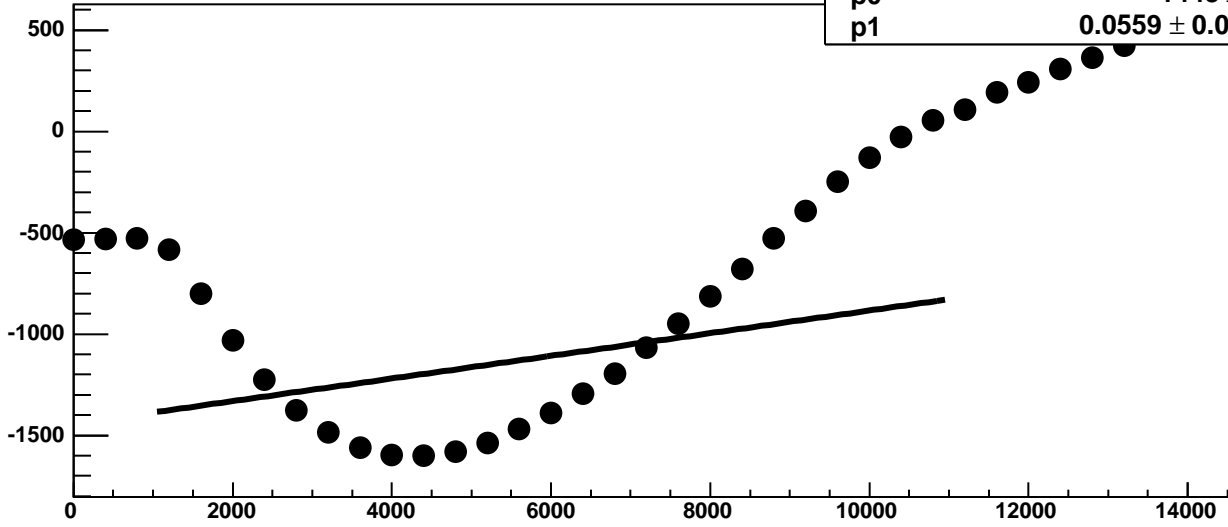
Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

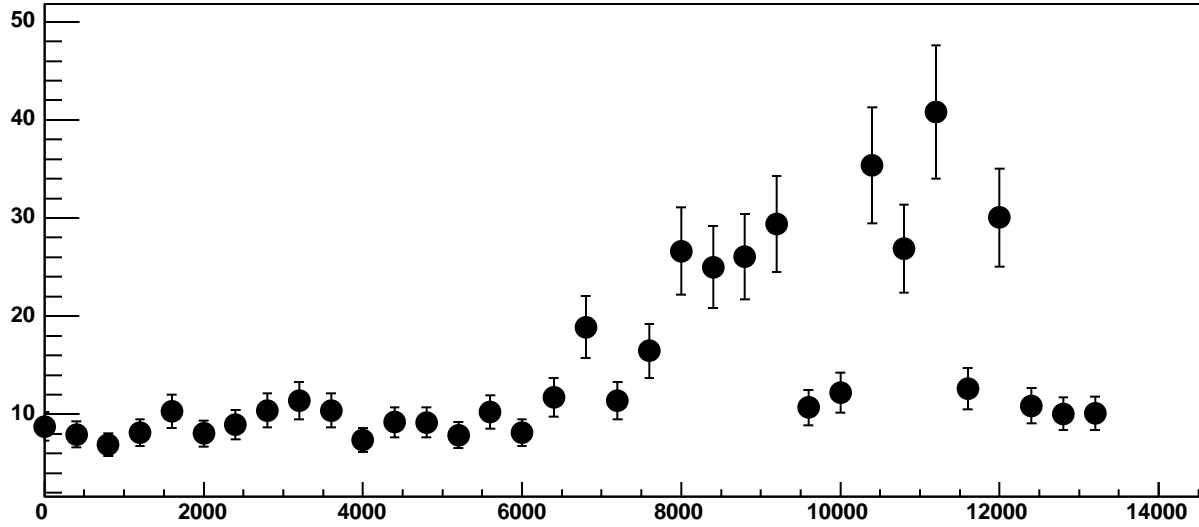


Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

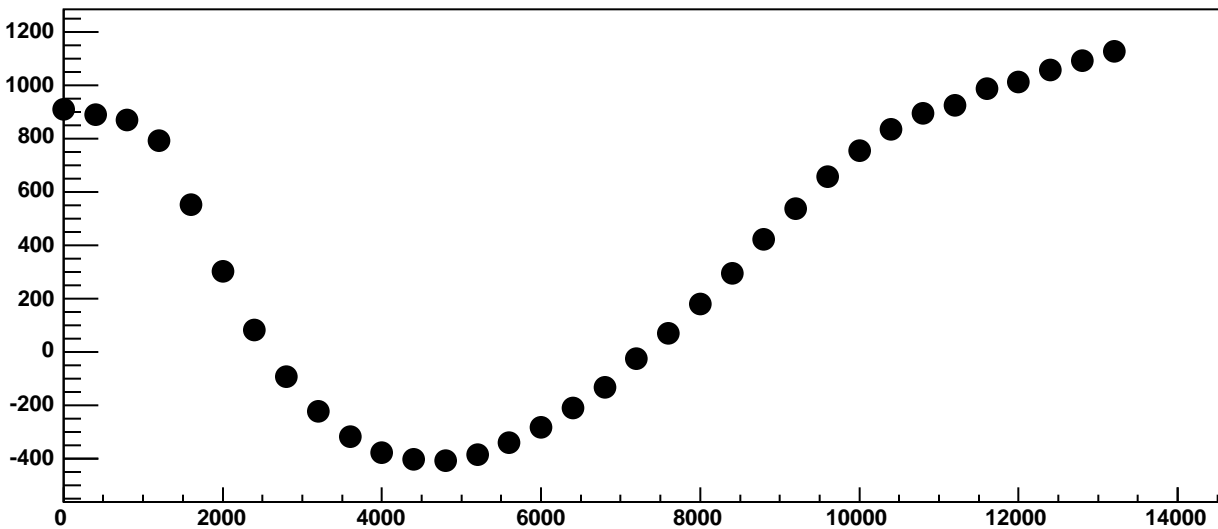


$\chi^2 / \text{ndf}$  7.019e+05 / 23  
p0 -1443 ± 1.084  
p1 0.0559 ± 0.0002061

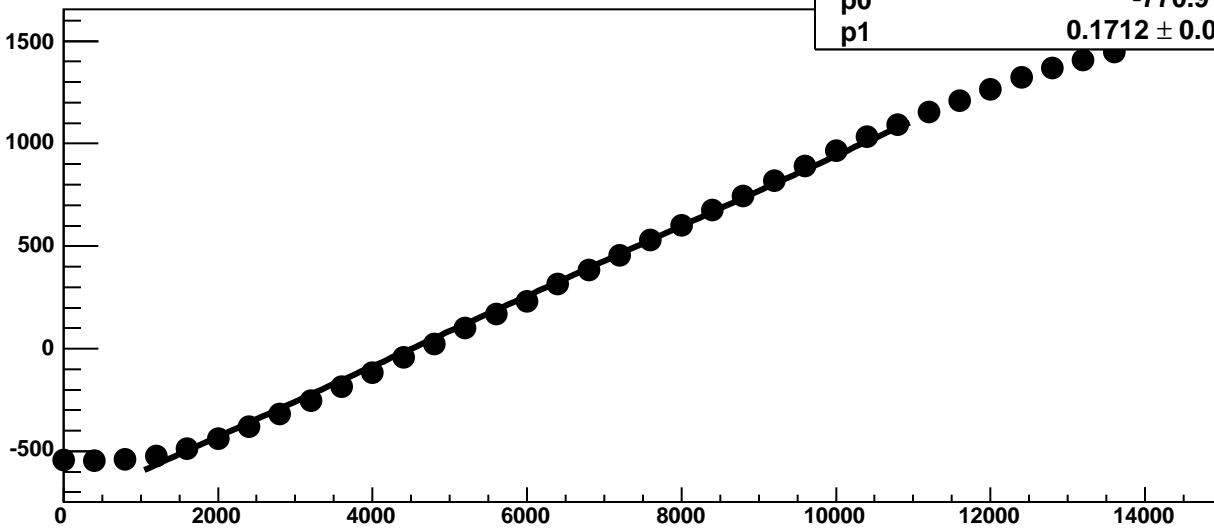
Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



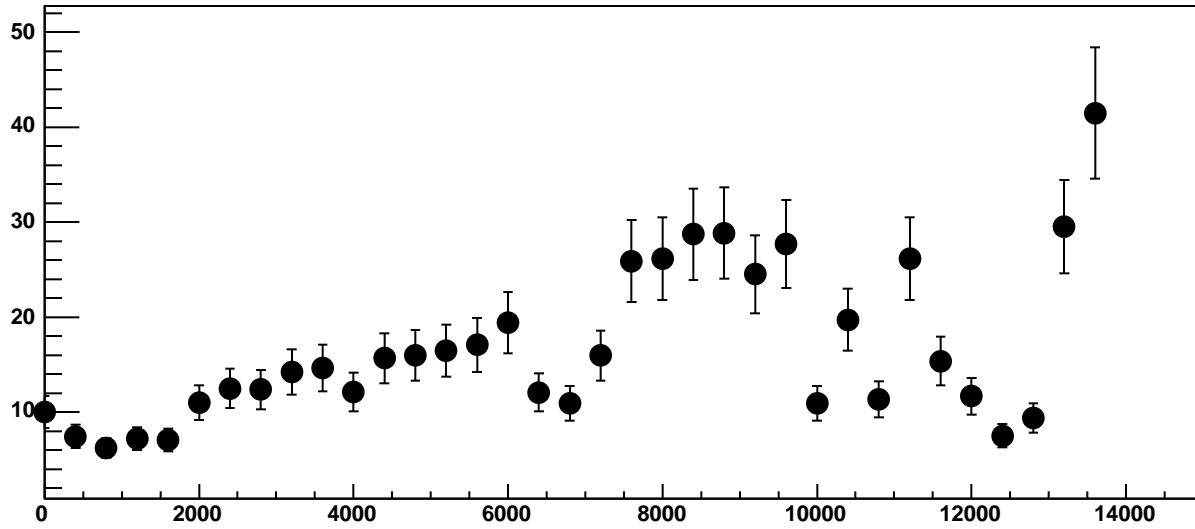
Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



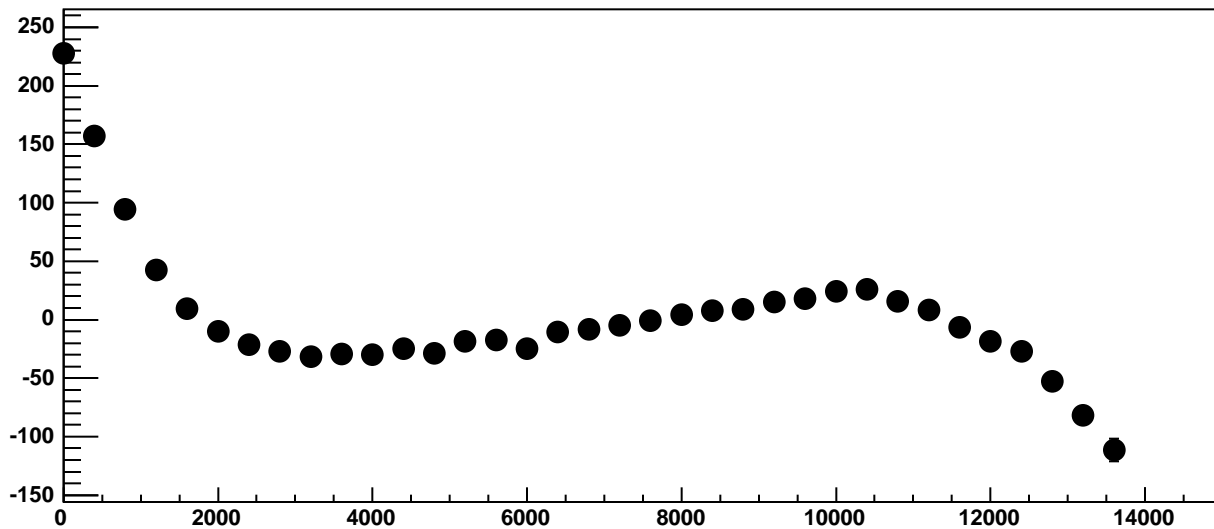
Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



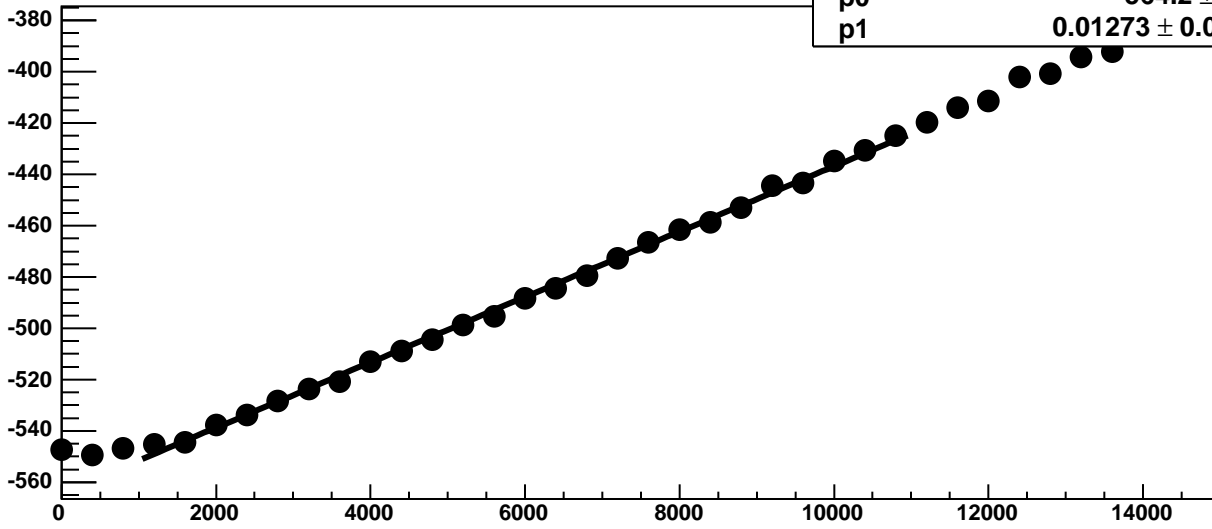
Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

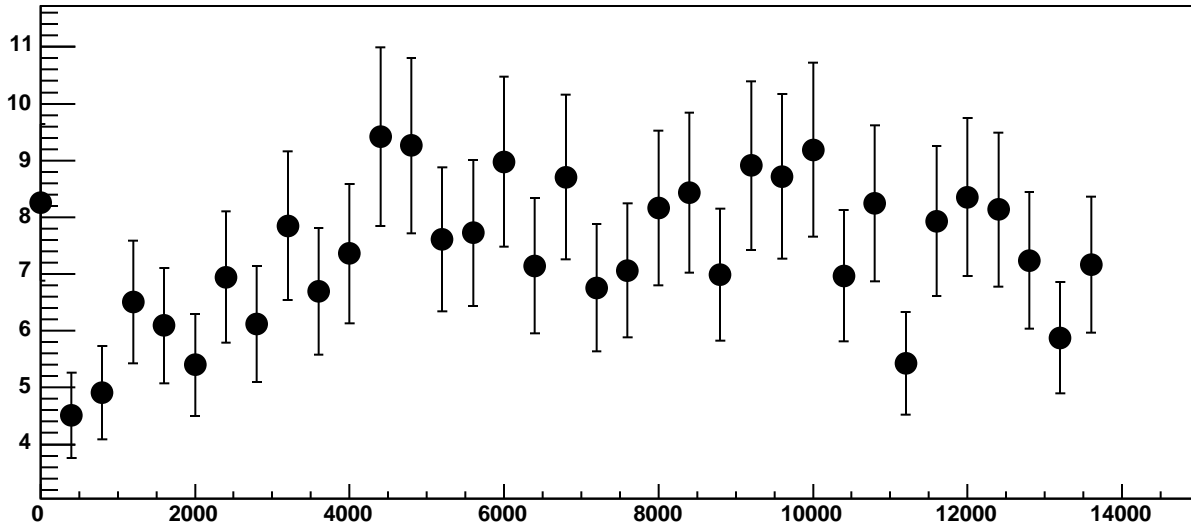


Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

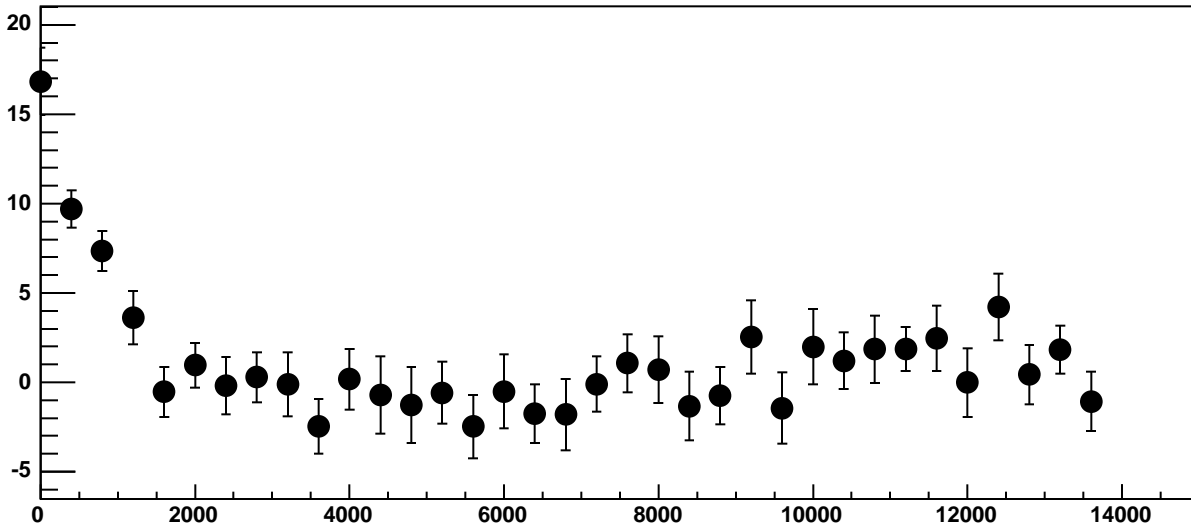


$\chi^2 / \text{ndf}$  19.54 / 23  
p0  $-564.2 \pm 0.7136$   
p1  $0.01273 \pm 0.0001139$

Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

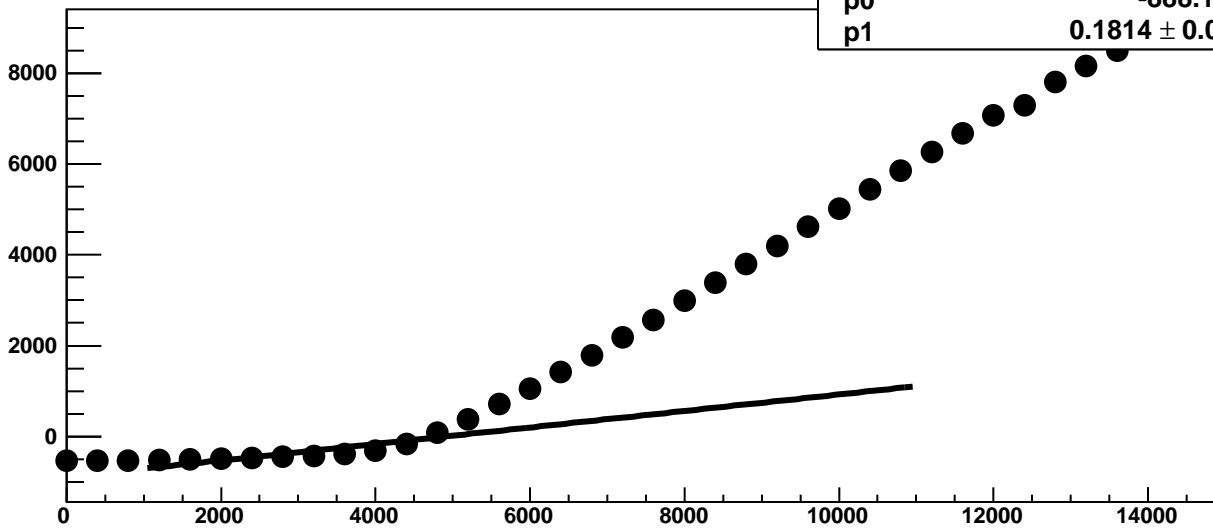


Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

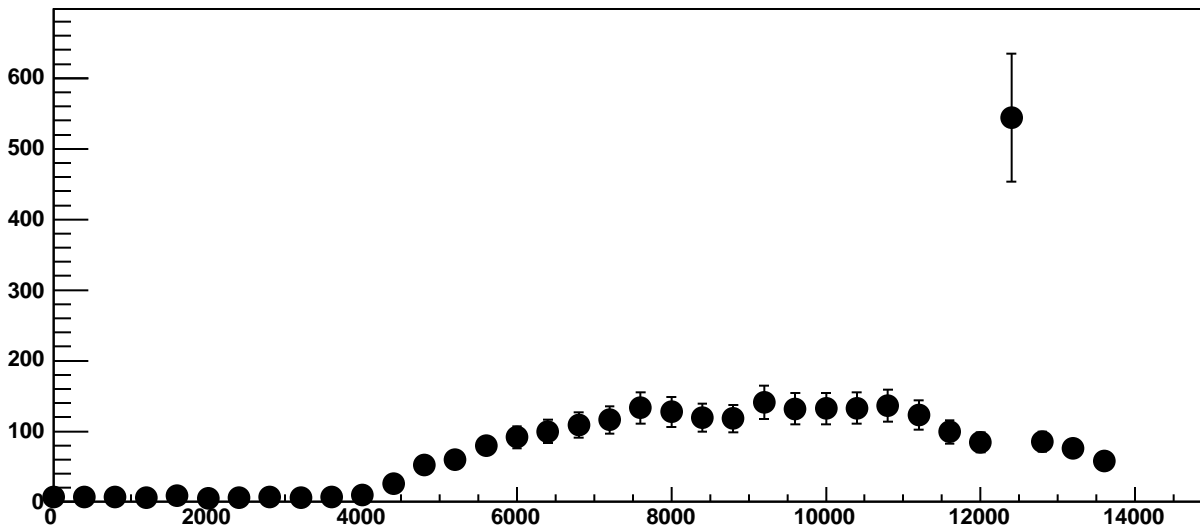


Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

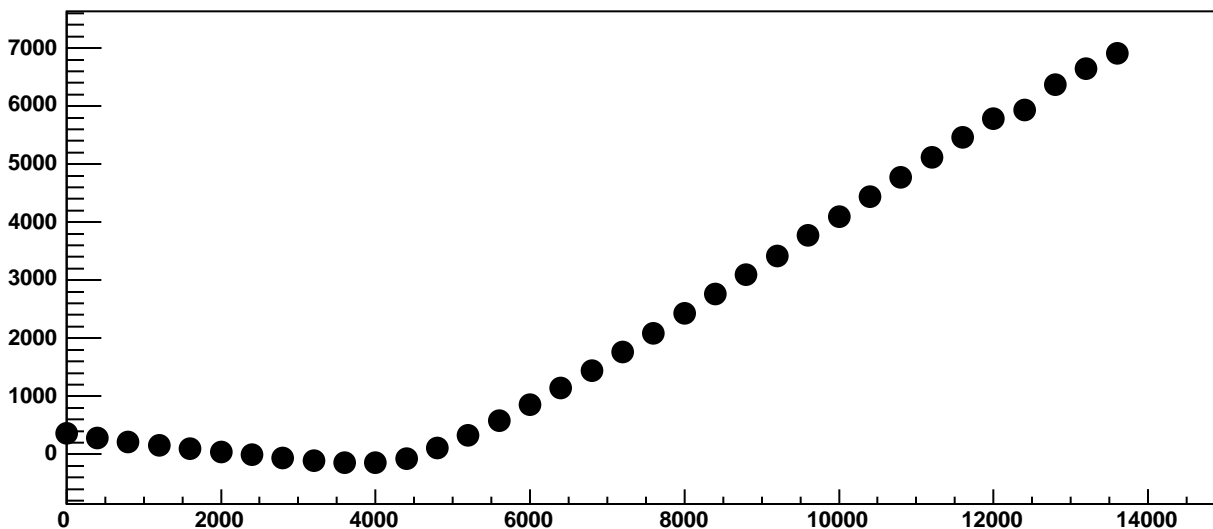
$\chi^2 / \text{ndf}$  1.723e+05 / 23  
p0 -888.1 ± 1.518  
p1 0.1814 ± 0.0005596



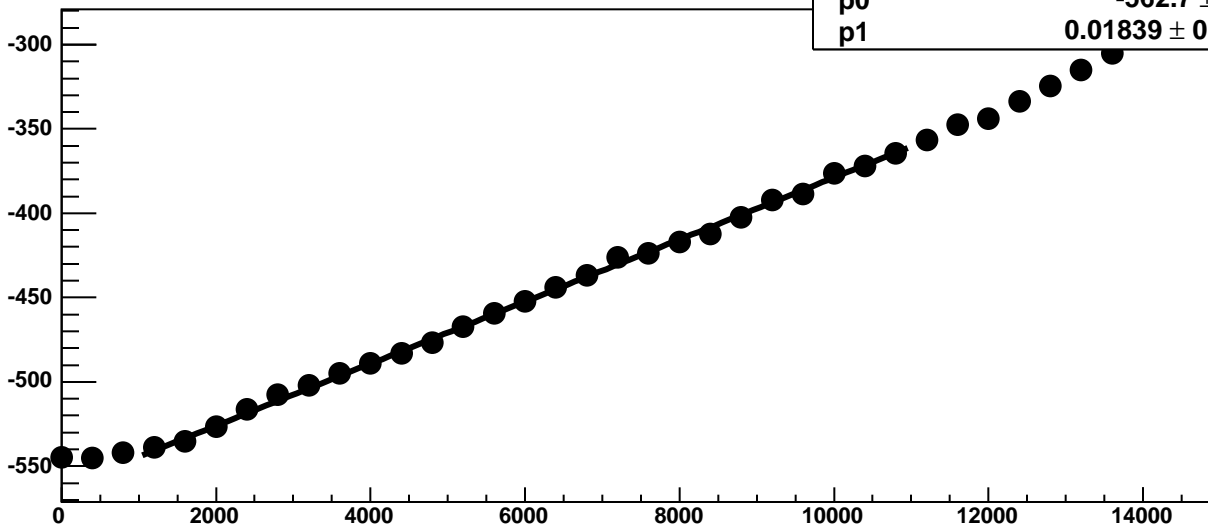
Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



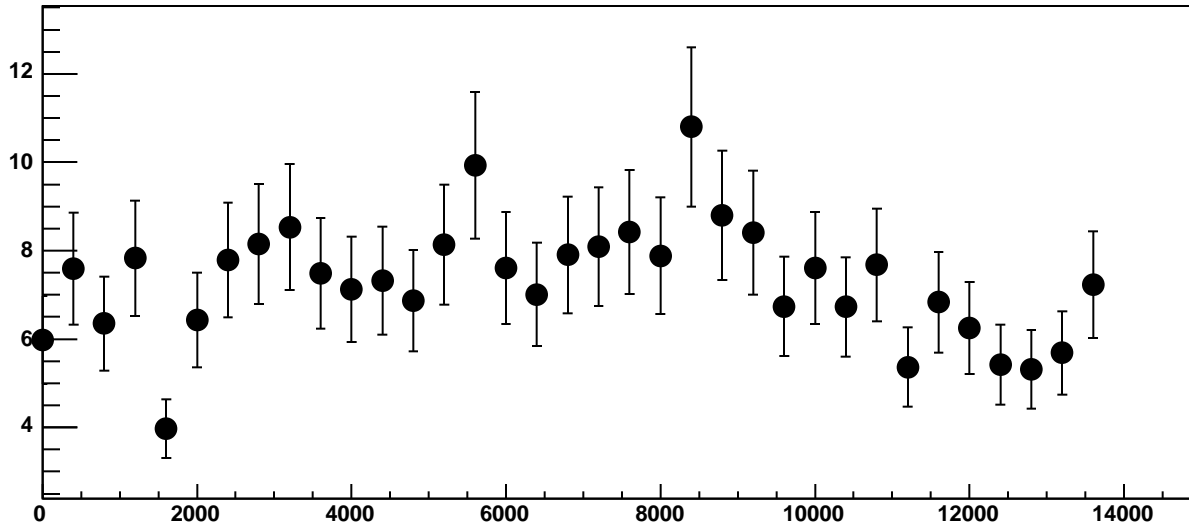
Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



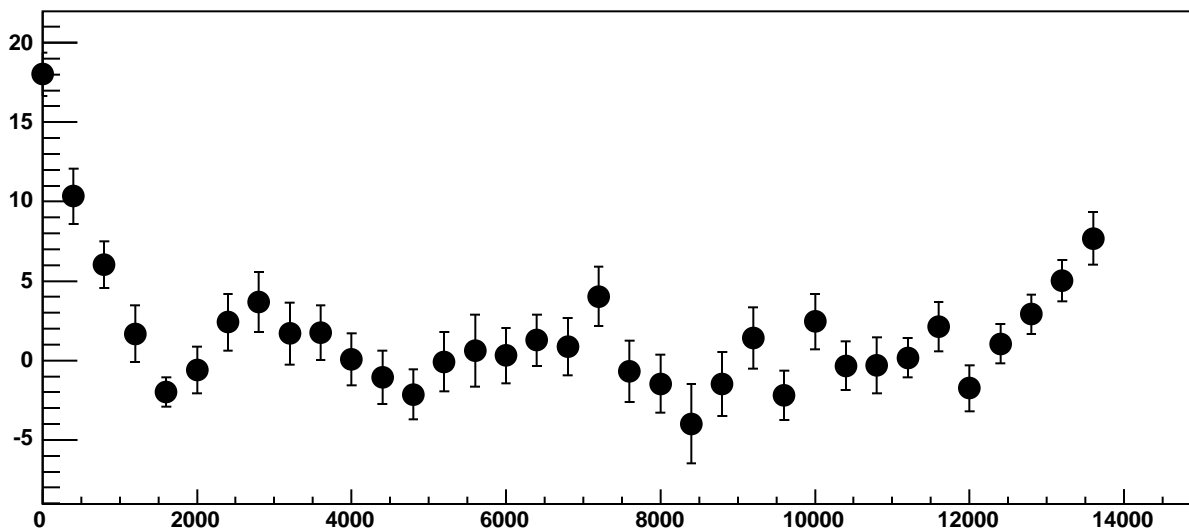
Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



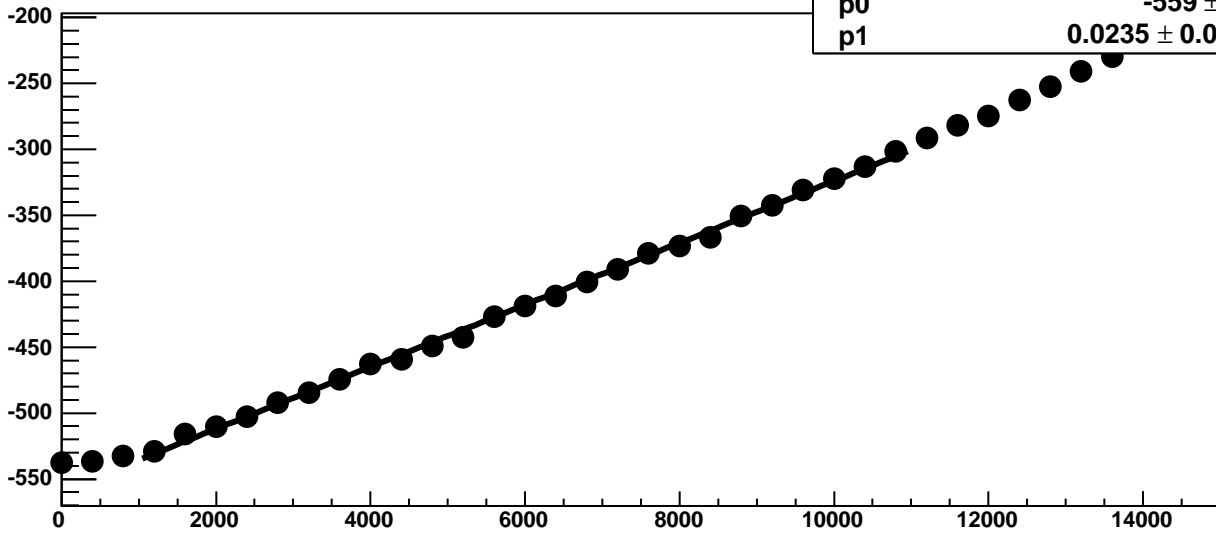
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

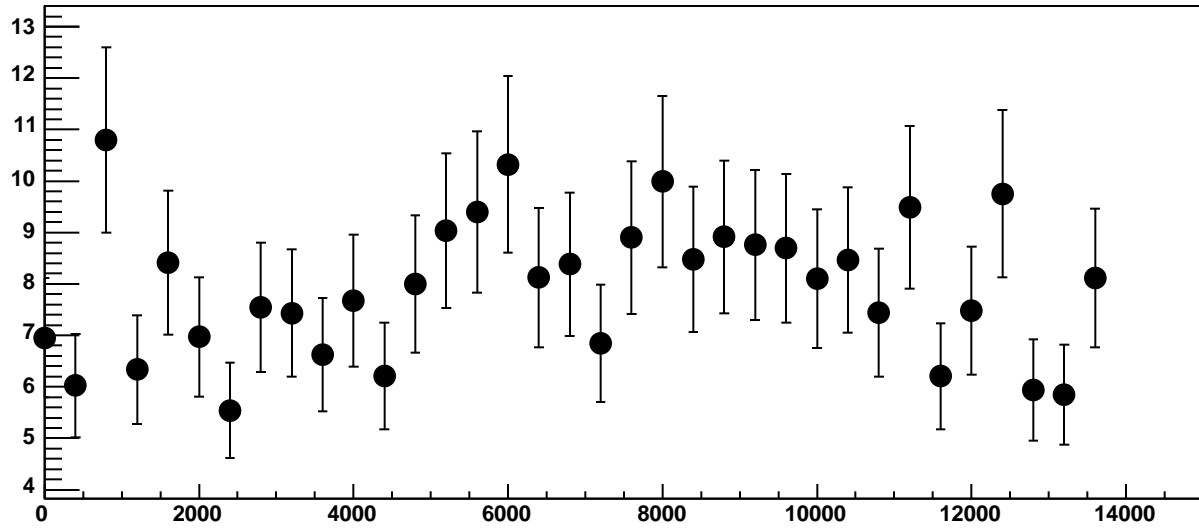


Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

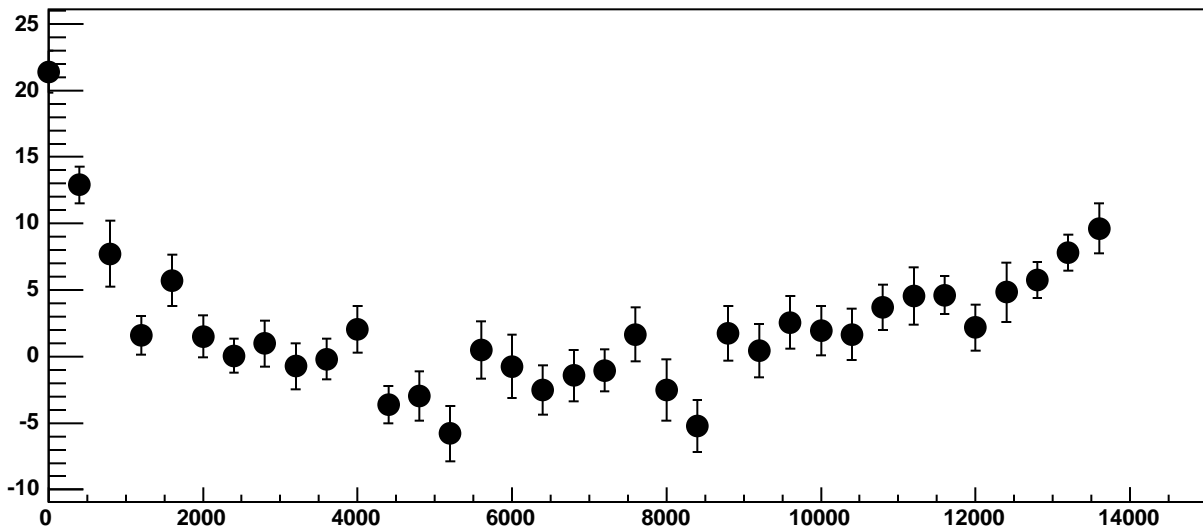


$\chi^2 / \text{ndf}$  50.49 / 23  
p0  $-559 \pm 0.7521$   
p1  $0.0235 \pm 0.0001201$

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC





Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

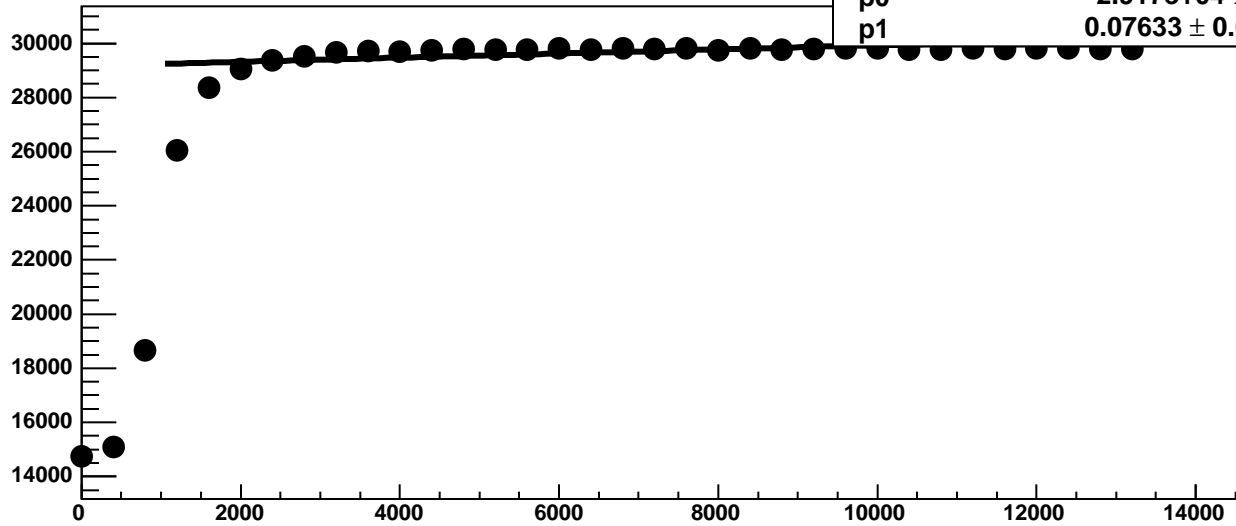
4306 / 23

p0

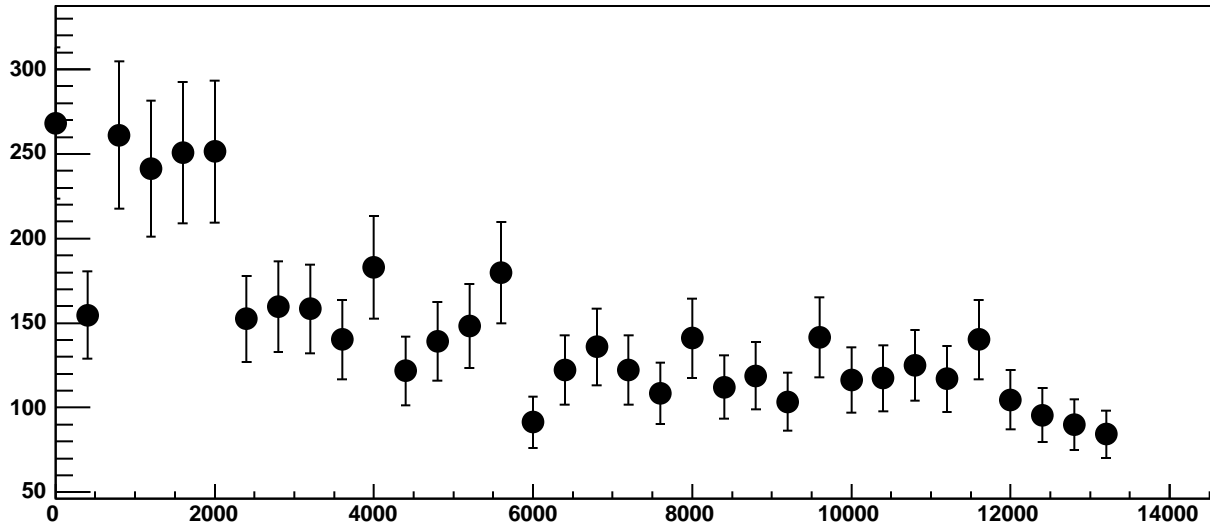
$2.917\text{e}+04 \pm 17.49$

p1

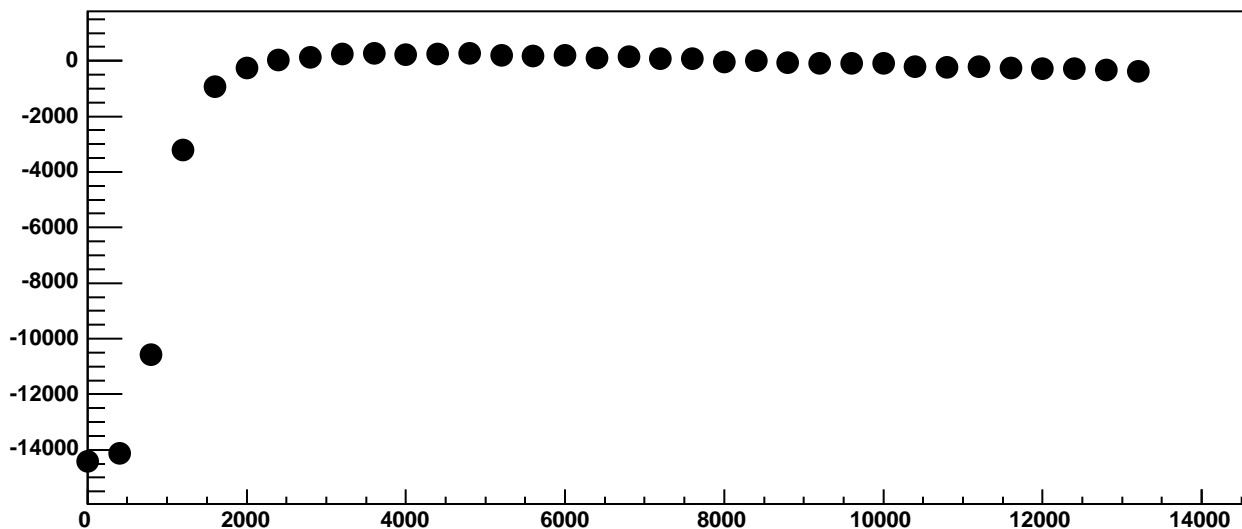
$0.07633 \pm 0.002395$



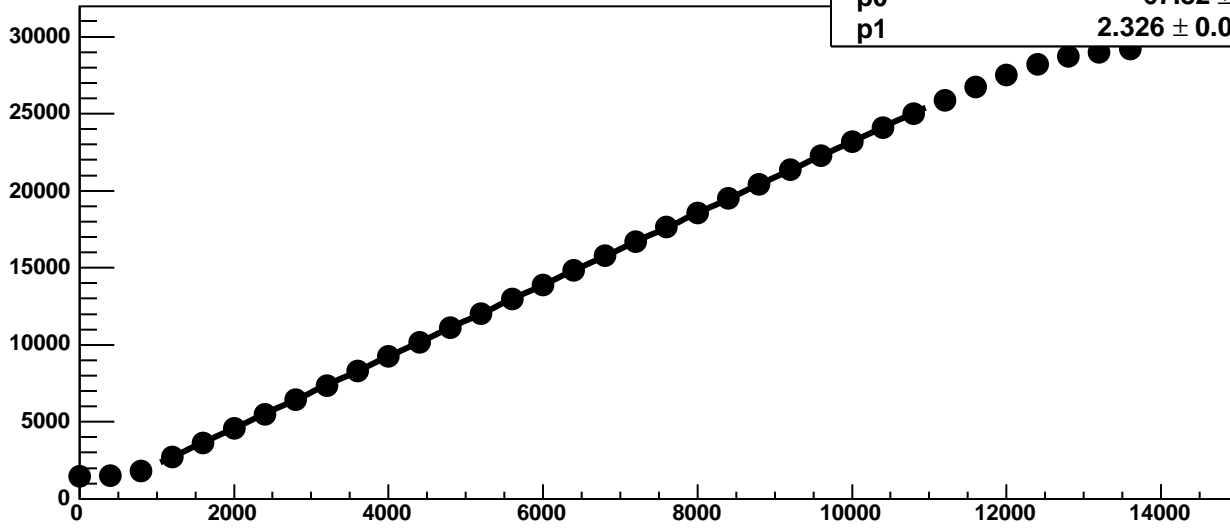
Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC

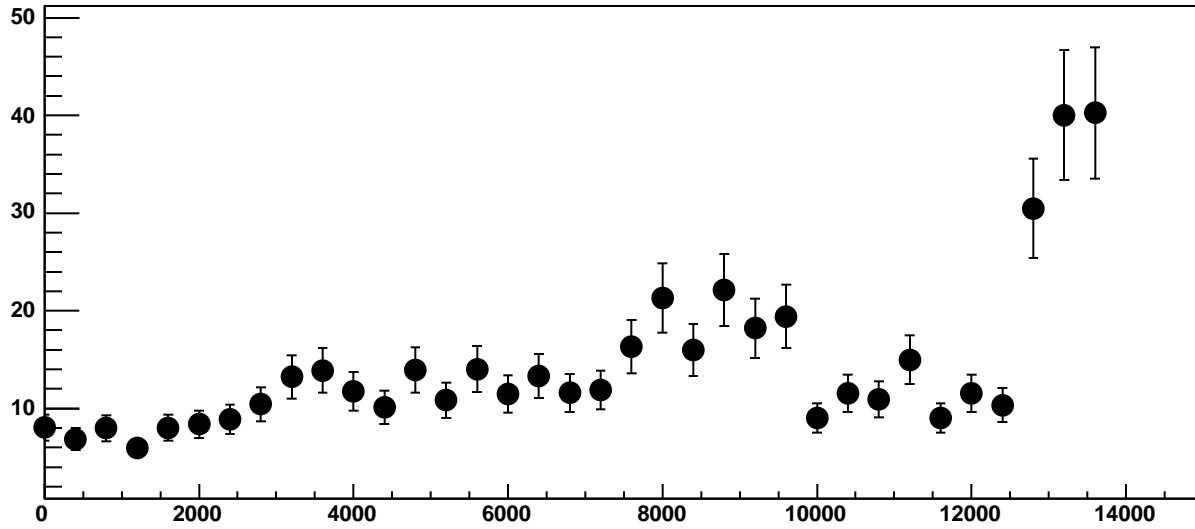


Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC

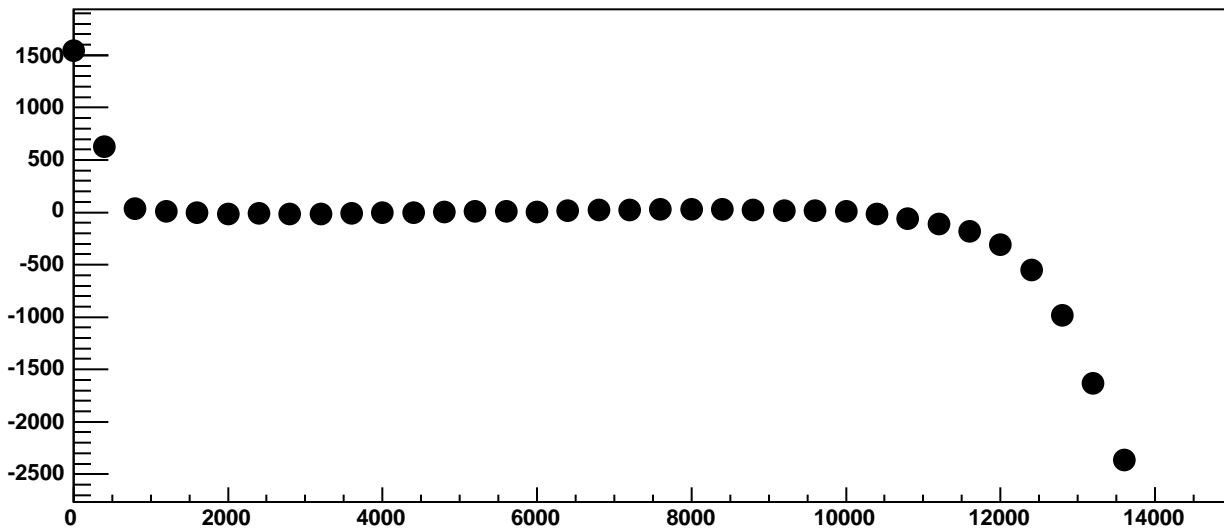


$\chi^2 / \text{ndf}$  1238 / 23  
p0  $-67.82 \pm 0.9416$   
p1  $2.326 \pm 0.0001623$

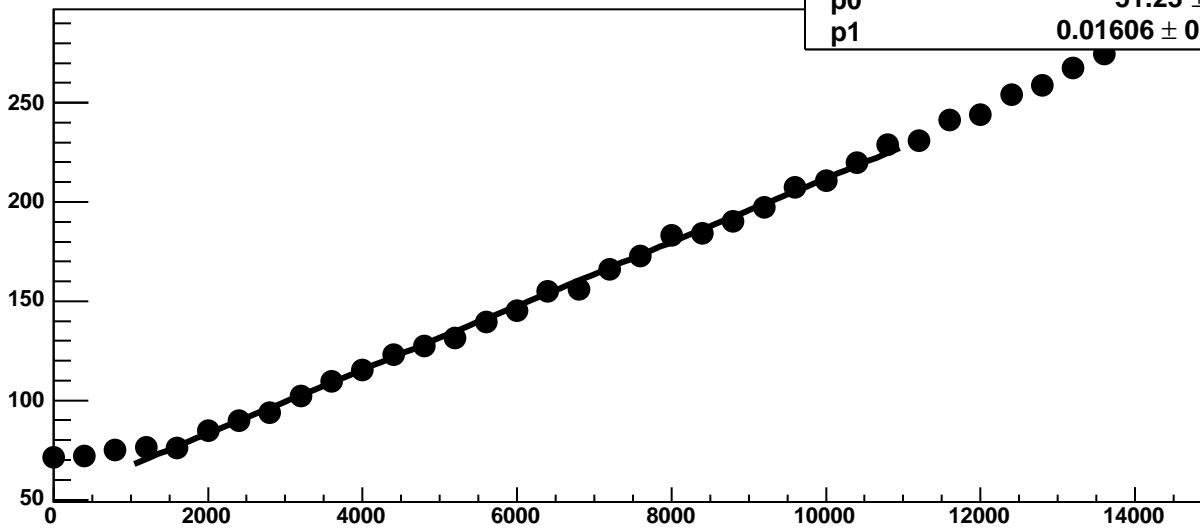
Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

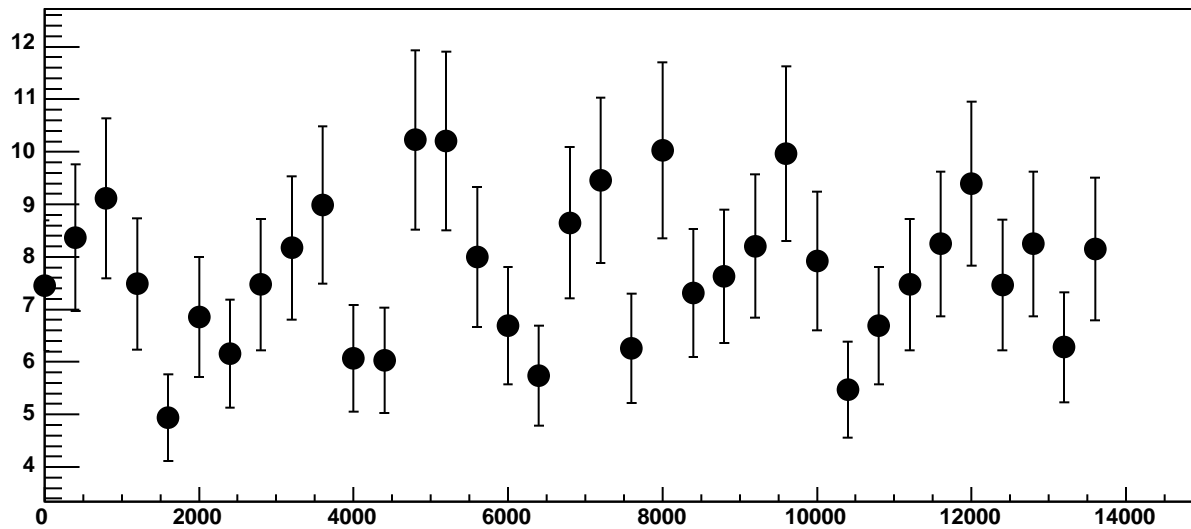


Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

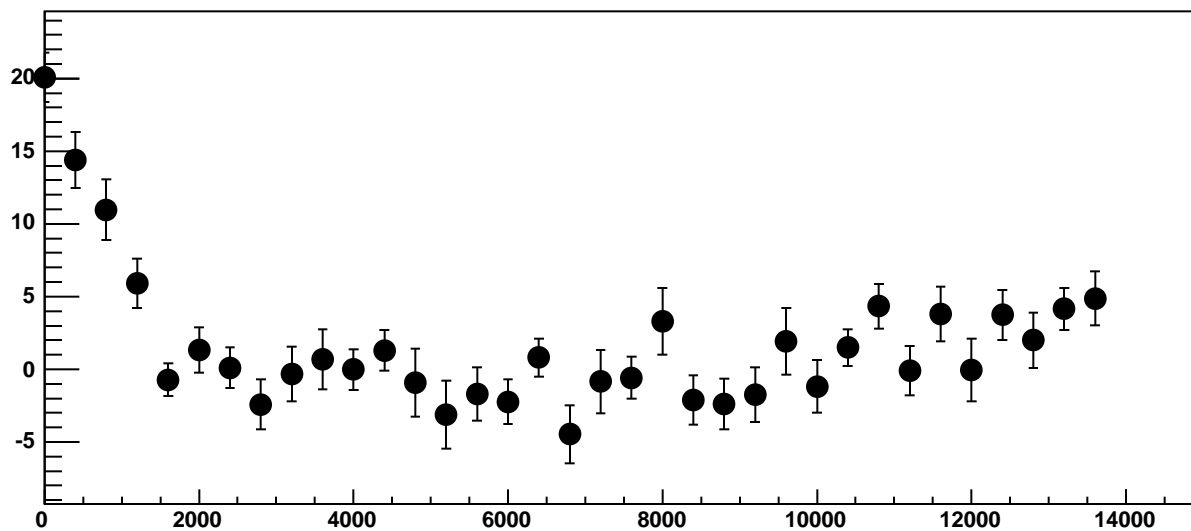


$\chi^2 / \text{ndf}$  43.58 / 23  
p0  $51.23 \pm 0.7057$   
p1  $0.01606 \pm 0.000108$

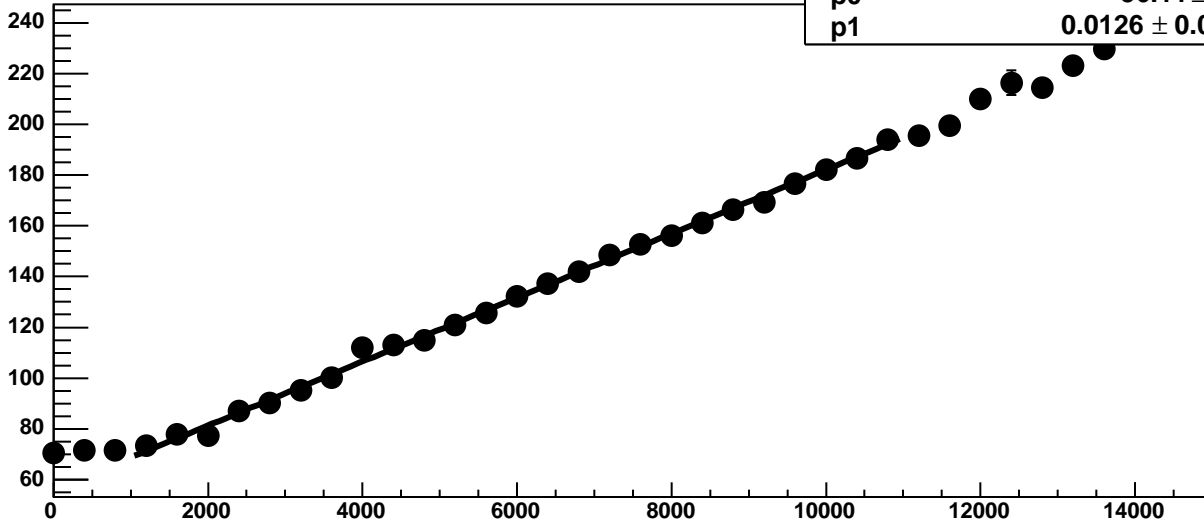
Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

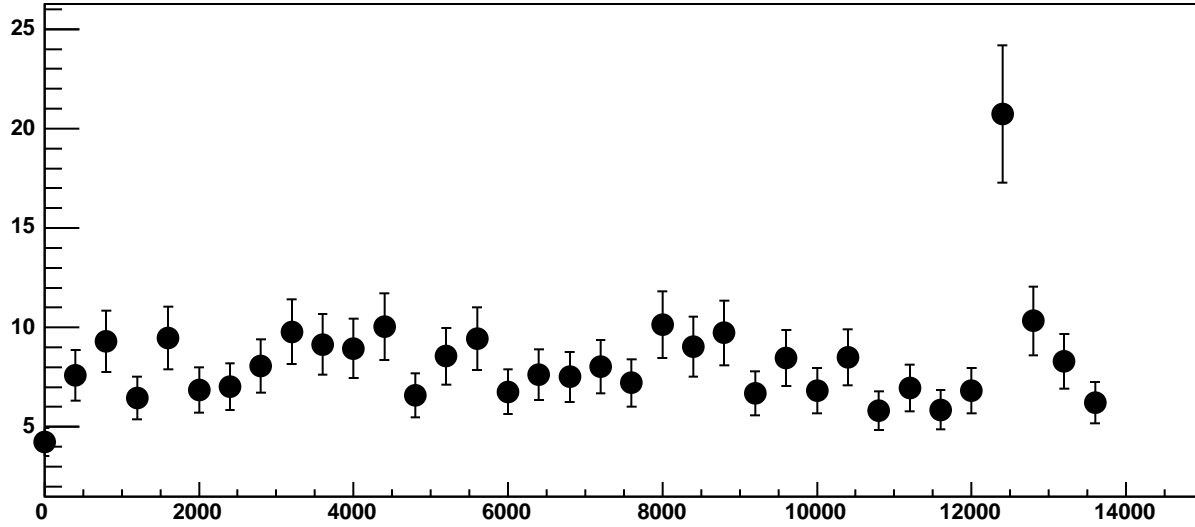


Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

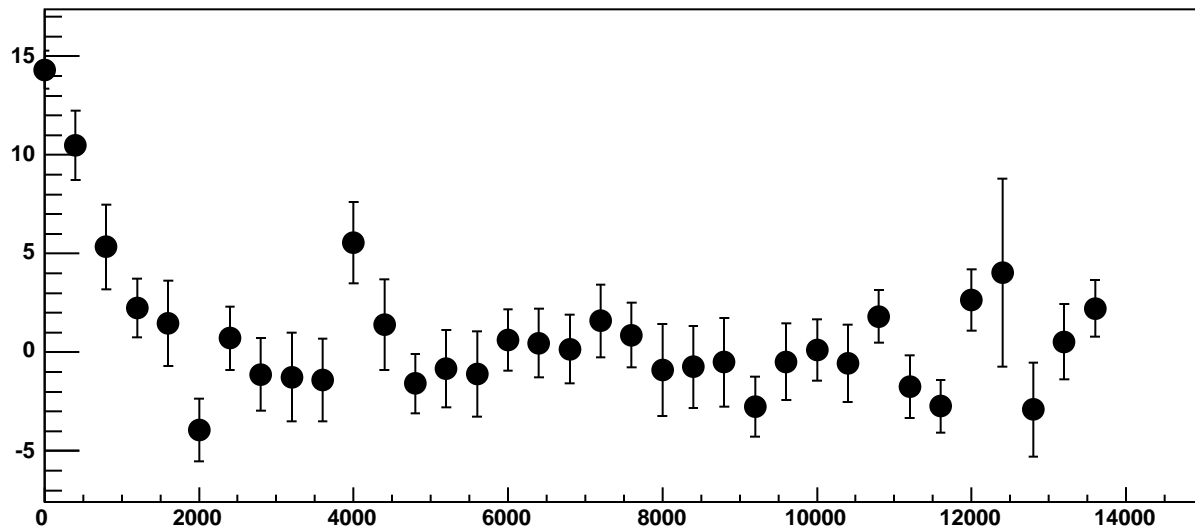


$\chi^2 / \text{ndf}$  26.42 / 23  
p0 56.11 ± 0.8056  
p1 0.0126 ± 0.0001181

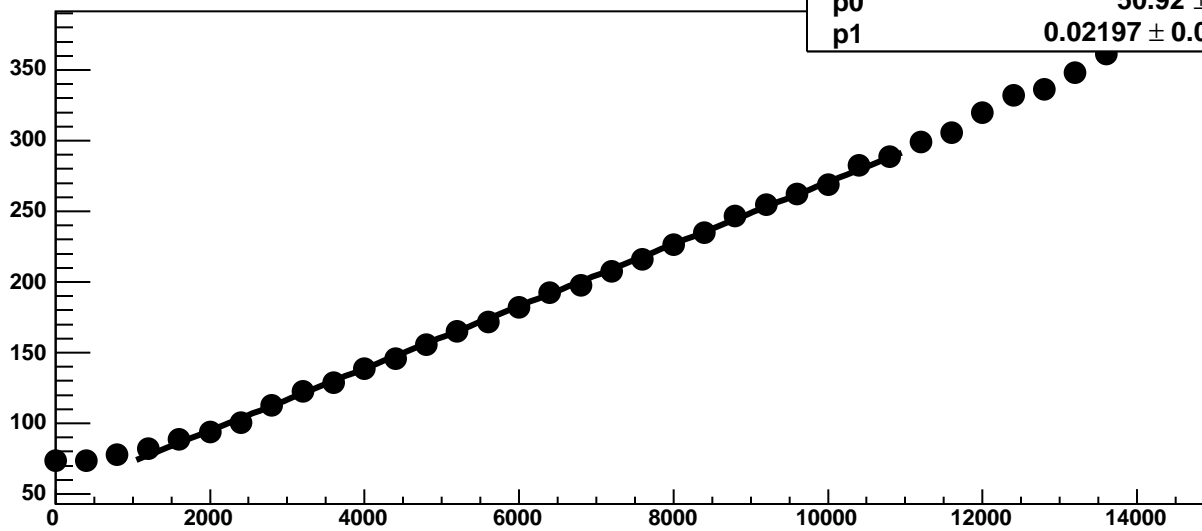
Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

22.8 / 23

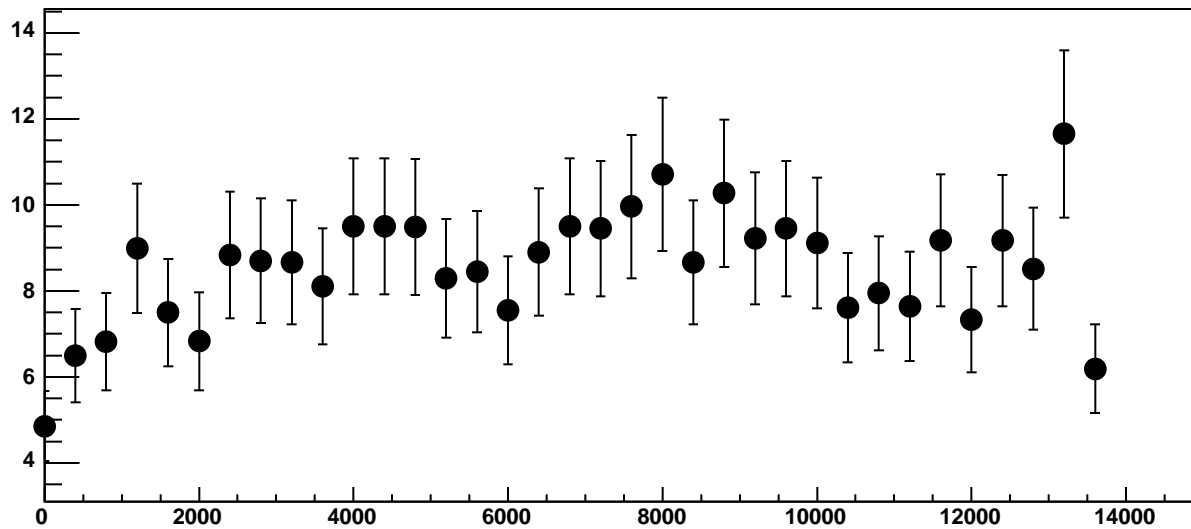
p0

$50.92 \pm 0.8743$

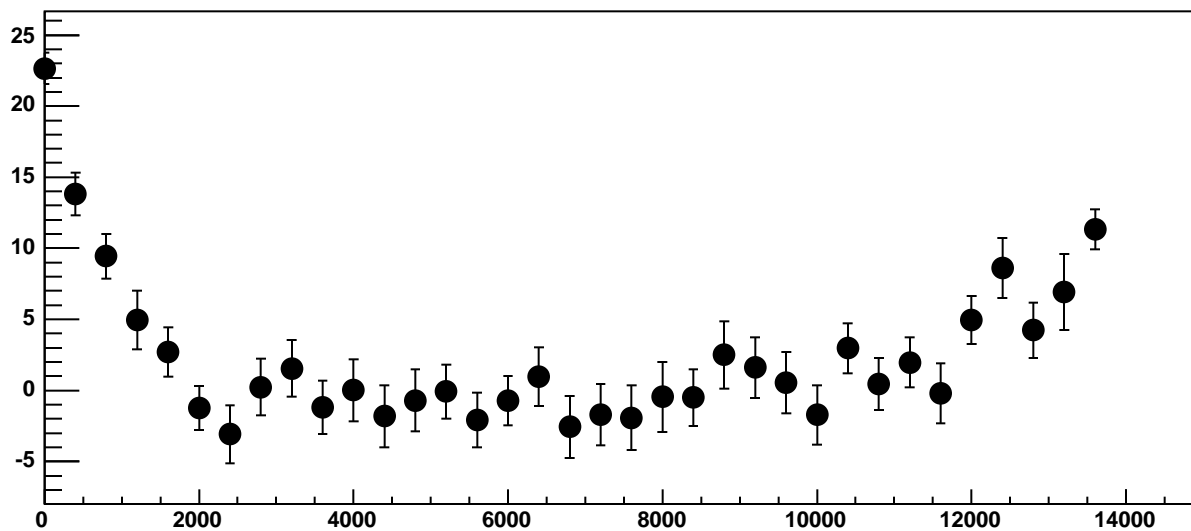
p1

$0.02197 \pm 0.0001336$

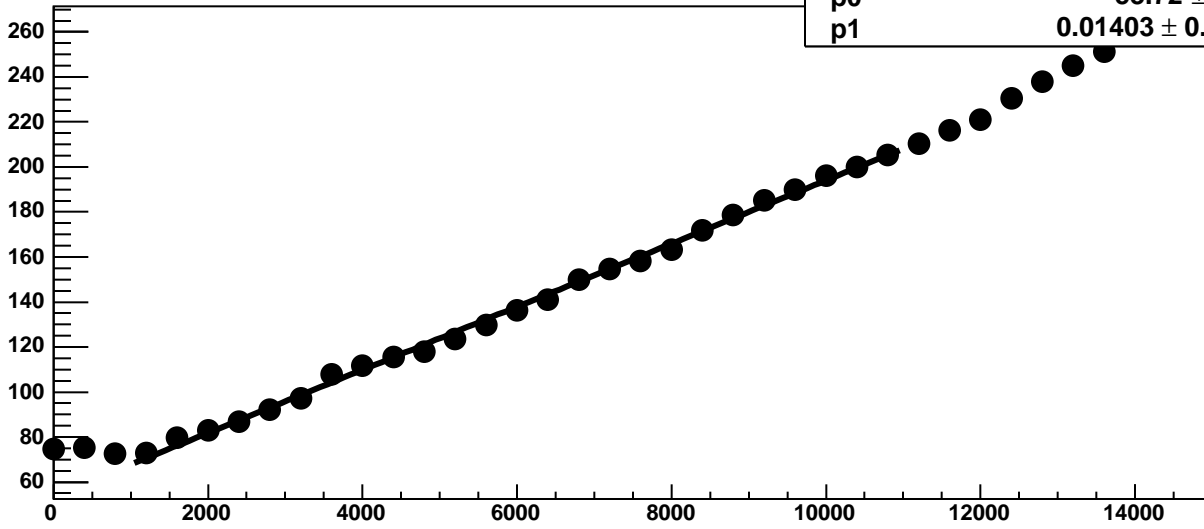
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

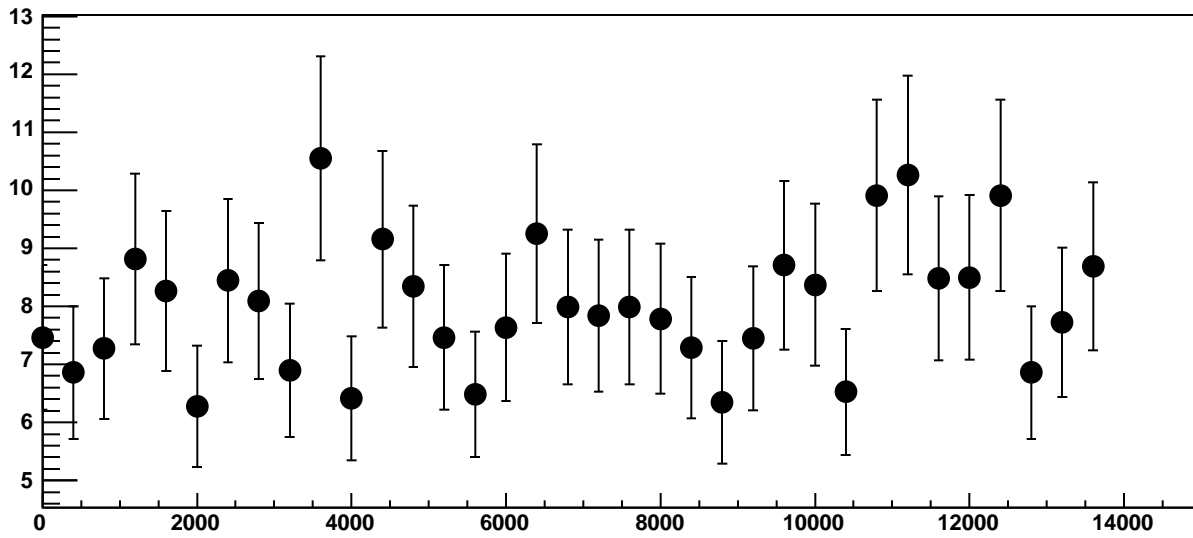


Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

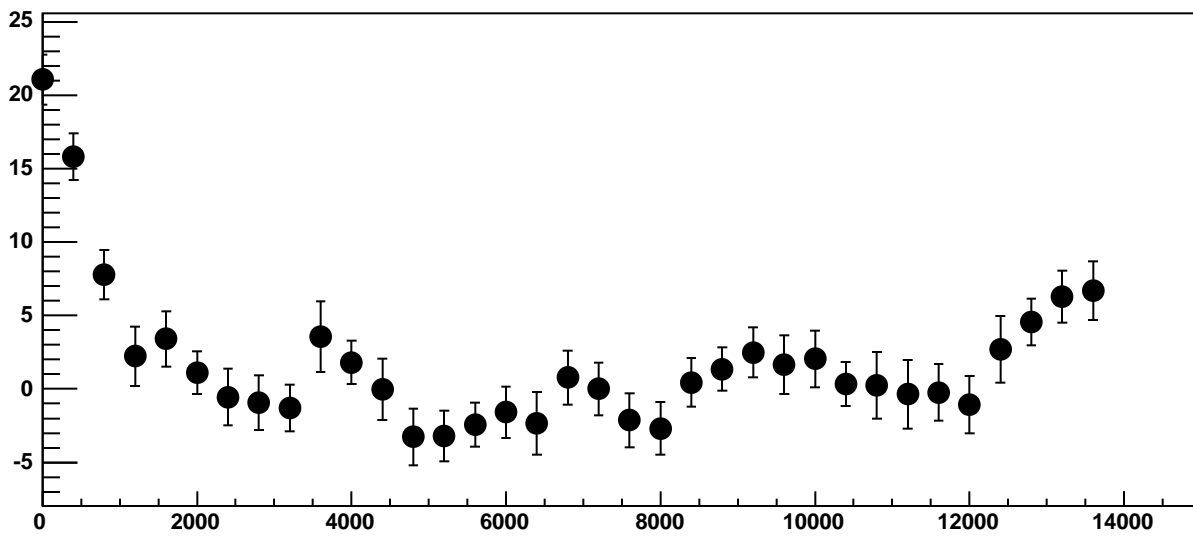


$\chi^2 / \text{ndf}$  29.56 / 23  
p0 53.72 ± 0.8262  
p1 0.01403 ± 0.000124

Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

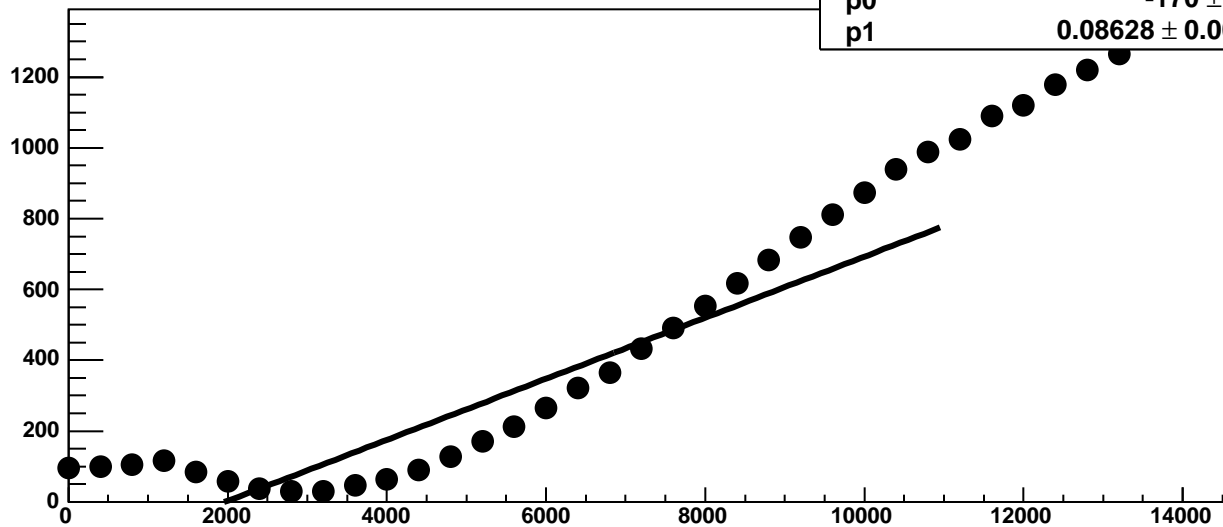


Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

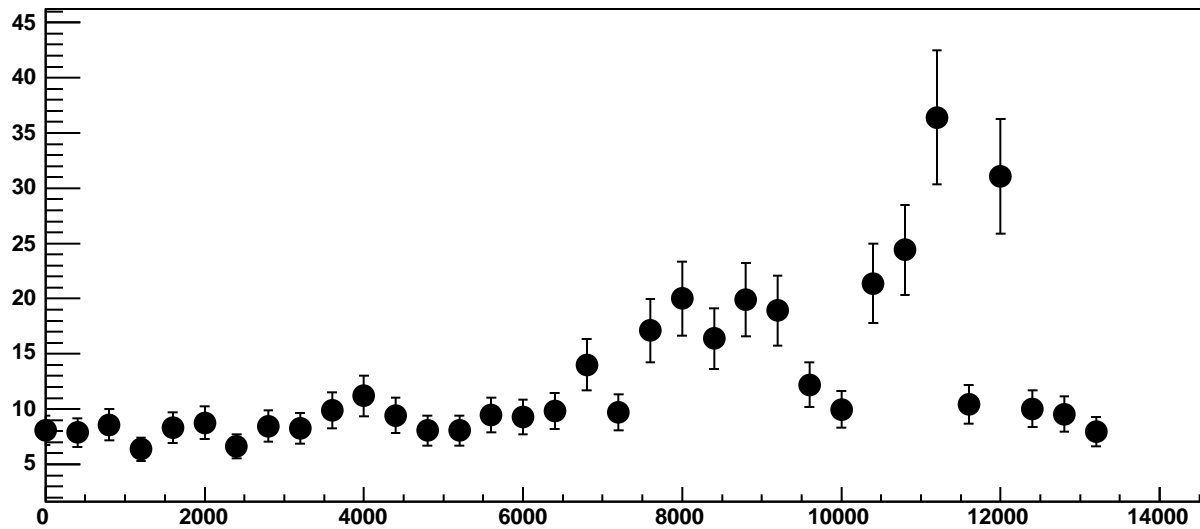


Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

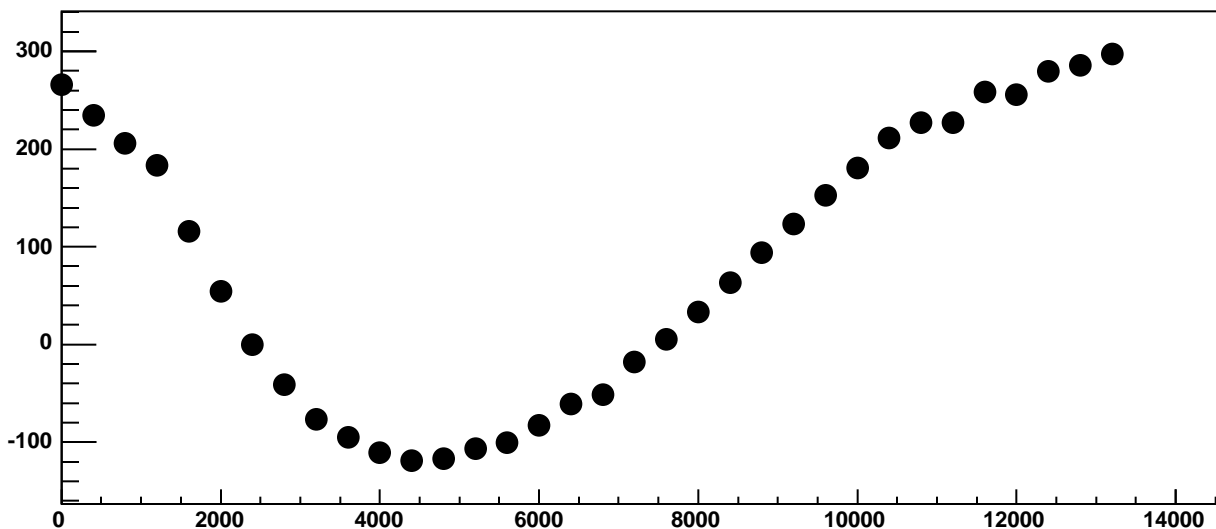
$\chi^2 / \text{ndf}$  5.508e+04 / 23  
p0 -170 ± 0.9284  
p1 0.08628 ± 0.0001766



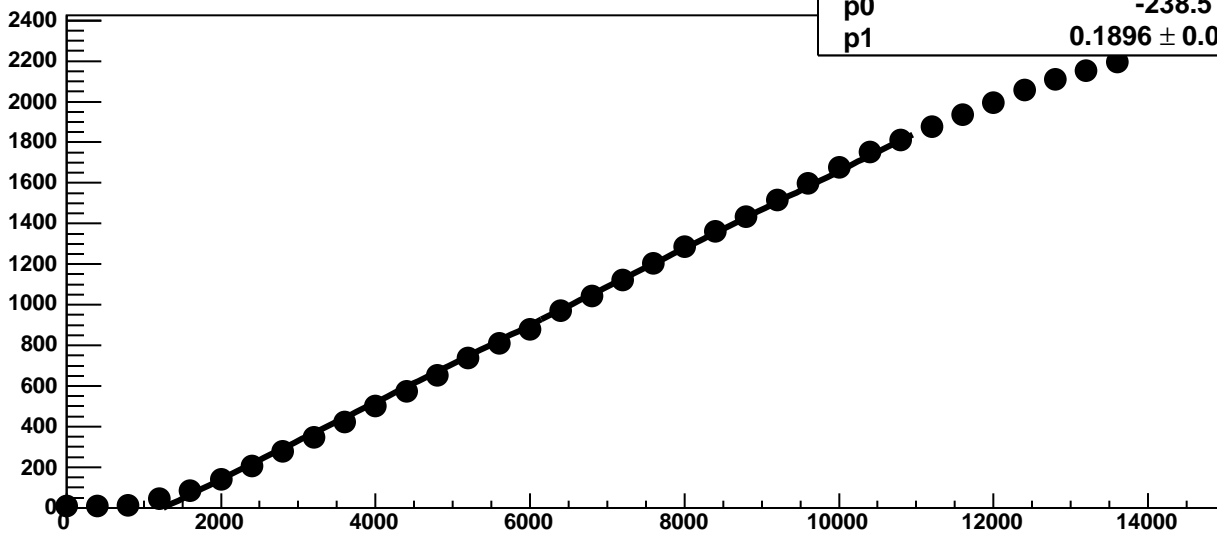
Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

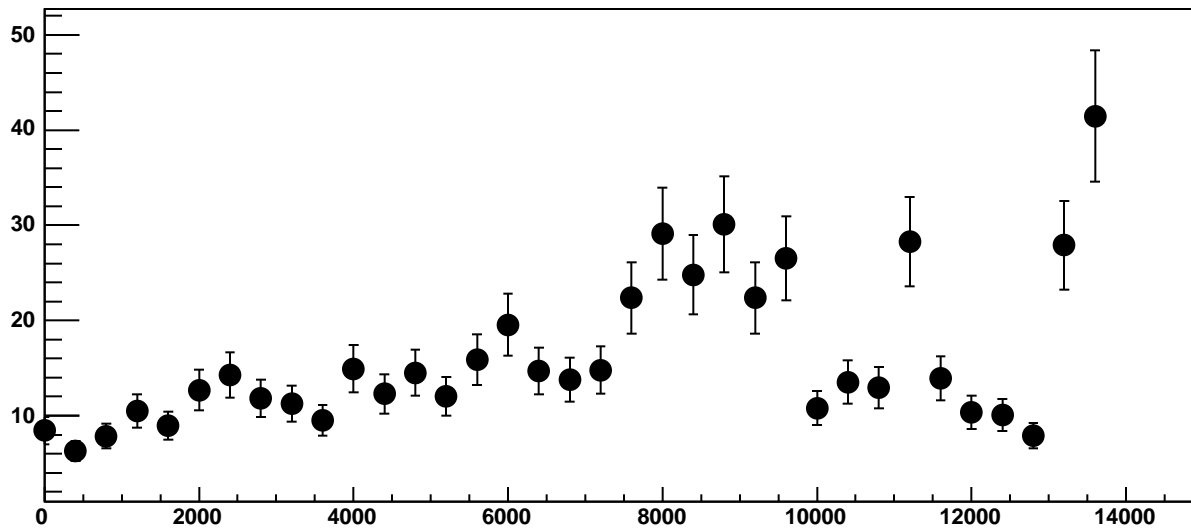


Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

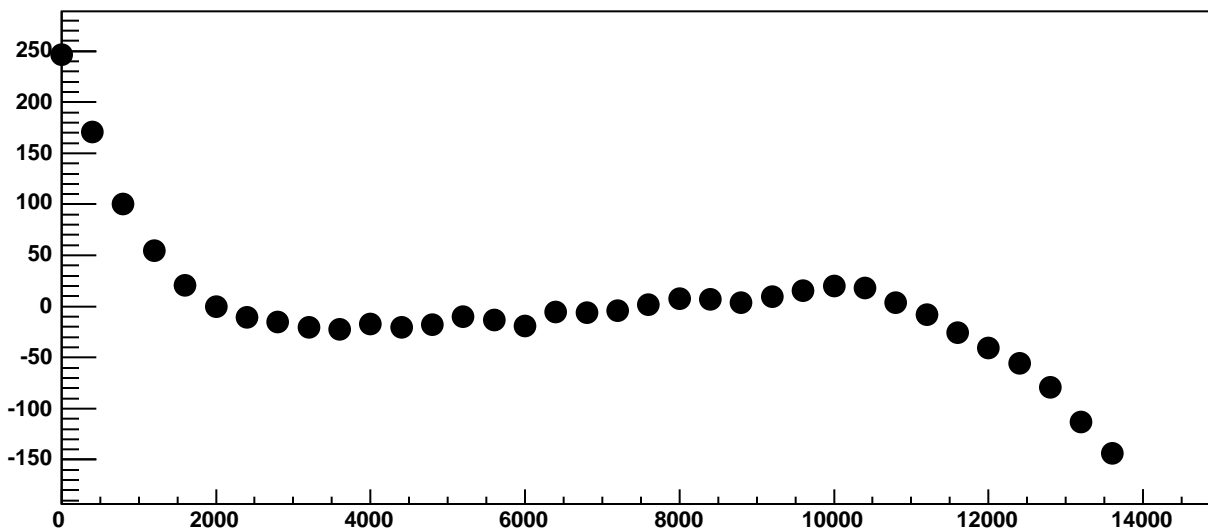


$\chi^2 / \text{ndf}$  1097 / 23  
p0  $-238.5 \pm 1.235$   
p1  $0.1896 \pm 0.0002092$

Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

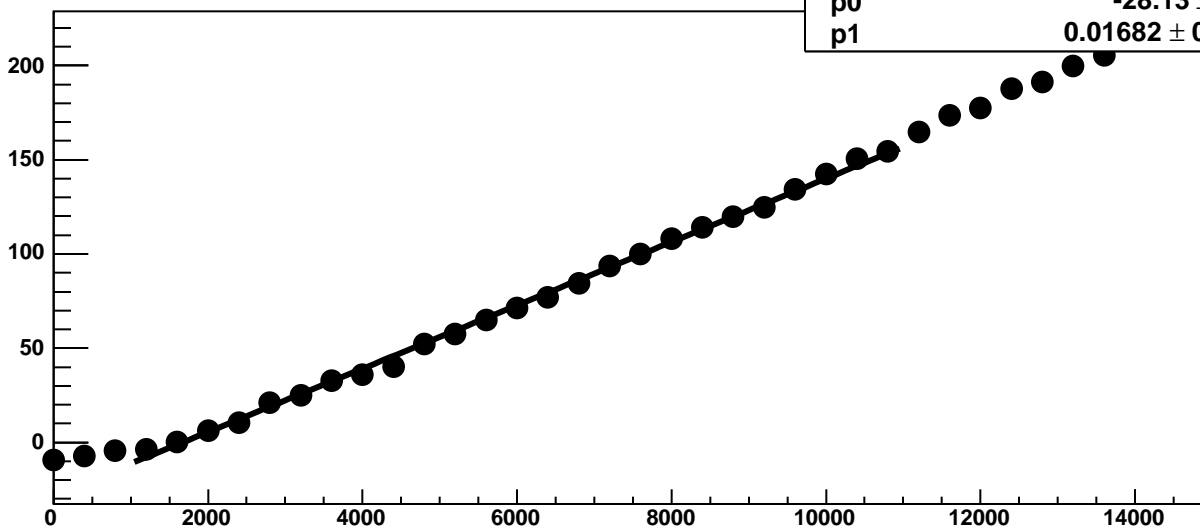


Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



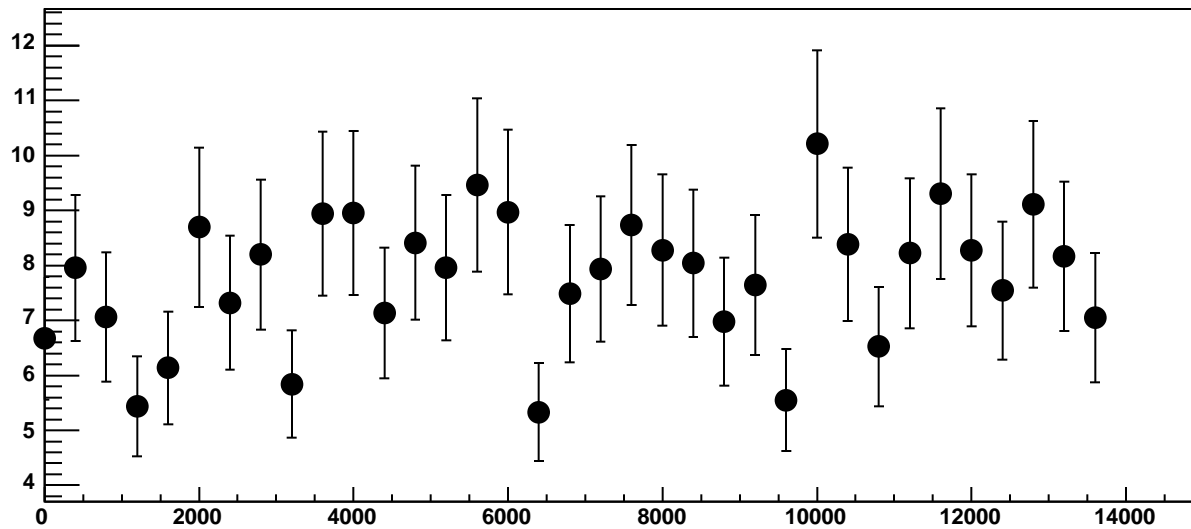


Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

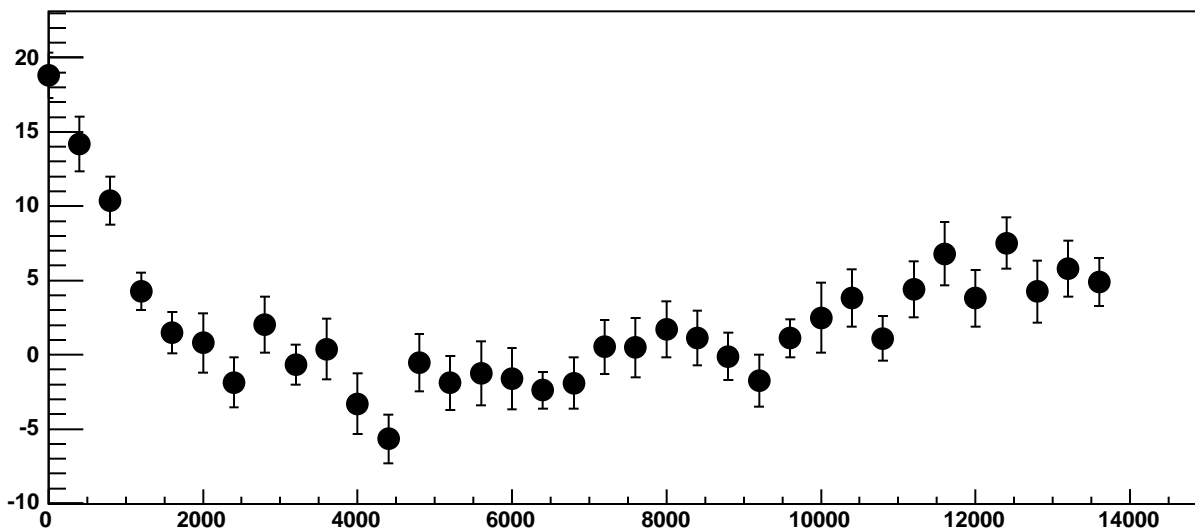


$\chi^2 / \text{ndf}$  45.99 / 23  
p0  $-28.13 \pm 0.7311$   
p1  $0.01682 \pm 0.000111$

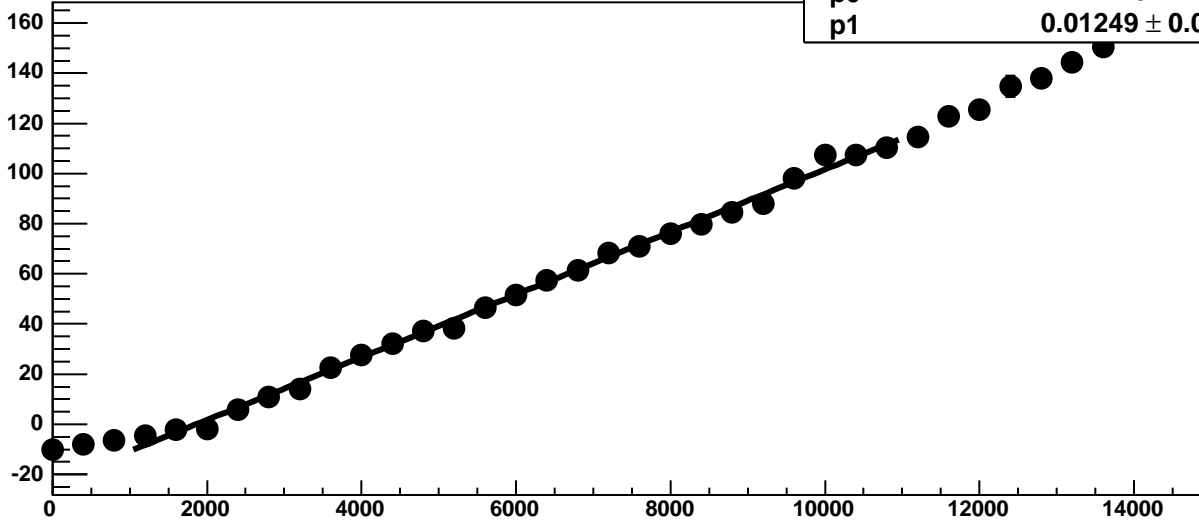
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



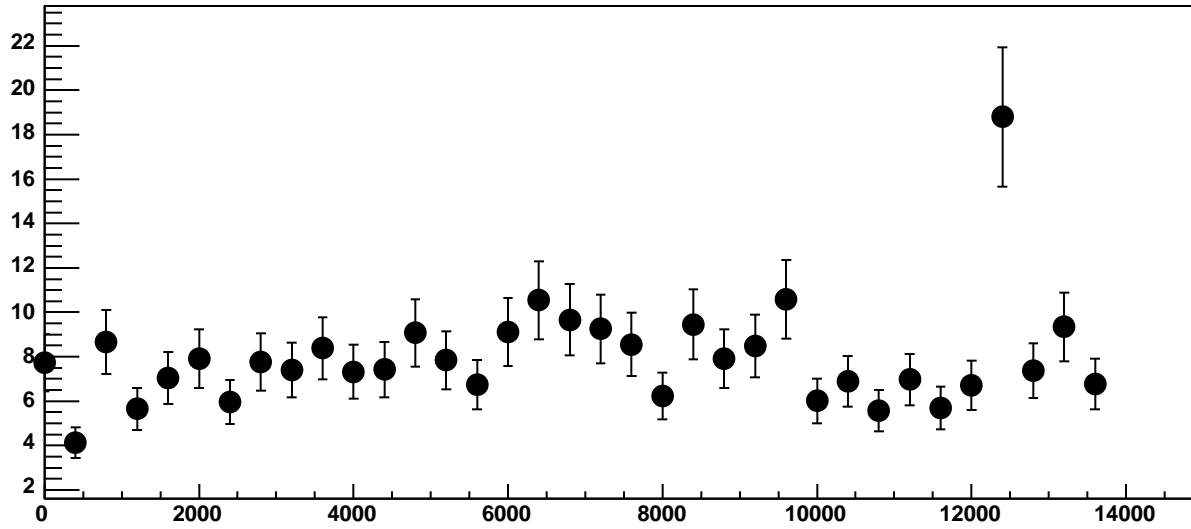
Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



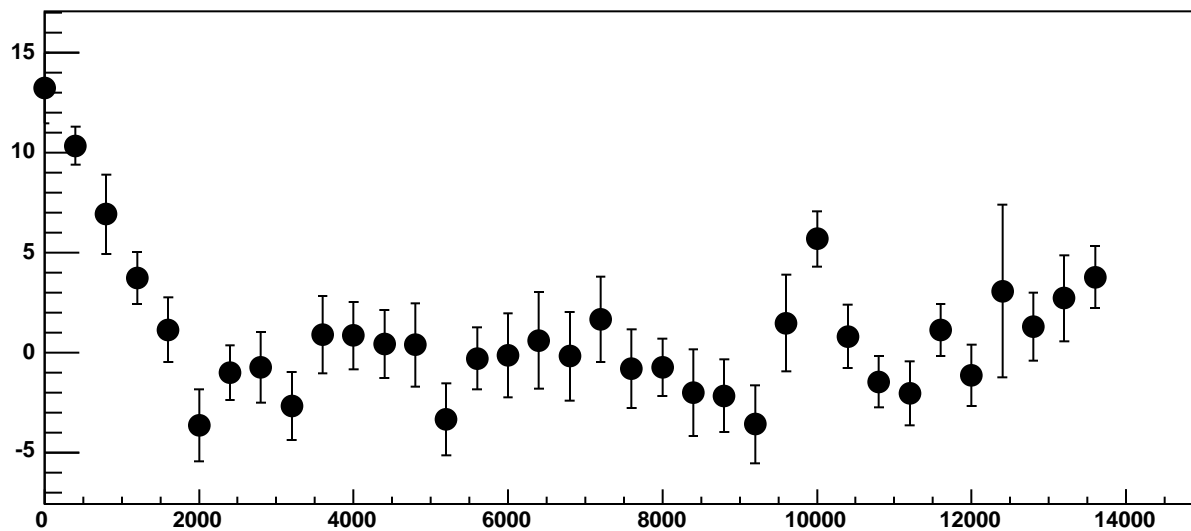
Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



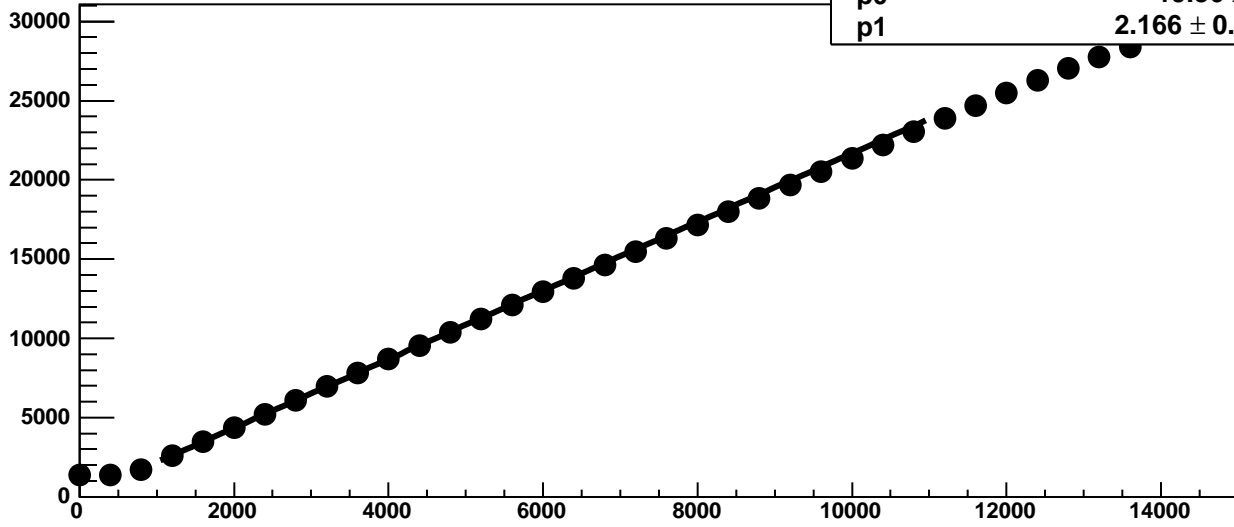
Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

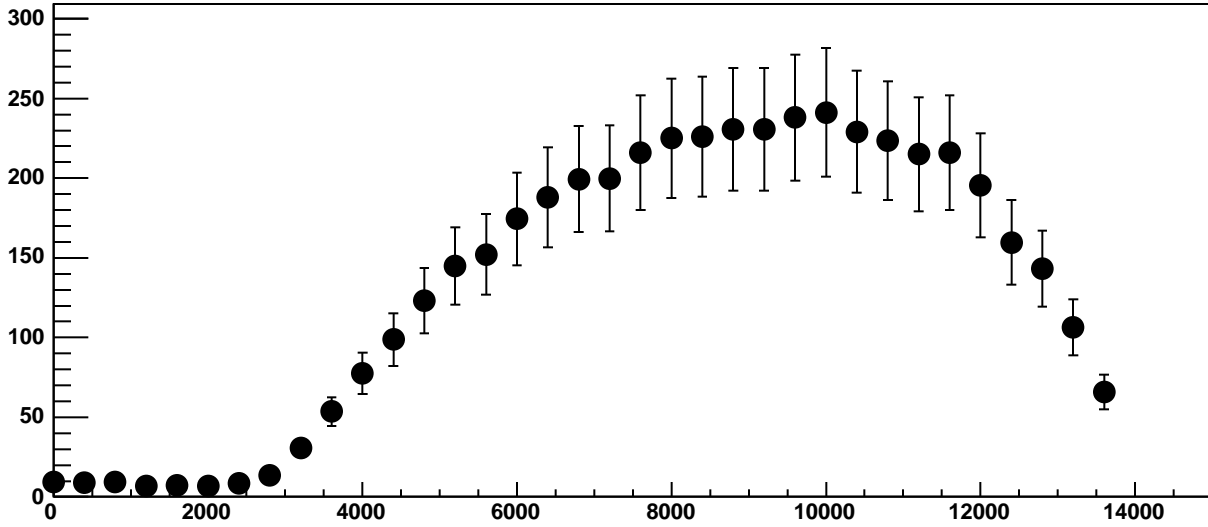


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

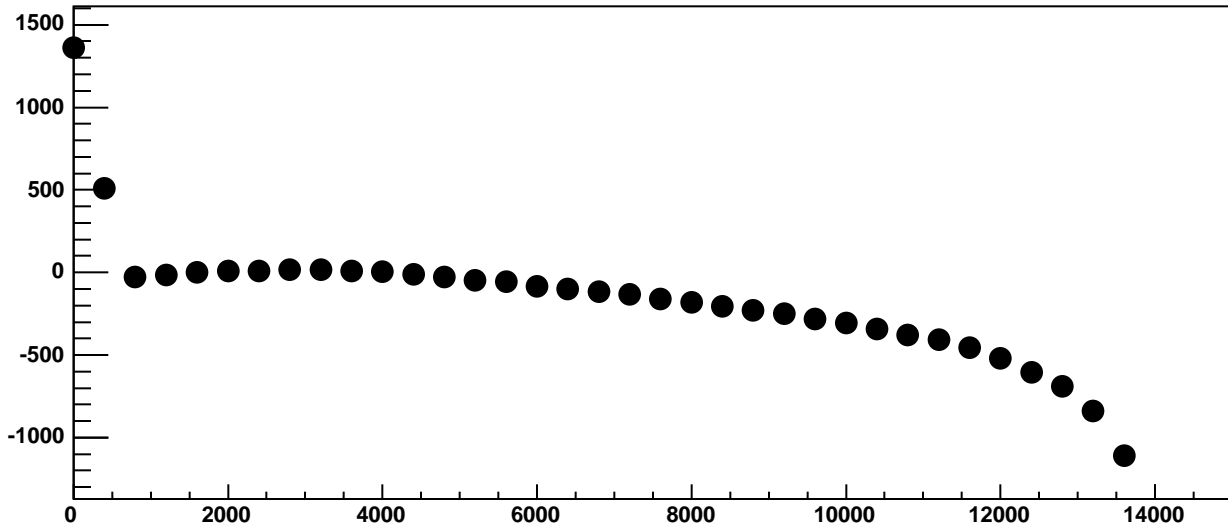


$\chi^2 / \text{ndf}$  435.8 / 23  
p0  $19.36 \pm 2.376$   
p1  $2.166 \pm 0.001187$

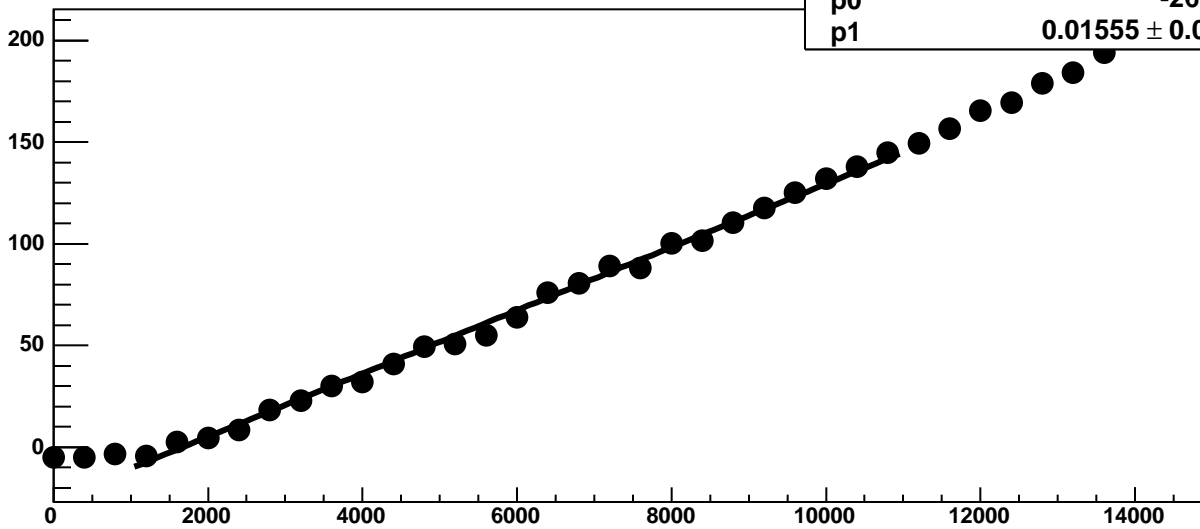
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



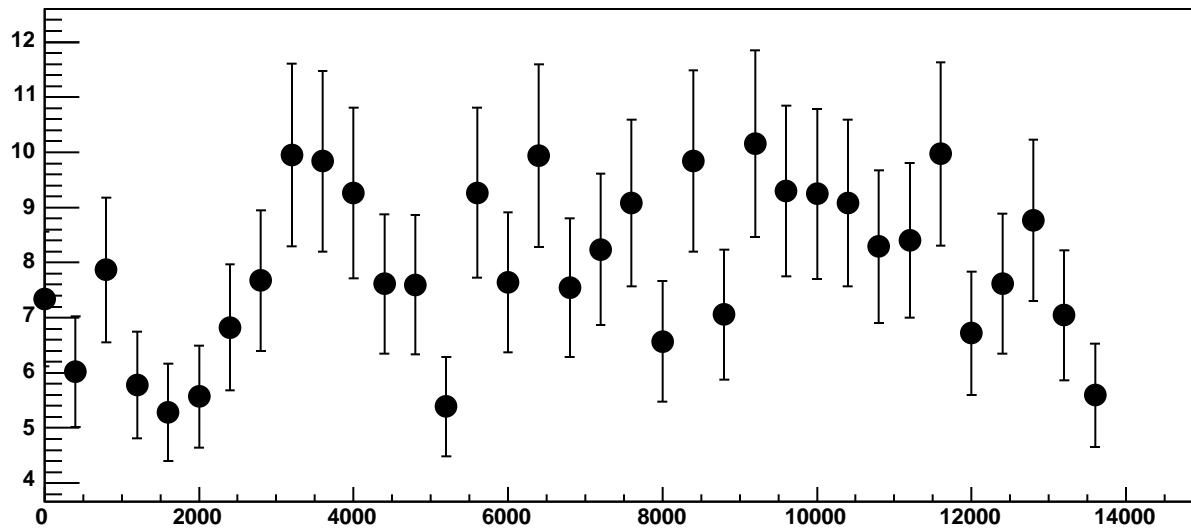
Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



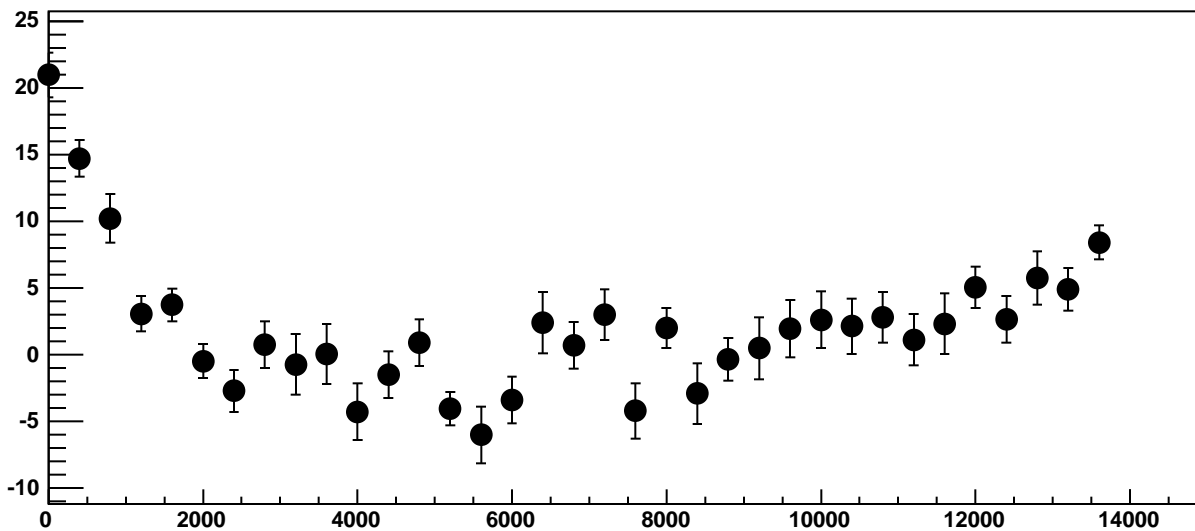
Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



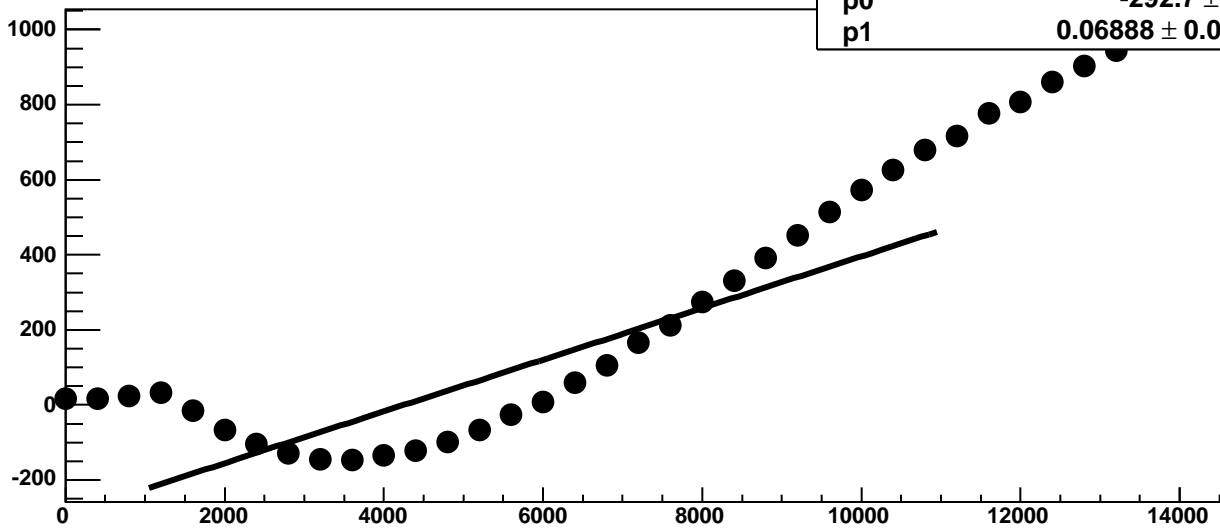
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

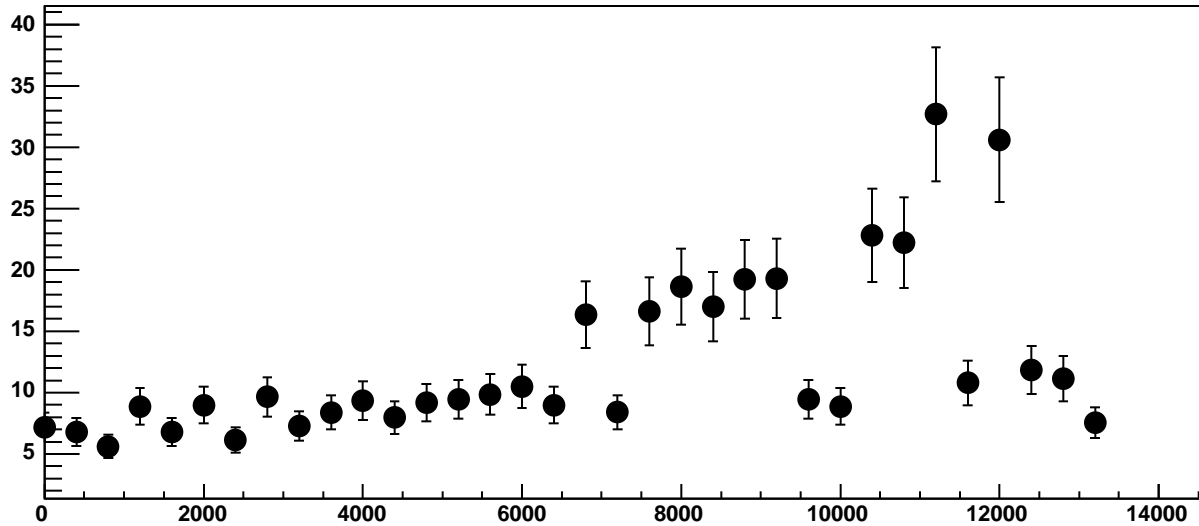


Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

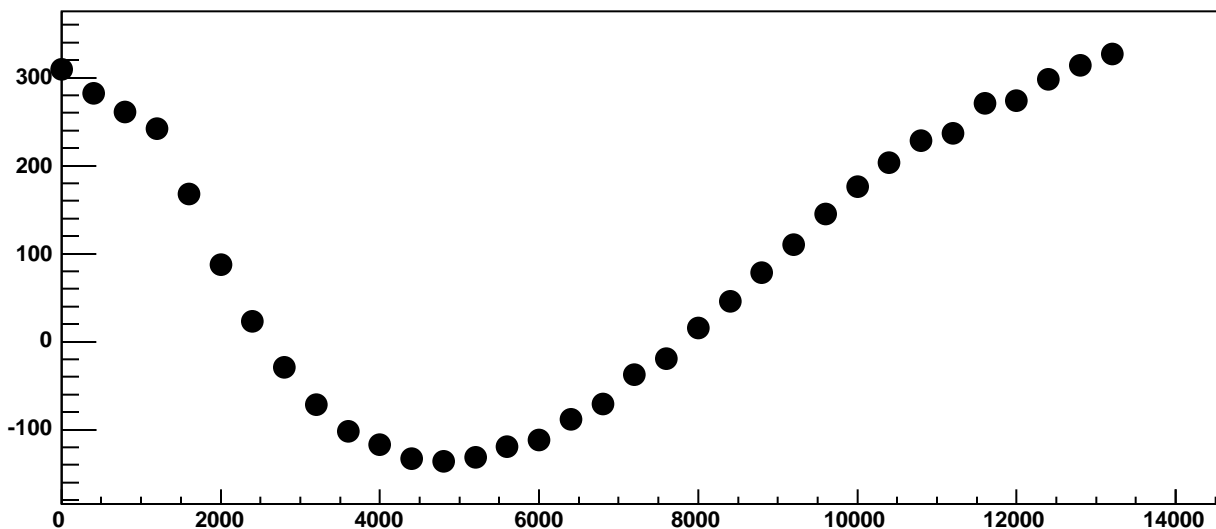


$\chi^2 / \text{ndf}$  7.295e+04 / 23  
p0 -292.7 ± 0.9195  
p1 0.06888 ± 0.0001691

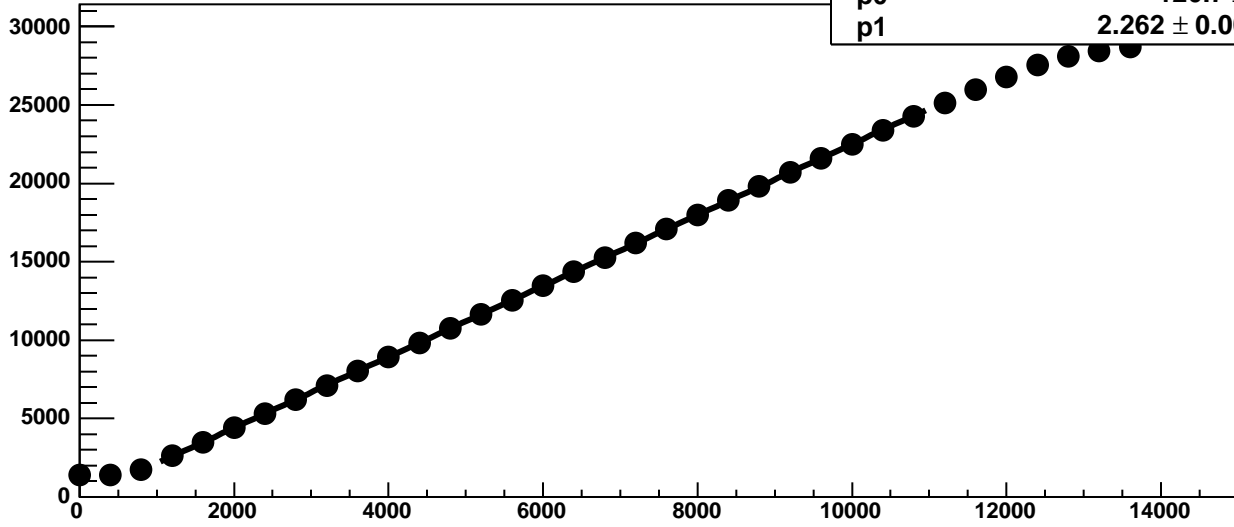
Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



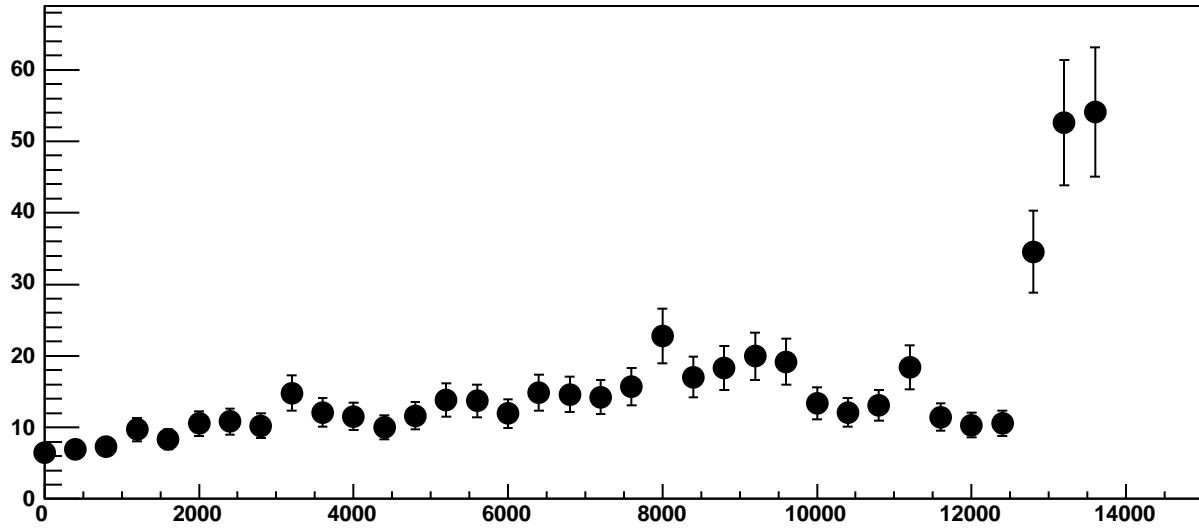
Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



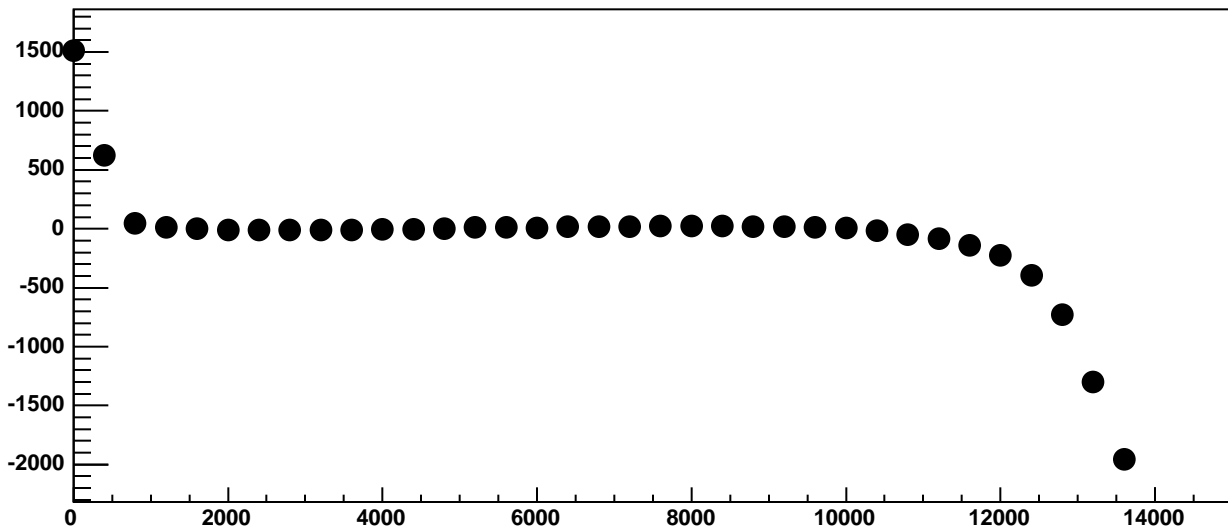
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



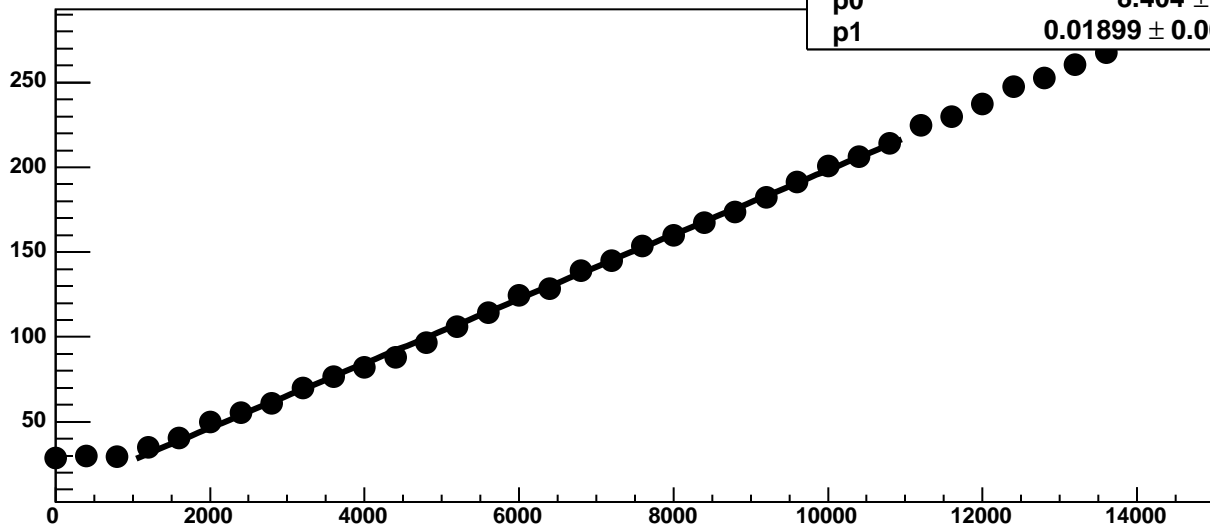
Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

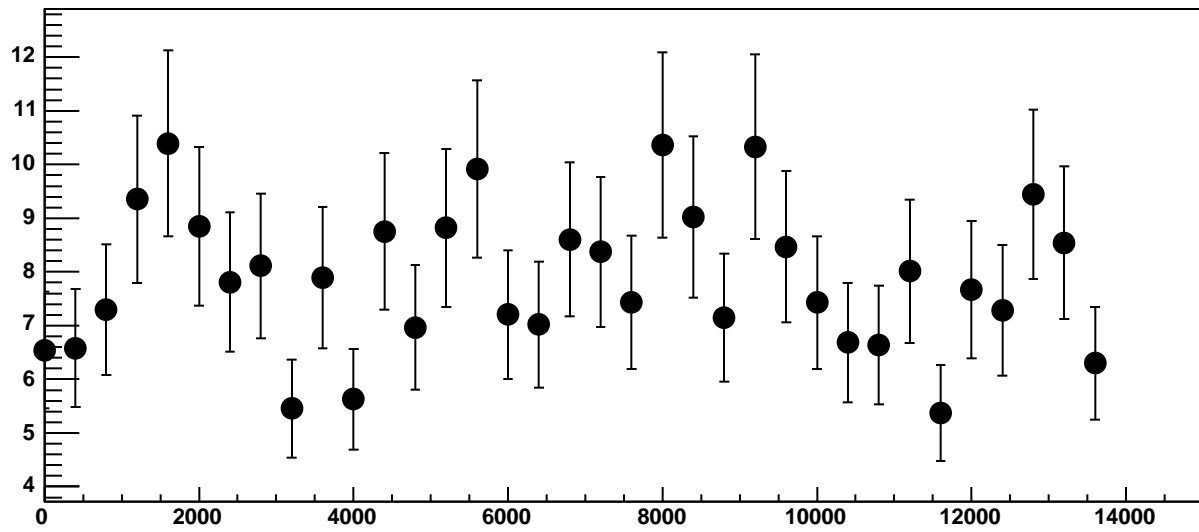


Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

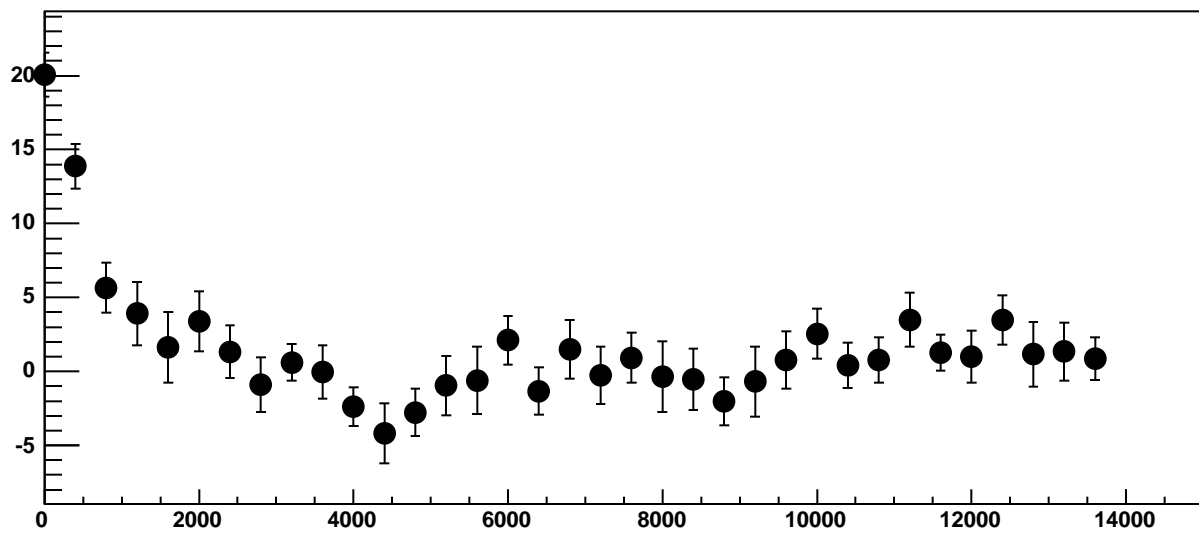


$\chi^2 / \text{ndf}$  26.31 / 23  
p0  $8.404 \pm 0.8285$   
p1  $0.01899 \pm 0.0001238$

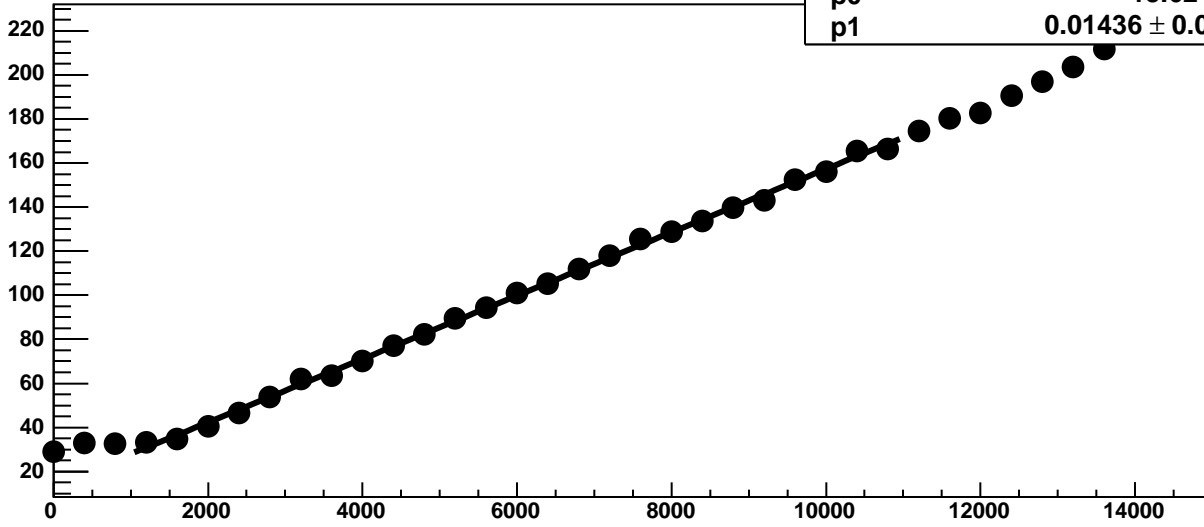
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



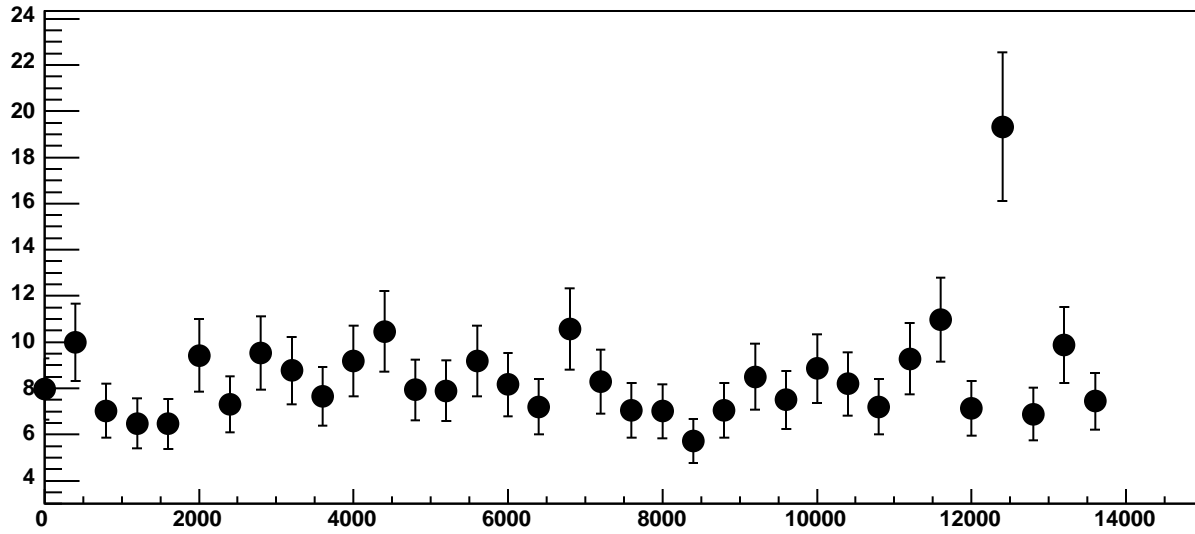
Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



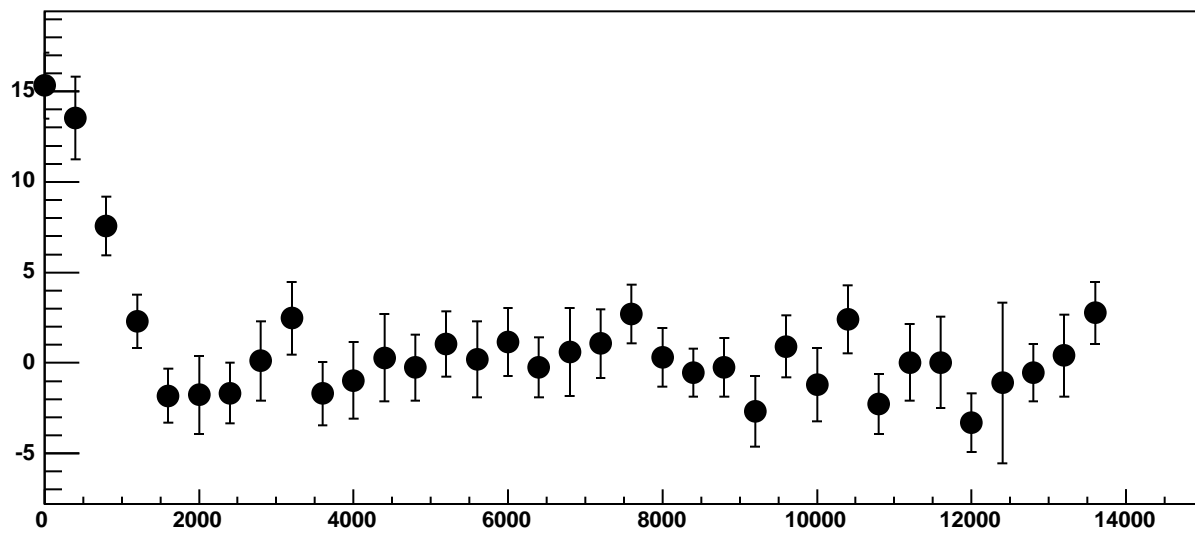
Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

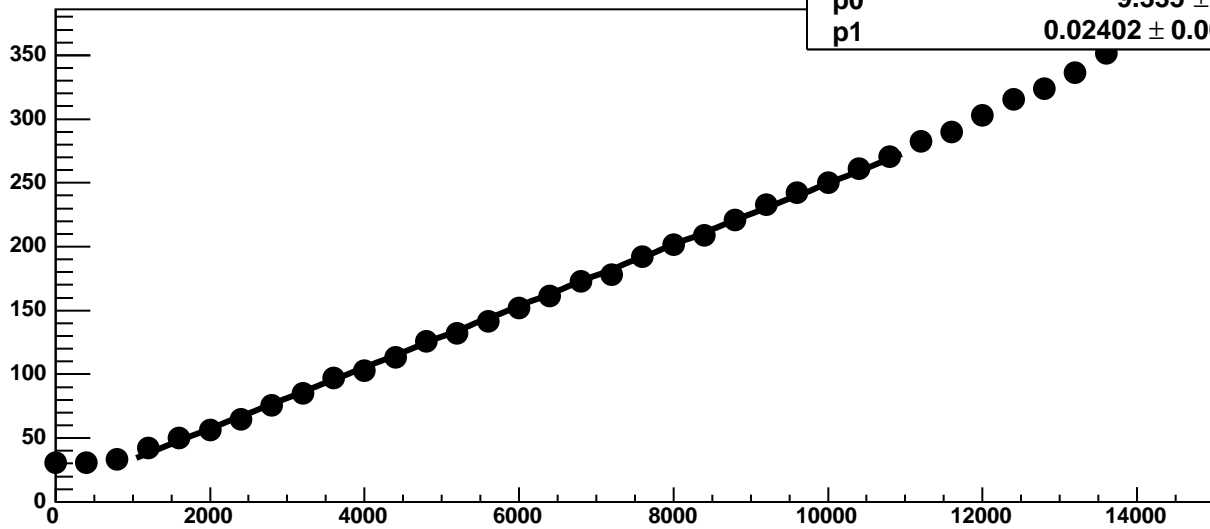


Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



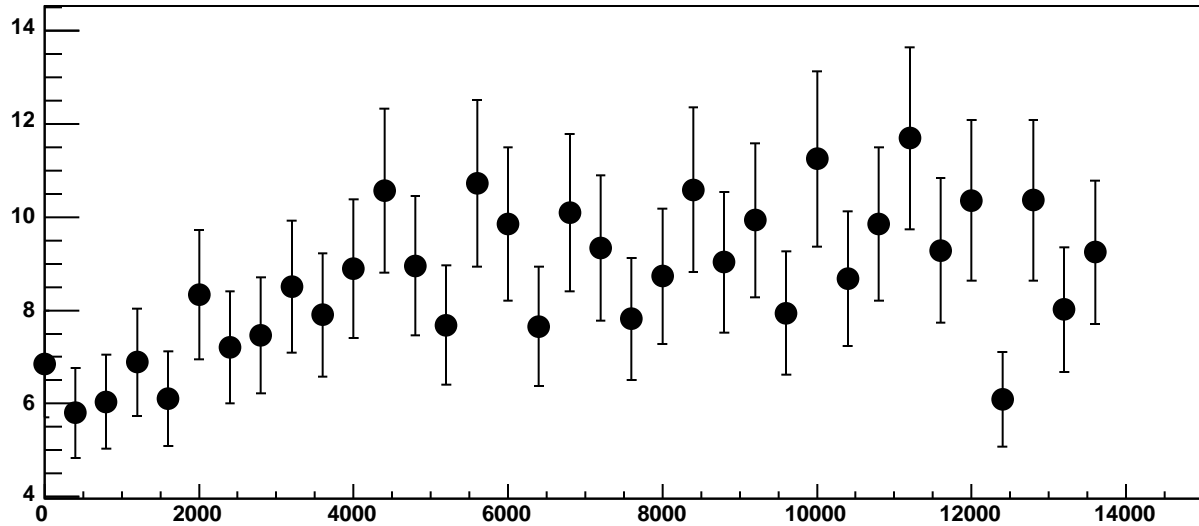


Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

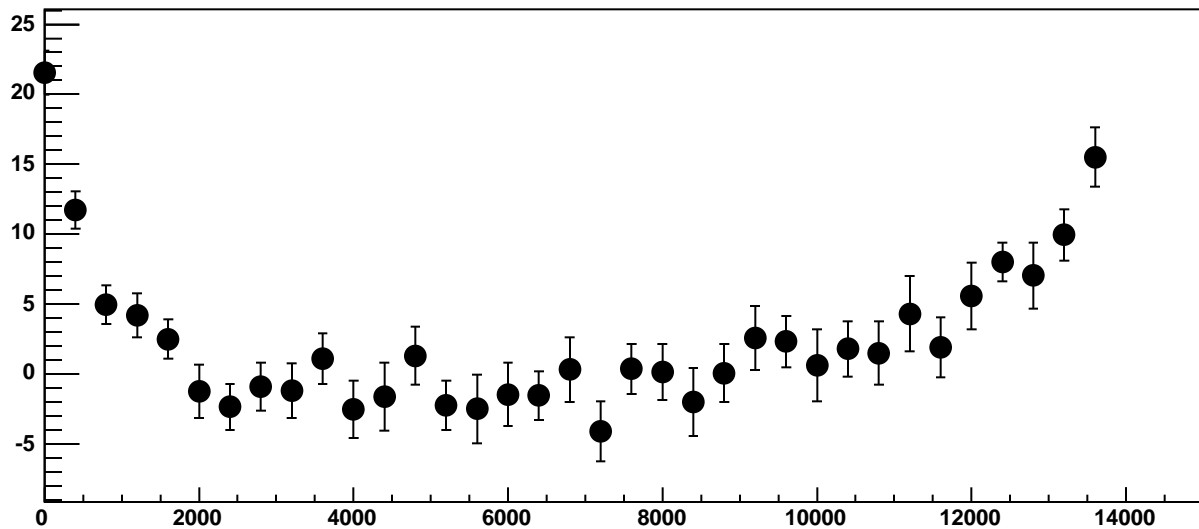


$\chi^2 / \text{ndf}$  28.5 / 23  
p0  $9.335 \pm 0.8145$   
p1  $0.02402 \pm 0.0001309$

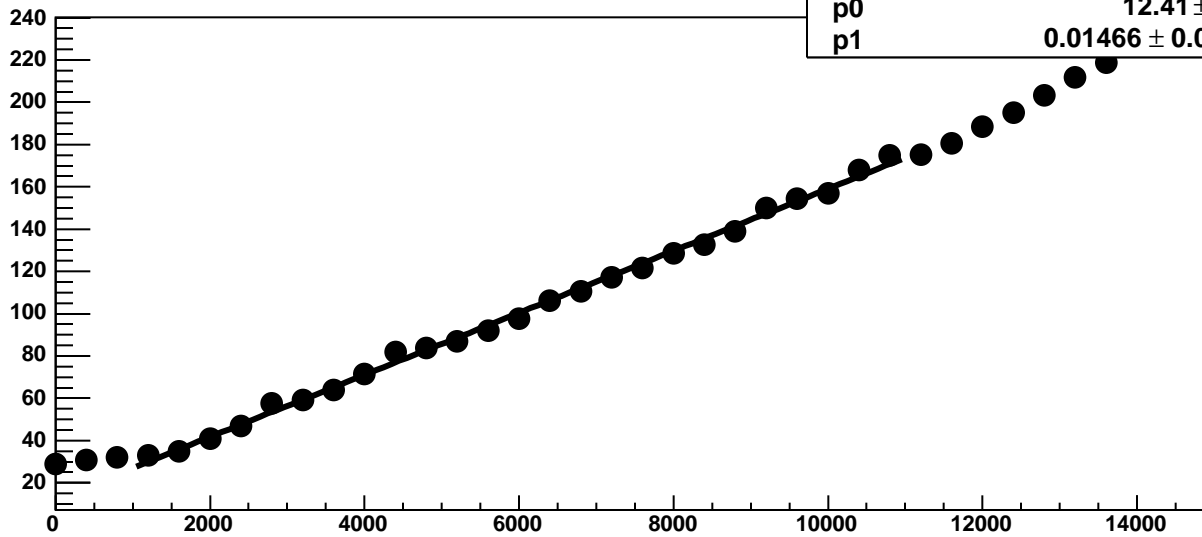
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



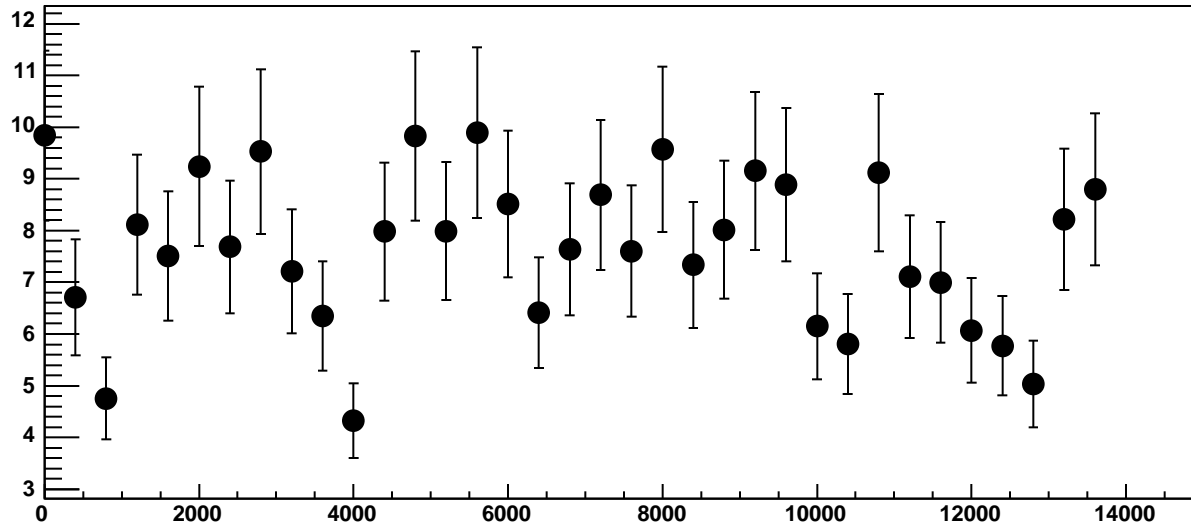
Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



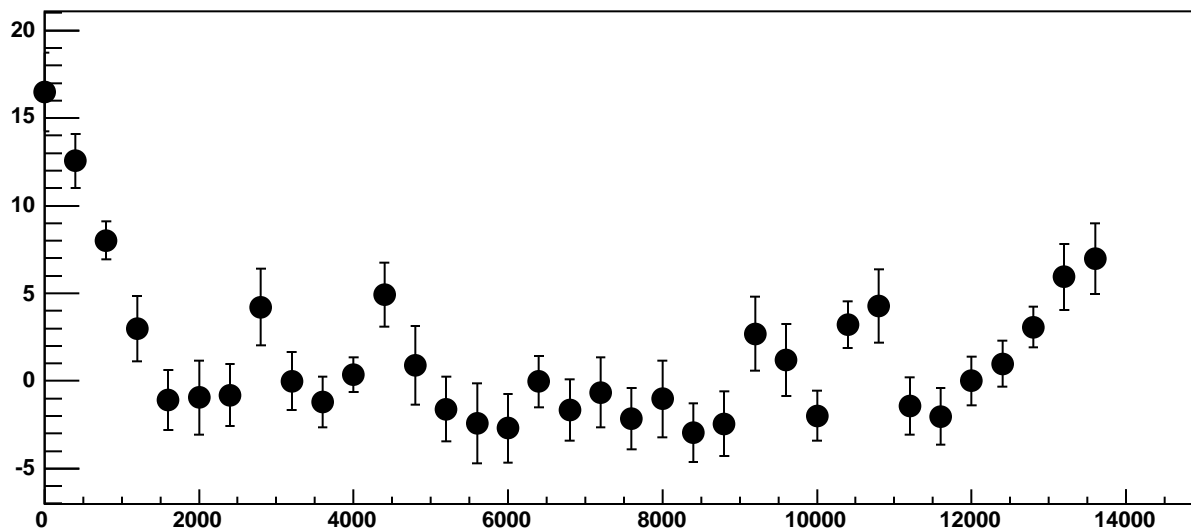
Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



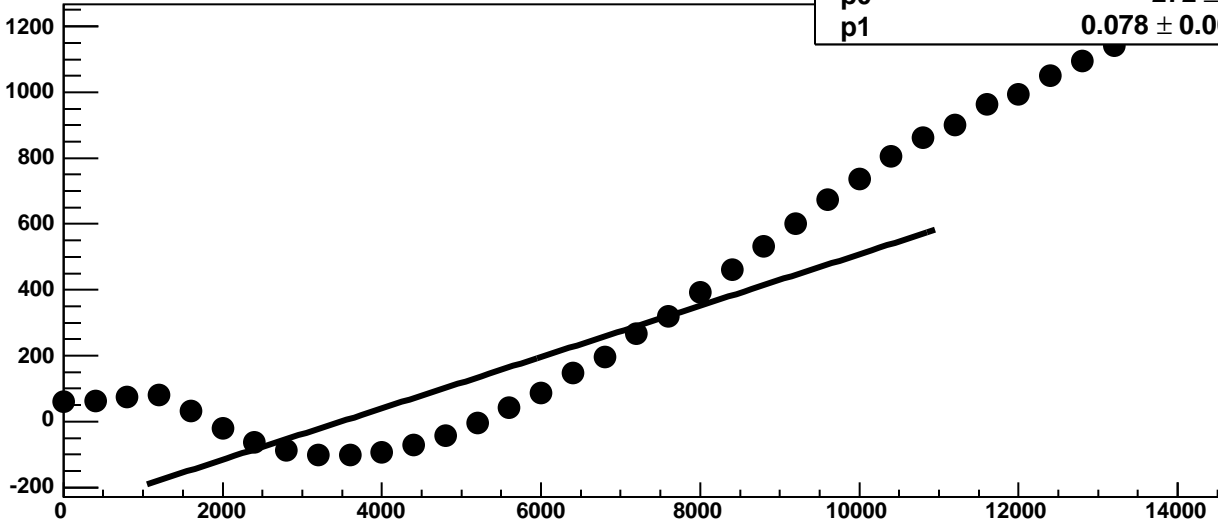
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



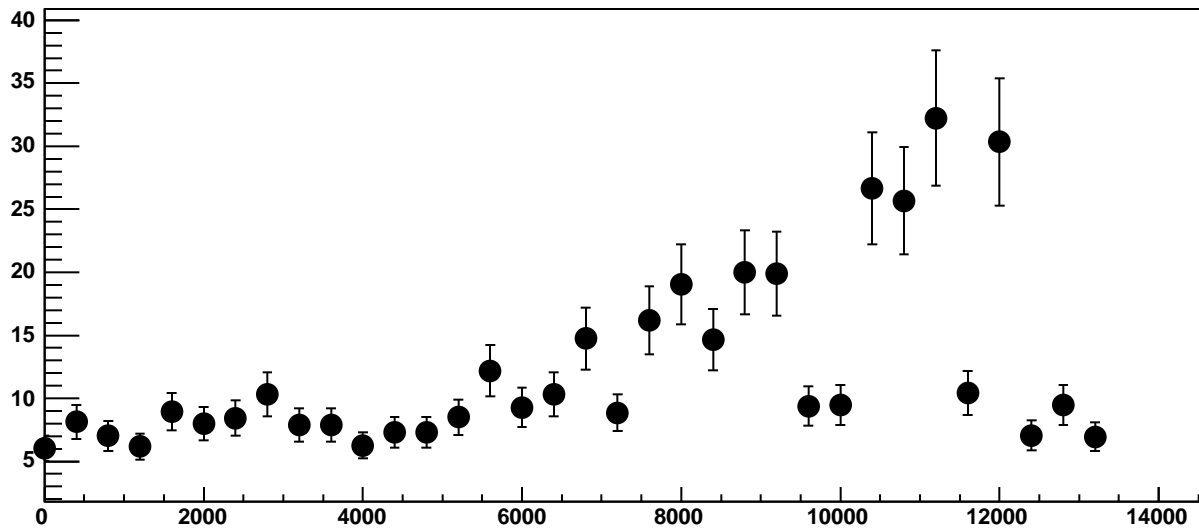
Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



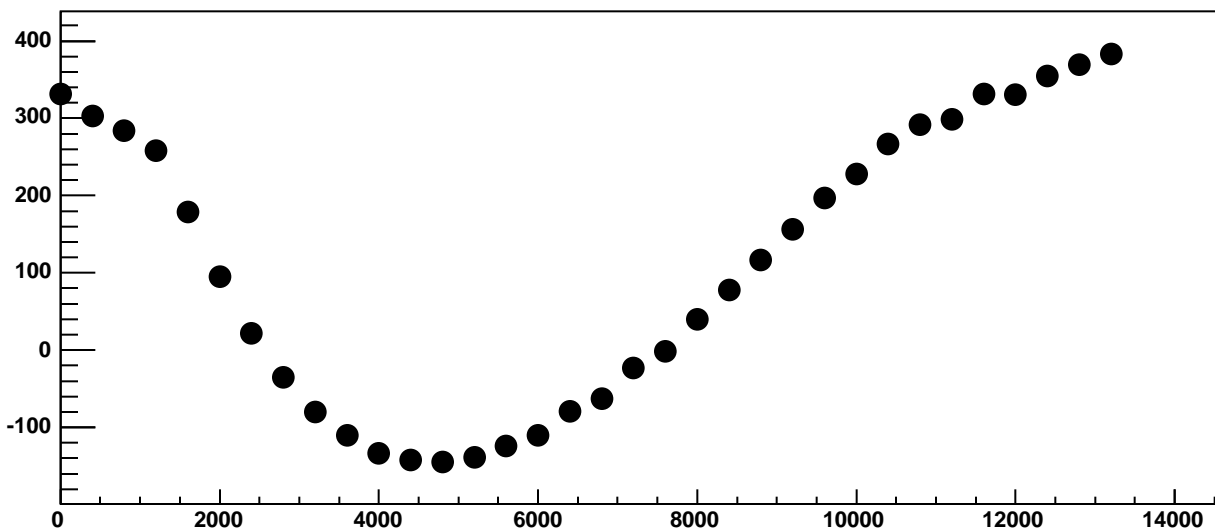
Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



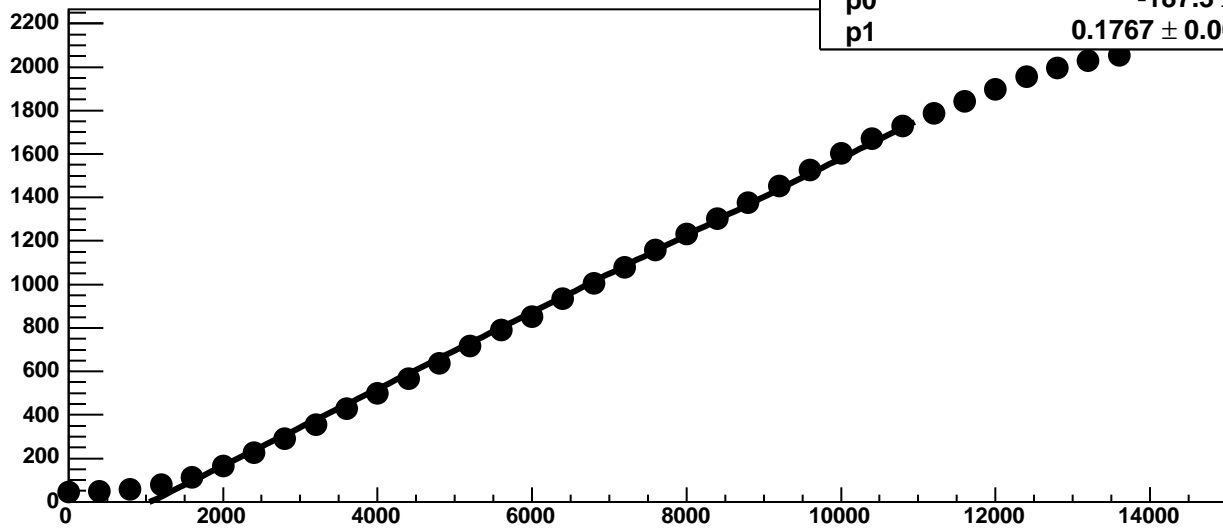
Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



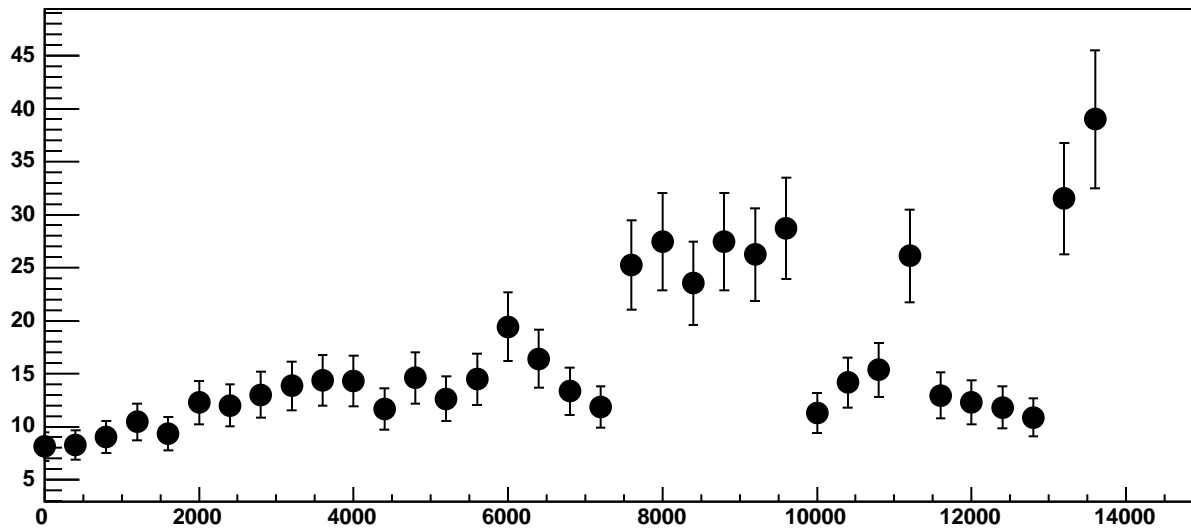
Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



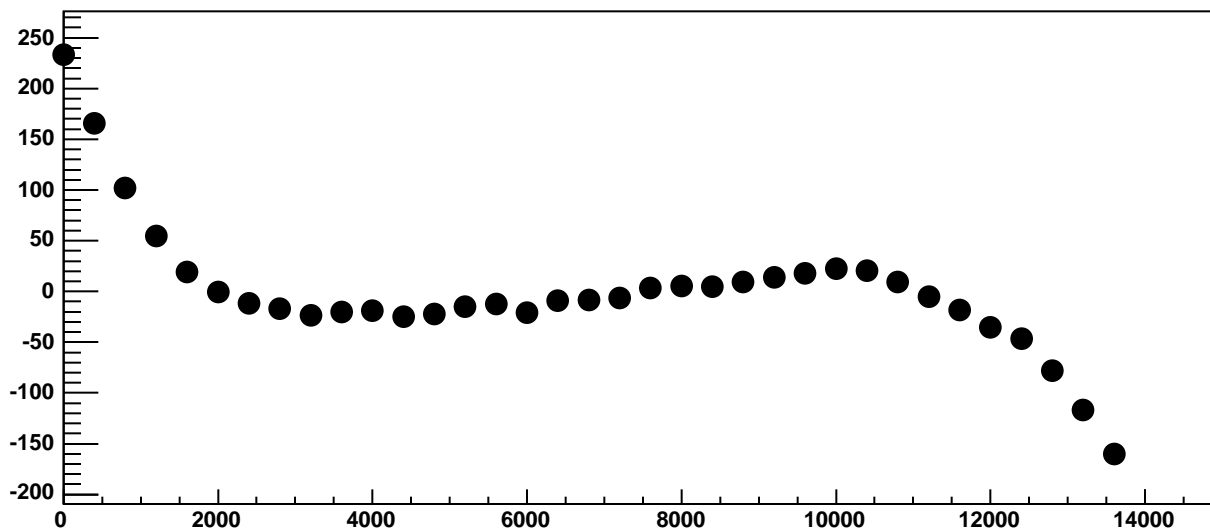
Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



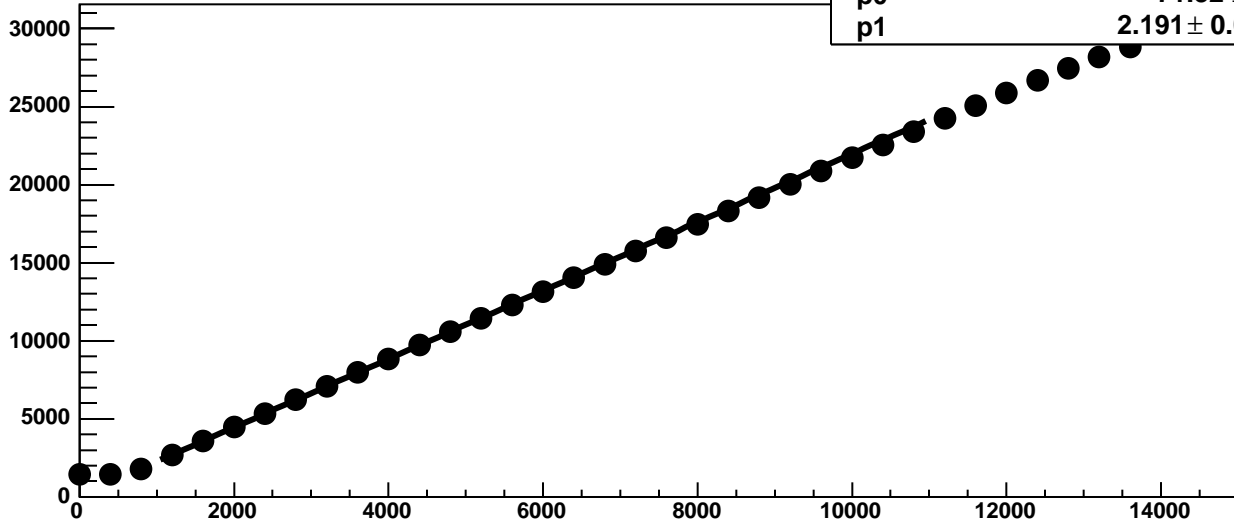
Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

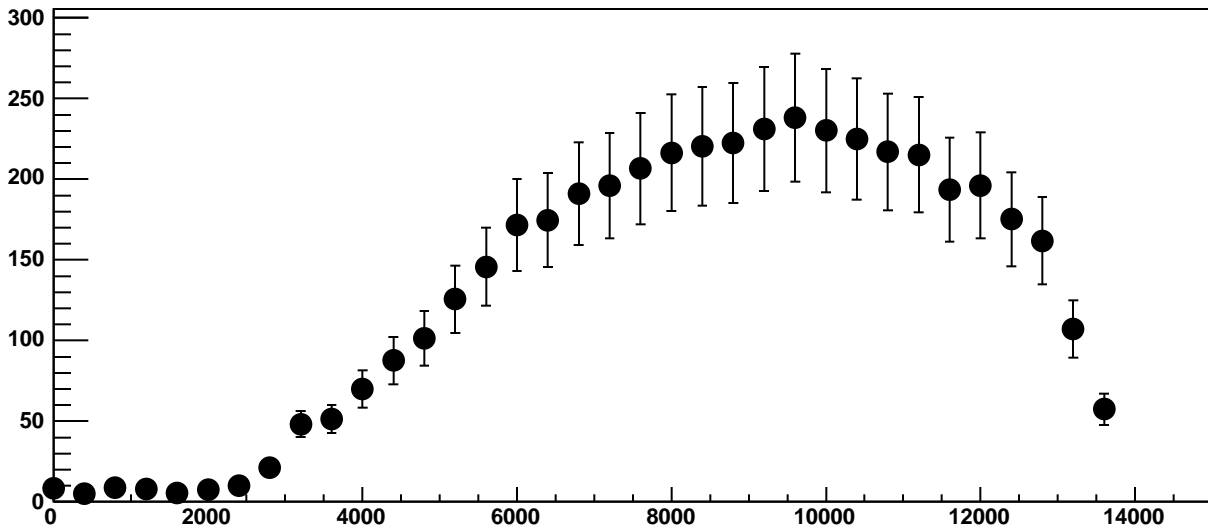


Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

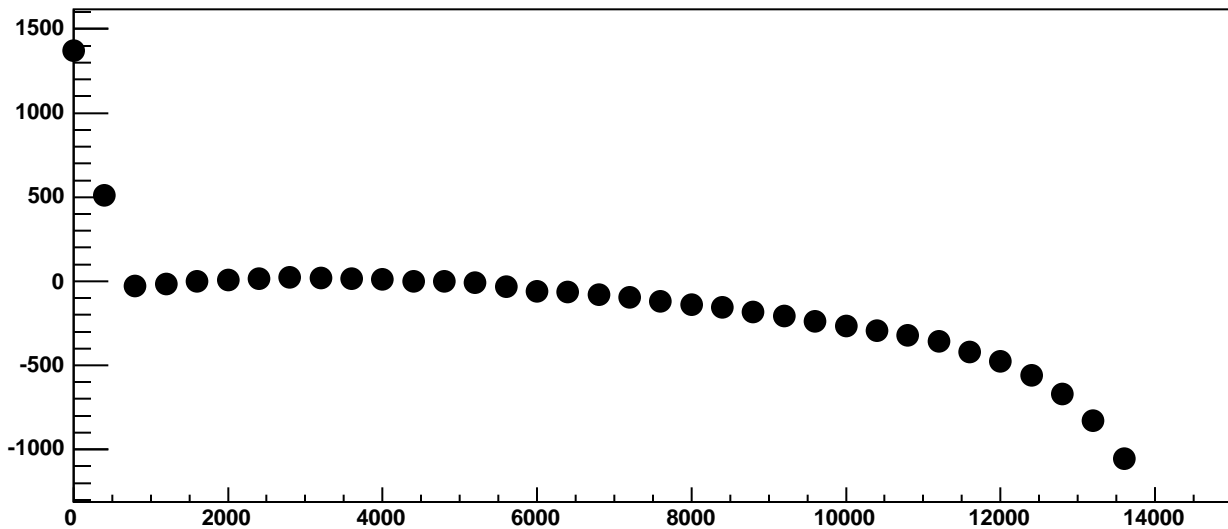


$\chi^2 / \text{ndf}$  346.9 / 23  
p0  $71.32 \pm 2.468$   
p1  $2.191 \pm 0.001282$

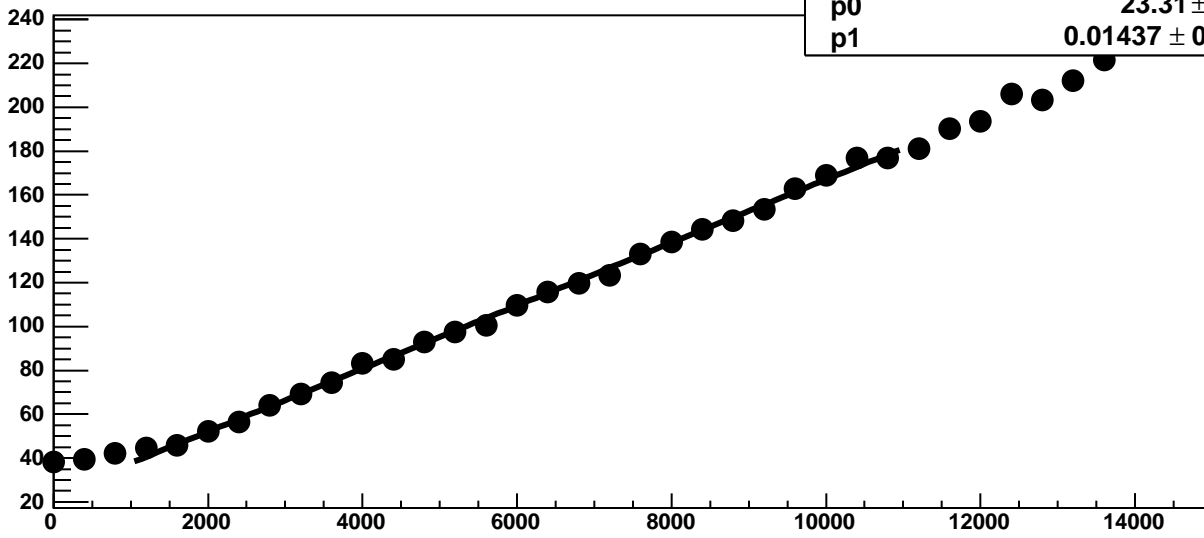
Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

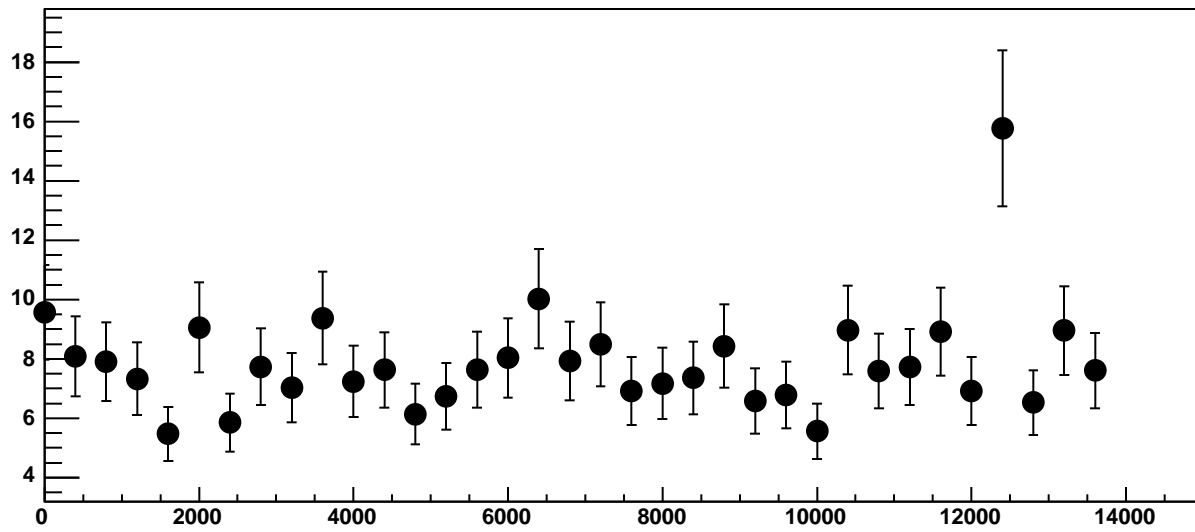


Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

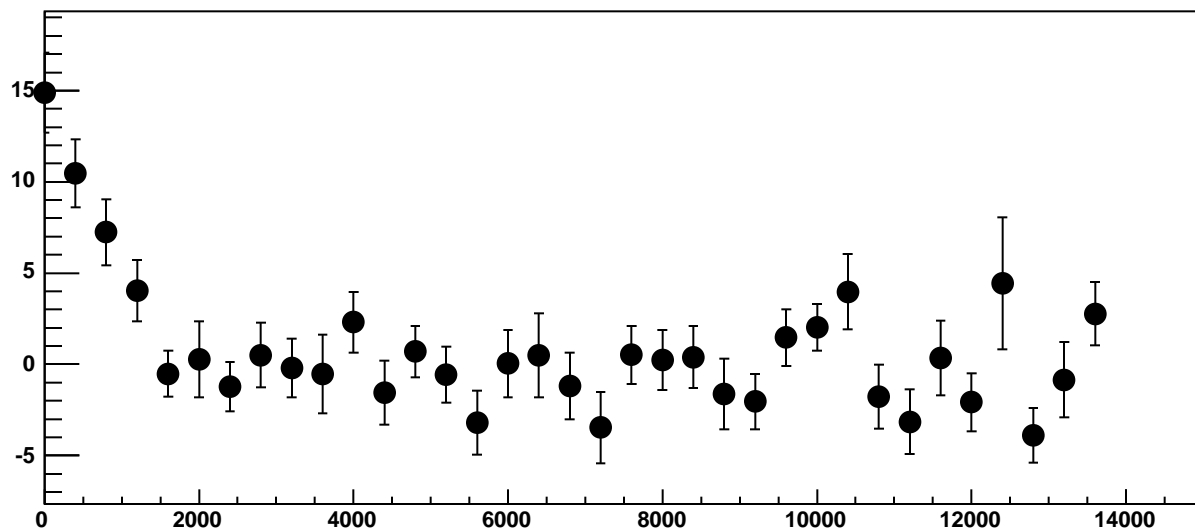


$\chi^2 / \text{ndf}$  27.9 / 23  
p0  $23.31 \pm 0.7355$   
p1  $0.01437 \pm 0.000111$

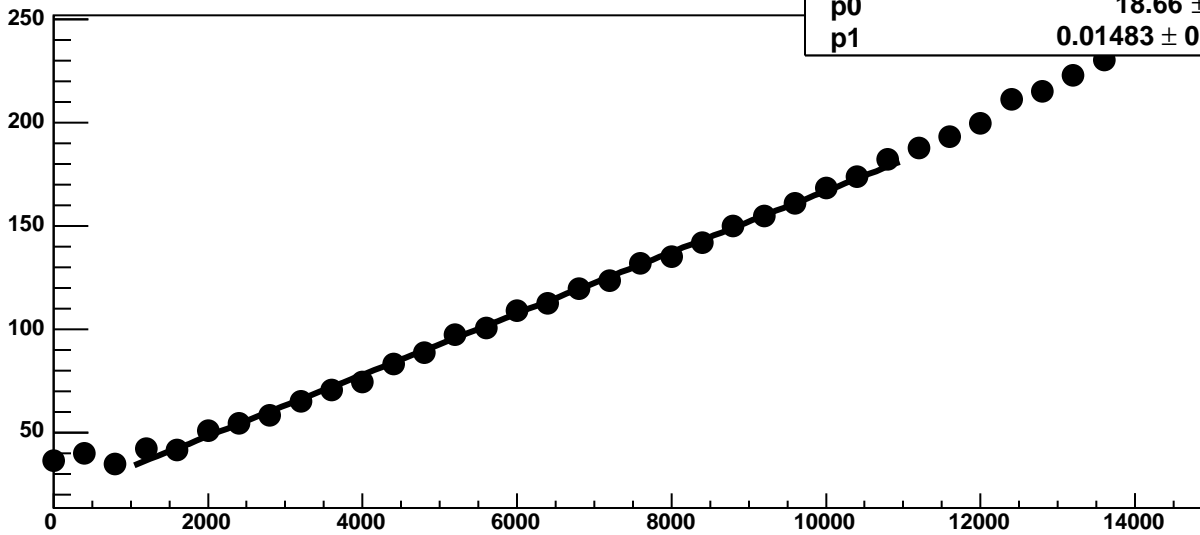
Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

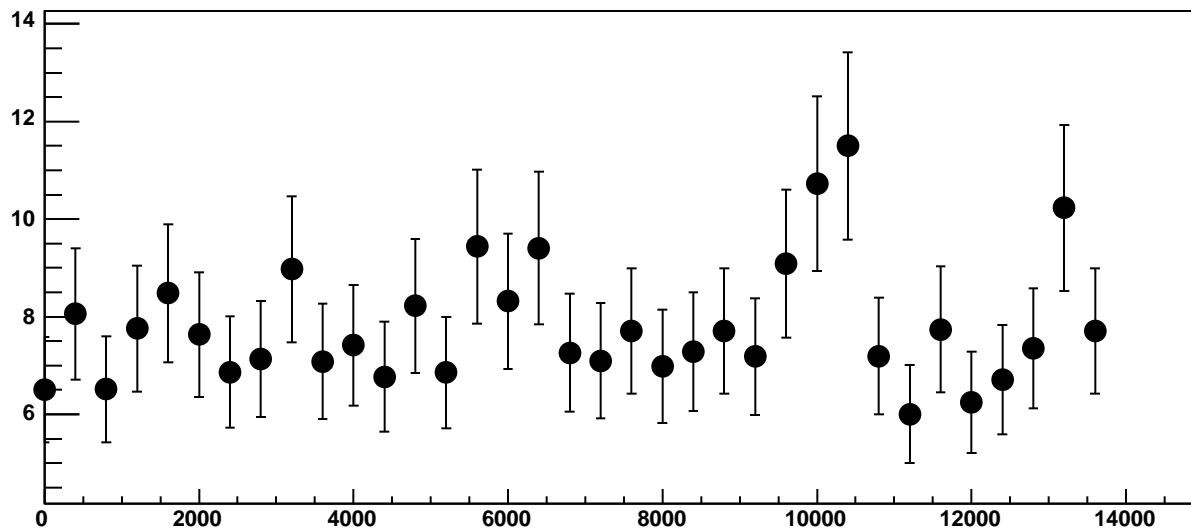


Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

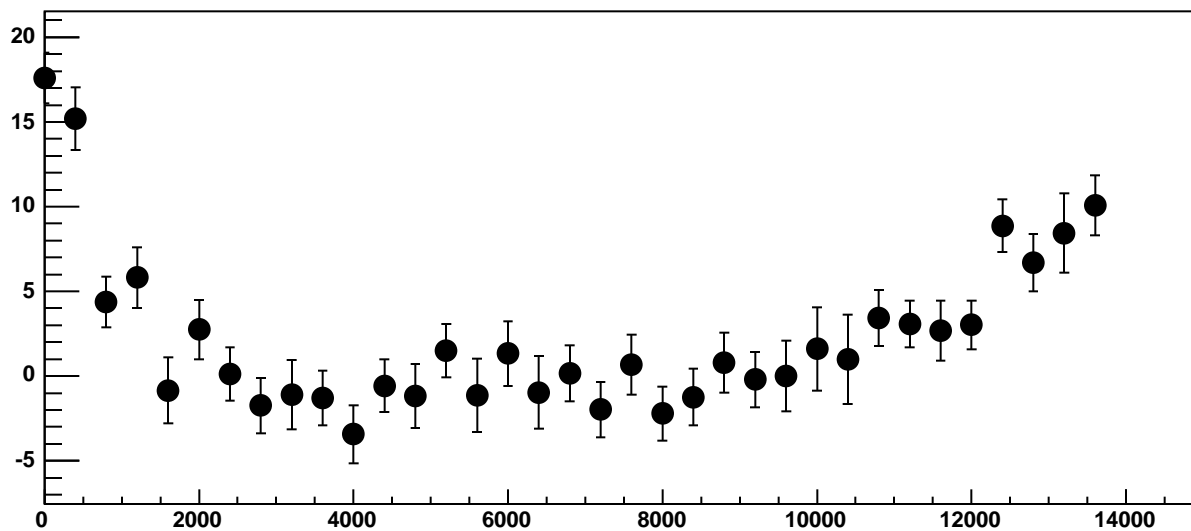


$\chi^2 / \text{ndf}$  31.09 / 23  
p0  $18.66 \pm 0.8217$   
p1  $0.01483 \pm 0.000127$

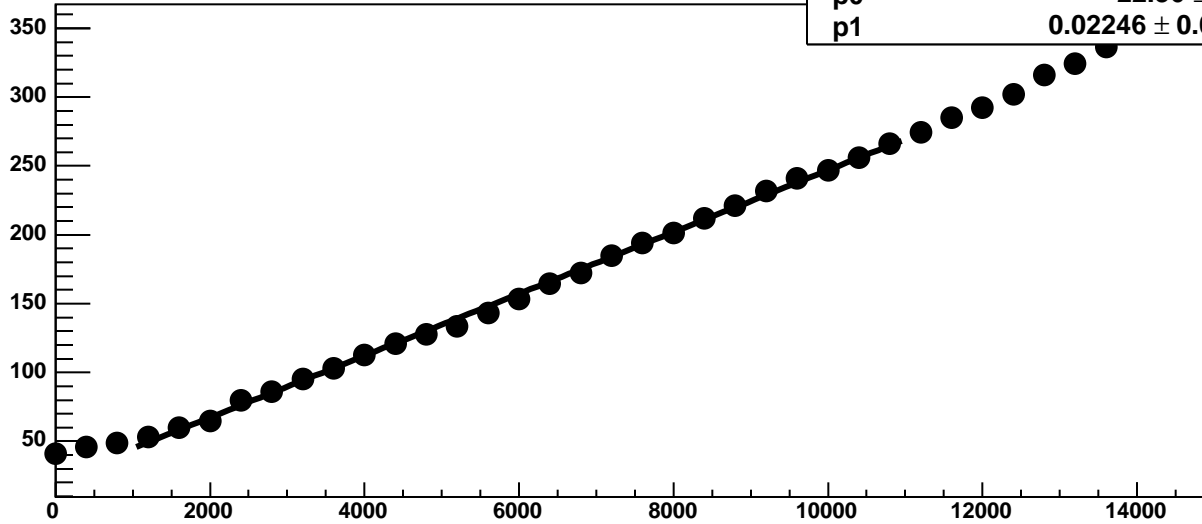
Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

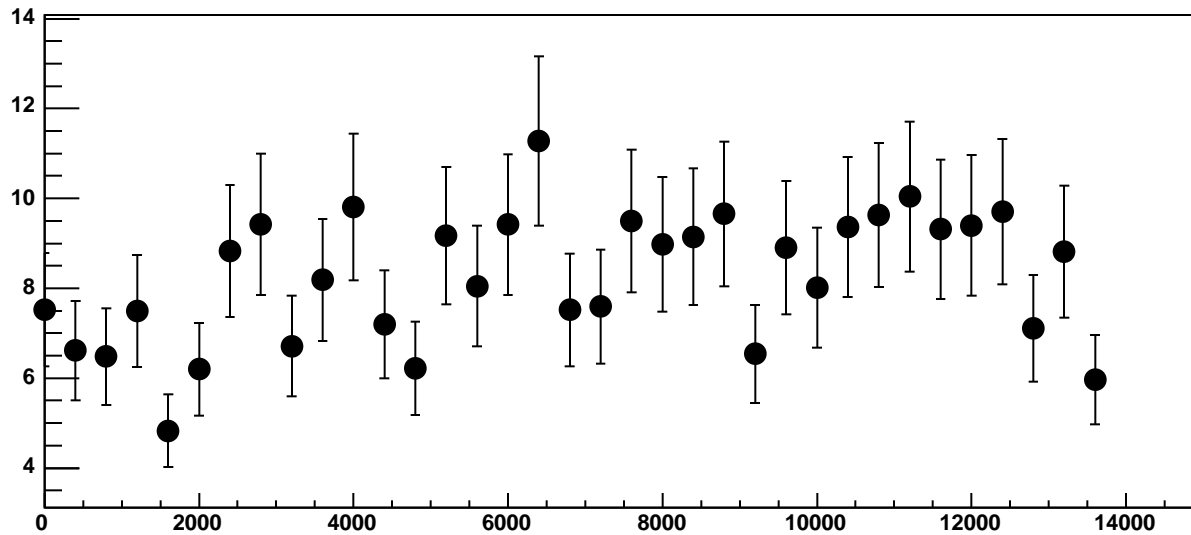


Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

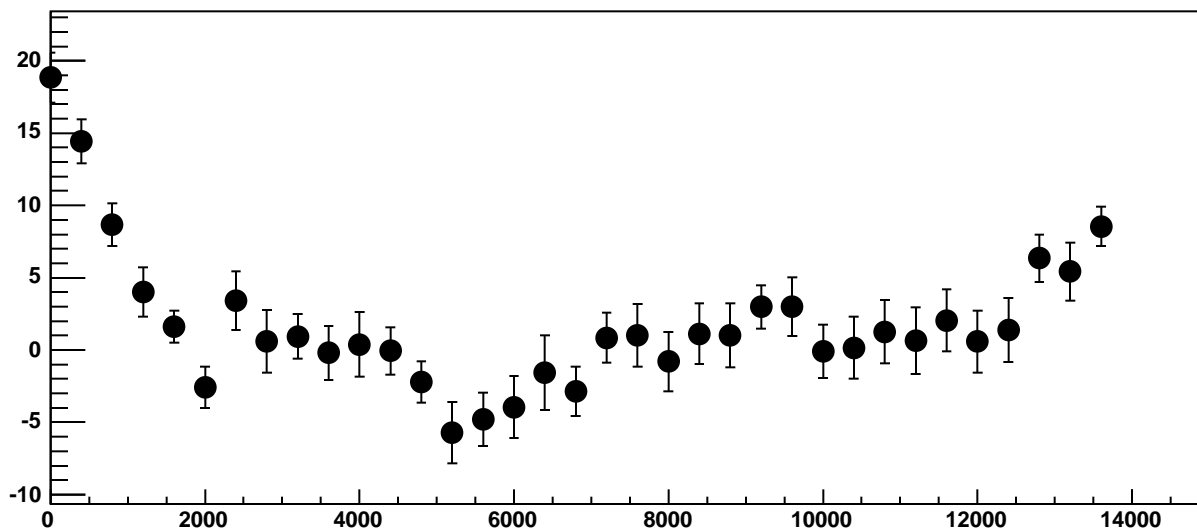


$\chi^2 / \text{ndf}$  44.69 / 23  
p0 22.36 ± 0.7492  
p1 0.02246 ± 0.0001211

Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

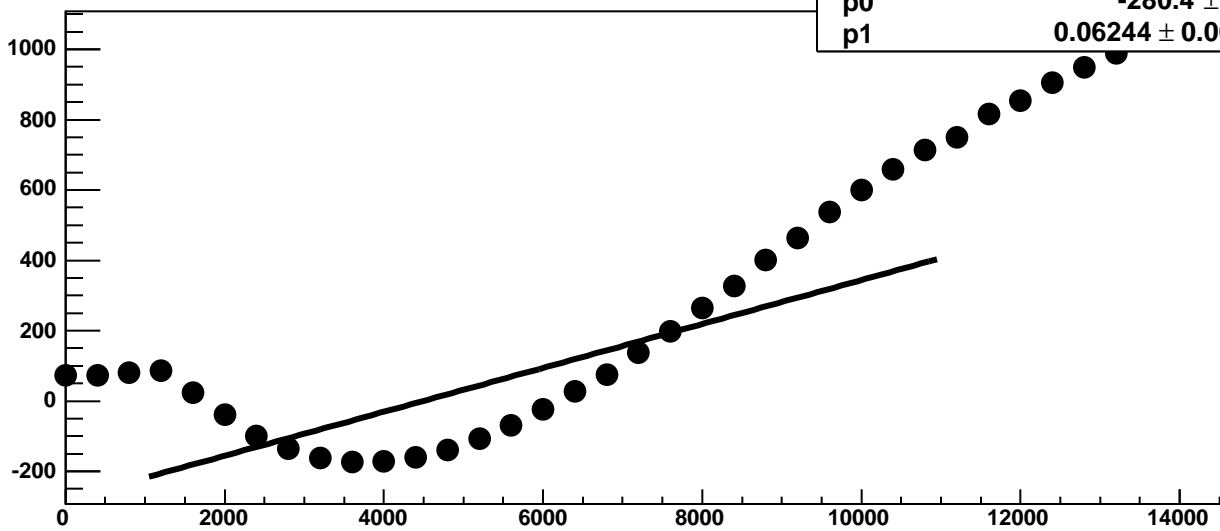


Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

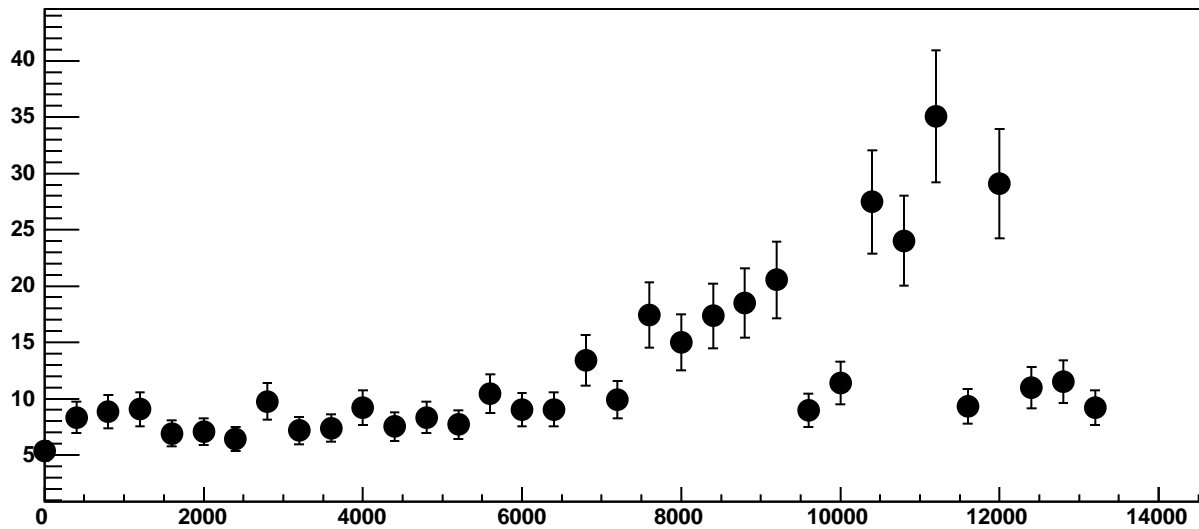




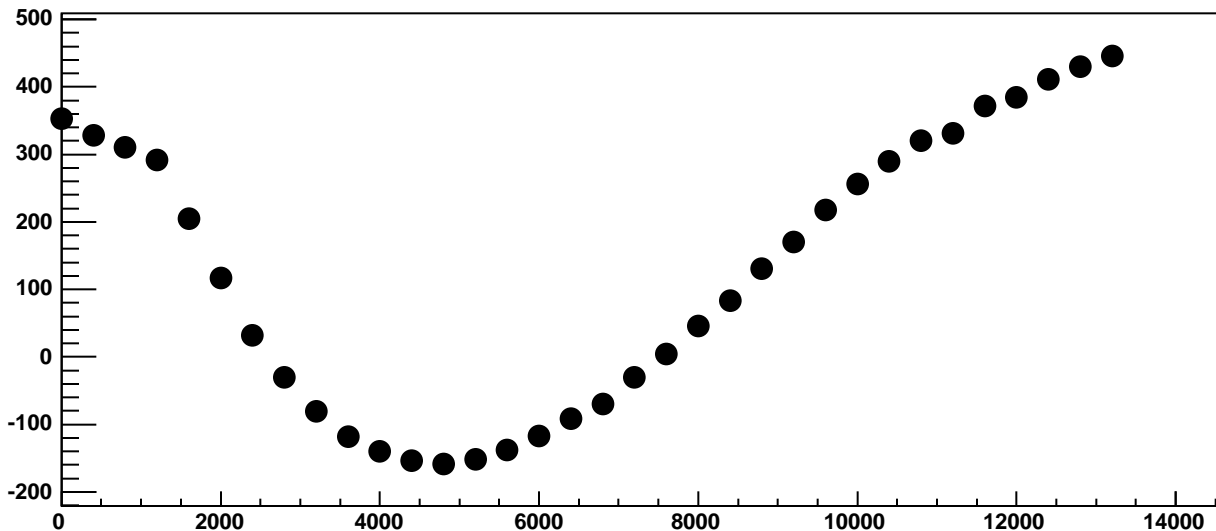
Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



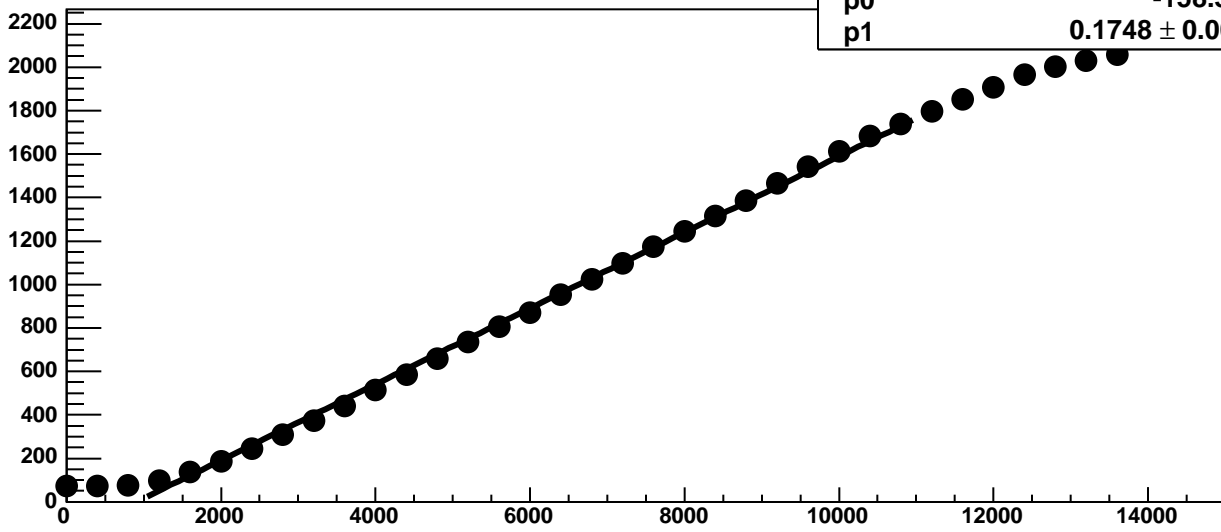
Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

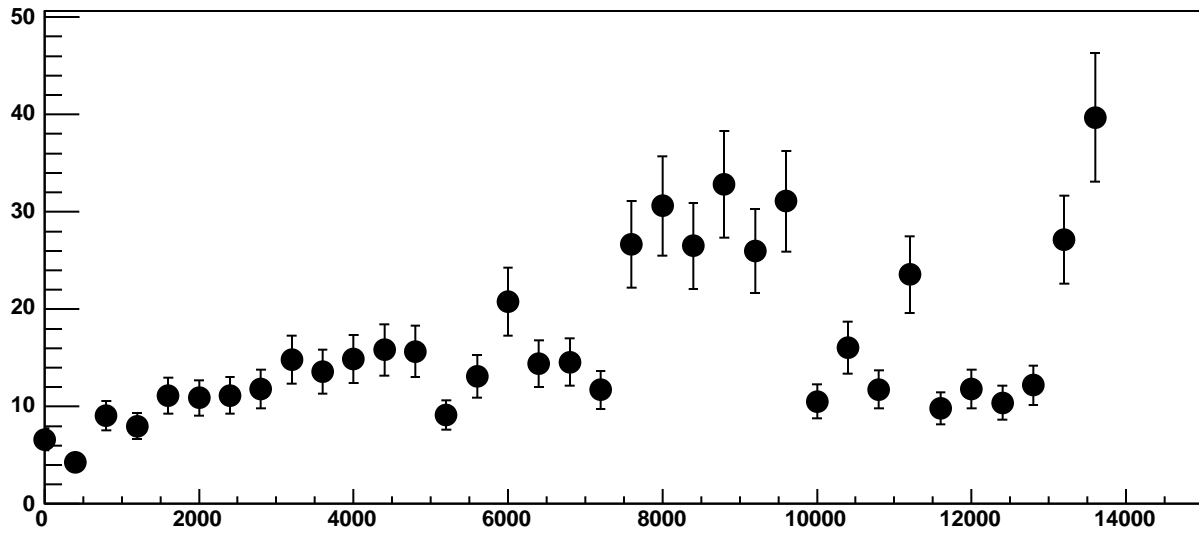


Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

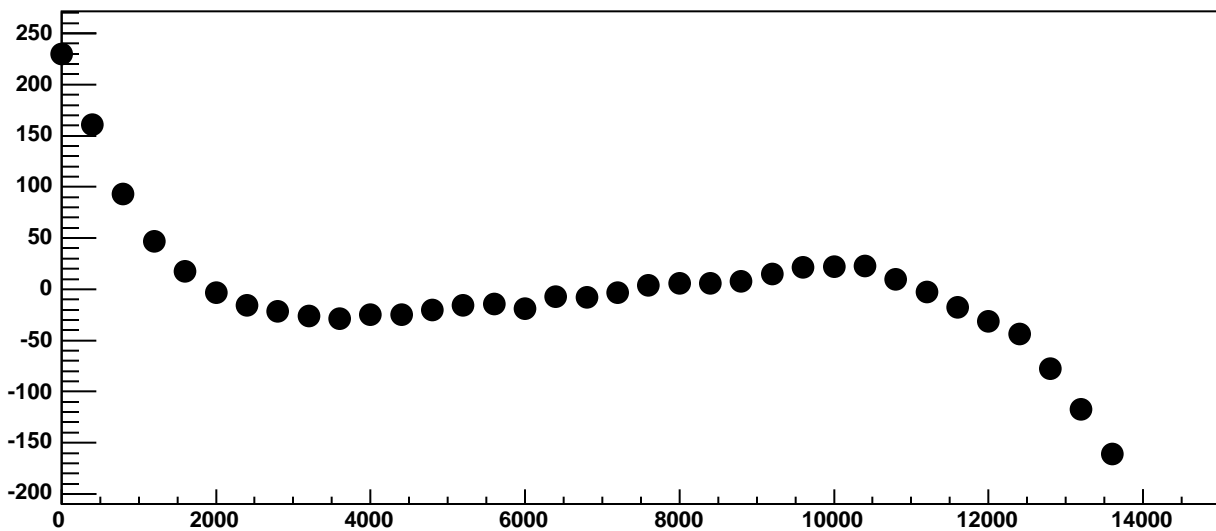


$\chi^2 / \text{ndf}$  1337 / 23  
p0  $-158.3 \pm 1.2$   
p1  $0.1748 \pm 0.0002044$

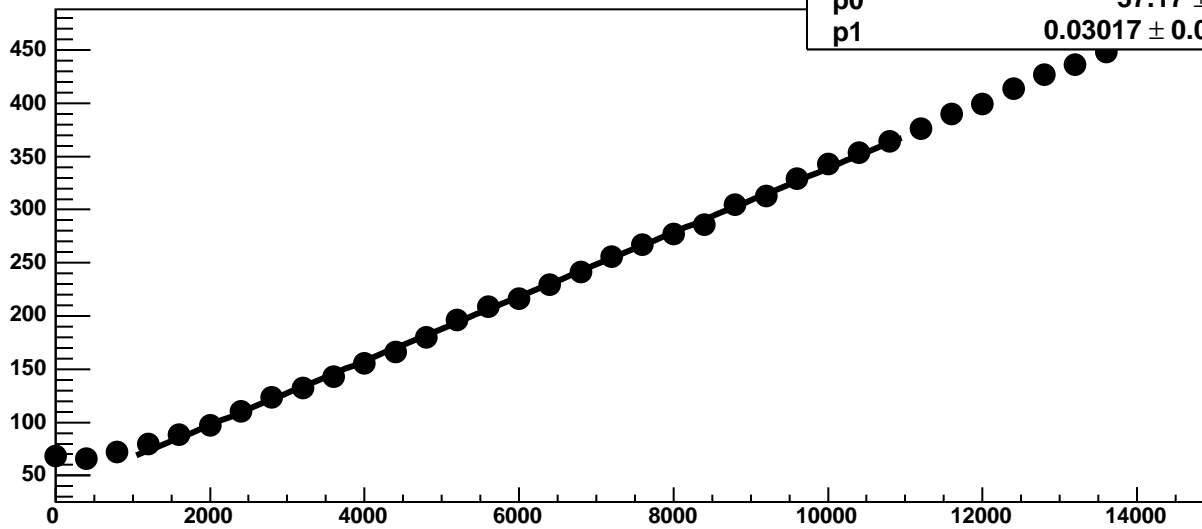
Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

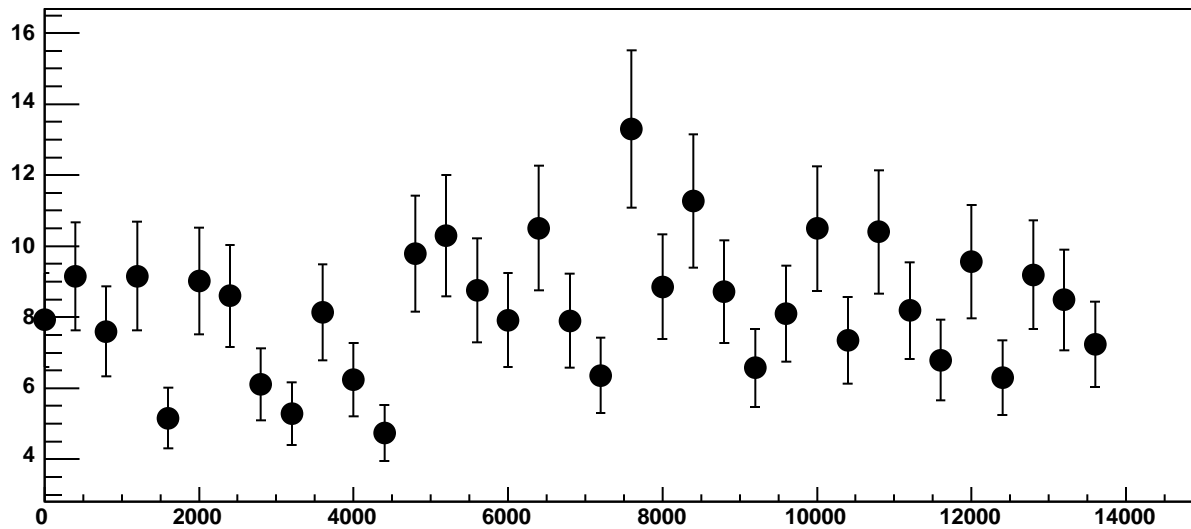


Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

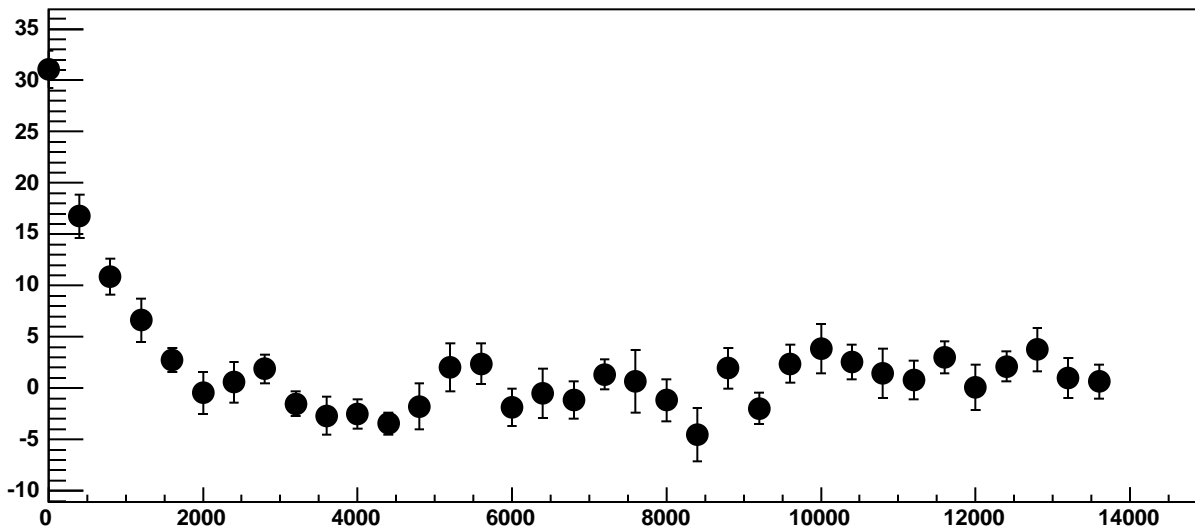


$\chi^2 / \text{ndf}$  52.08 / 23  
p0 37.17 ± 0.7466  
p1 0.03017 ± 0.0001224

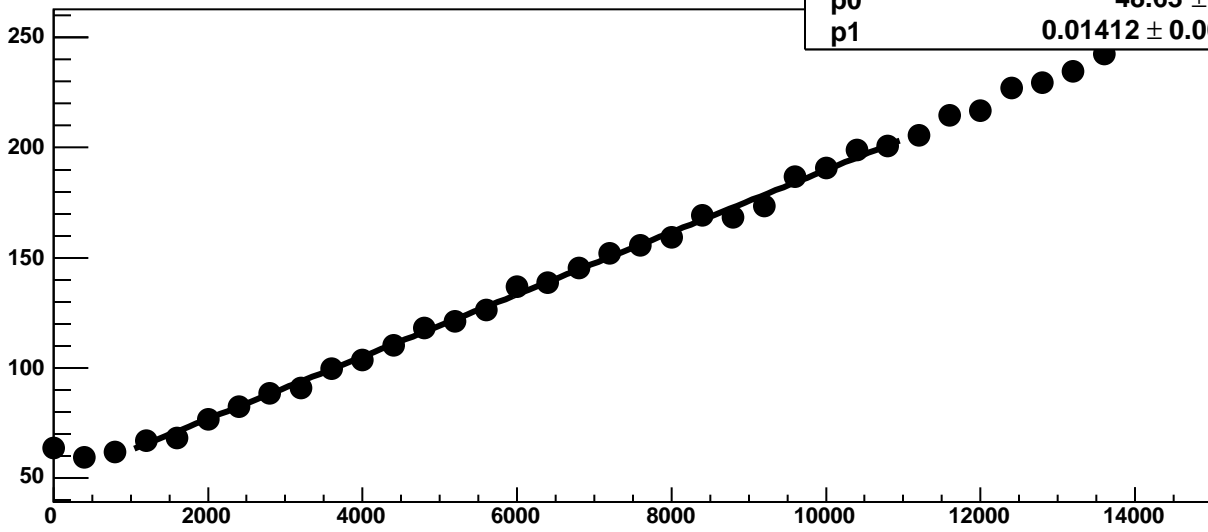
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

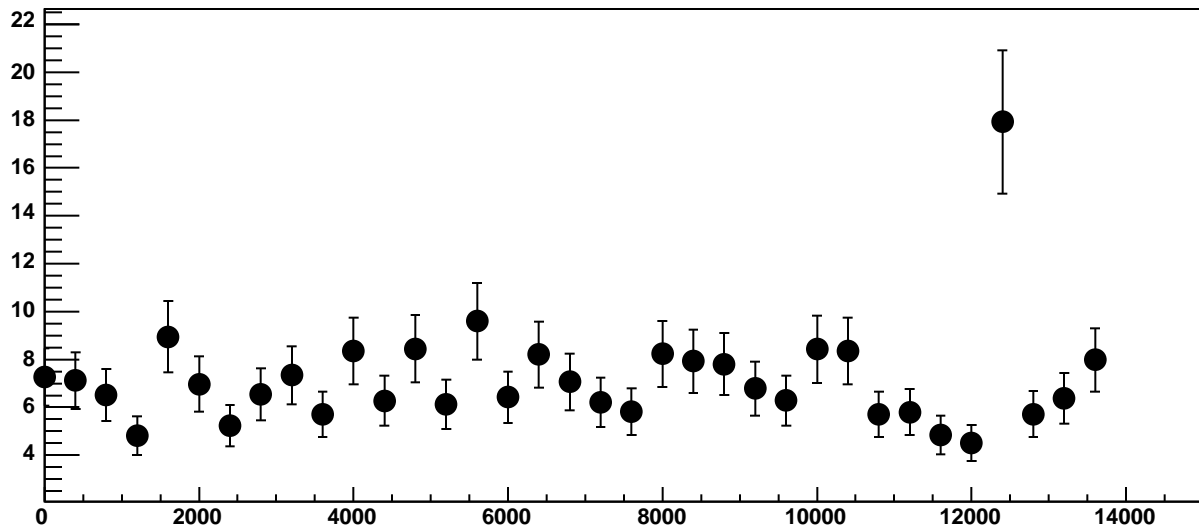


Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

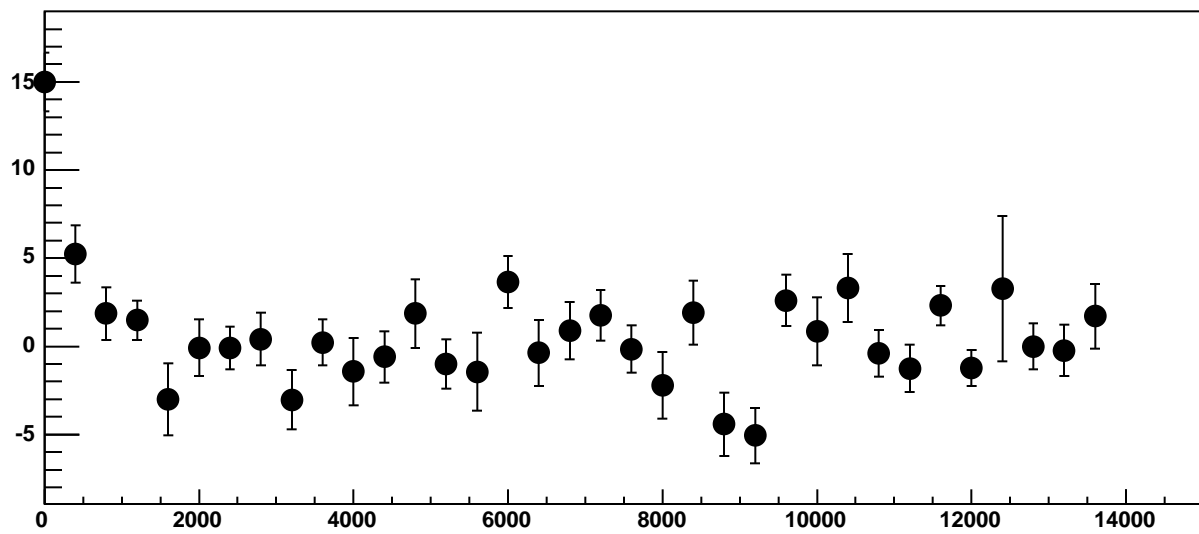


$\chi^2 / \text{ndf}$  43.53 / 23  
p0 48.63 ± 0.6709  
p1 0.01412 ± 0.0001038

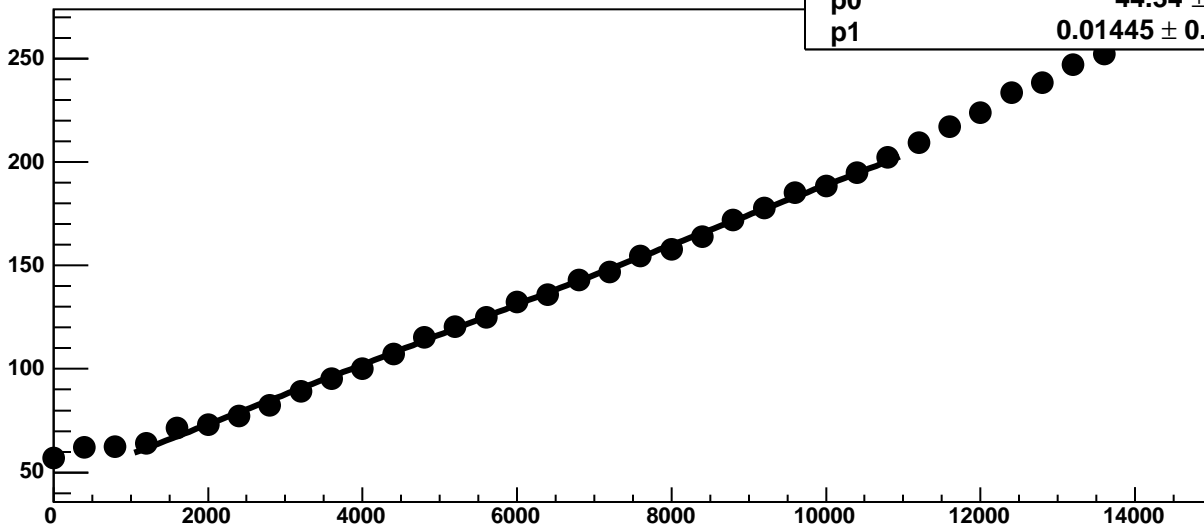
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



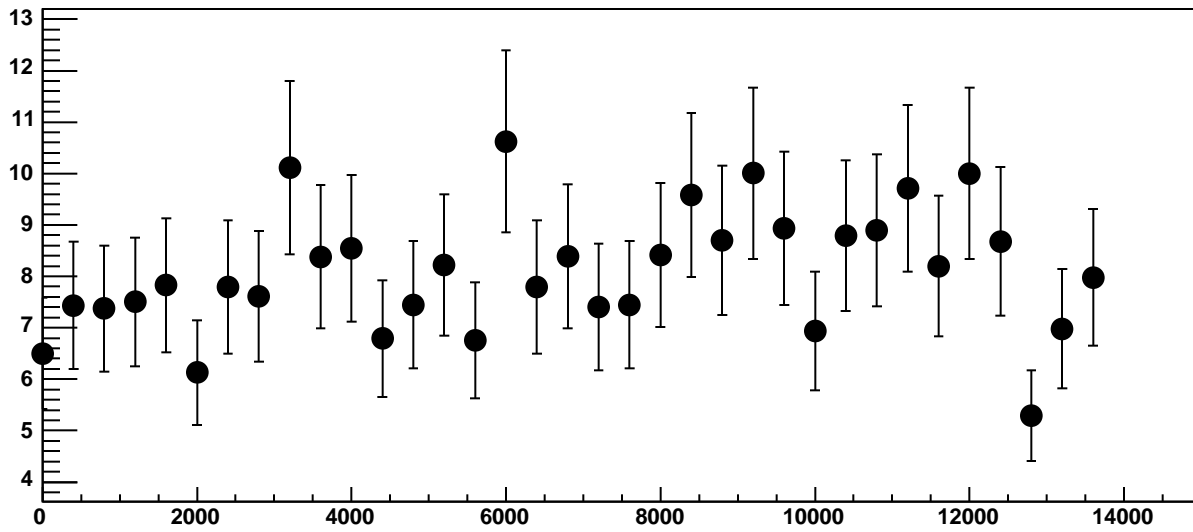
Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



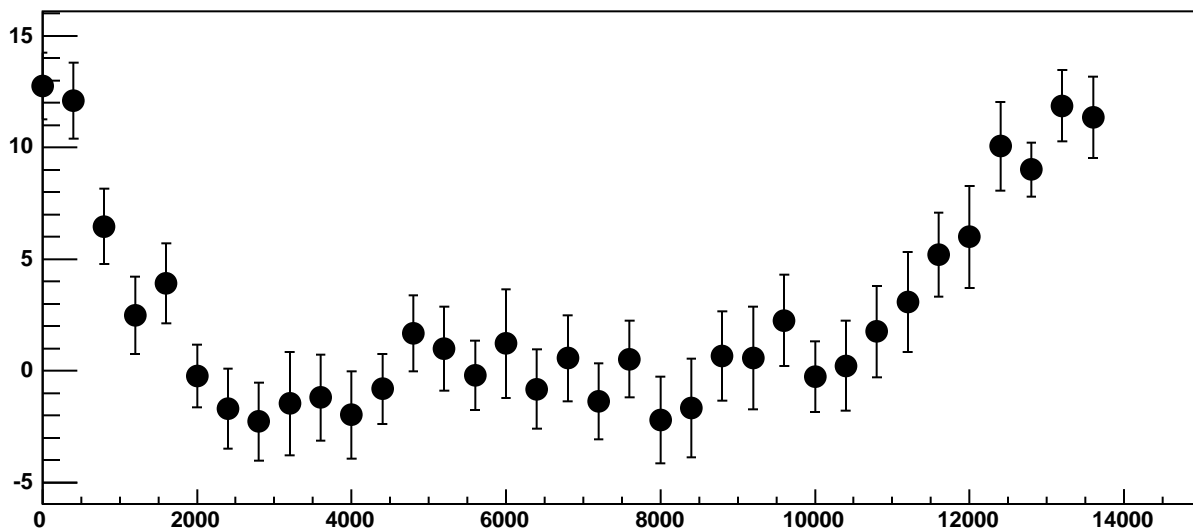
Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



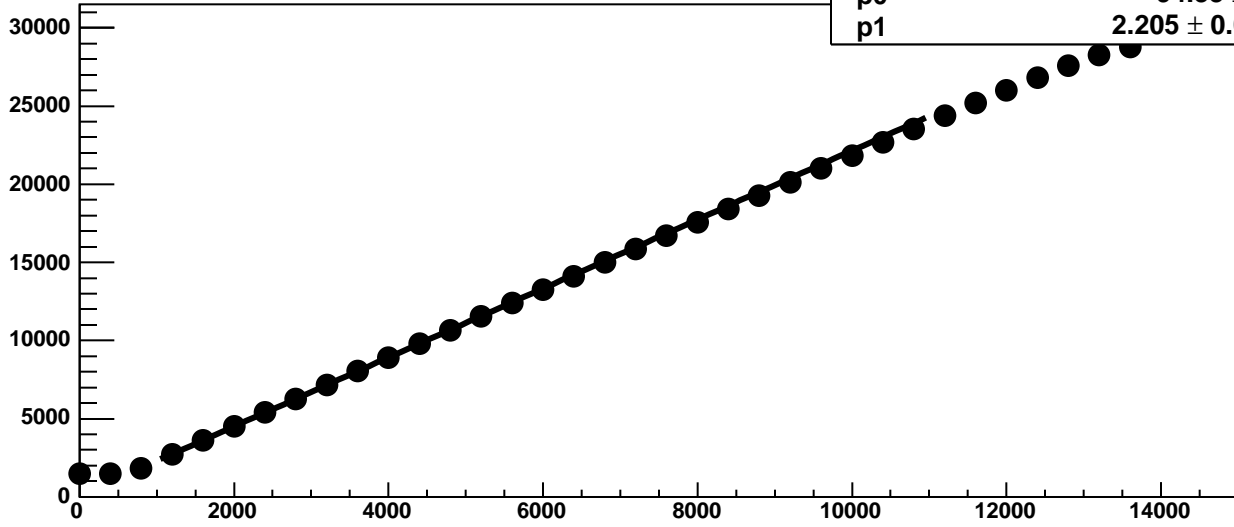
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



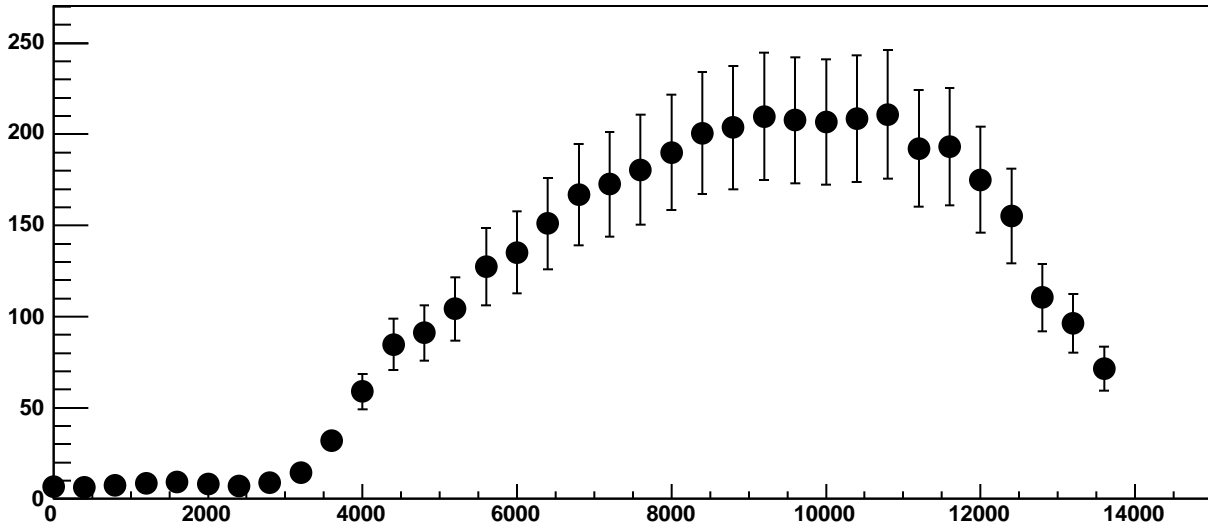
Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



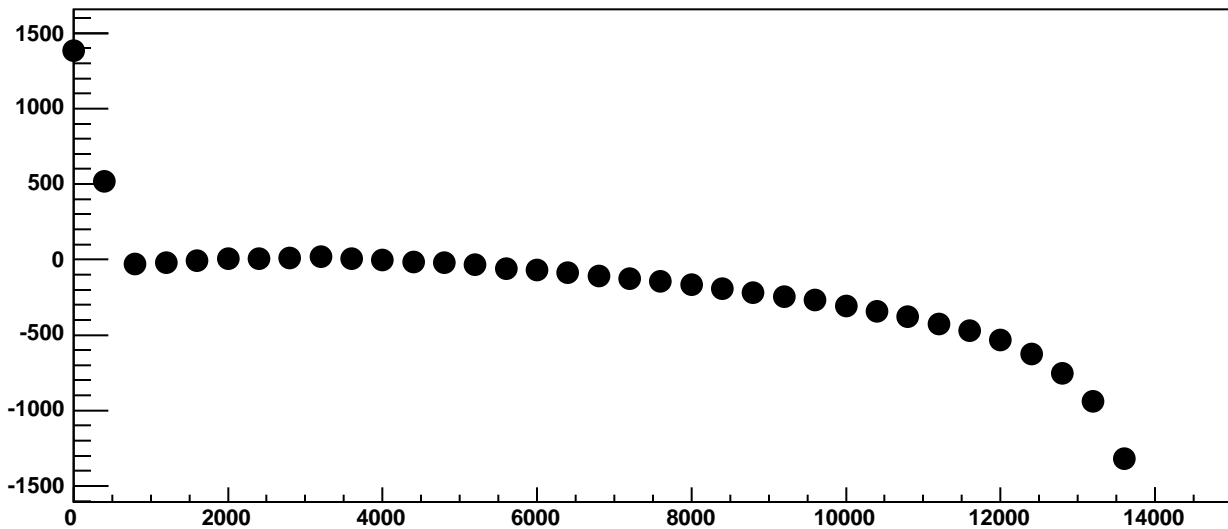
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



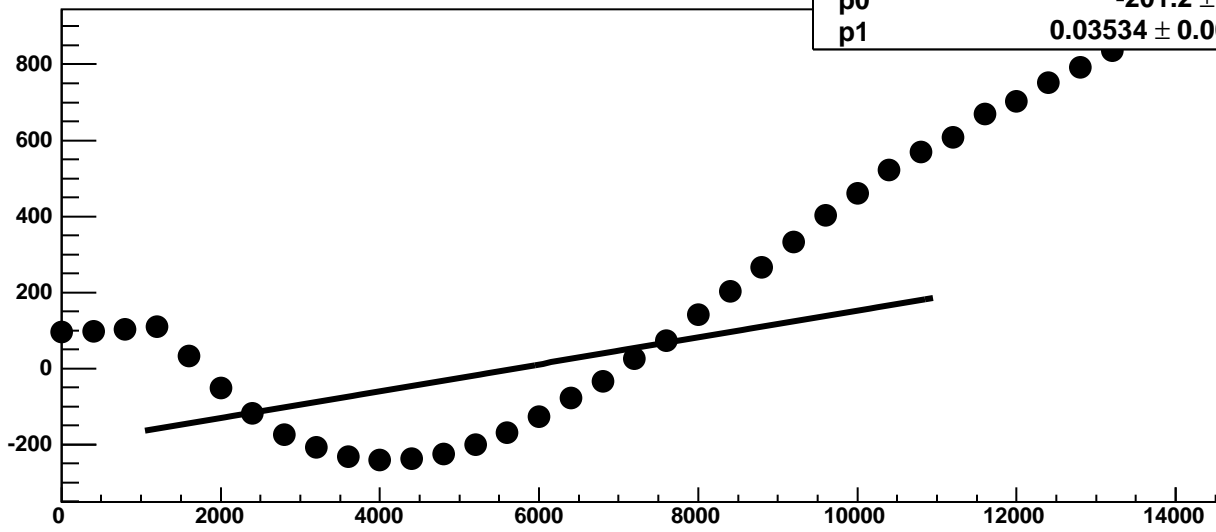
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



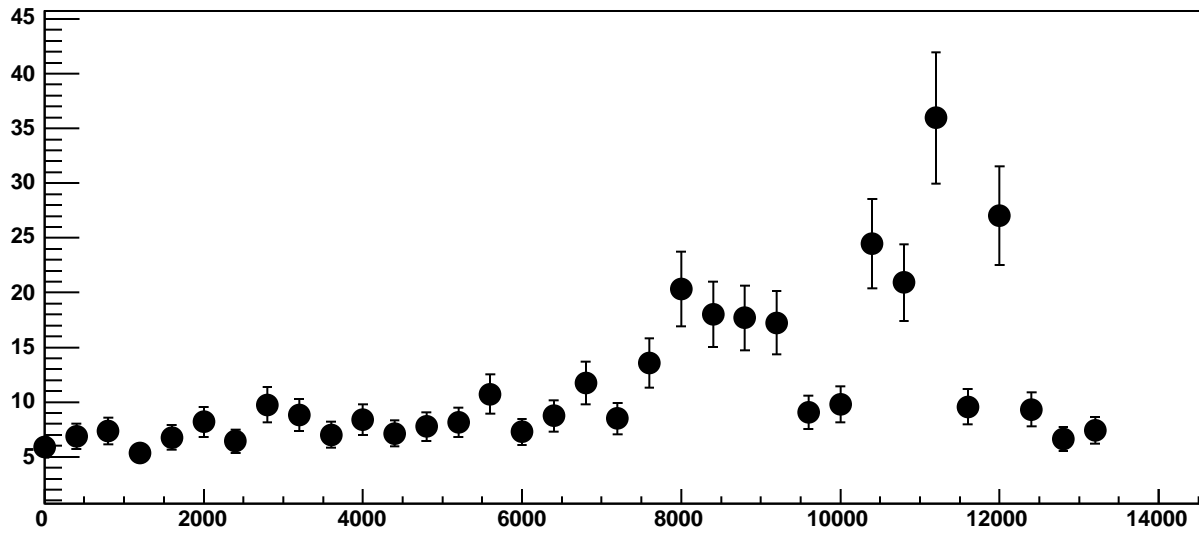
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



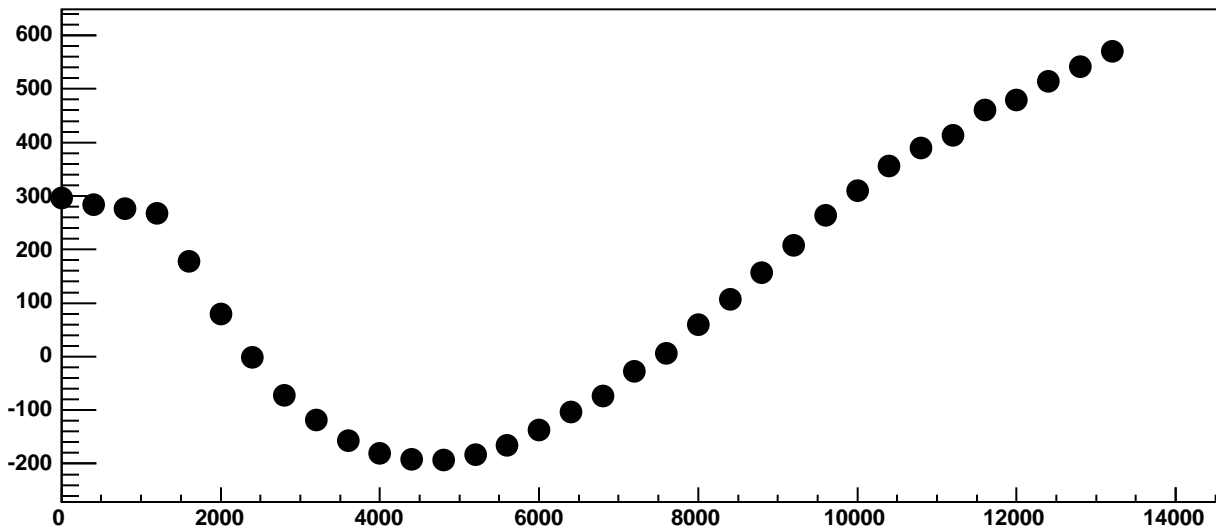
Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



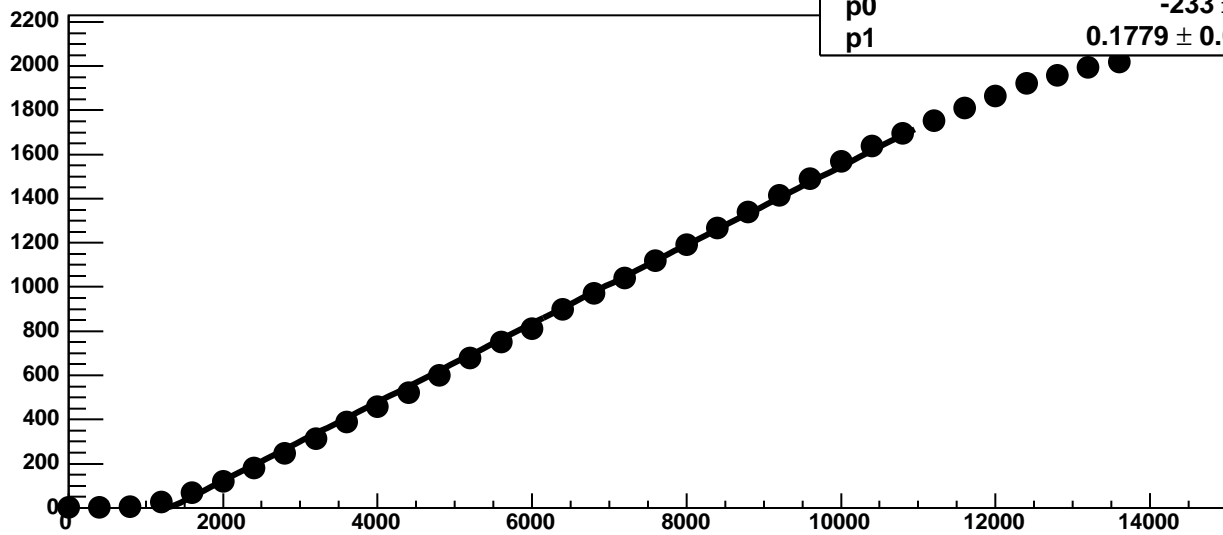
Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



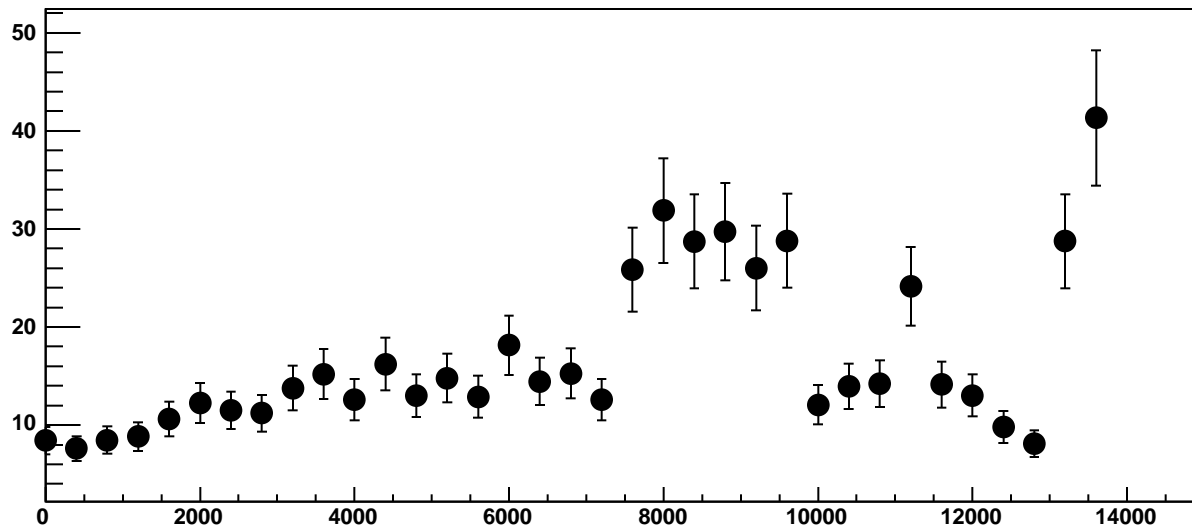
Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



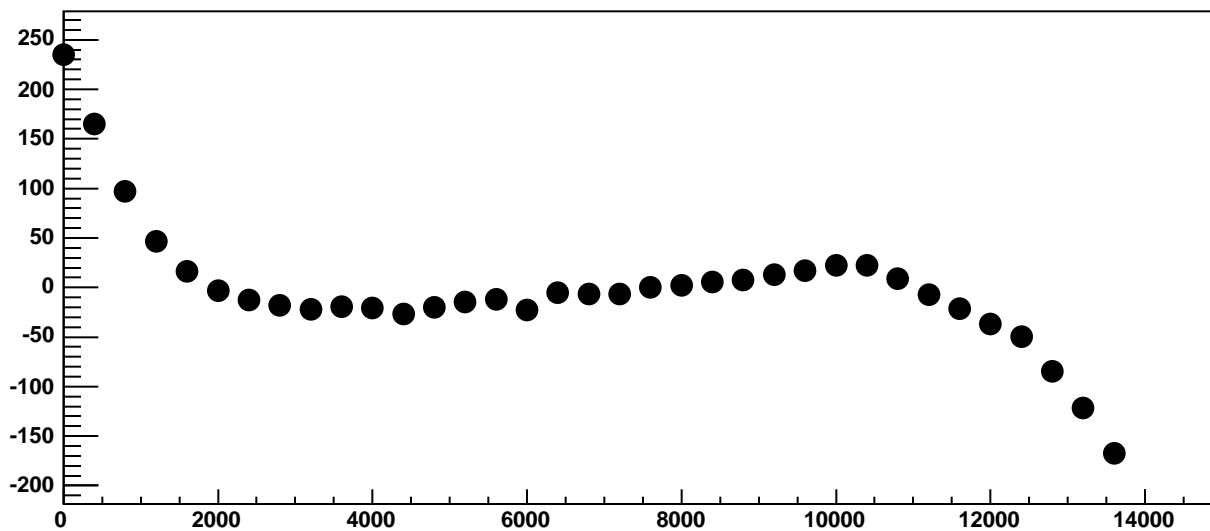
Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

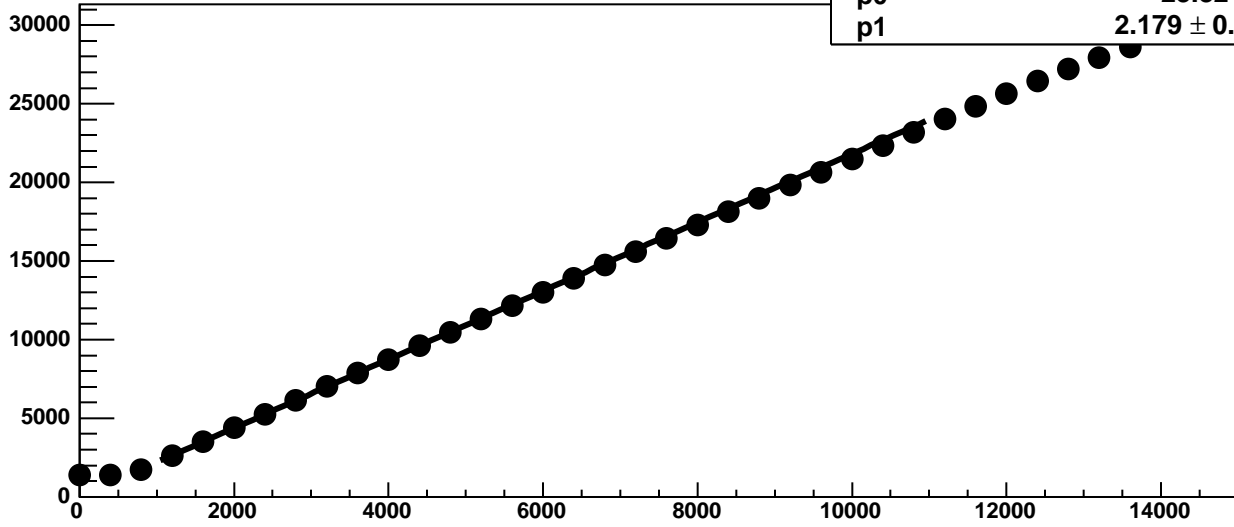


Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

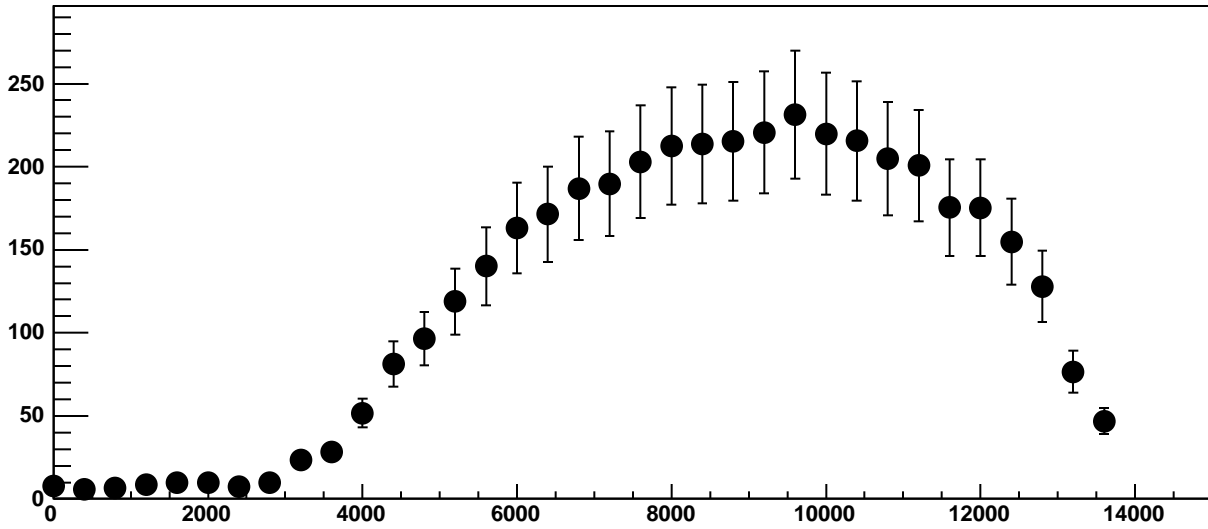




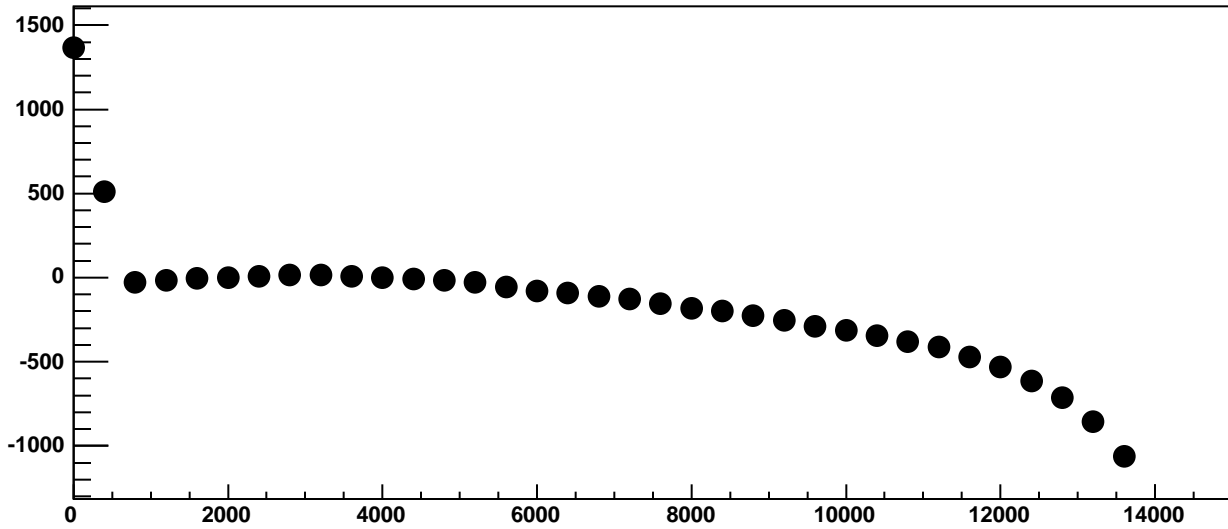
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



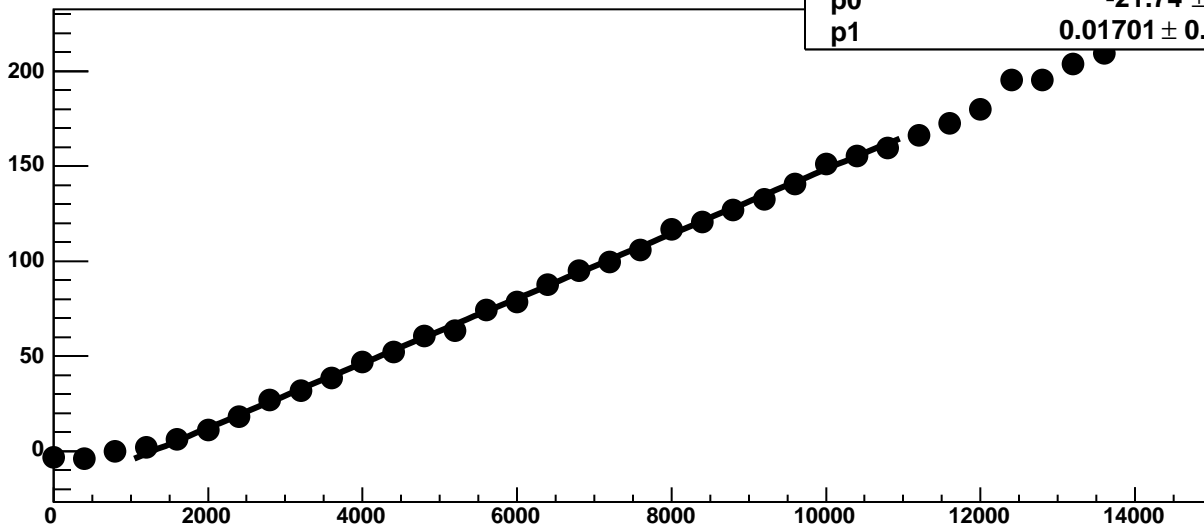
Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

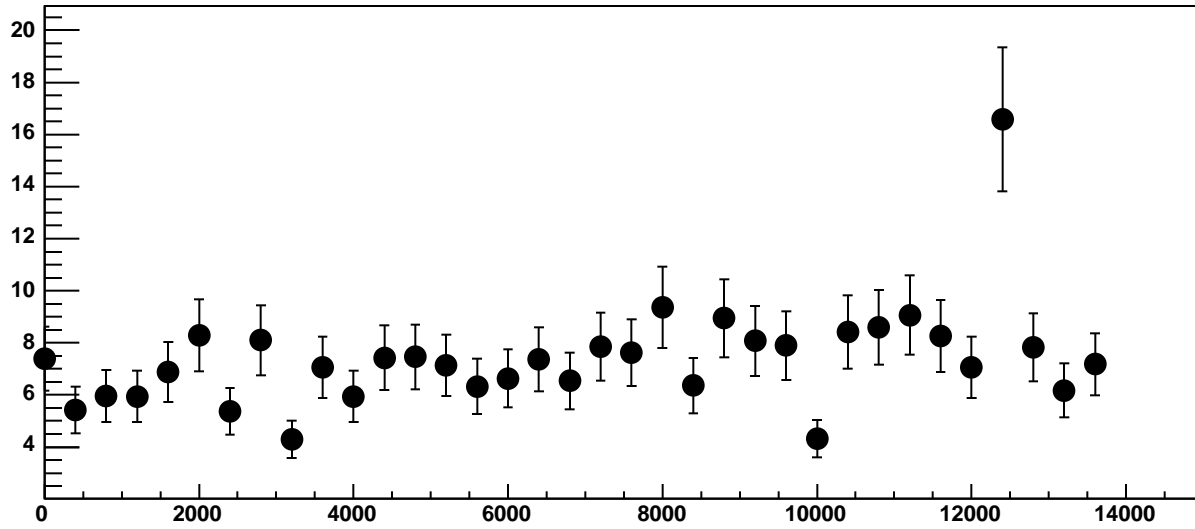


Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

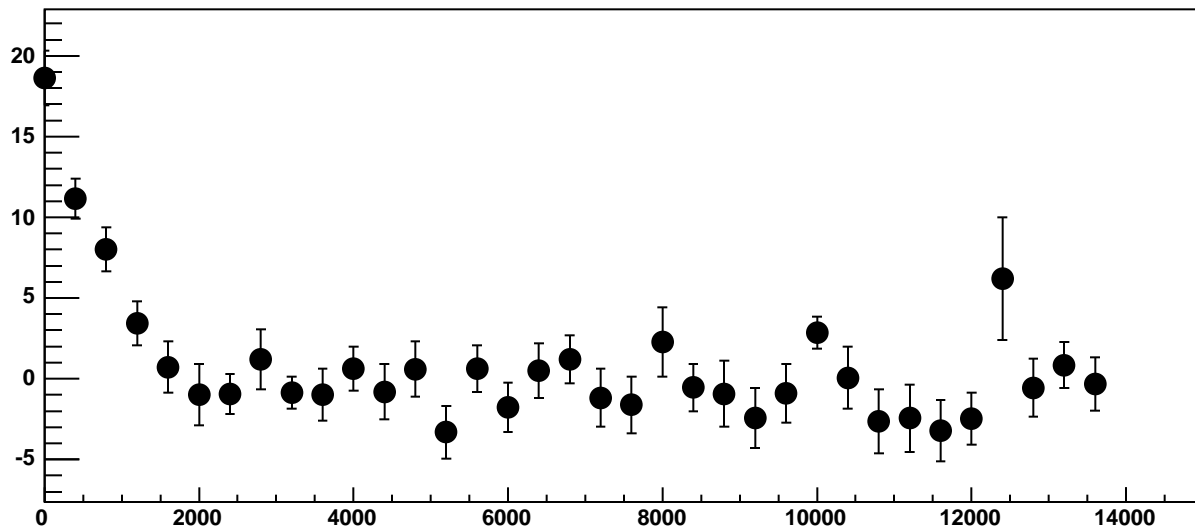


$\chi^2 / \text{ndf}$  30.68 / 23  
p0  $-21.74 \pm 0.6747$   
p1  $0.01701 \pm 0.000105$

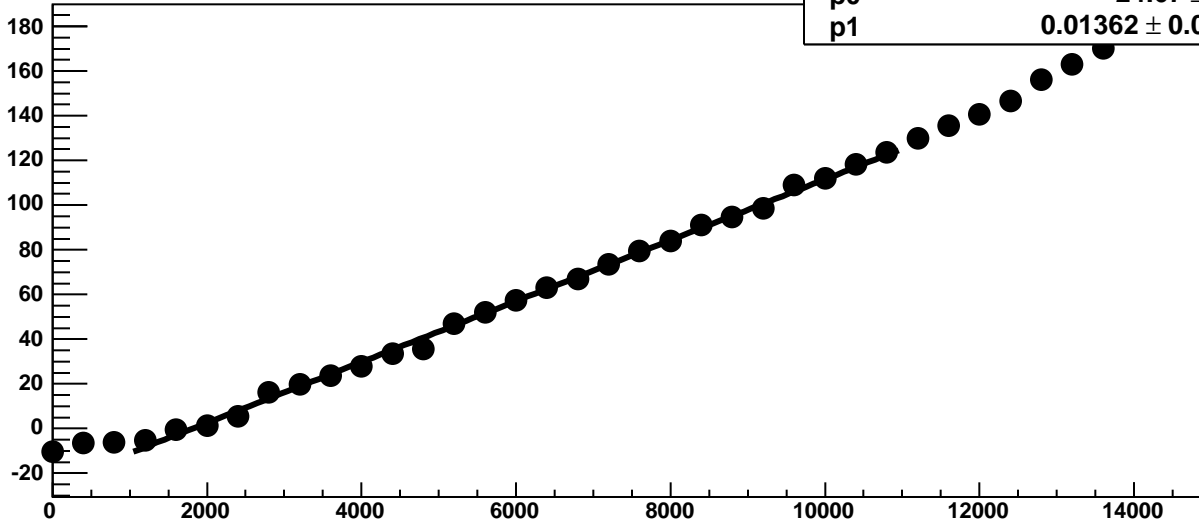
Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

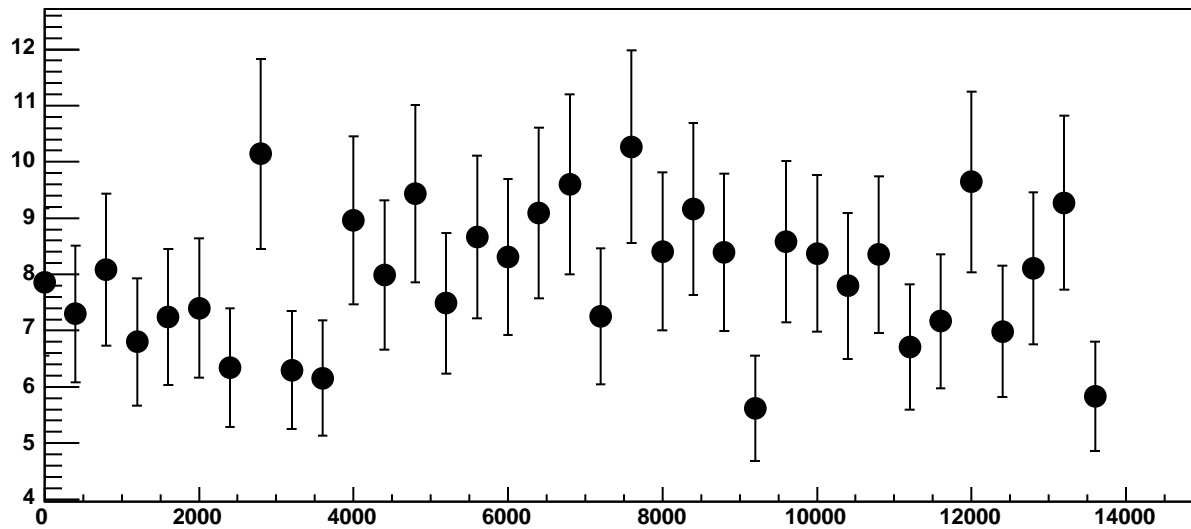


Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

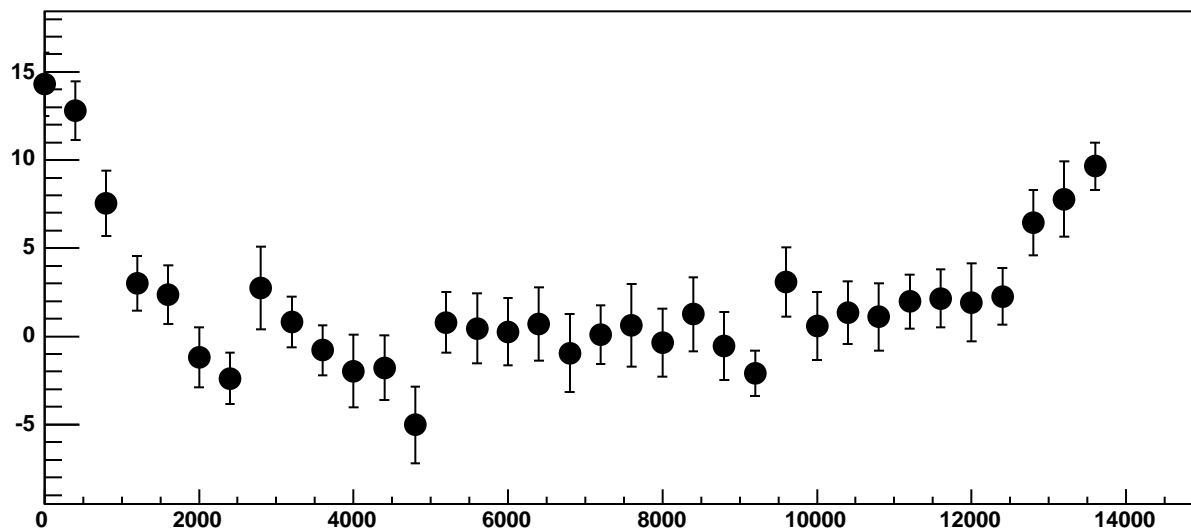


$\chi^2 / \text{ndf}$  25.41 / 23  
p0  $-24.67 \pm 0.7738$   
p1  $0.01362 \pm 0.0001193$

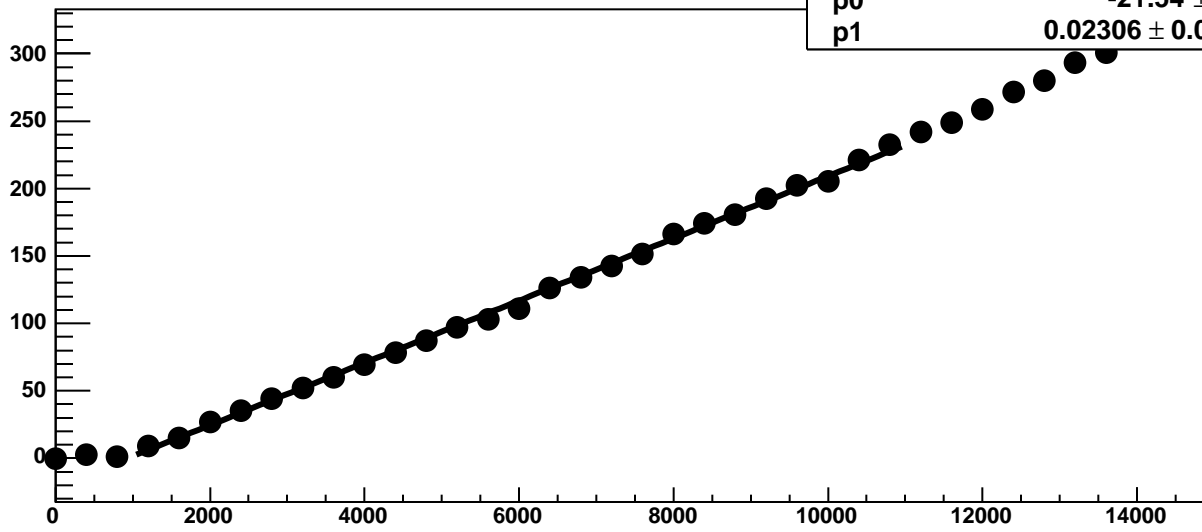
Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

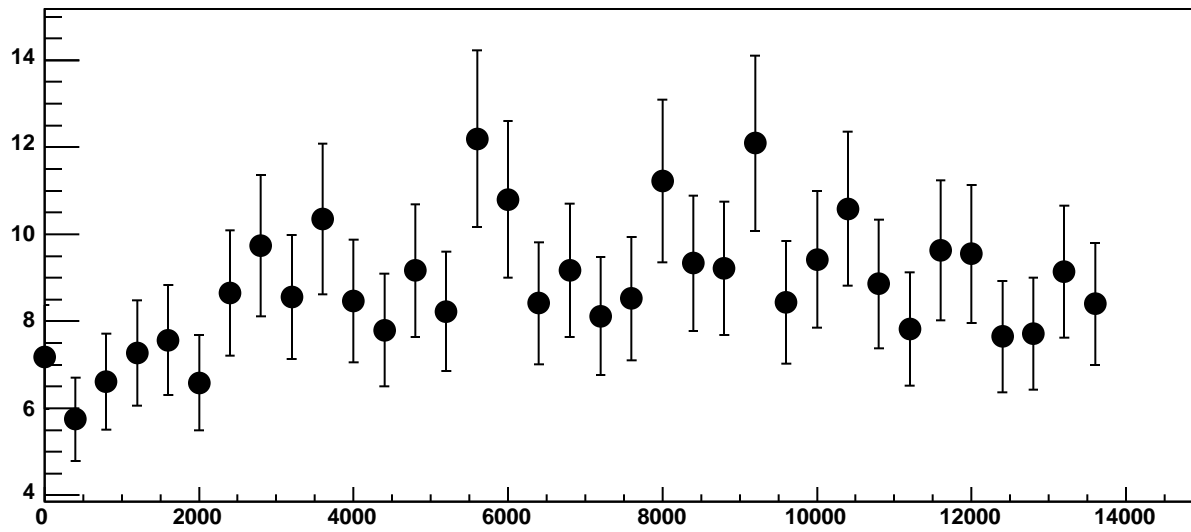


Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

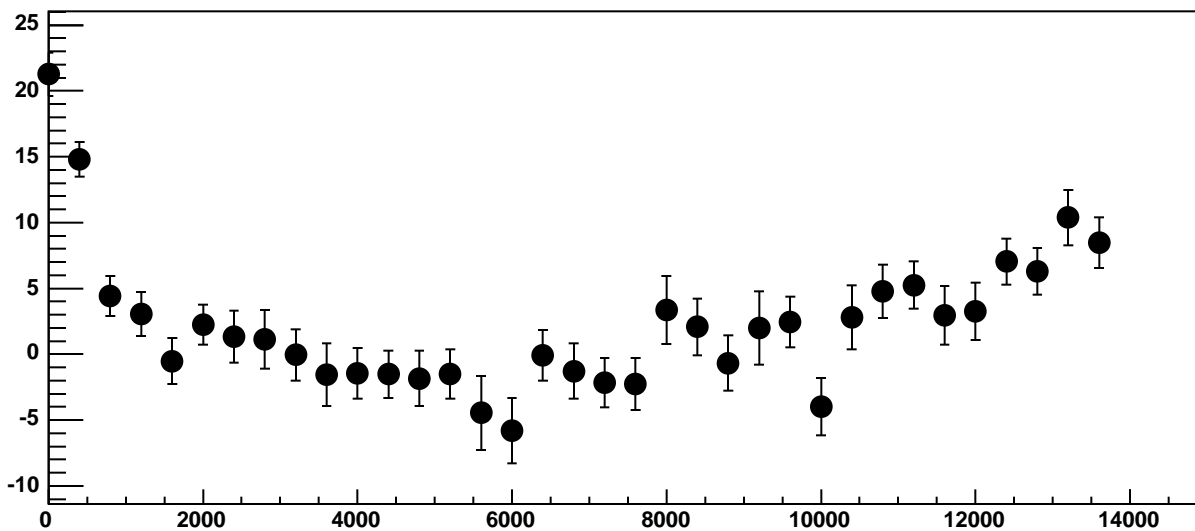


$\chi^2 / \text{ndf}$  35.66 / 23  
p0 -21.54 ± 0.8645  
p1 0.02306 ± 0.0001369

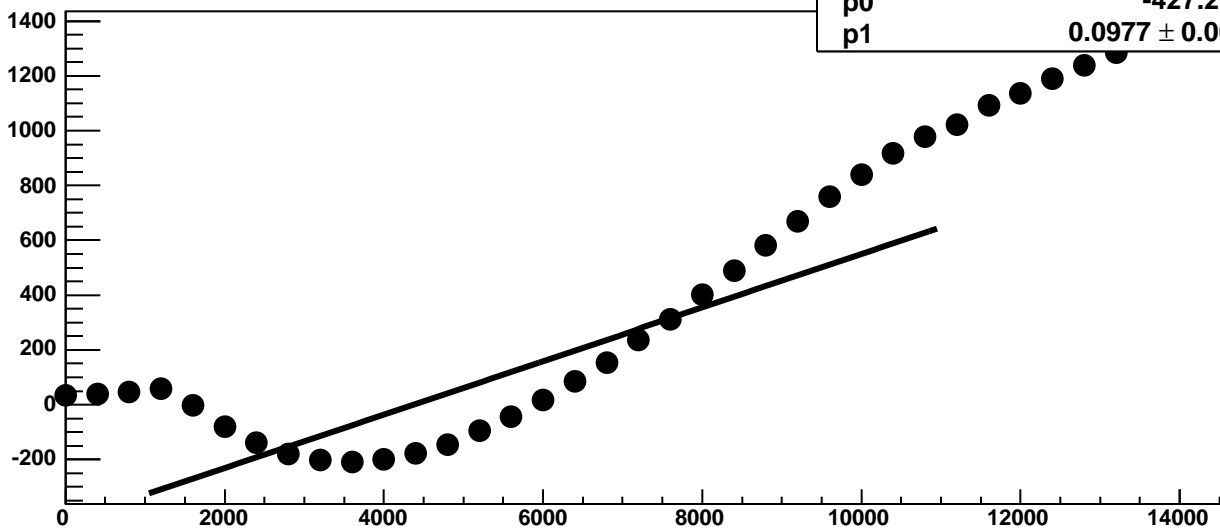
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

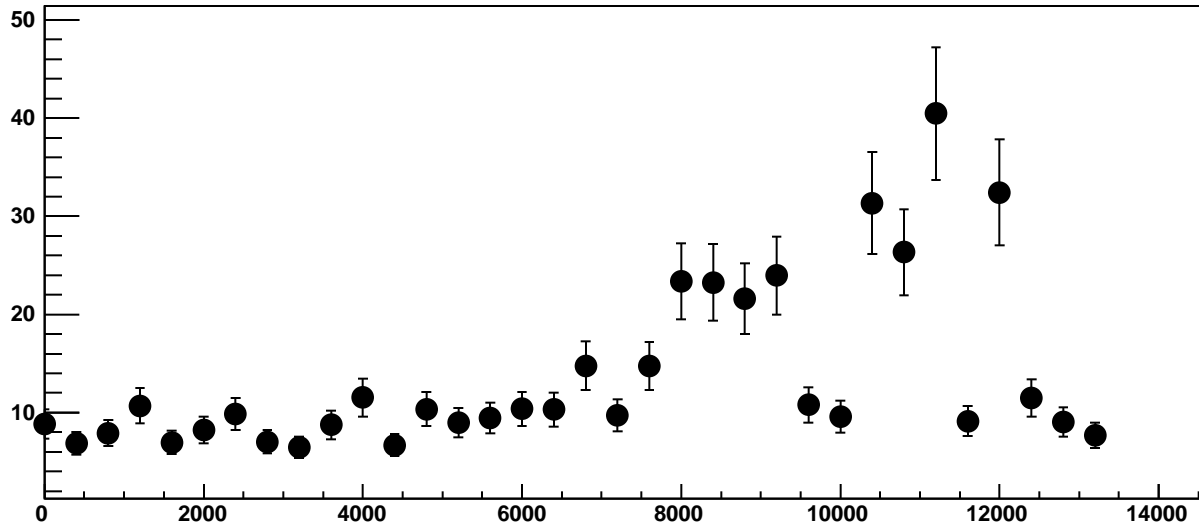


Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

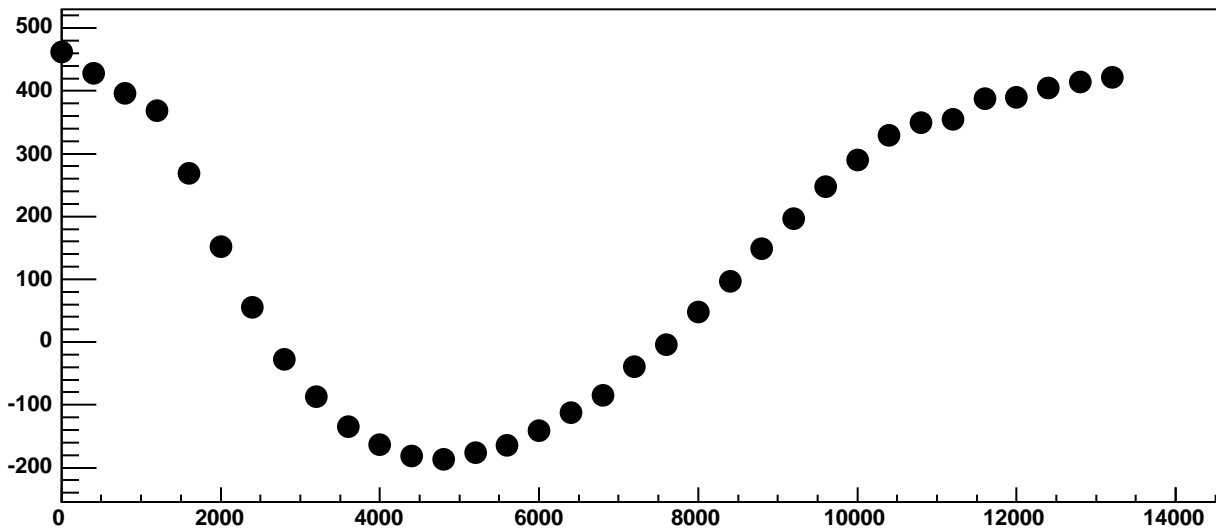


$\chi^2 / \text{ndf}$  1.452e+05 / 23  
p0 -427.2 ± 0.98  
p1 0.0977 ± 0.0001868

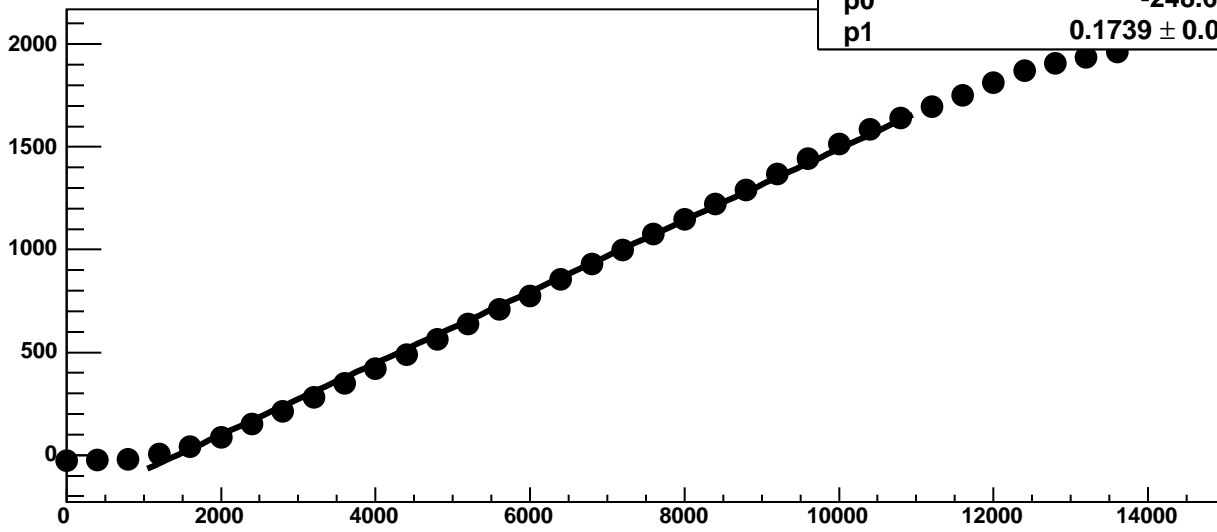
Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



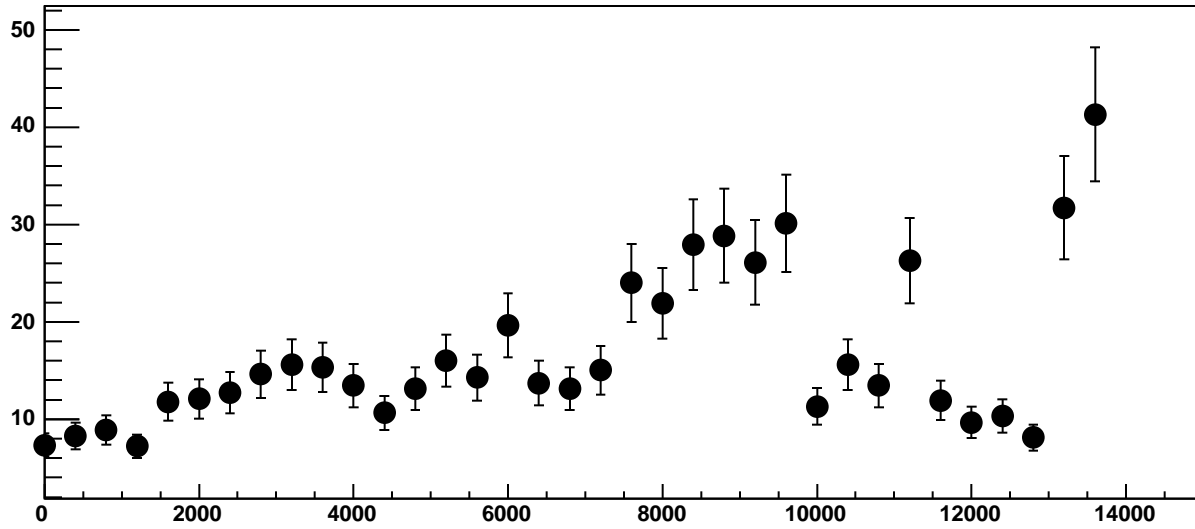
Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



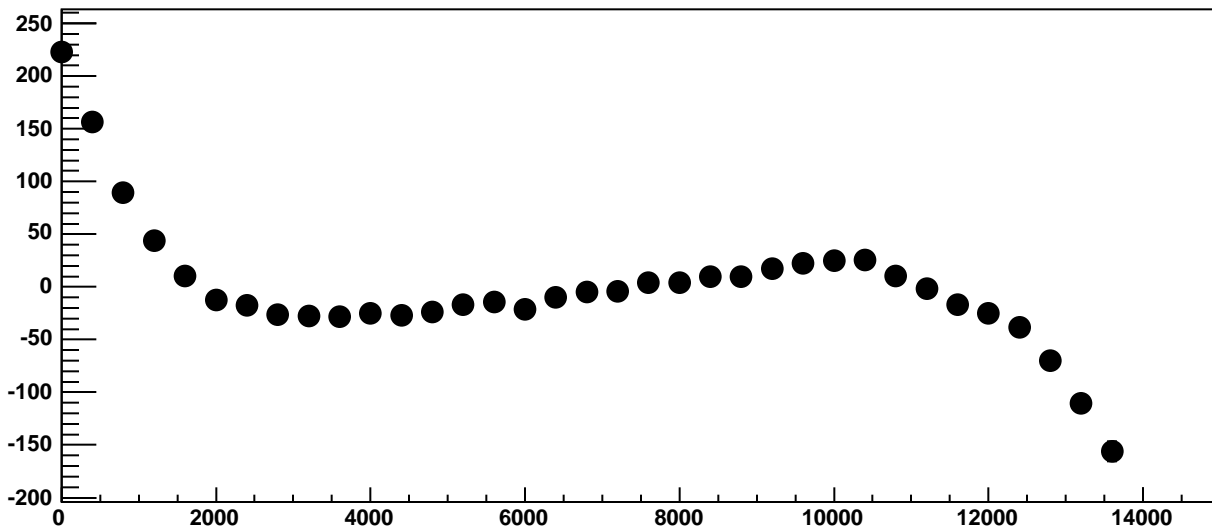
Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



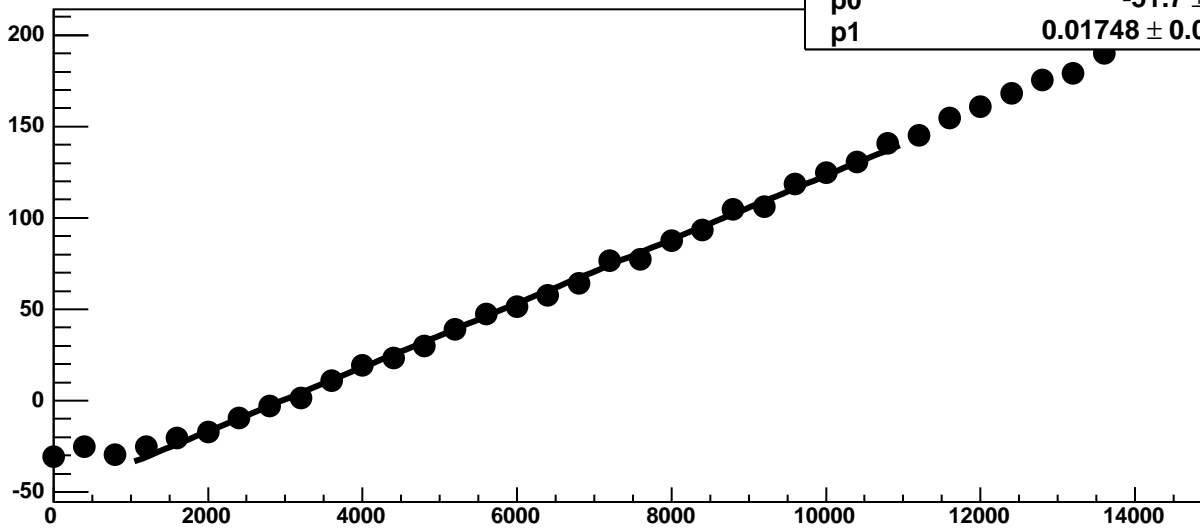
Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

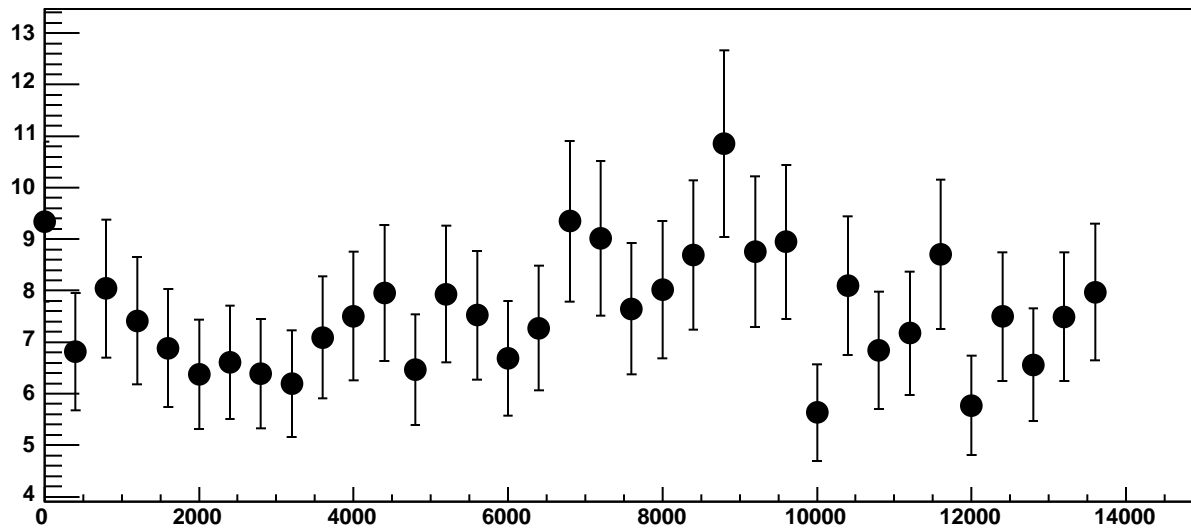


Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

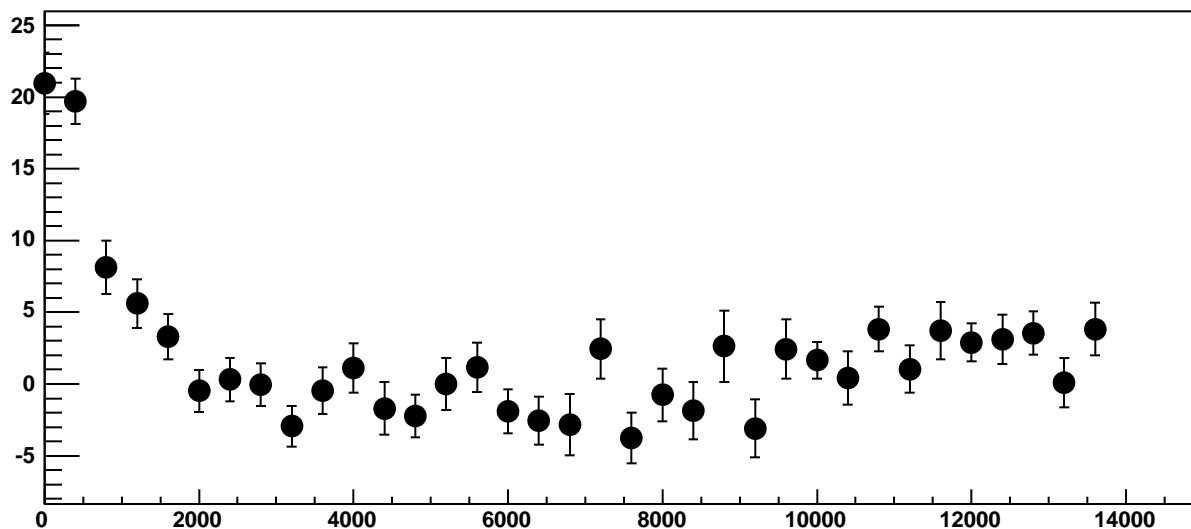


$\chi^2 / \text{ndf}$  48.89 / 23  
p0  $-51.7 \pm 0.7304$   
p1  $0.01748 \pm 0.0001138$

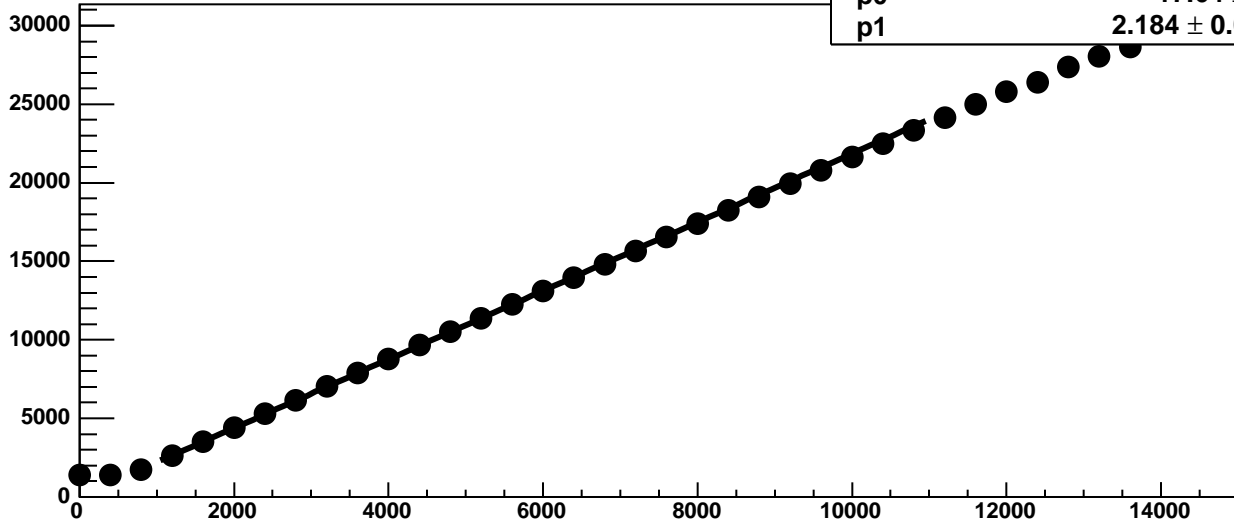
Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



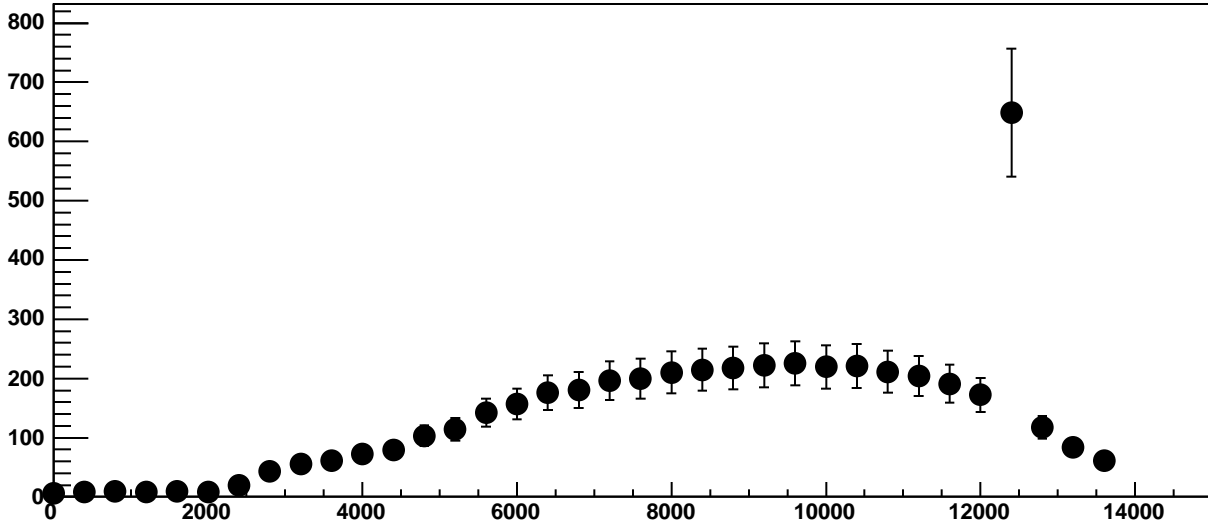
Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



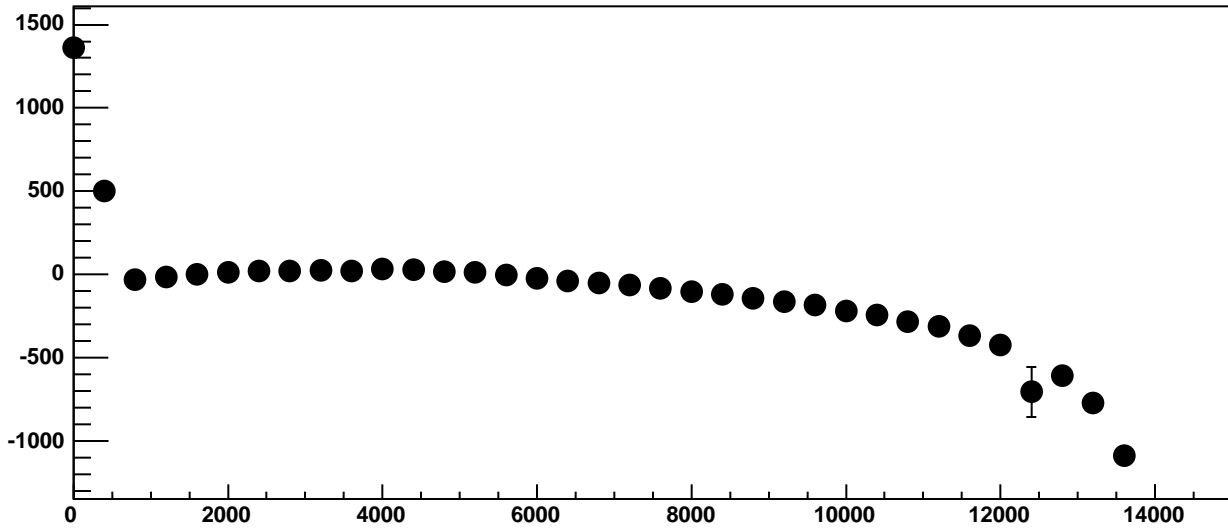
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

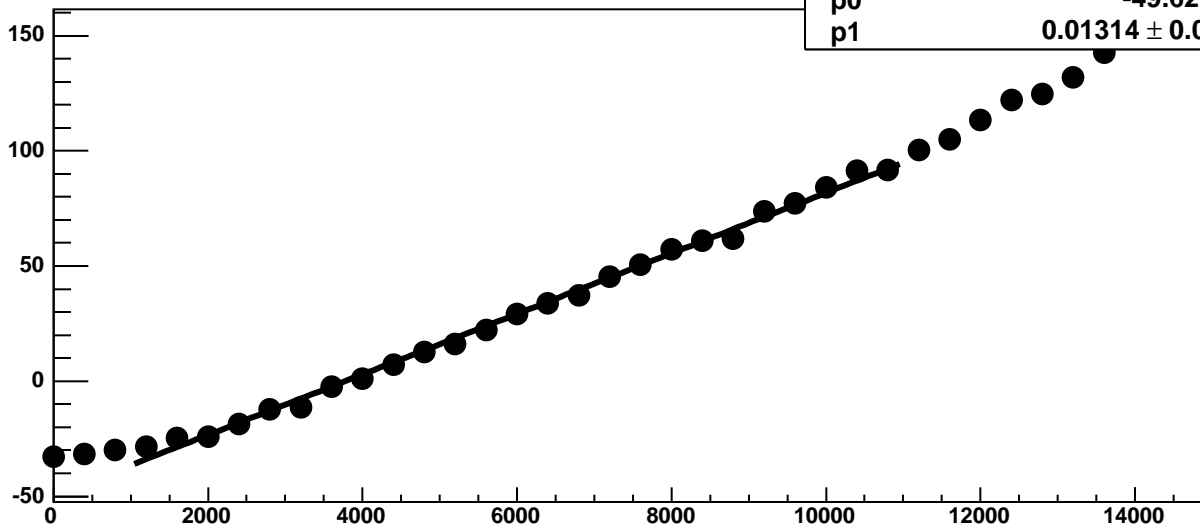


Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



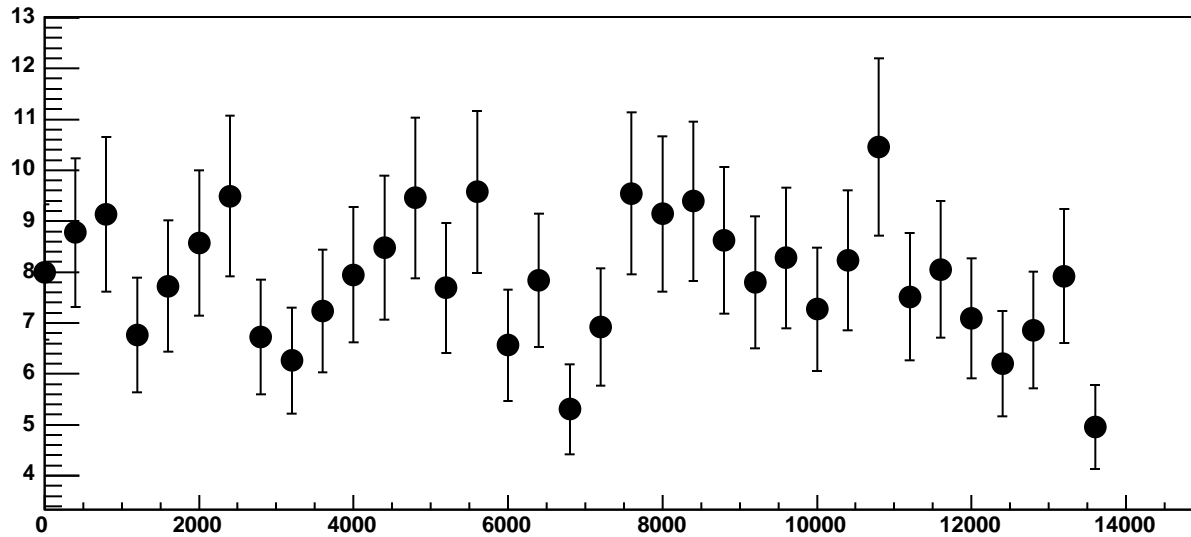


Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

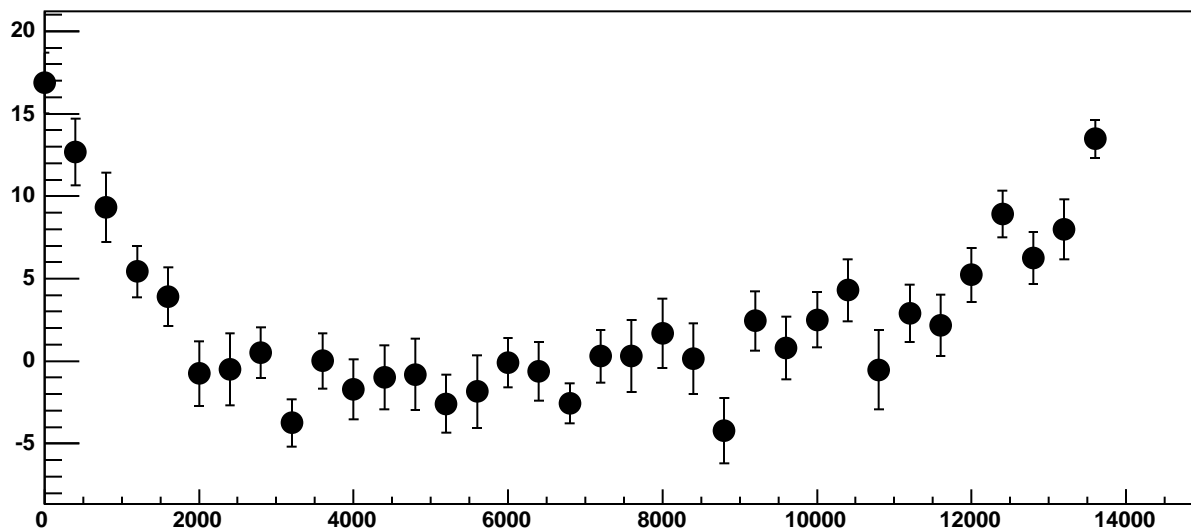


$\chi^2 / \text{ndf}$  47.66 / 23  
p0 -49.62 ± 0.813  
p1 0.01314 ± 0.0001264

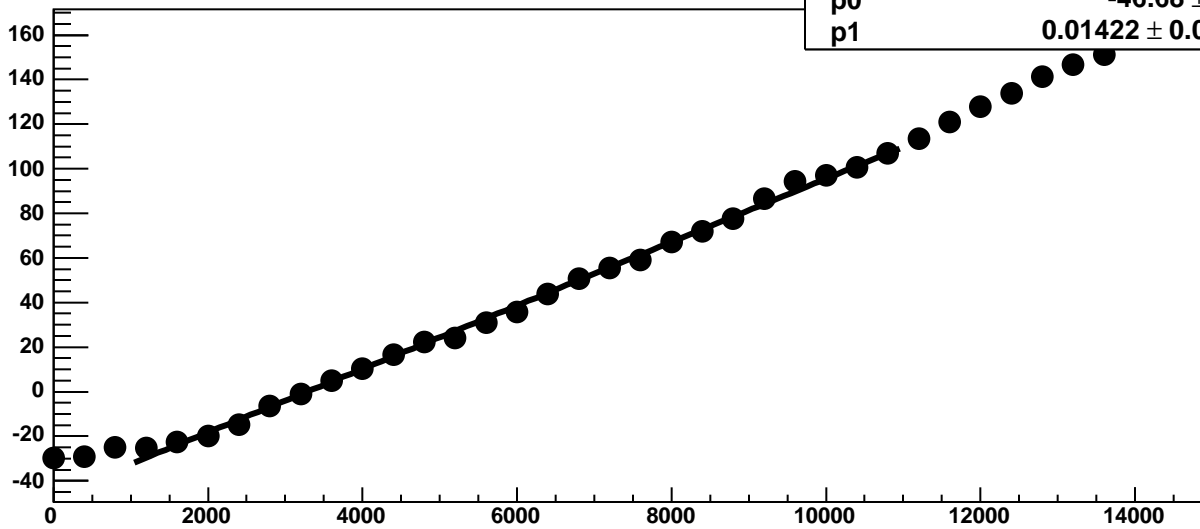
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

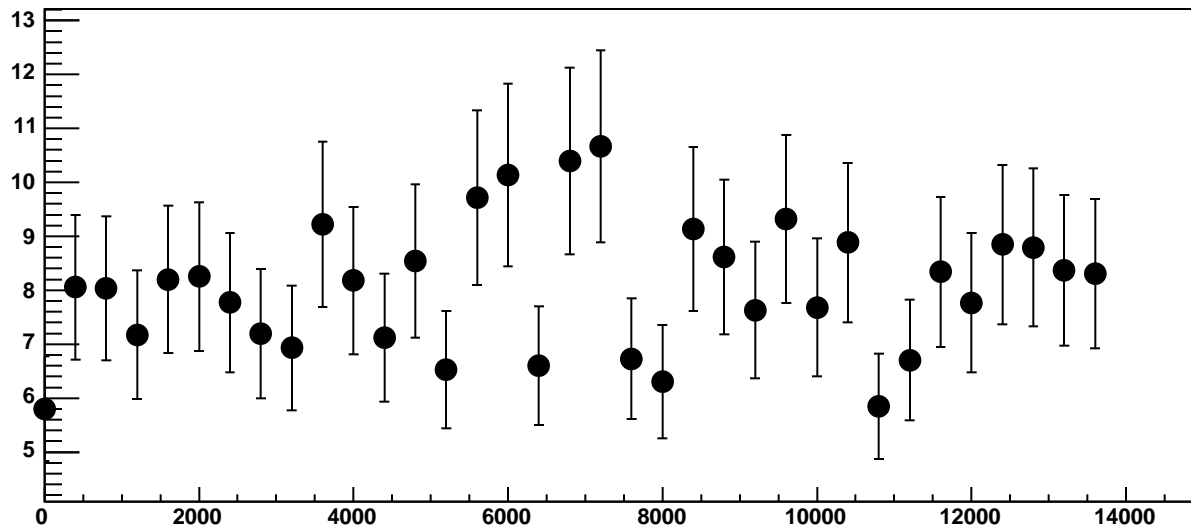


Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

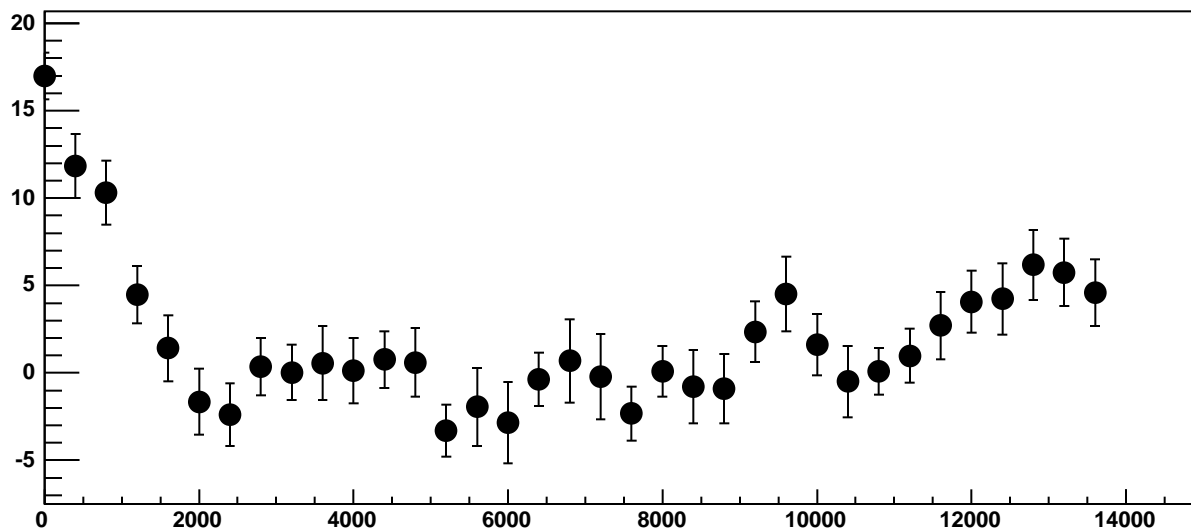


$\chi^2 / \text{ndf}$  27.91 / 23  
p0  $-46.68 \pm 0.8097$   
p1  $0.01422 \pm 0.0001204$

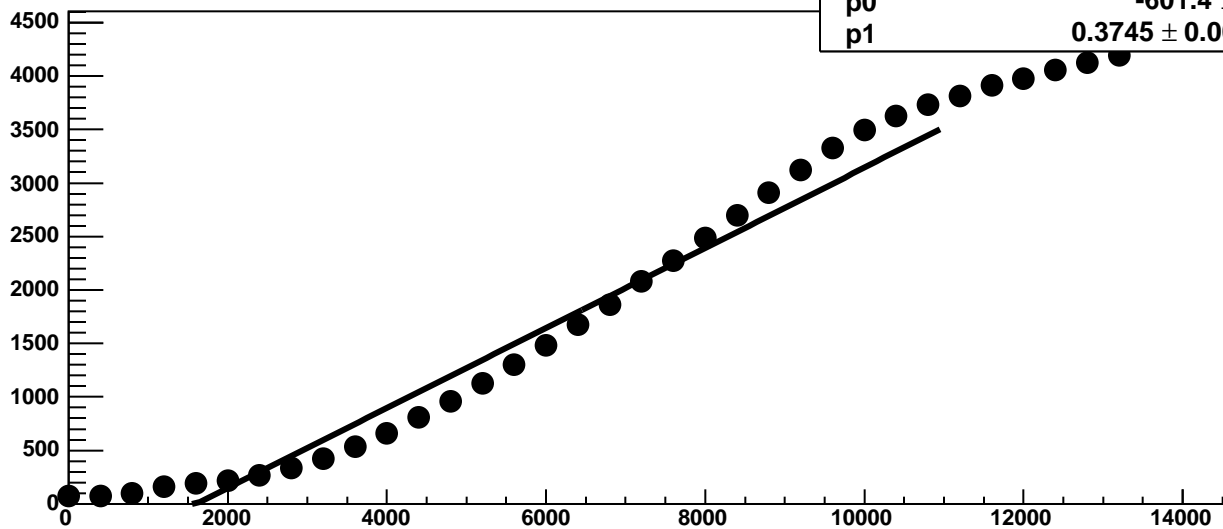
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

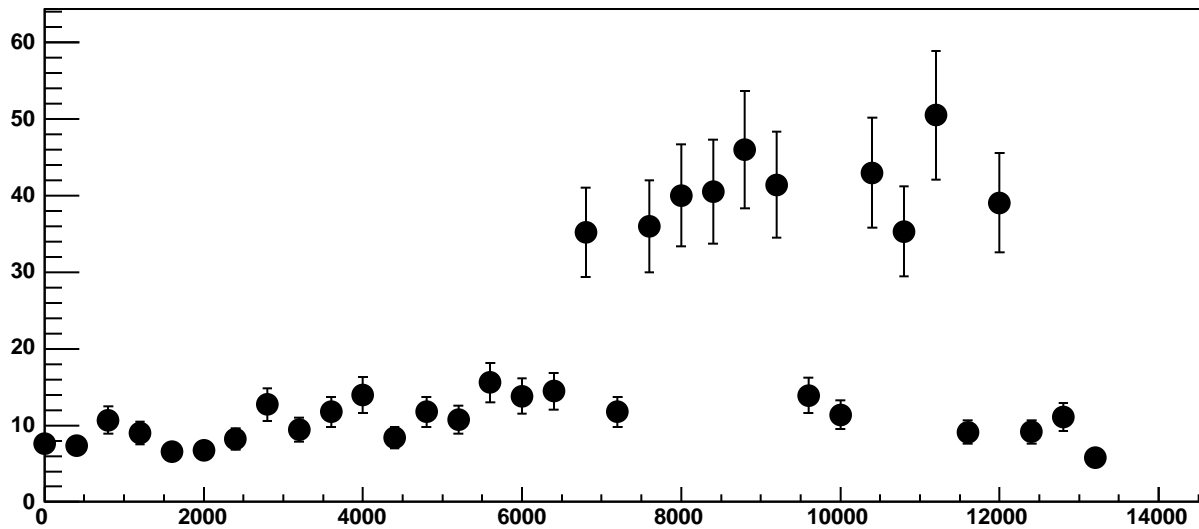


Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

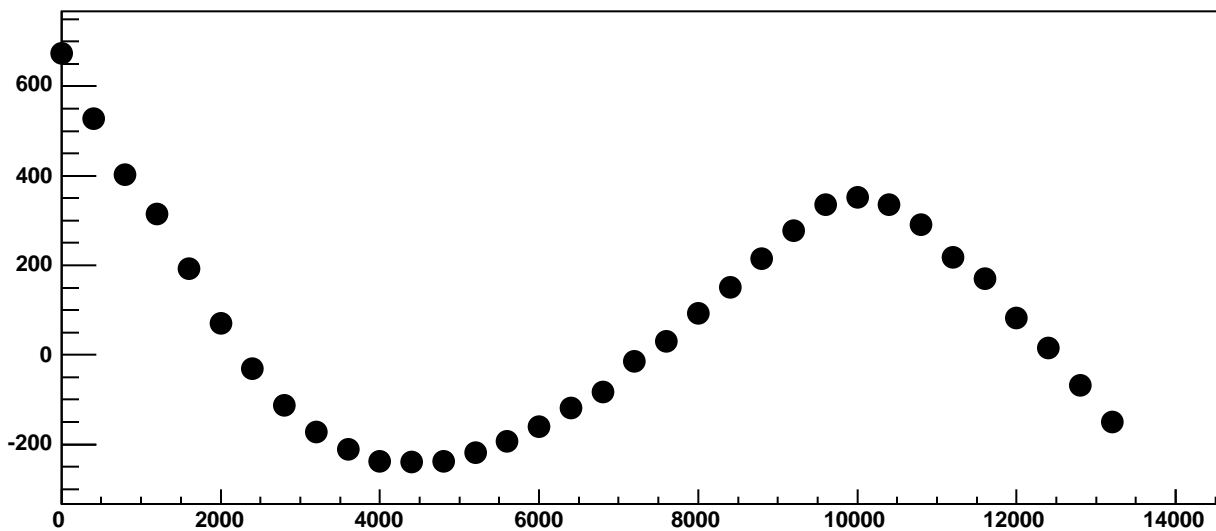


$\chi^2 / \text{ndf}$  1.326e+05 / 23  
p0 -601.4 ± 1.035  
p1 0.3745 ± 0.0002198

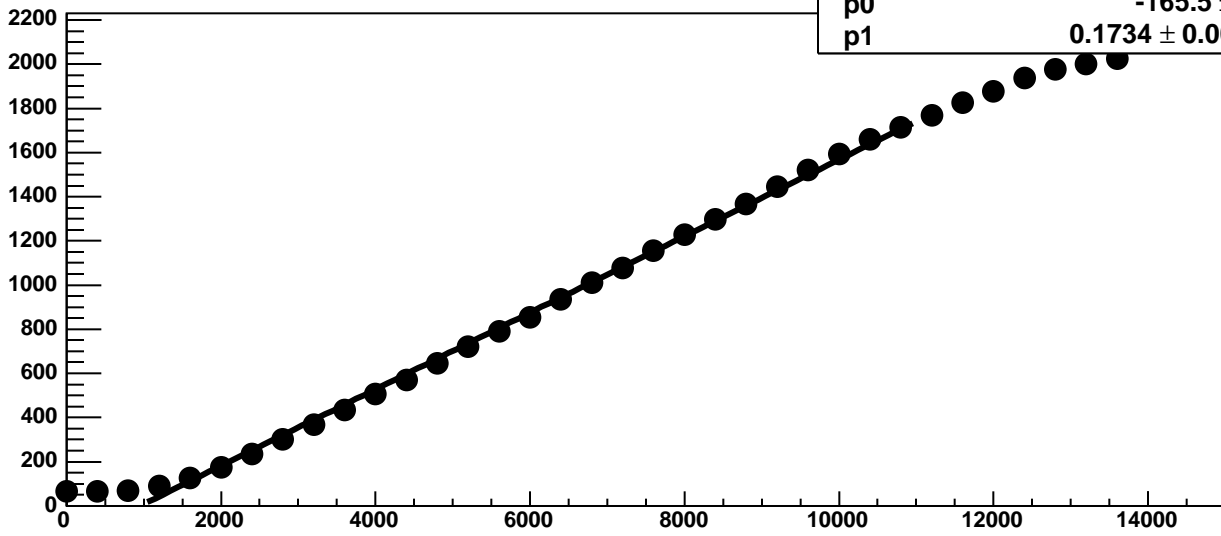
Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



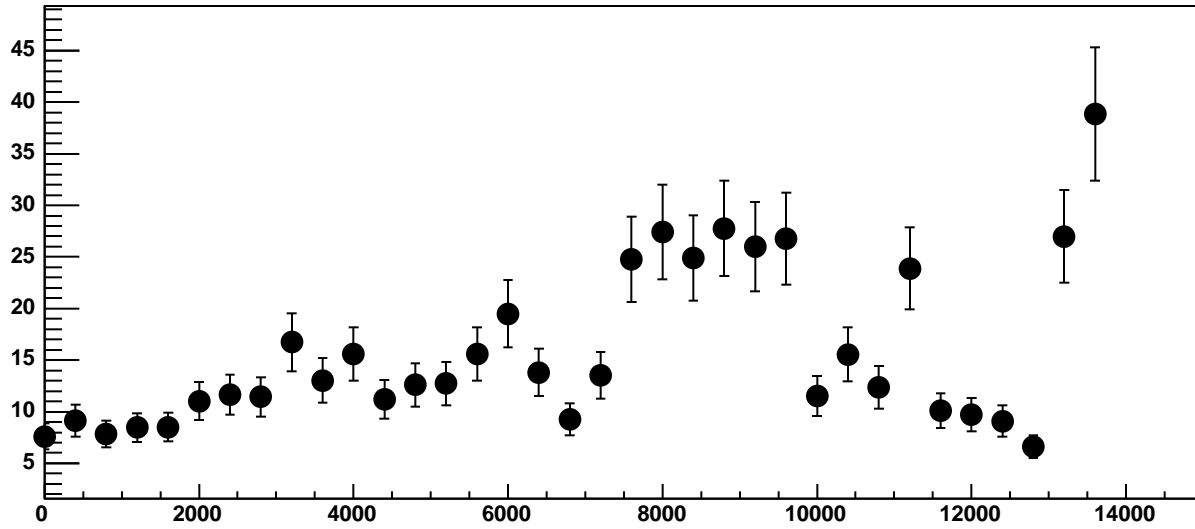
Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



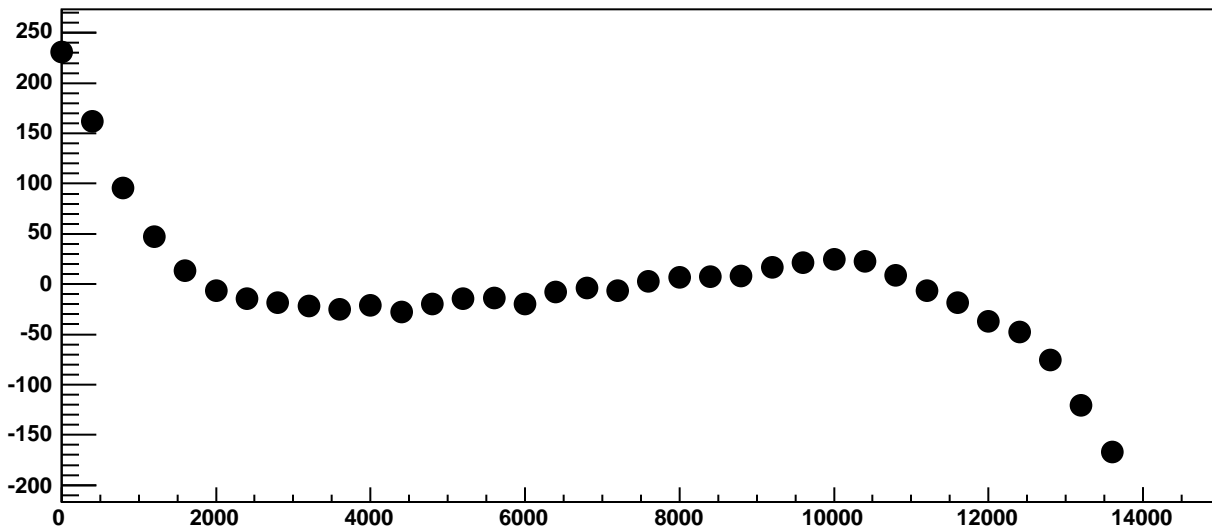
Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



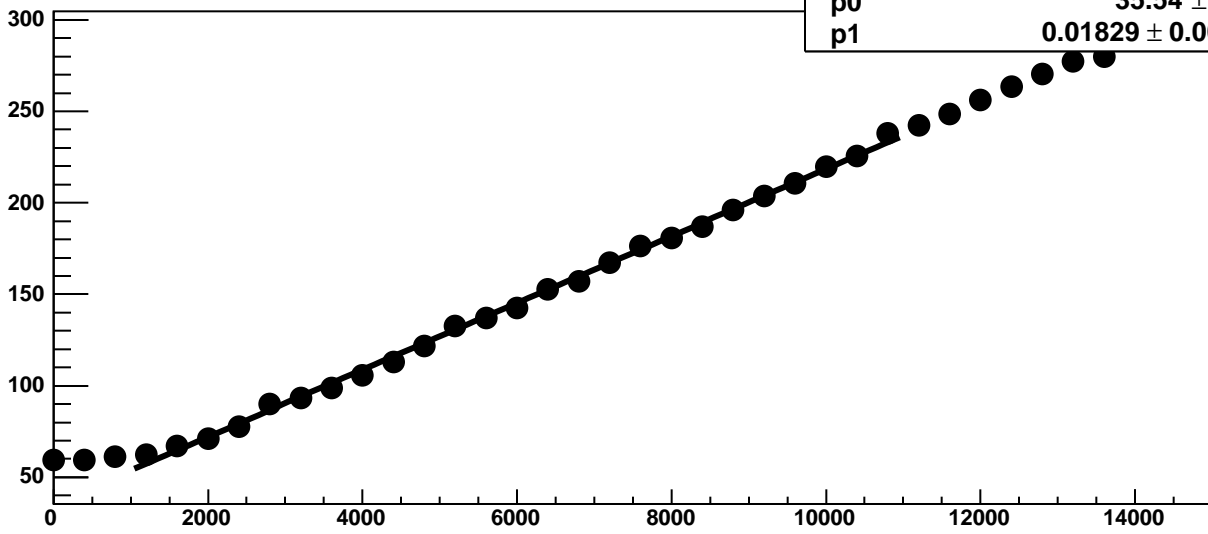
Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

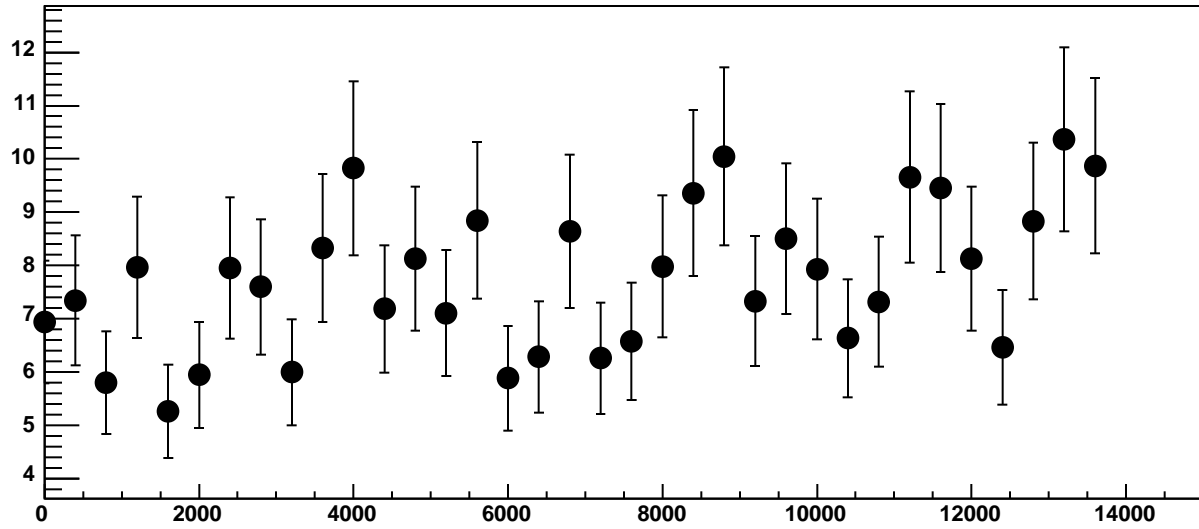


Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

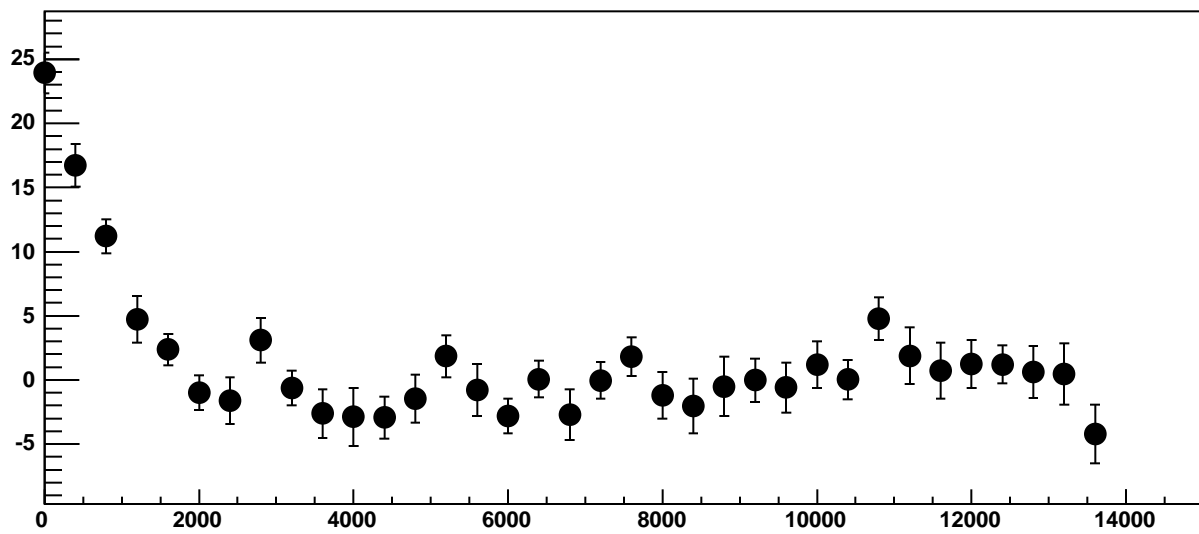


$\chi^2 / \text{ndf}$  41.71 / 23  
p0  $35.54 \pm 0.7298$   
p1  $0.01829 \pm 0.0001133$

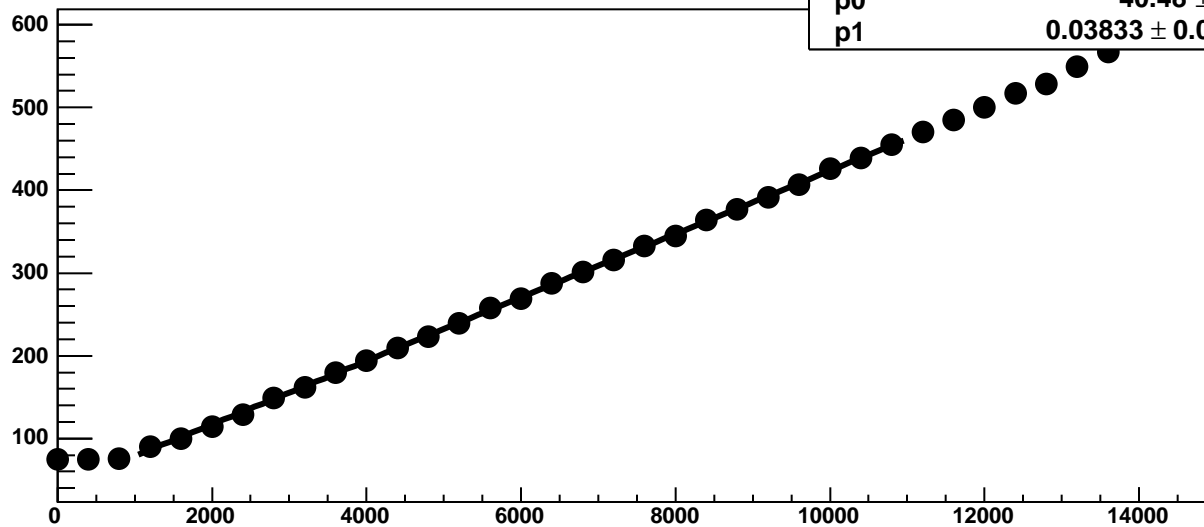
Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

32.14 / 23

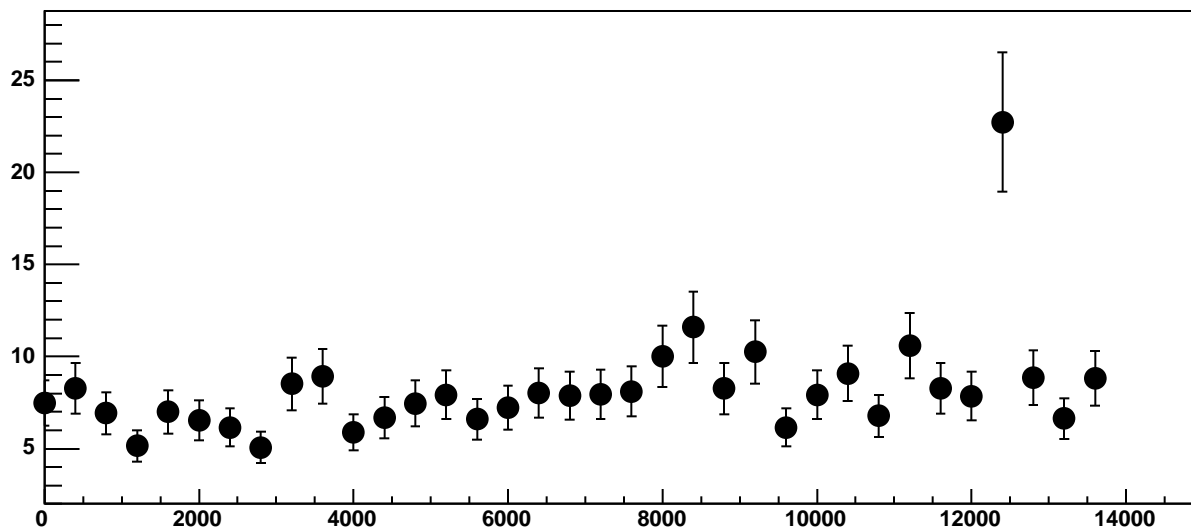
p0

$40.48 \pm 0.6808$

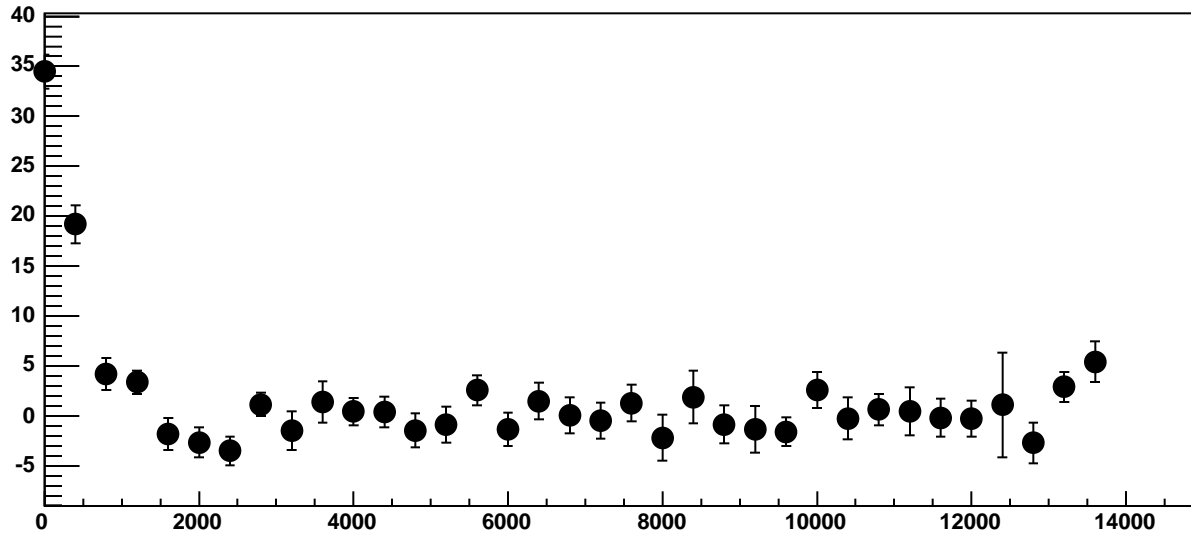
p1

$0.03833 \pm 0.0001108$

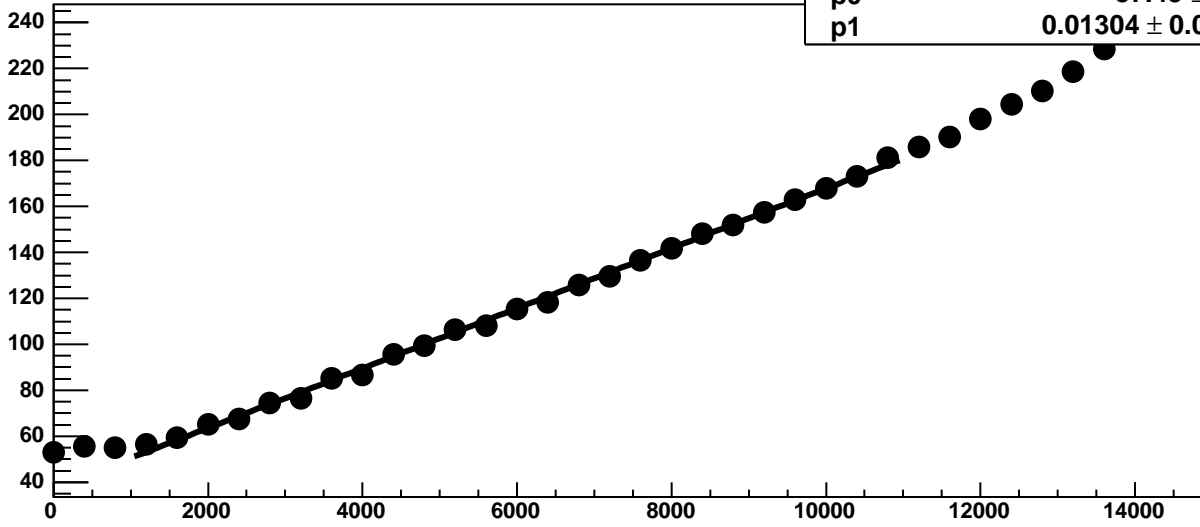
Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18.05 / 23

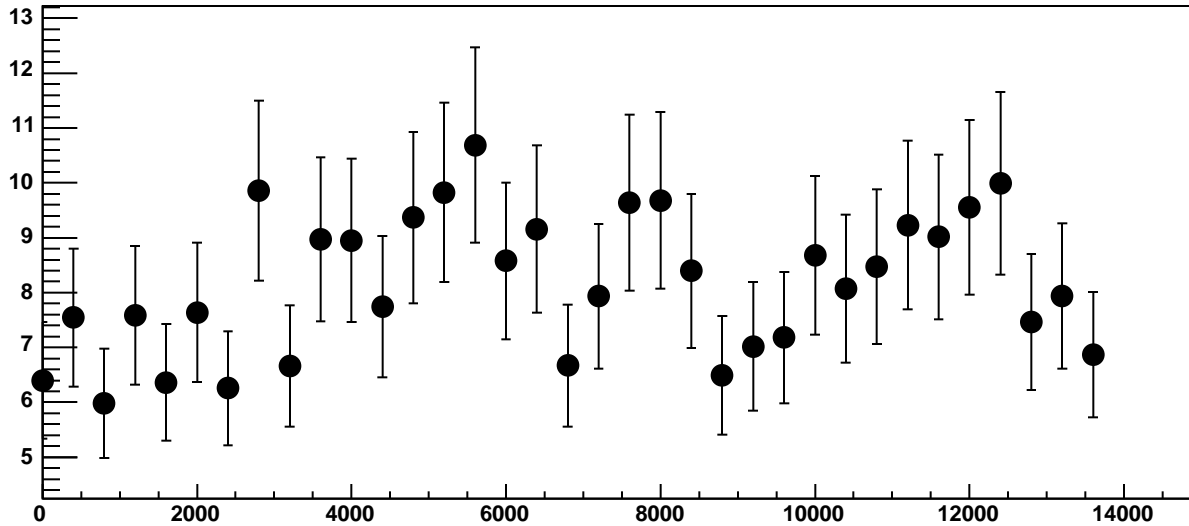
p0

$37.45 \pm 0.7965$

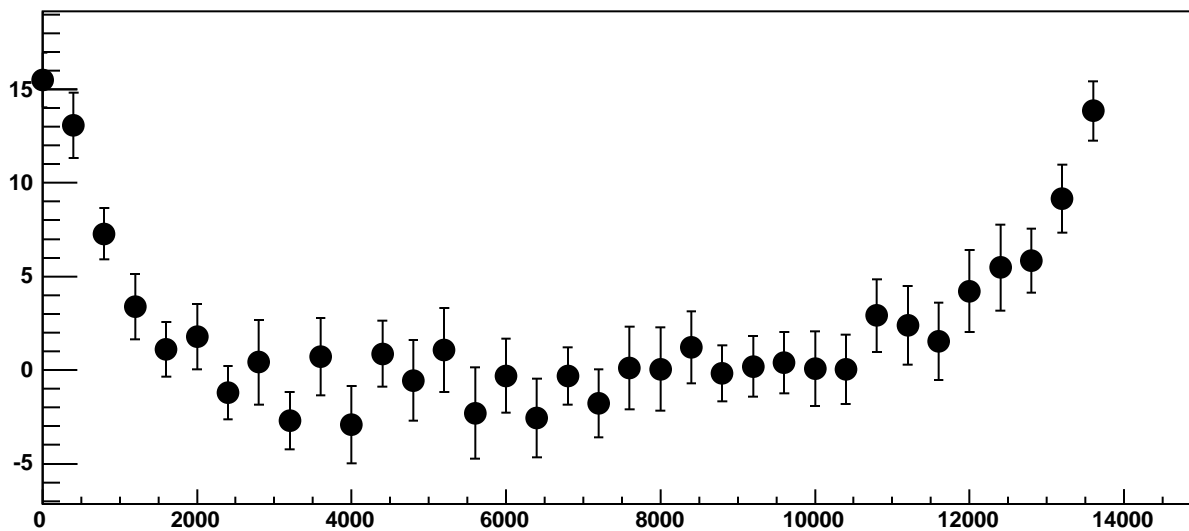
p1

$0.01304 \pm 0.0001207$

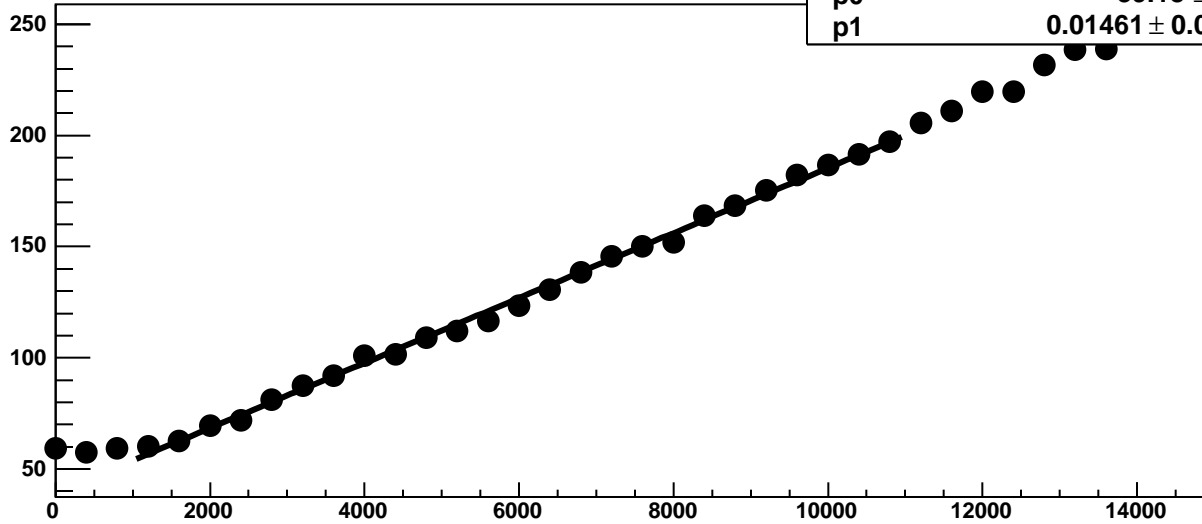
Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



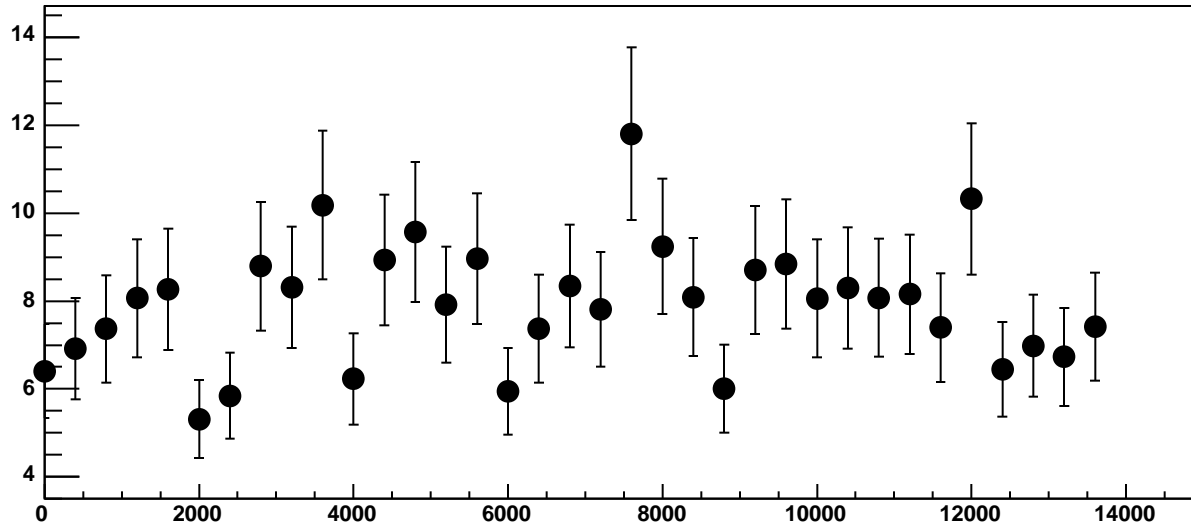
Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



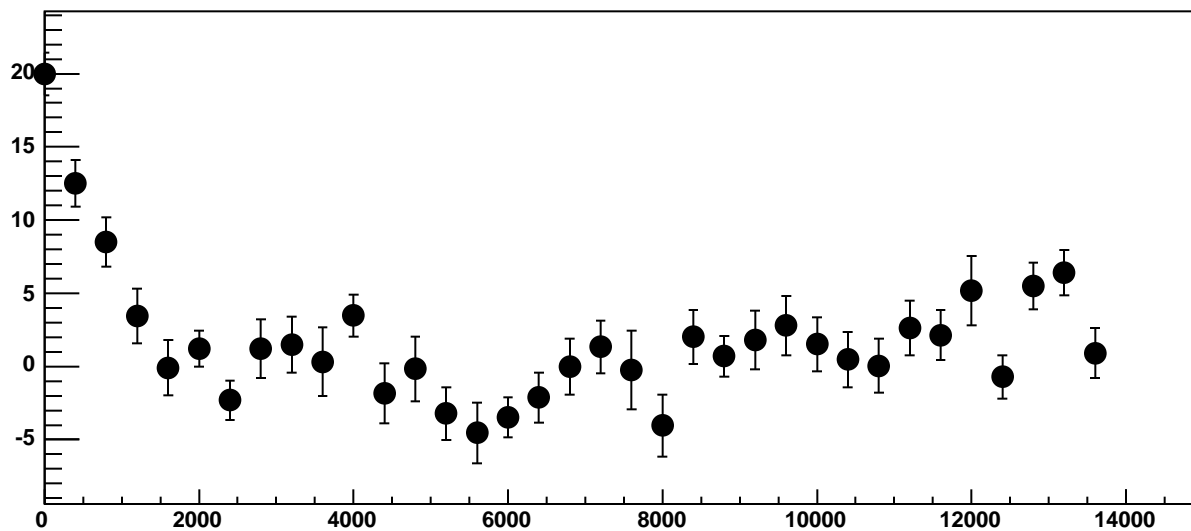
Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

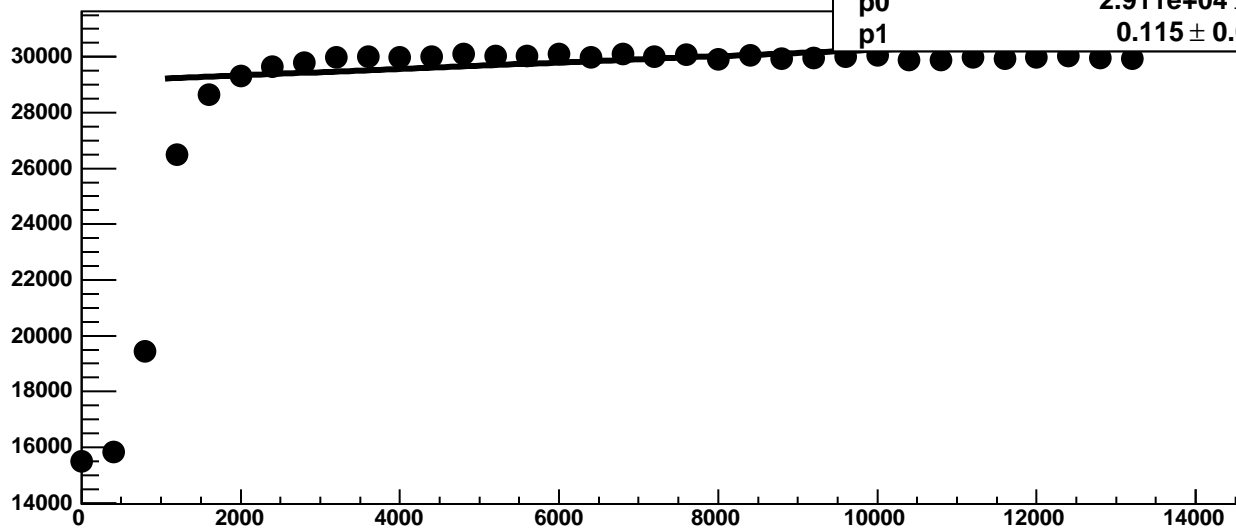


Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

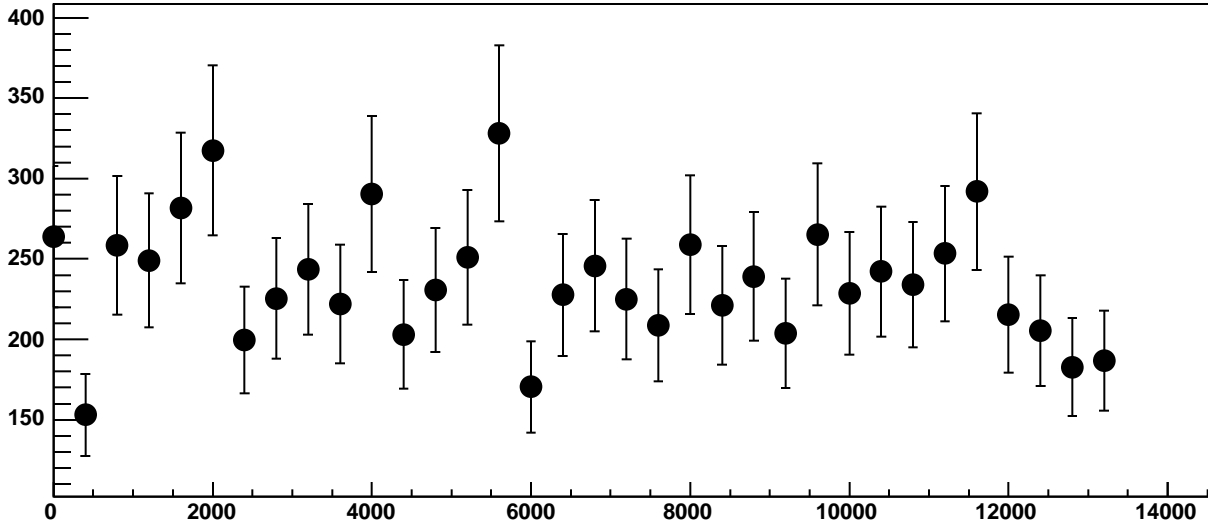




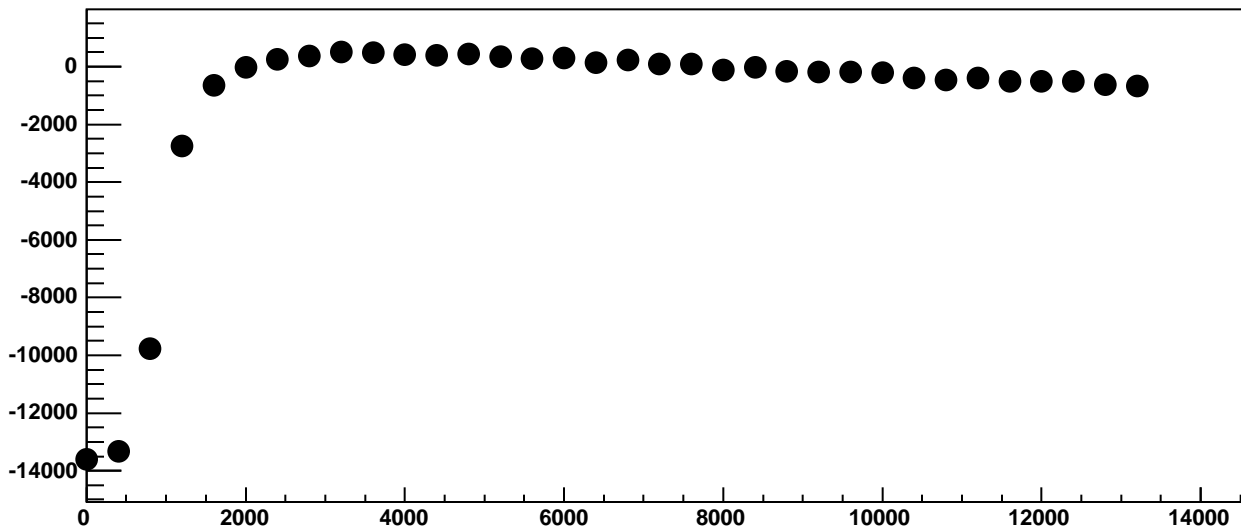
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



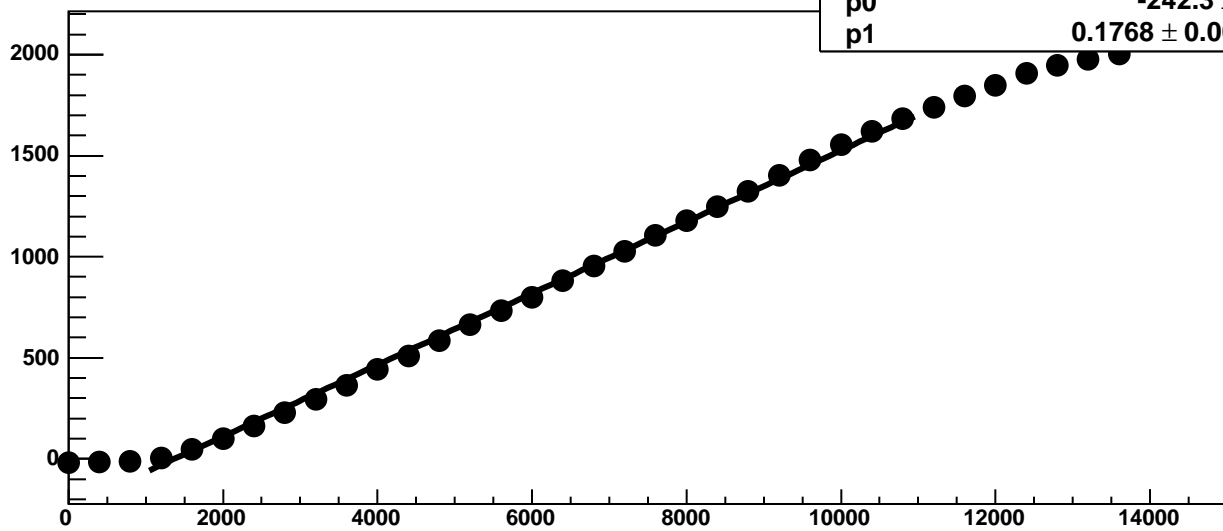
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



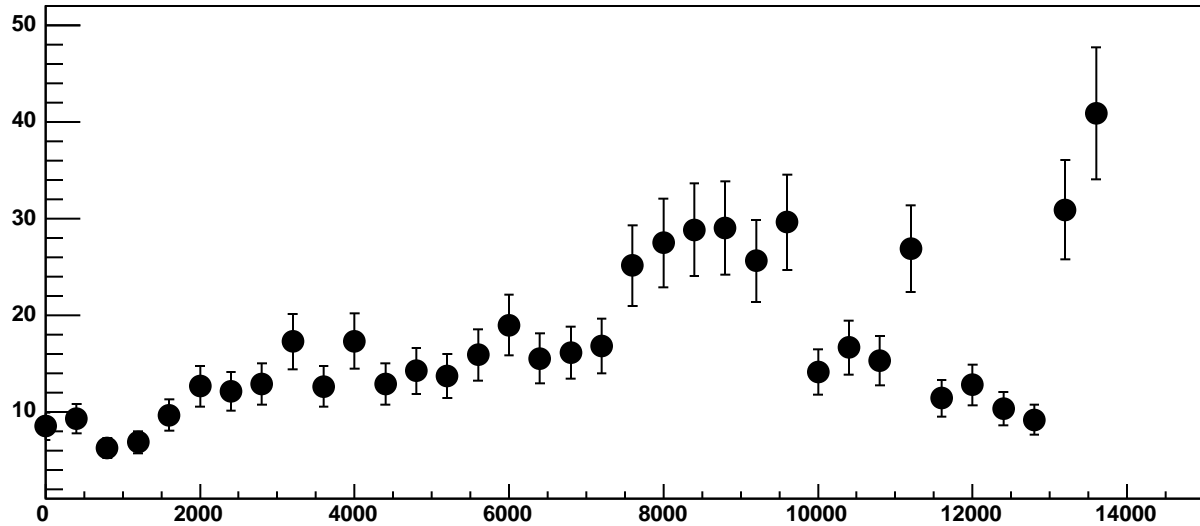
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



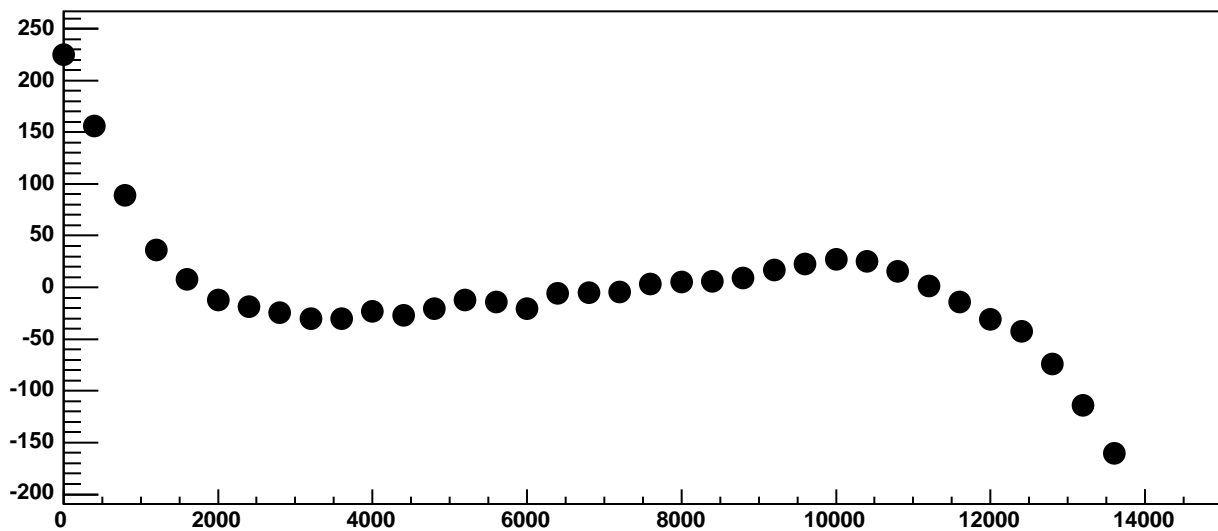
Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



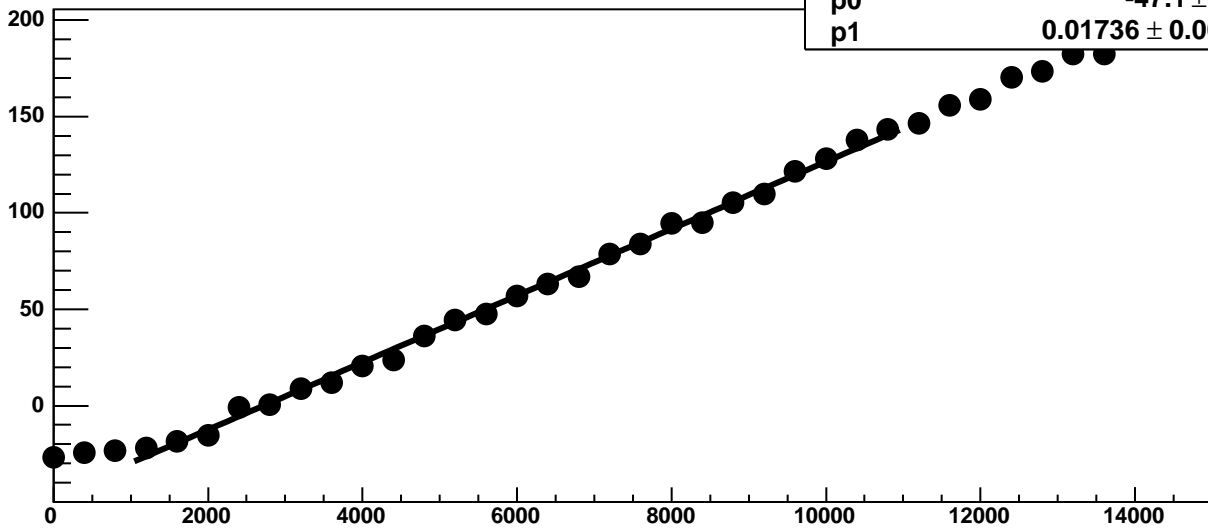
Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

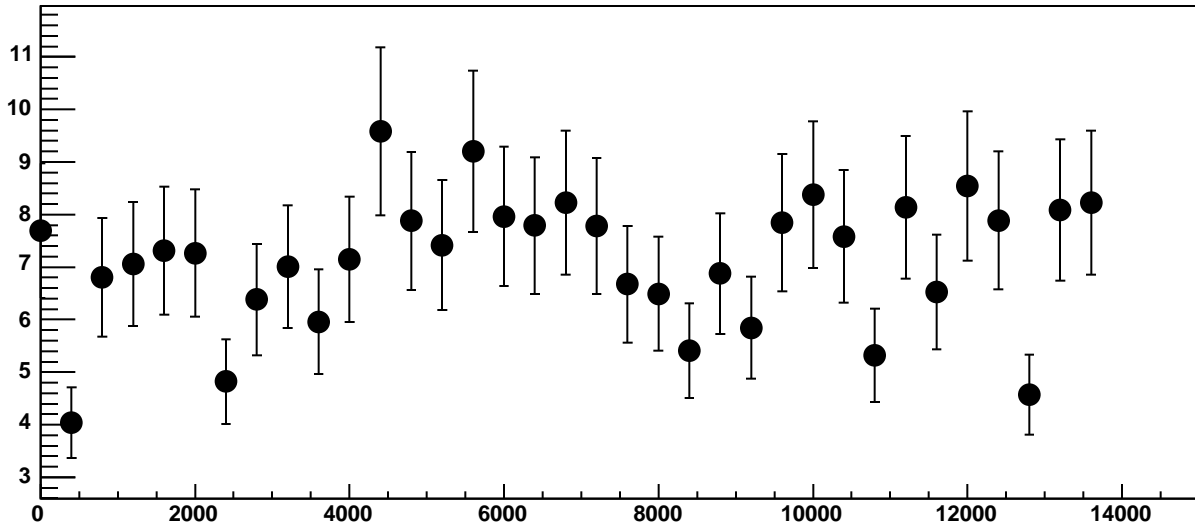


Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

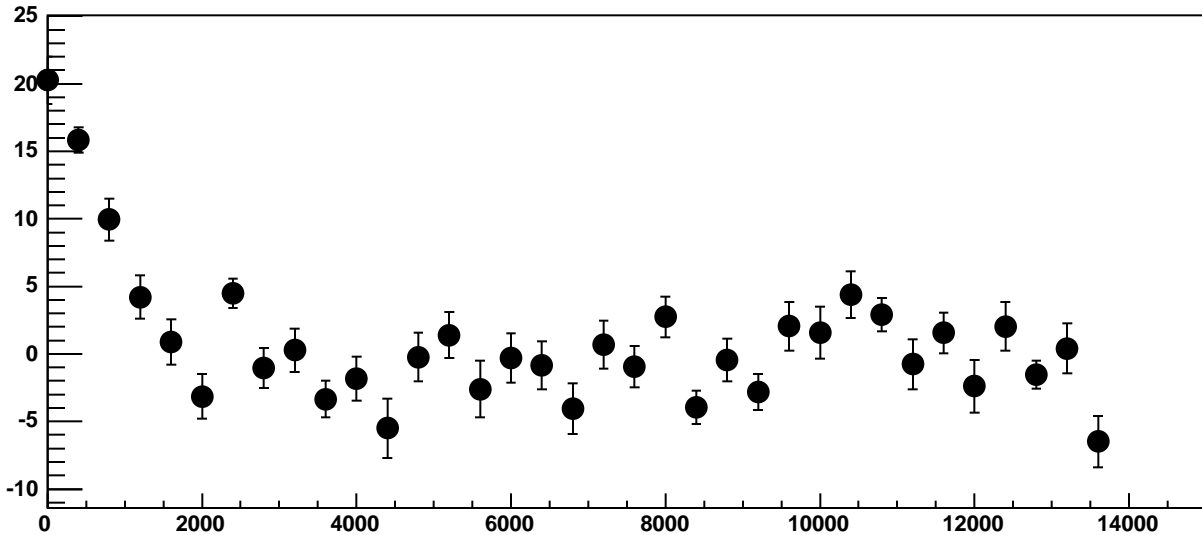


$\chi^2 / \text{ndf}$  80.77 / 23  
p0  $-47.1 \pm 0.7027$   
p1  $0.01736 \pm 0.0001043$

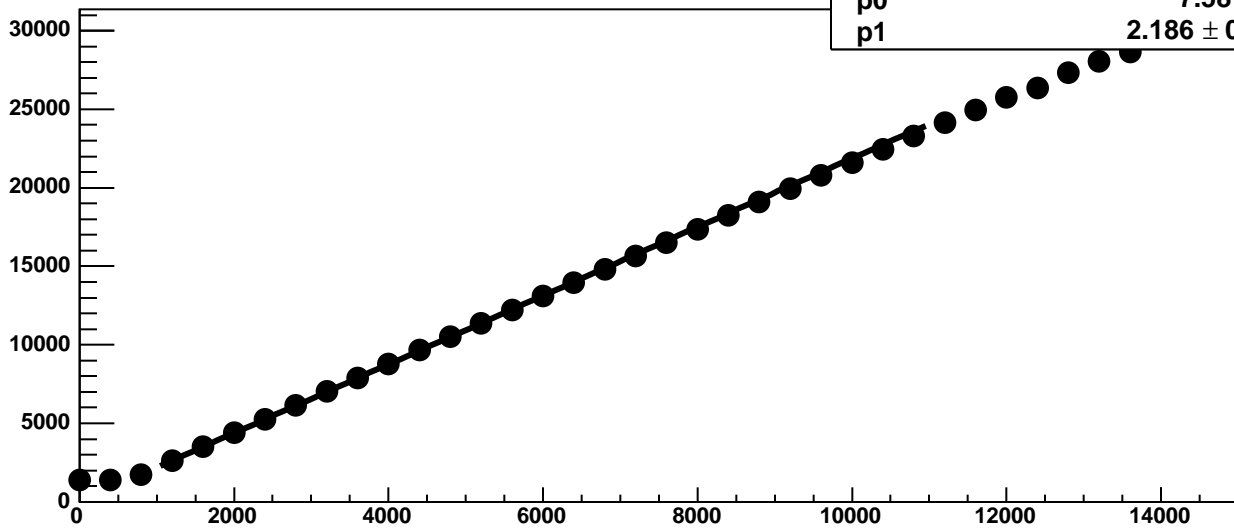
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



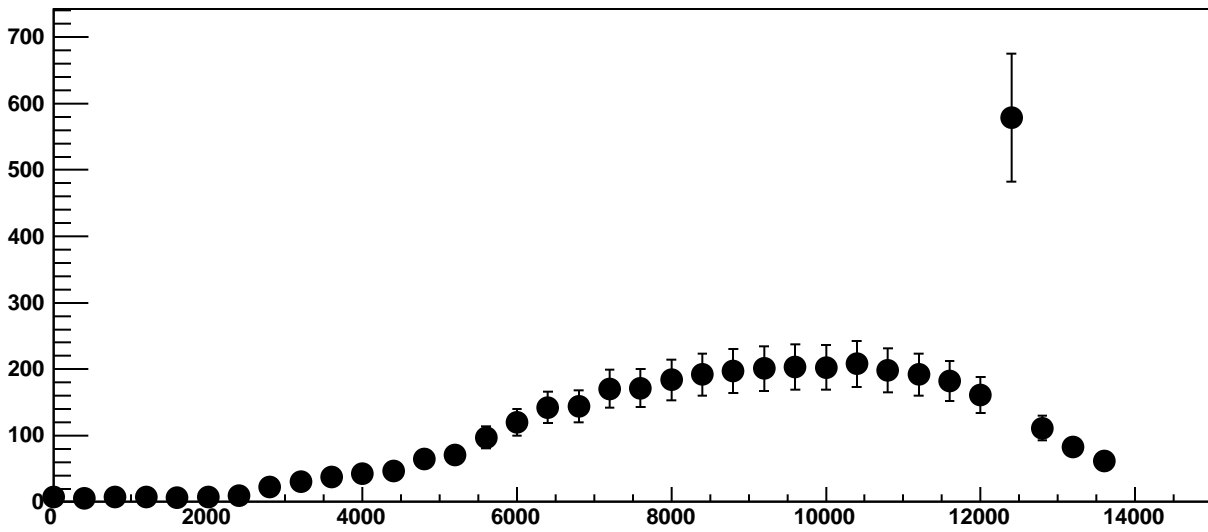
Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



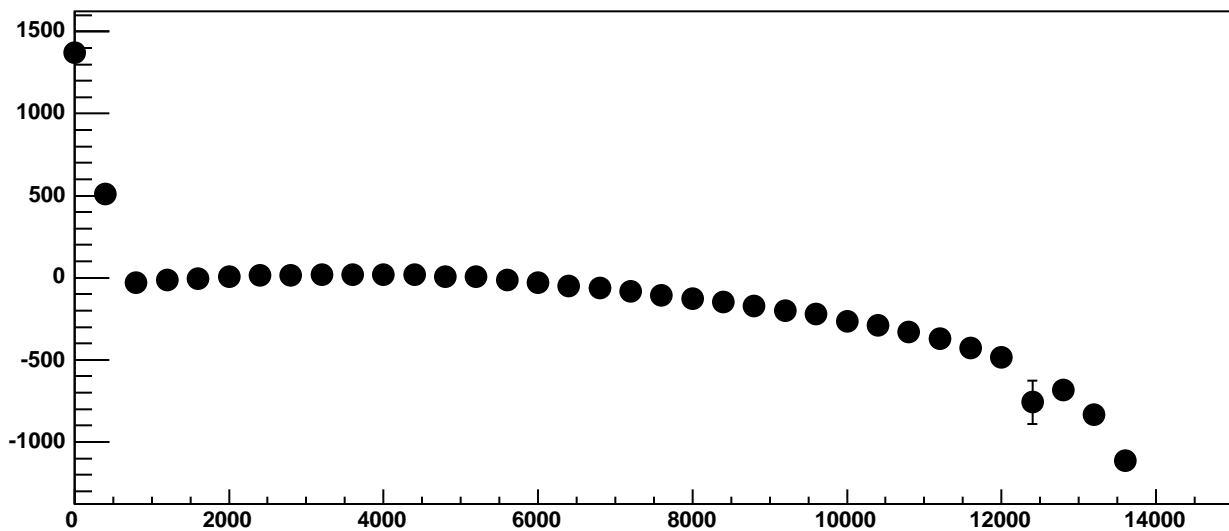
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



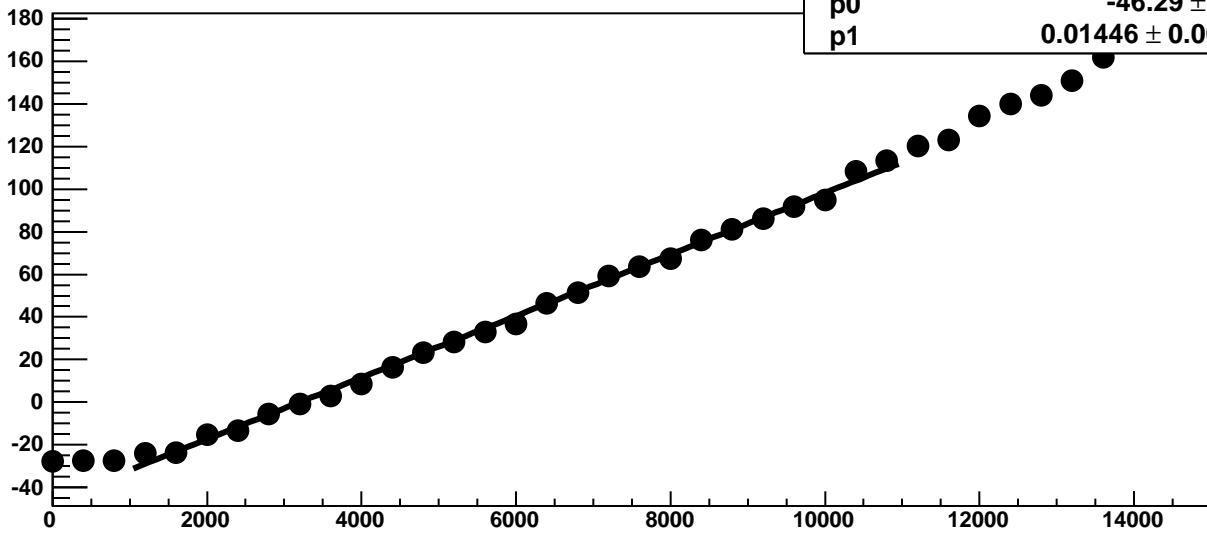
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



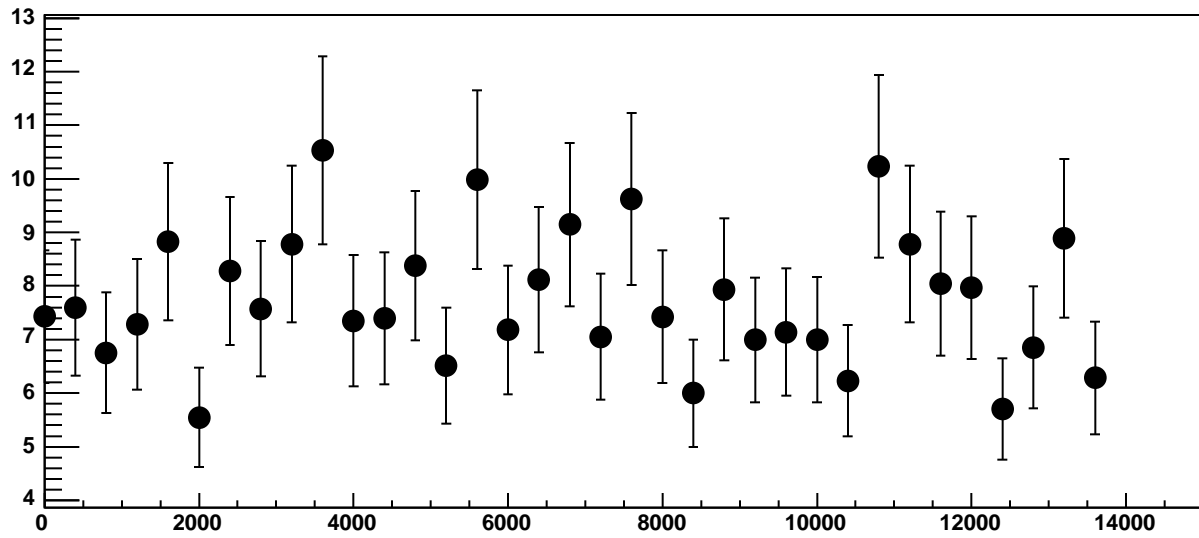
Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC



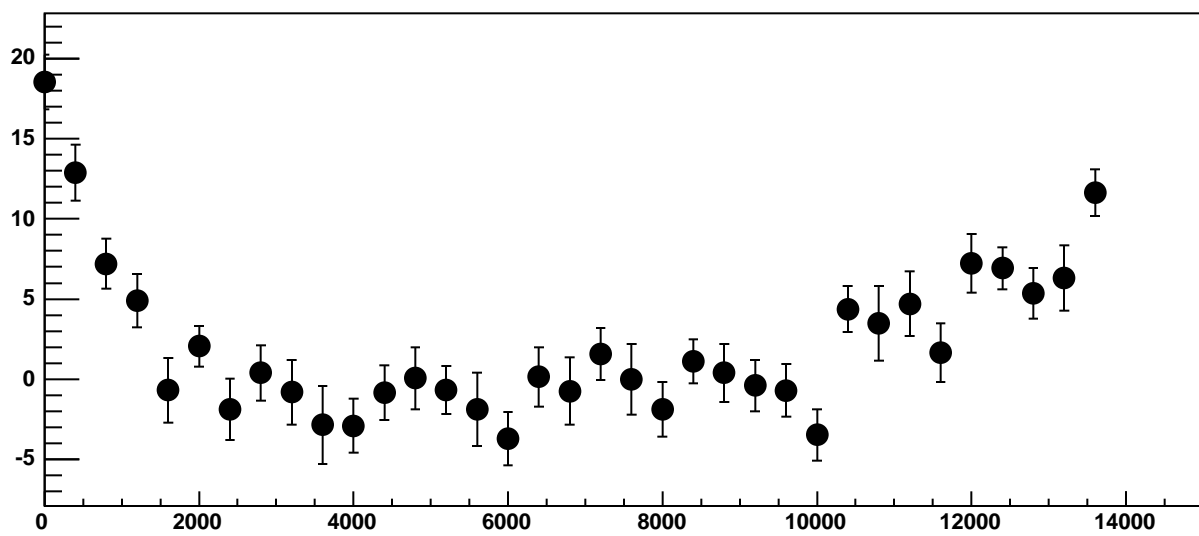
Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



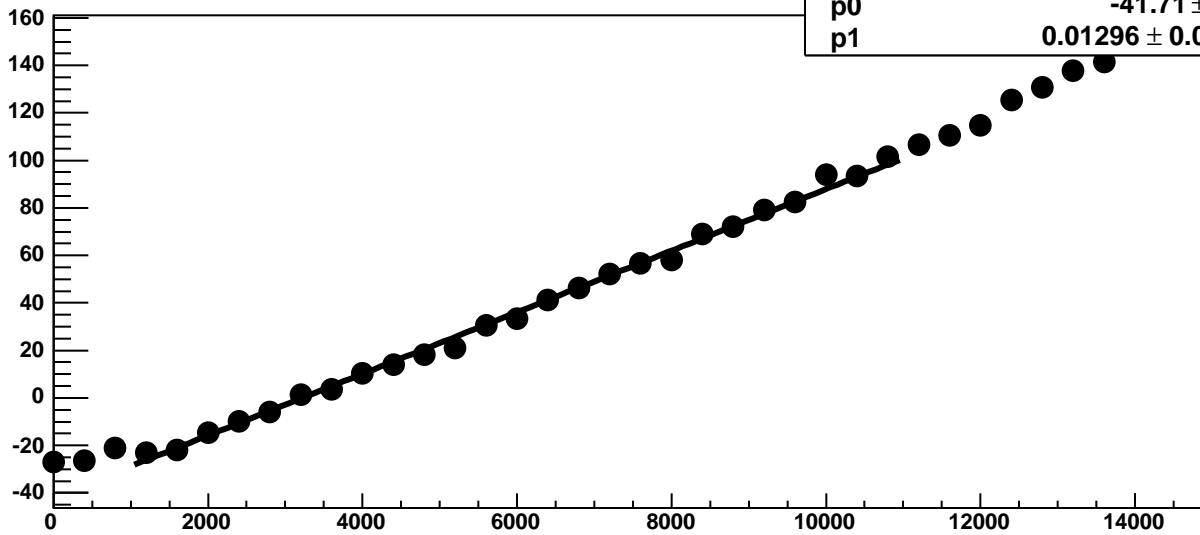
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

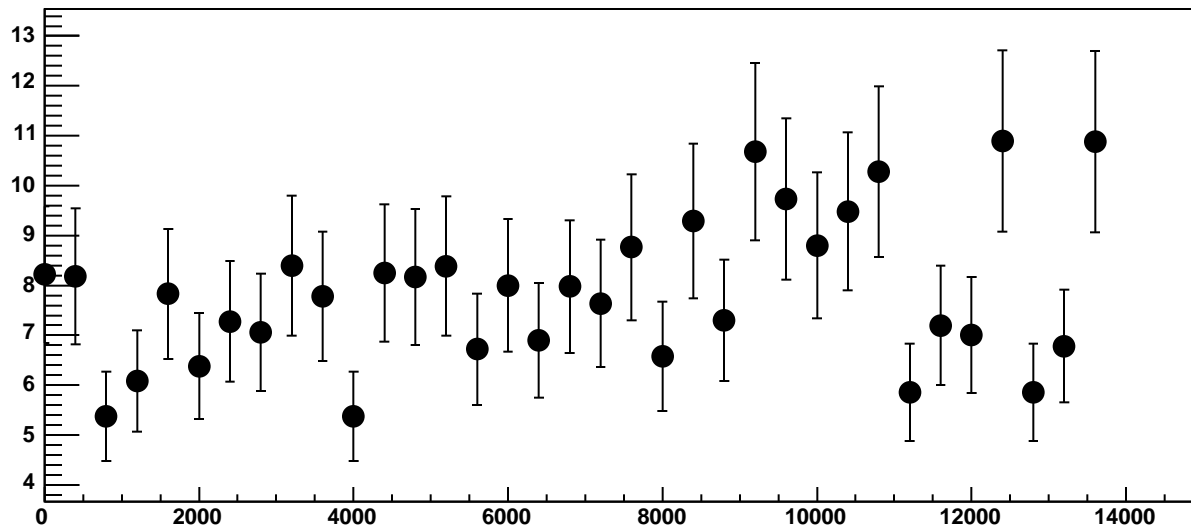


Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

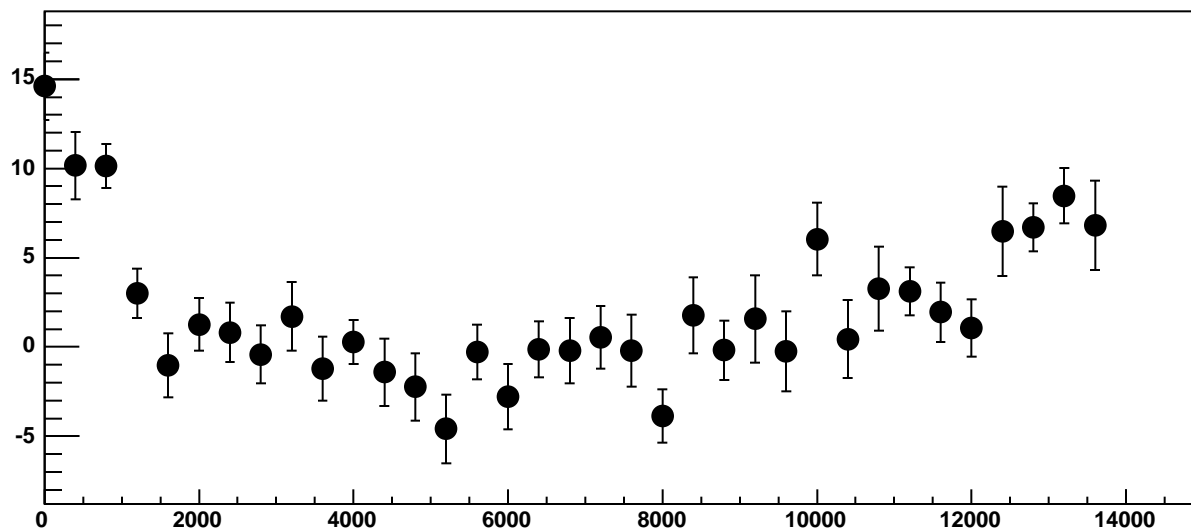


$\chi^2 / \text{ndf}$  36.09 / 23  
p0 -41.71 ± 0.7705  
p1 0.01296 ± 0.0001262

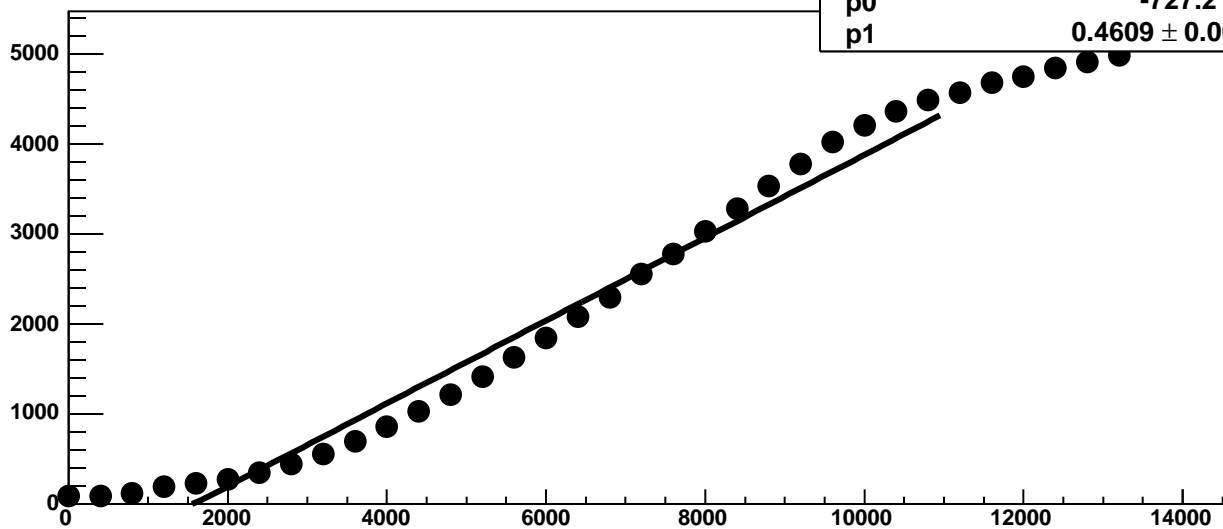
Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



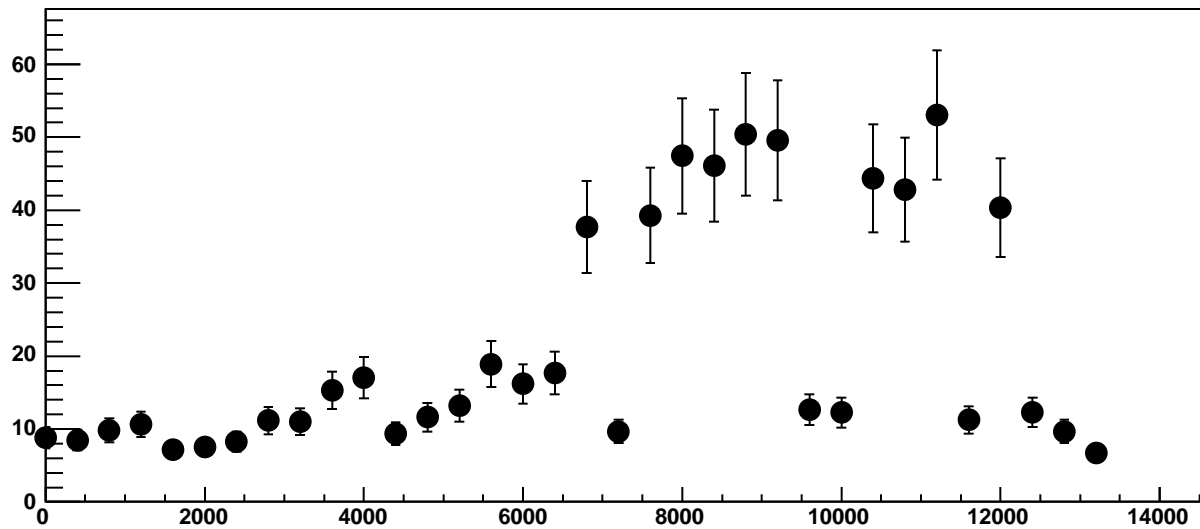
Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



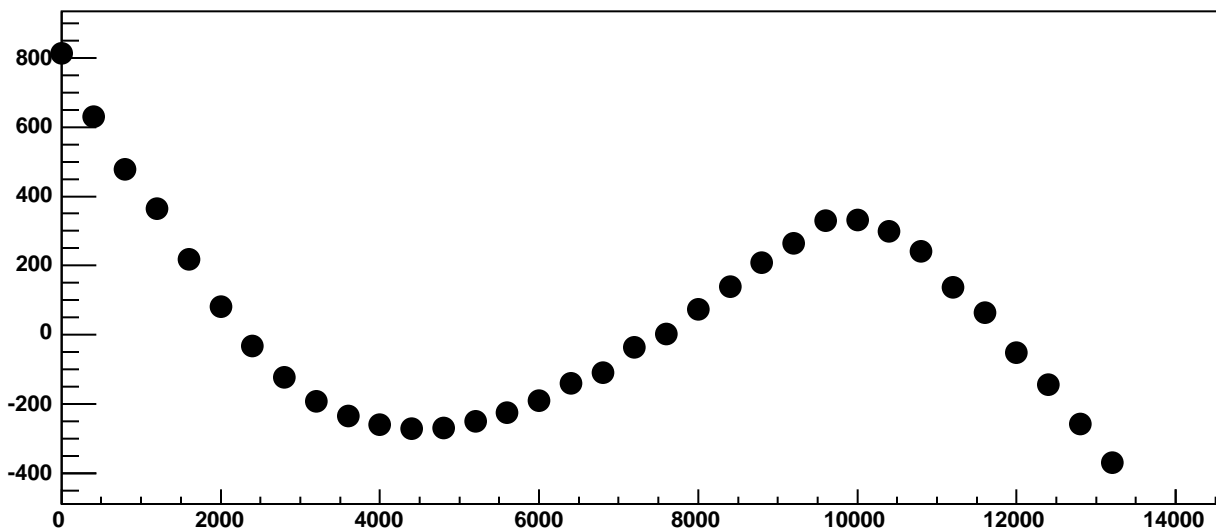
Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



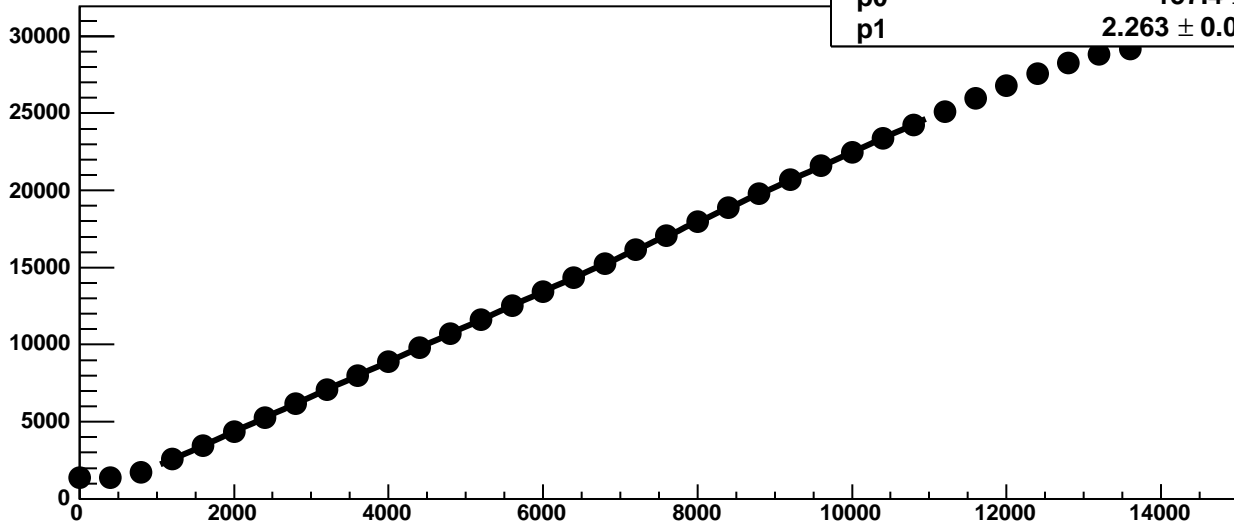
Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

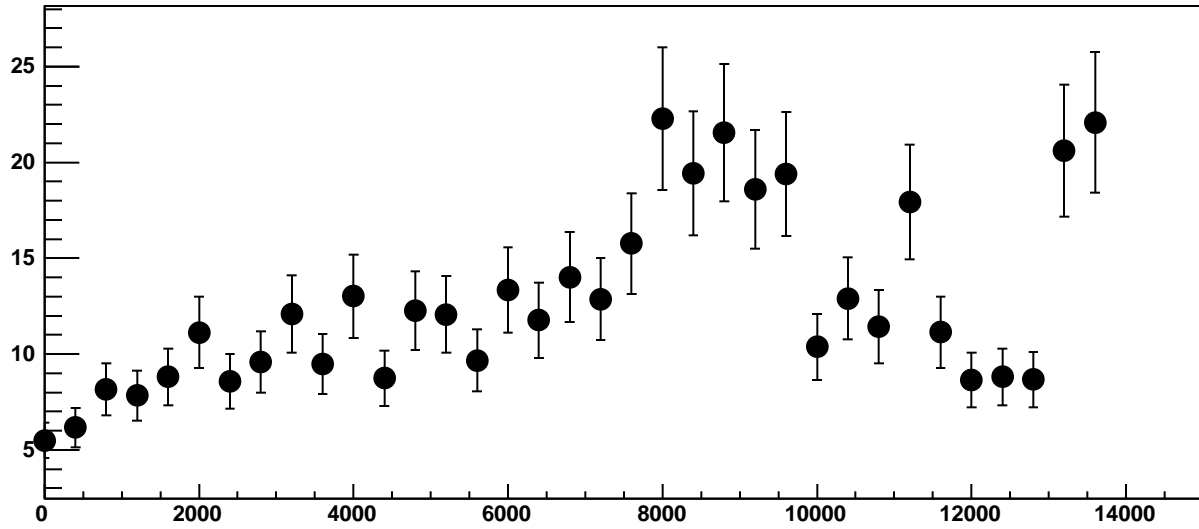


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC

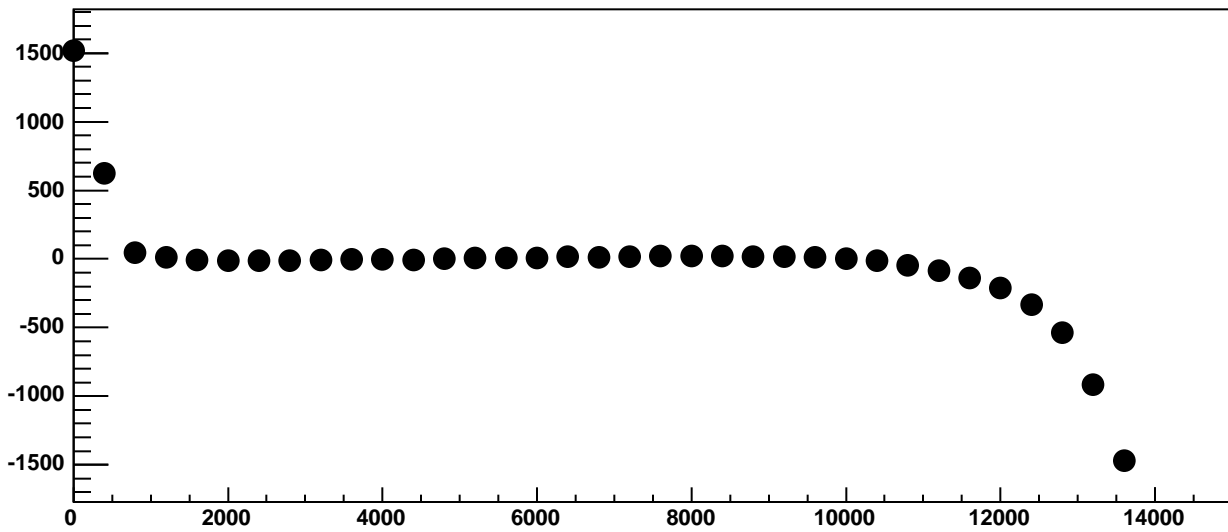


$\chi^2 / \text{ndf}$  742.8 / 23  
p0 -157.4 ± 1.046  
p1 2.263 ± 0.0001811

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

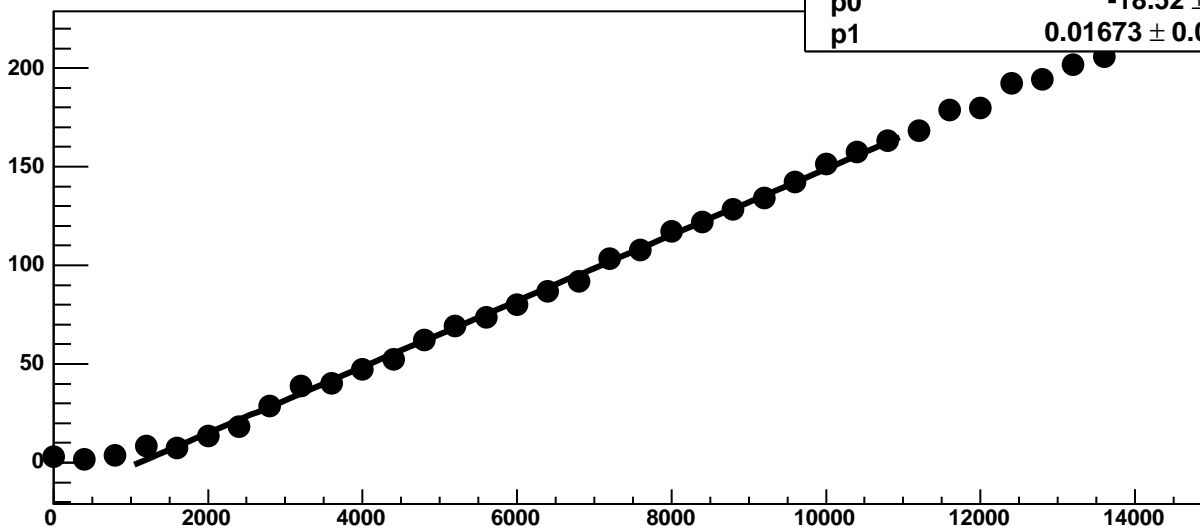


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



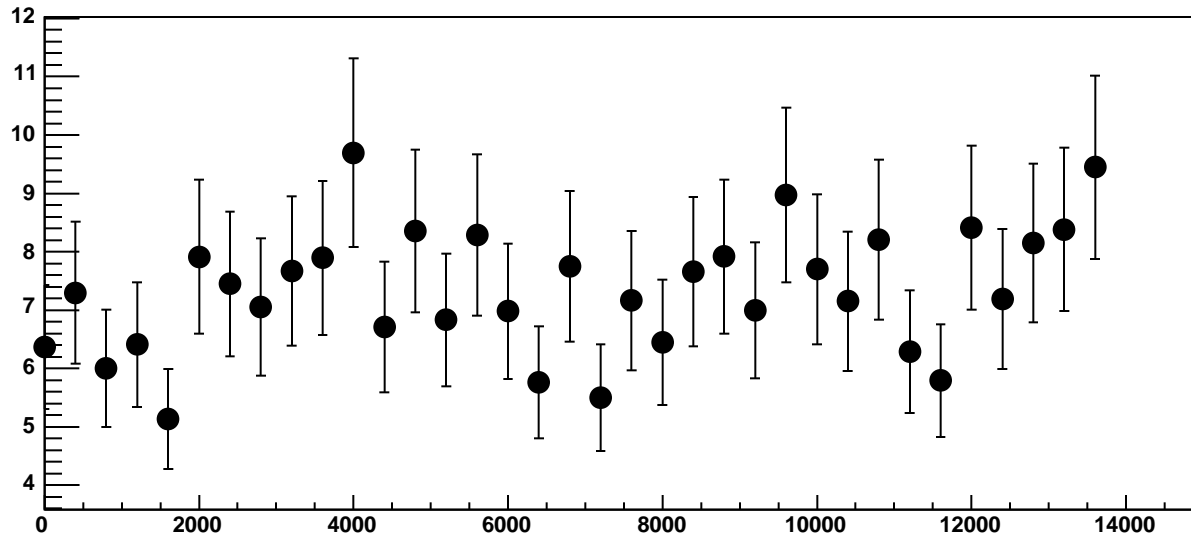


Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

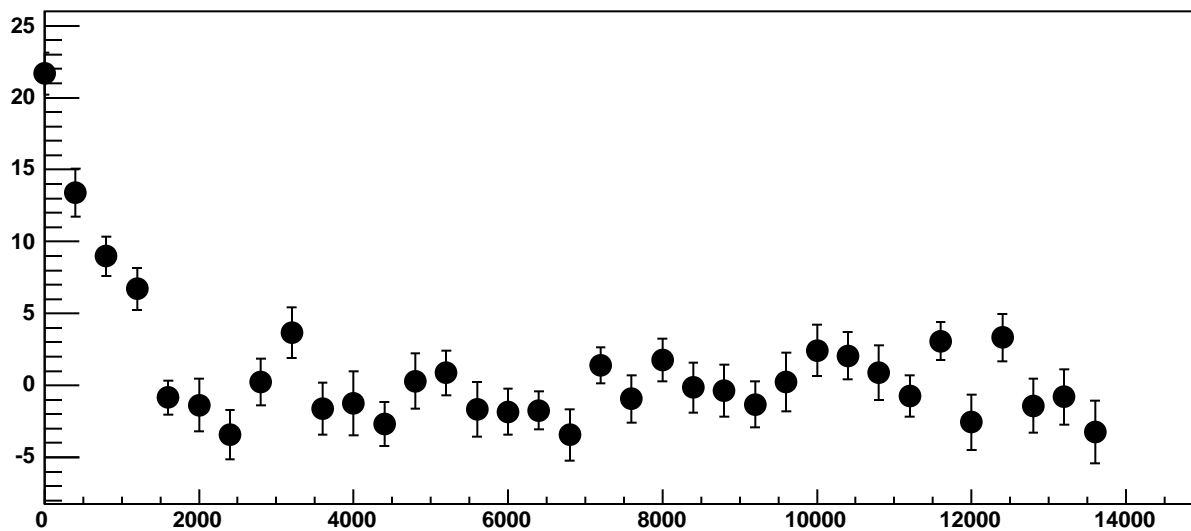


$\chi^2 / \text{ndf}$  49.66 / 23  
p0  $-18.52 \pm 0.7327$   
p1  $0.01673 \pm 0.0001129$

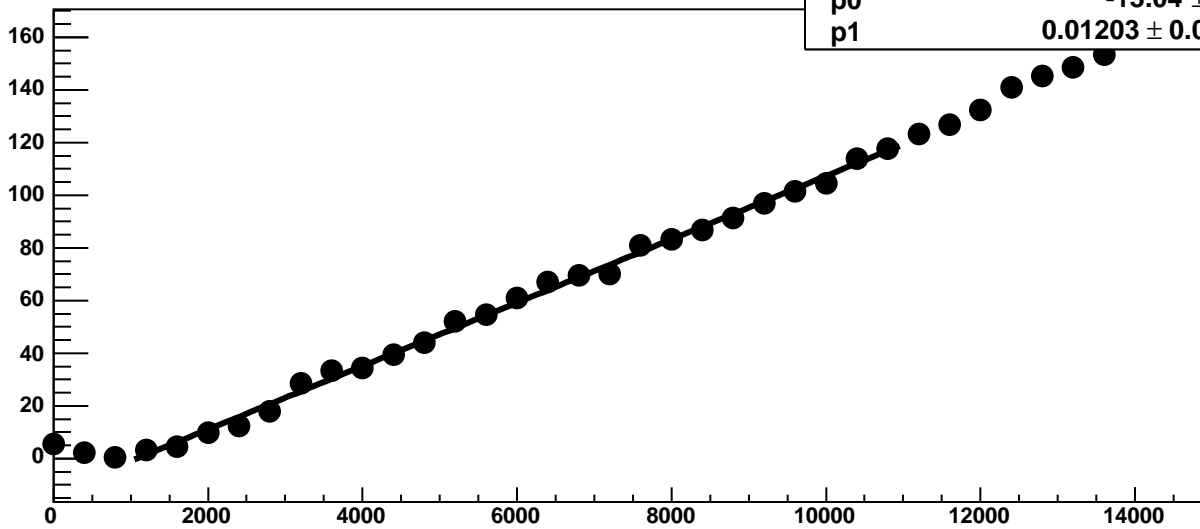
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



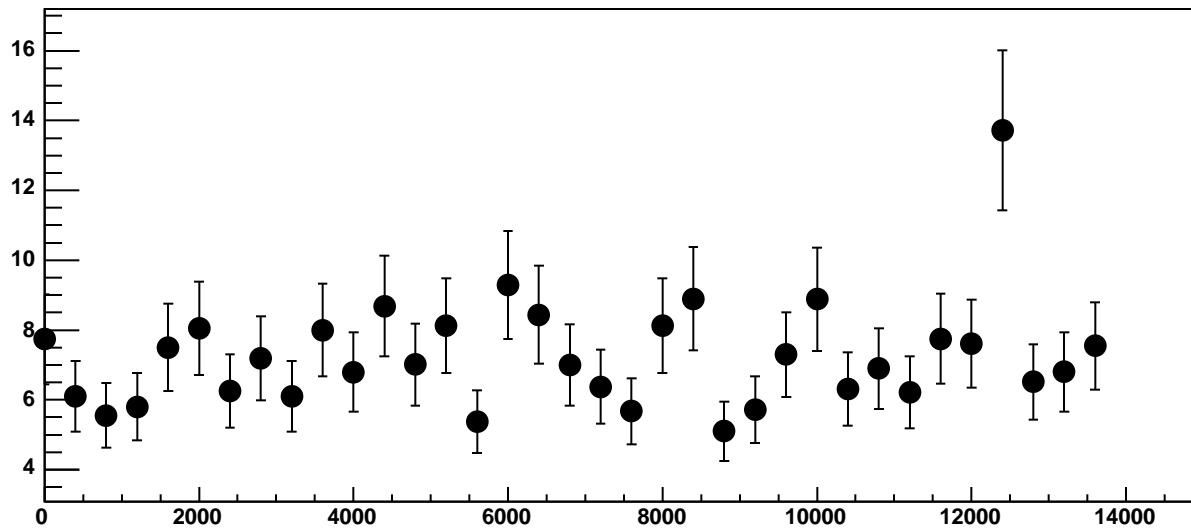
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



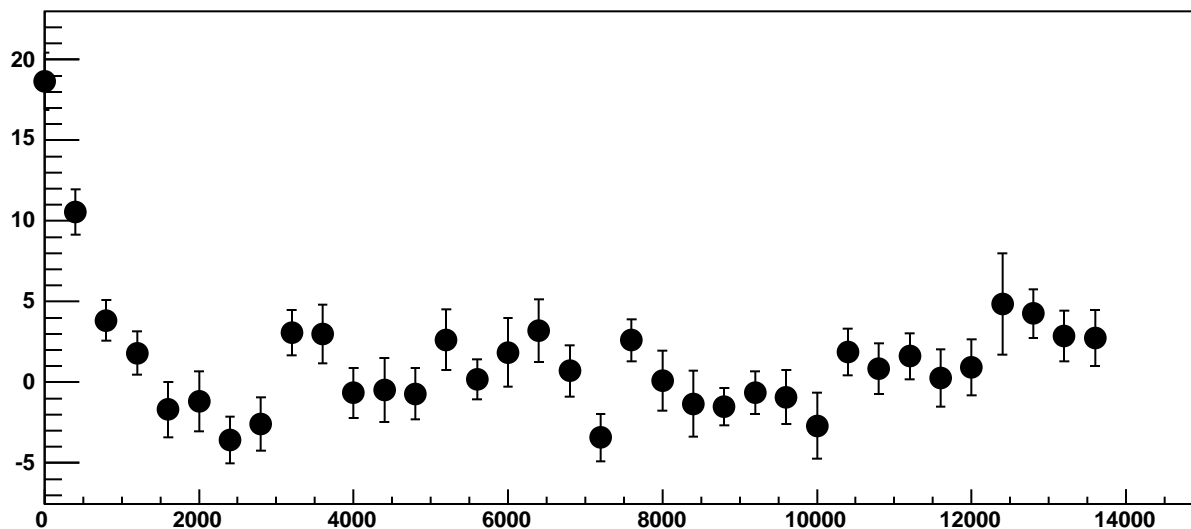
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



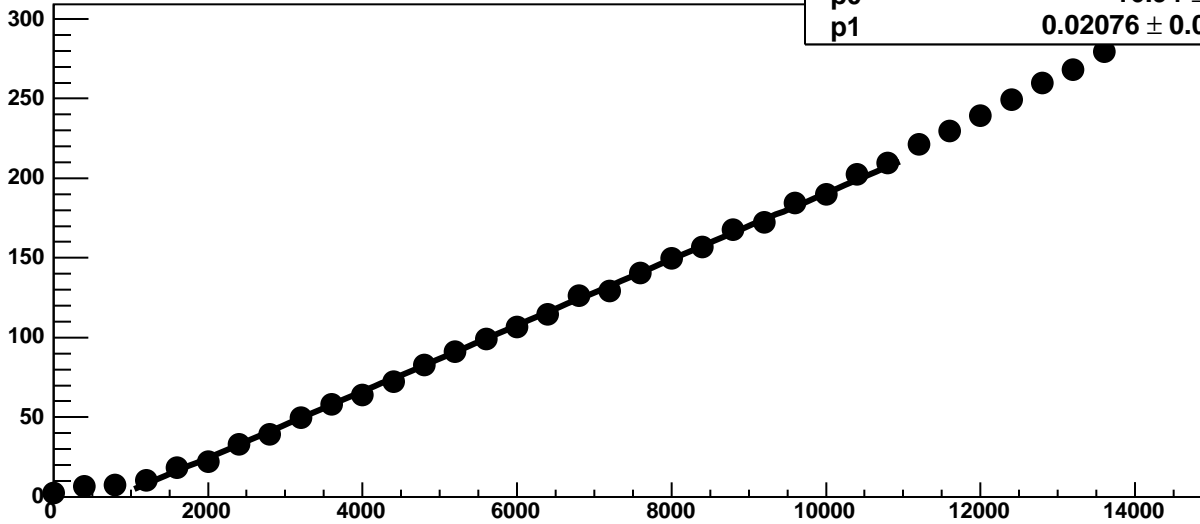
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

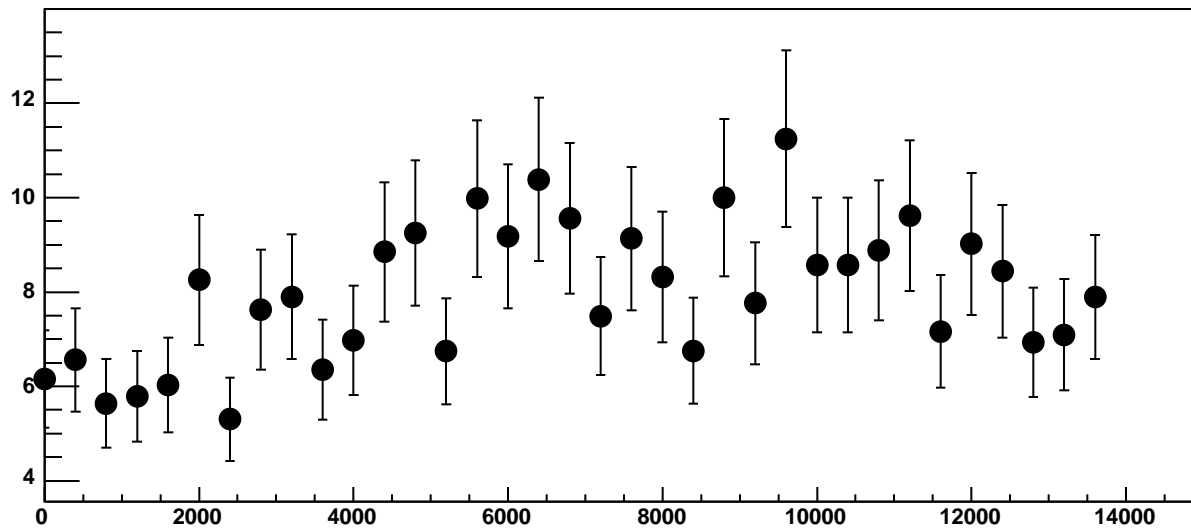


Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

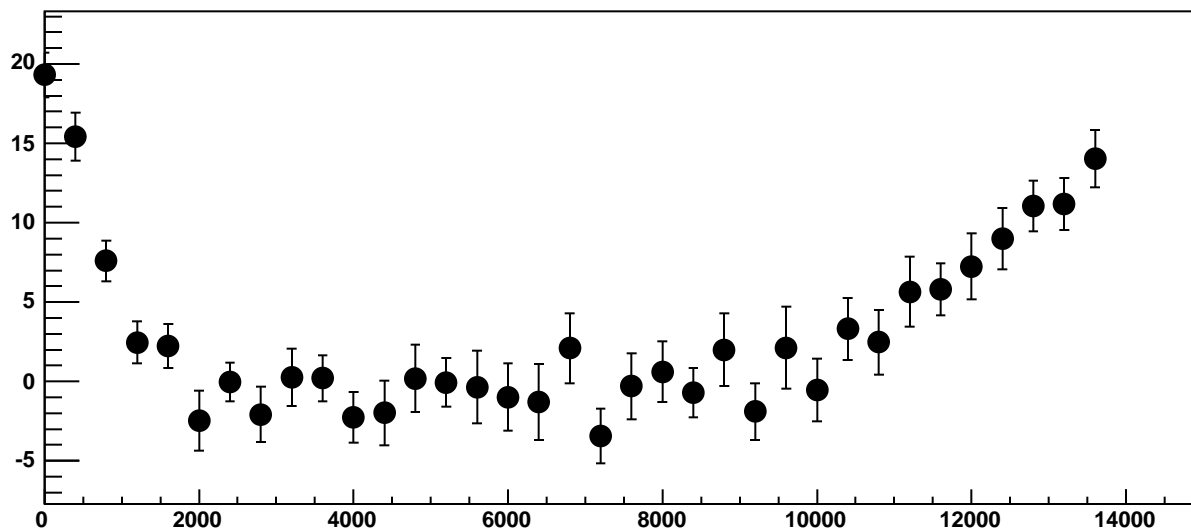


$\chi^2 / \text{ndf}$  24.83 / 23  
p0 -16.94 ± 0.7277  
p1 0.02076 ± 0.0001196

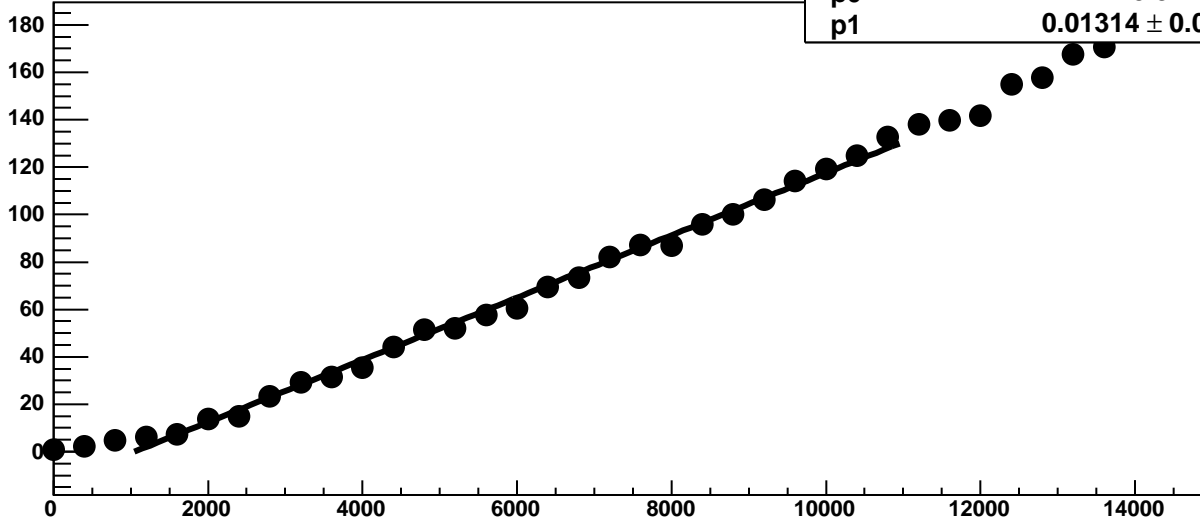
Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



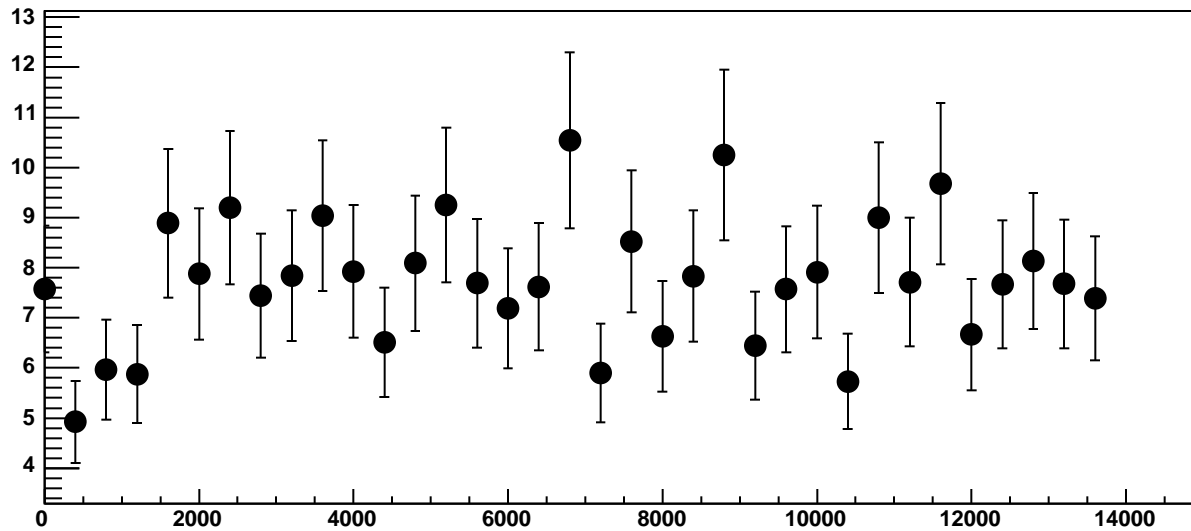
Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC



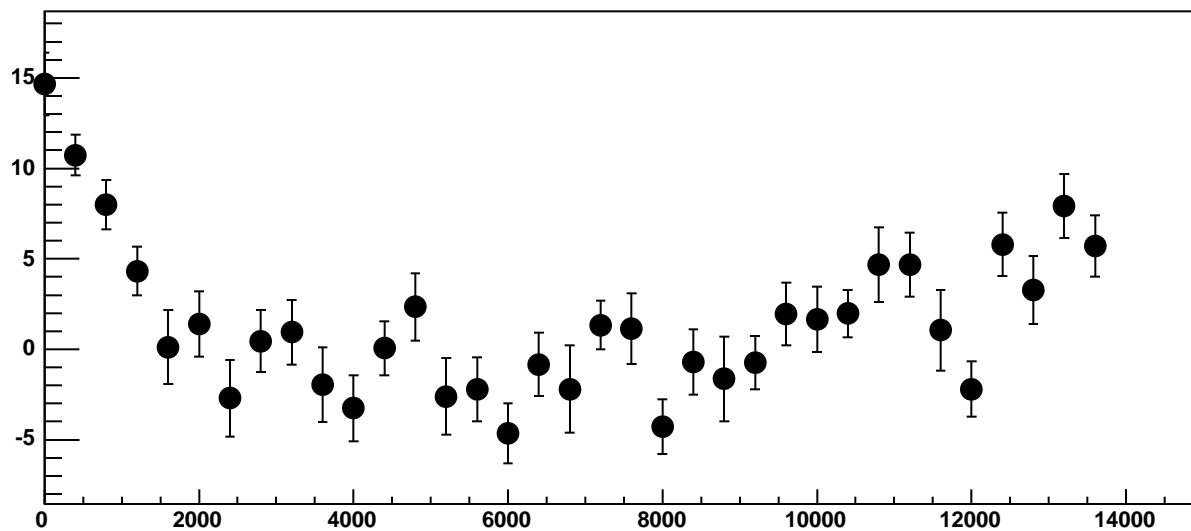
Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



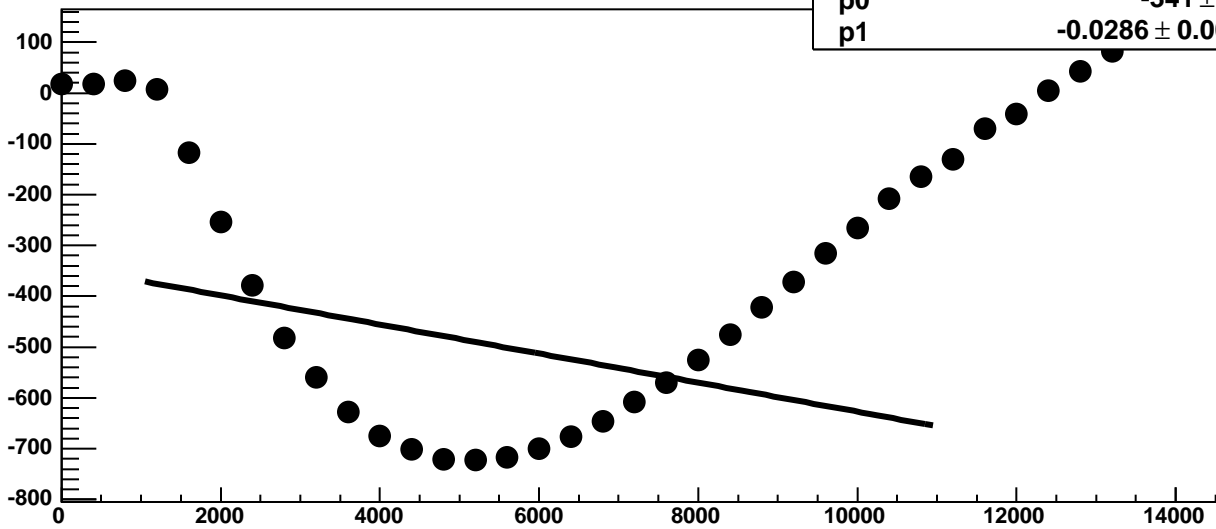
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

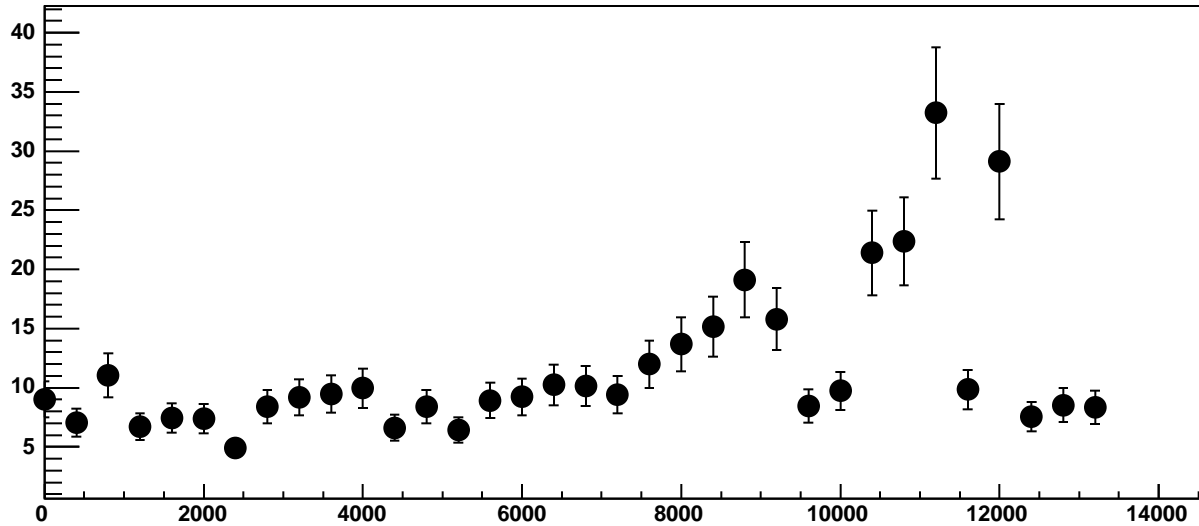


Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

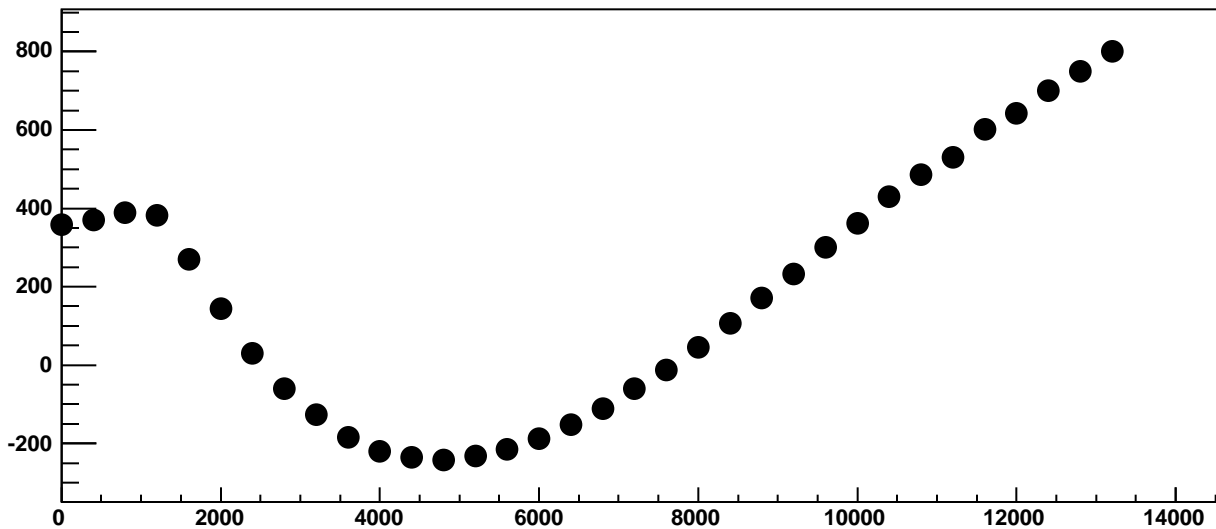


$\chi^2 / \text{ndf}$  2.799e+05 / 23  
p0 -341 ± 0.8348  
p1 -0.0286 ± 0.0001563

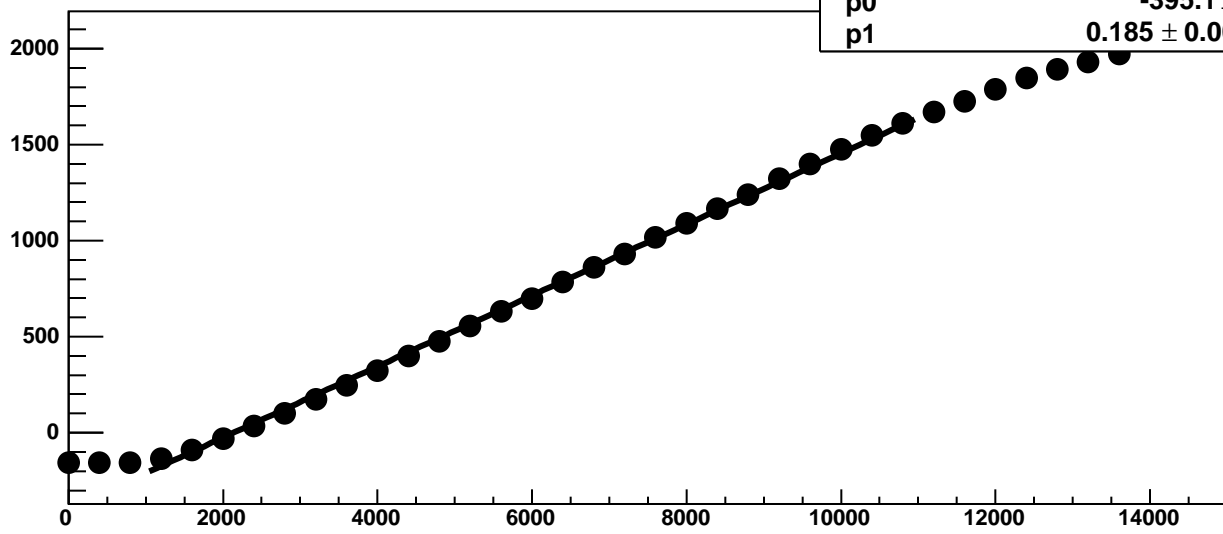
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



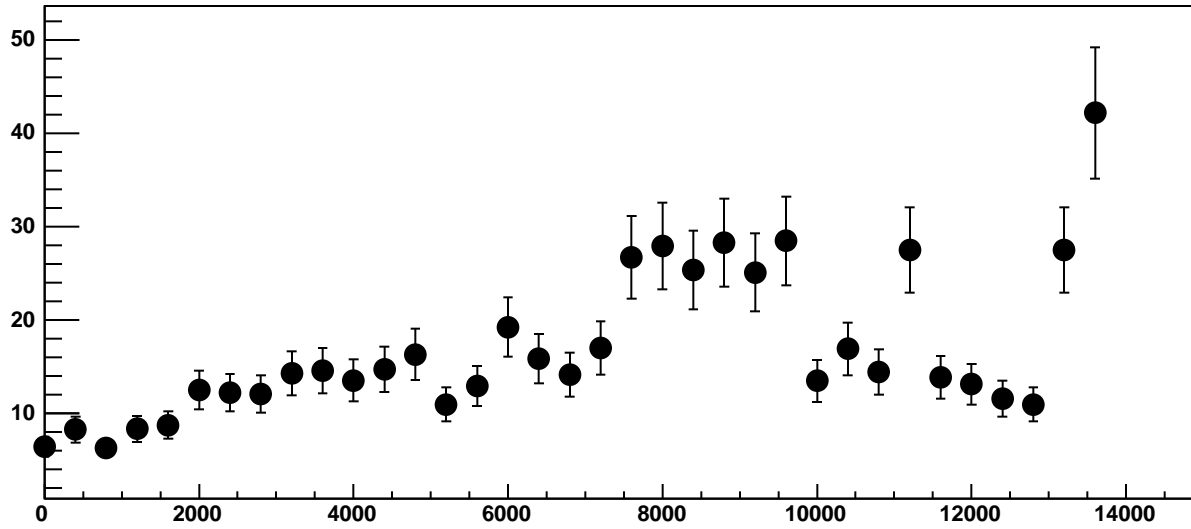
Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



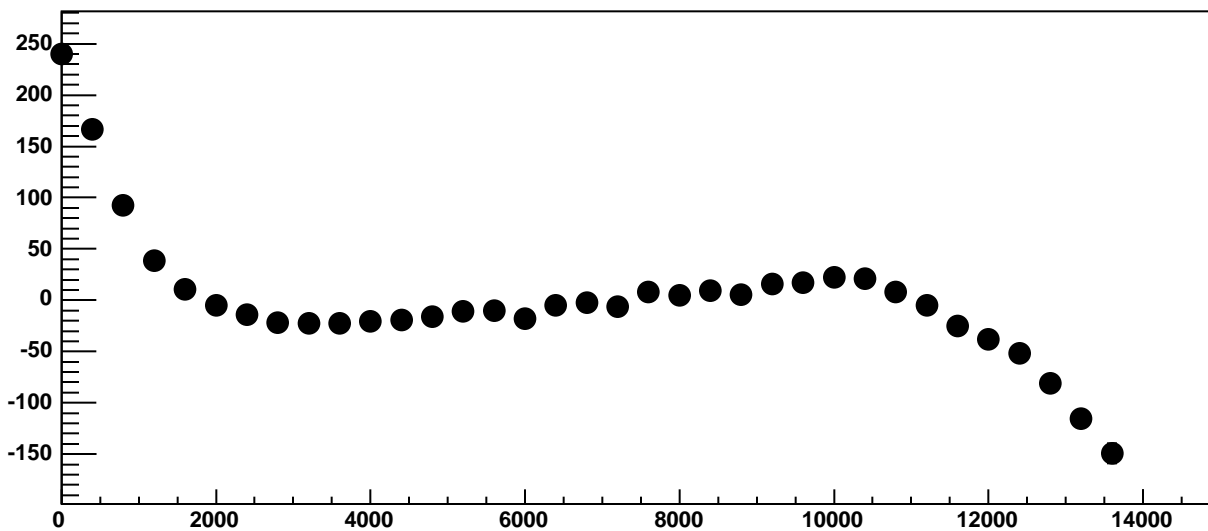
Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



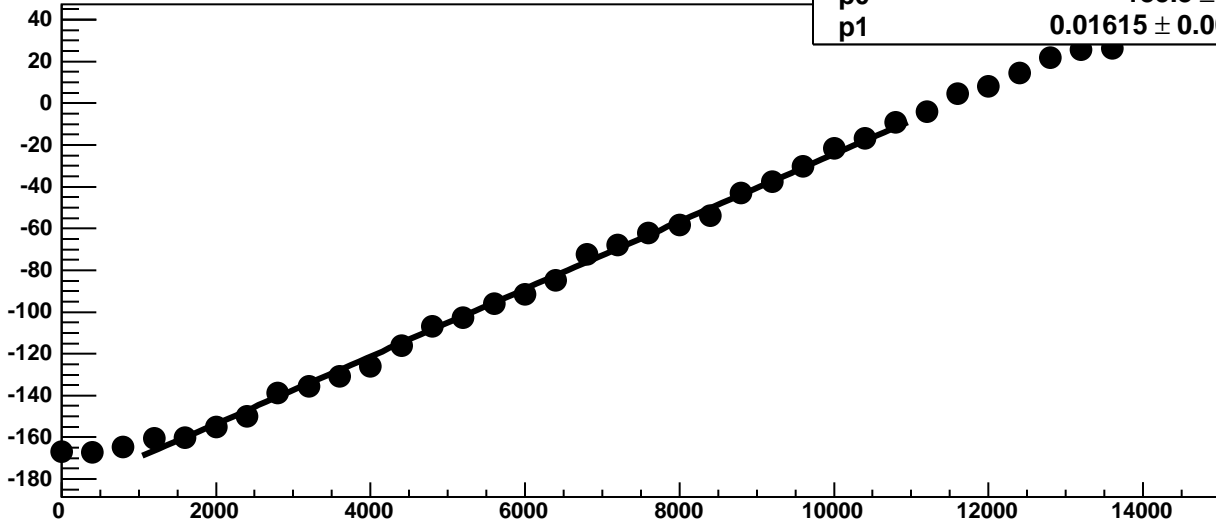
Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

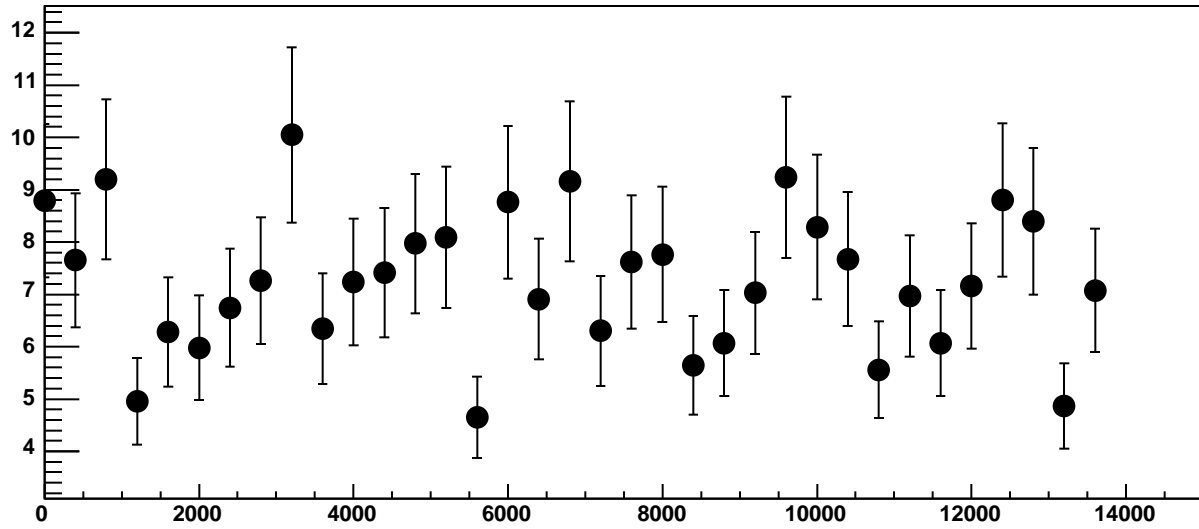


Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

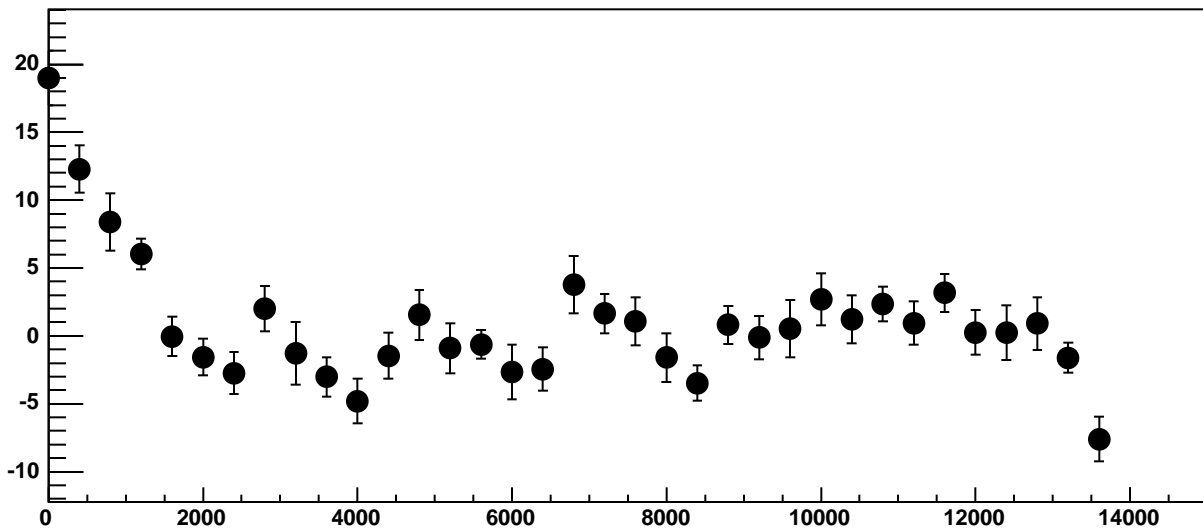


$\chi^2 / \text{ndf}$  72.48 / 23  
p0  $-185.8 \pm 0.6752$   
p1  $0.01615 \pm 0.0001033$

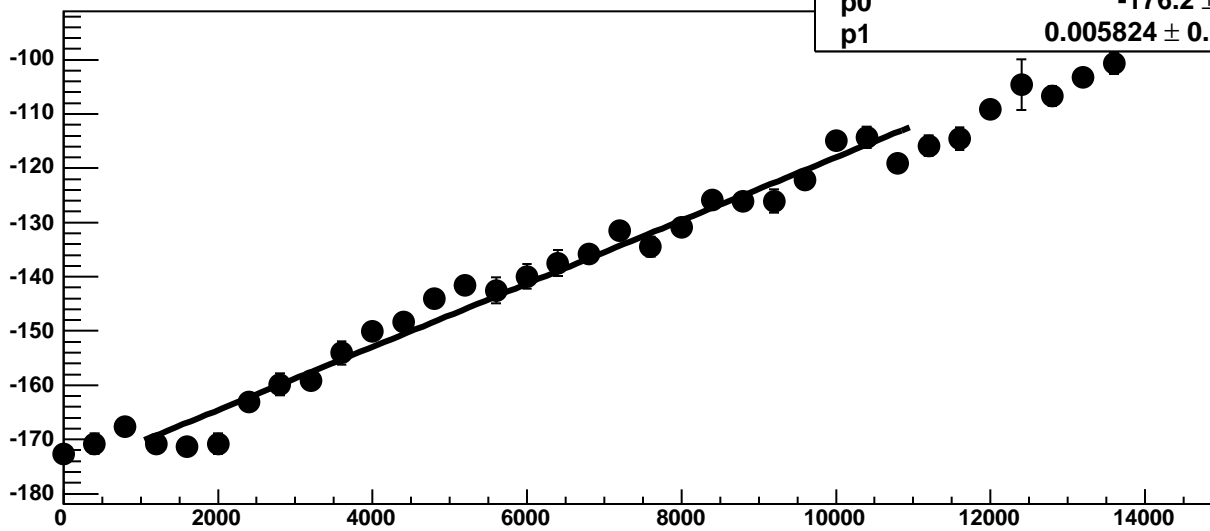
Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



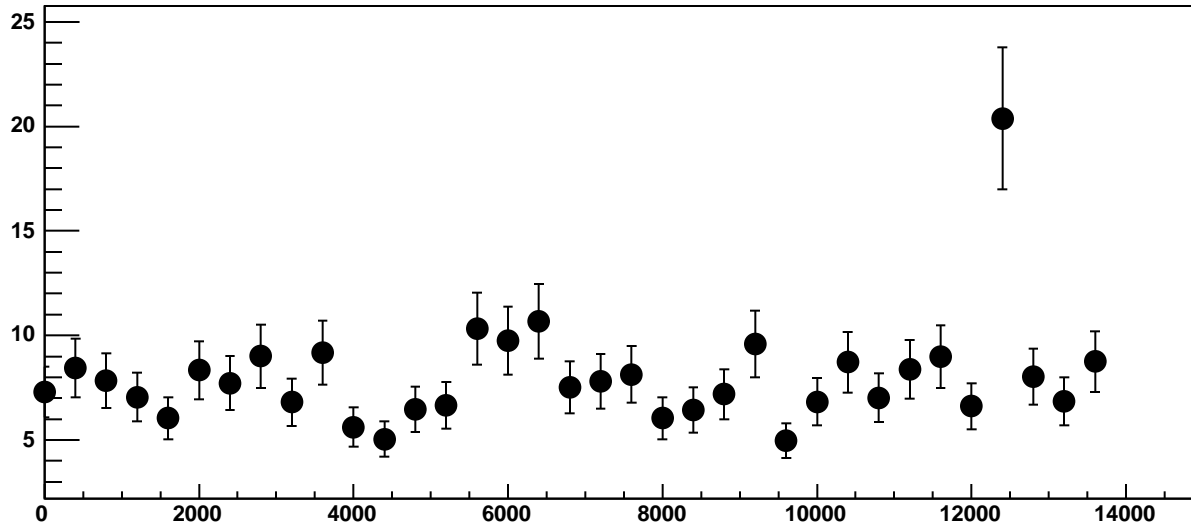
Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



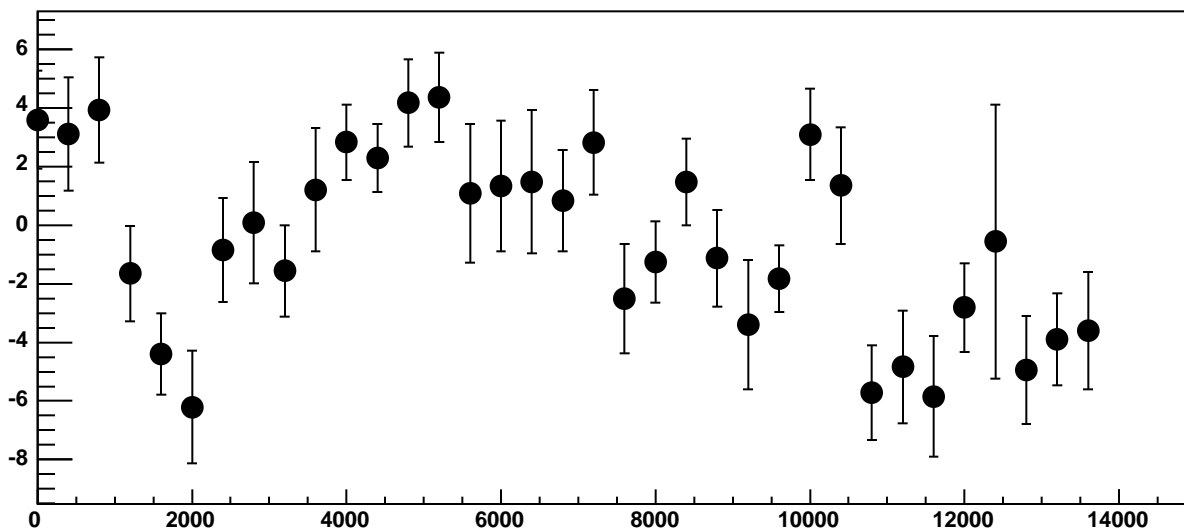
Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

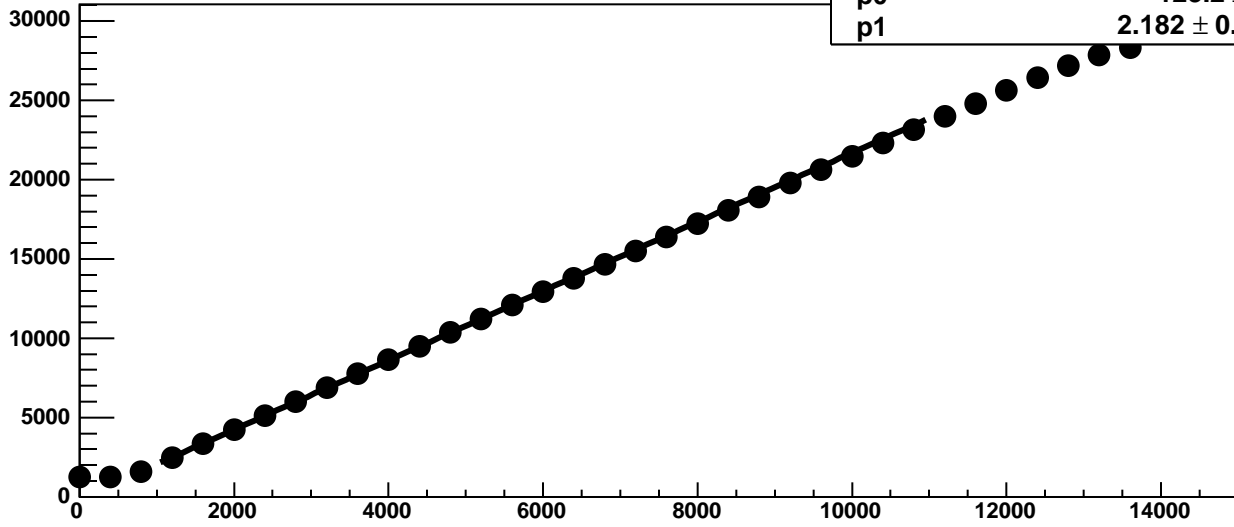


Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

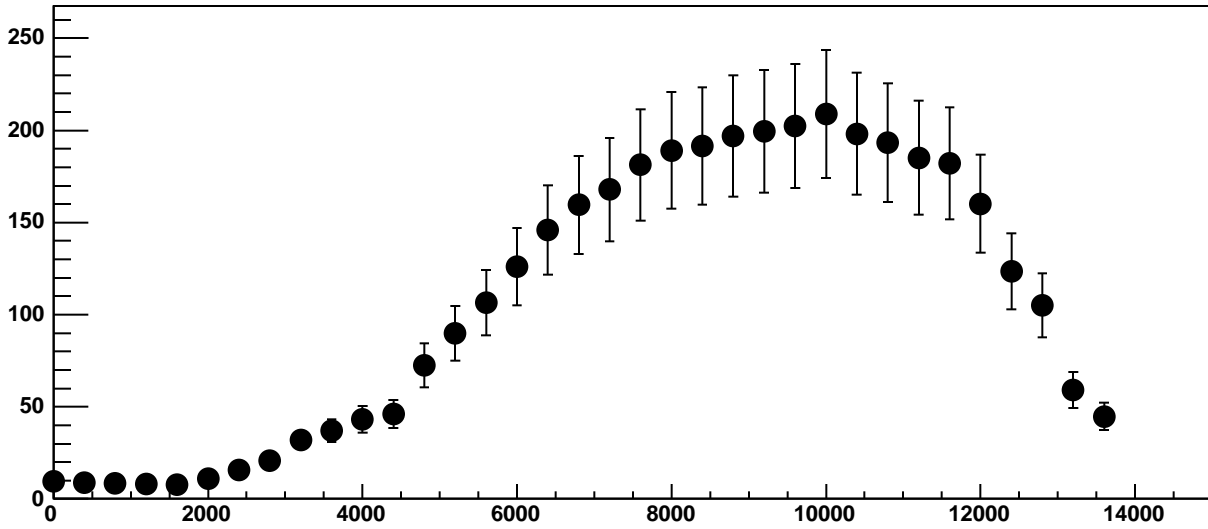




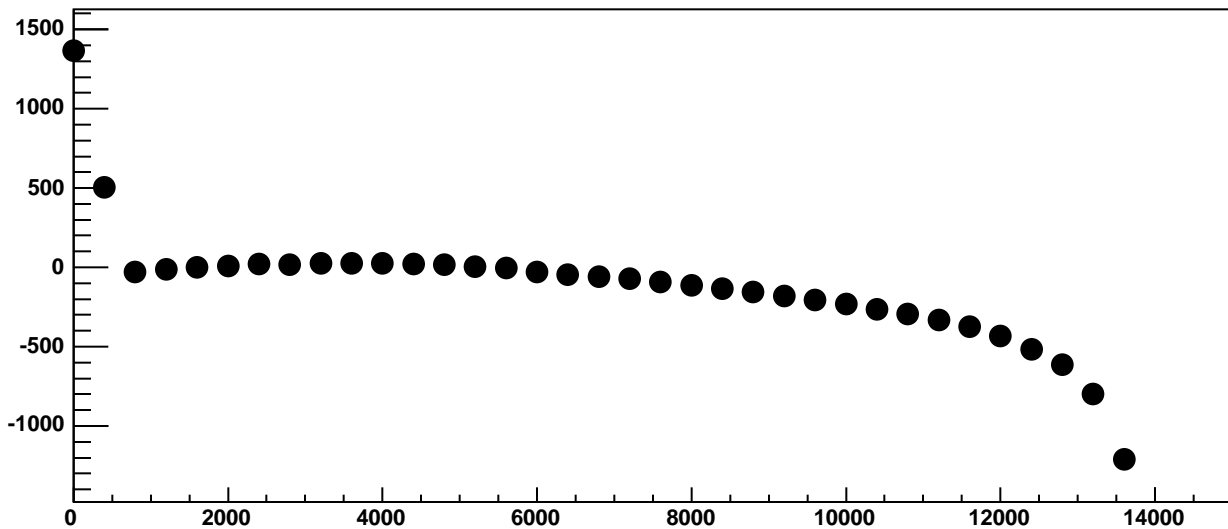
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



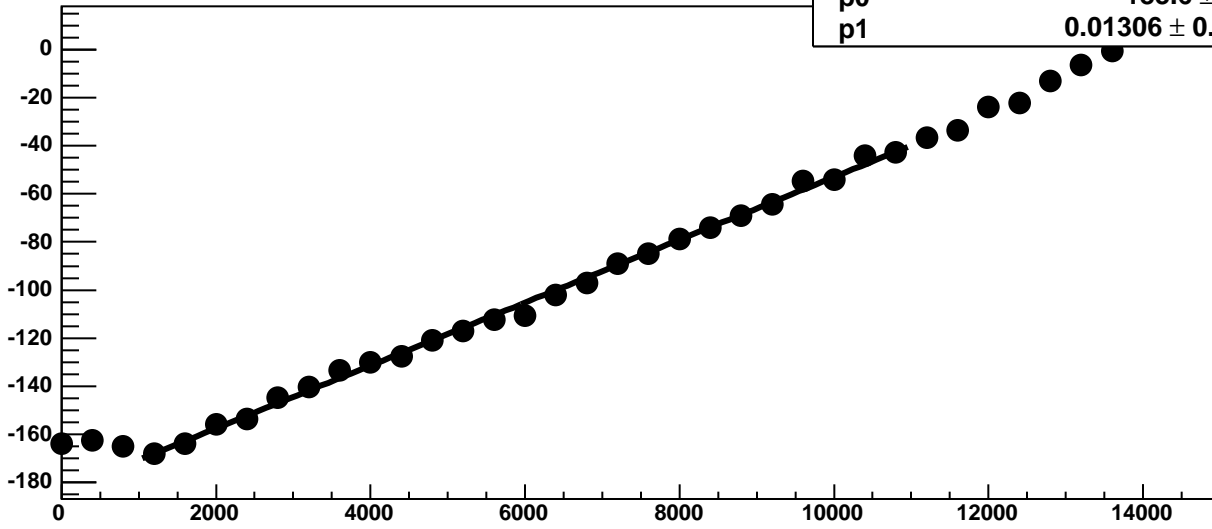
Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

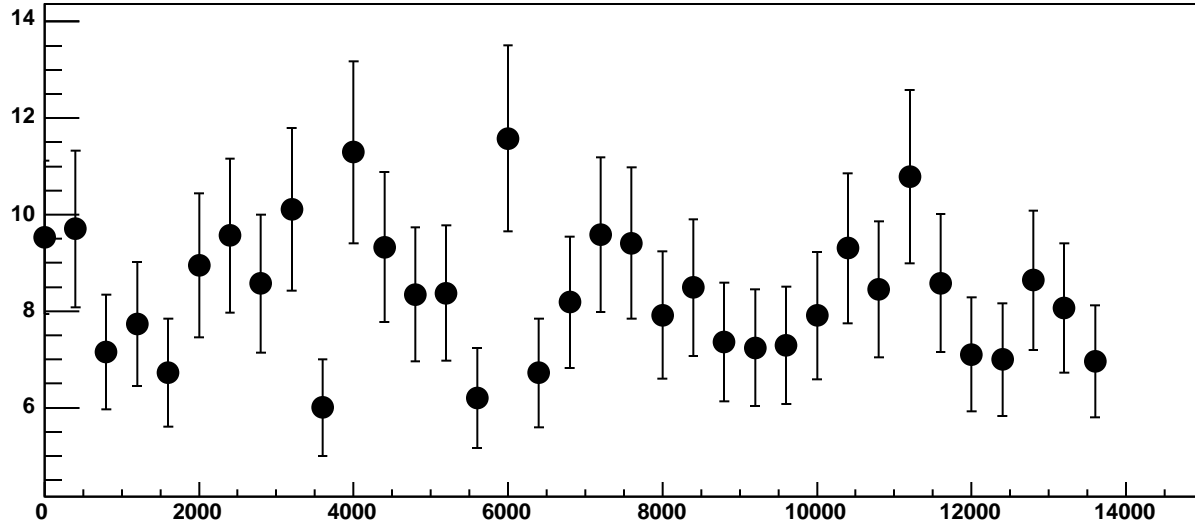


Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

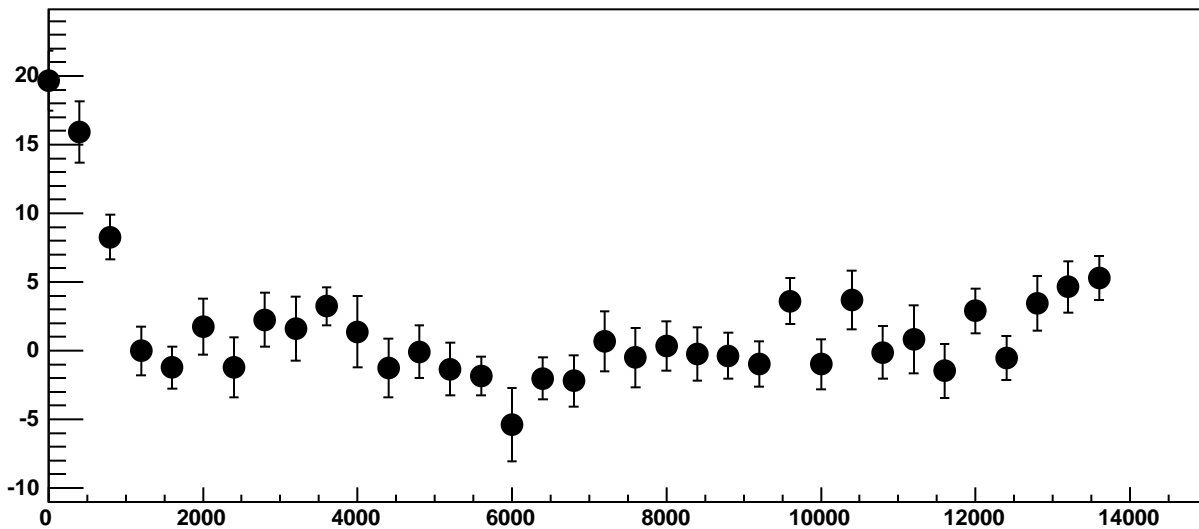


$\chi^2 / \text{ndf}$  27.46 / 23  
p0 -183.6 ± 0.8538  
p1 0.01306 ± 0.000128

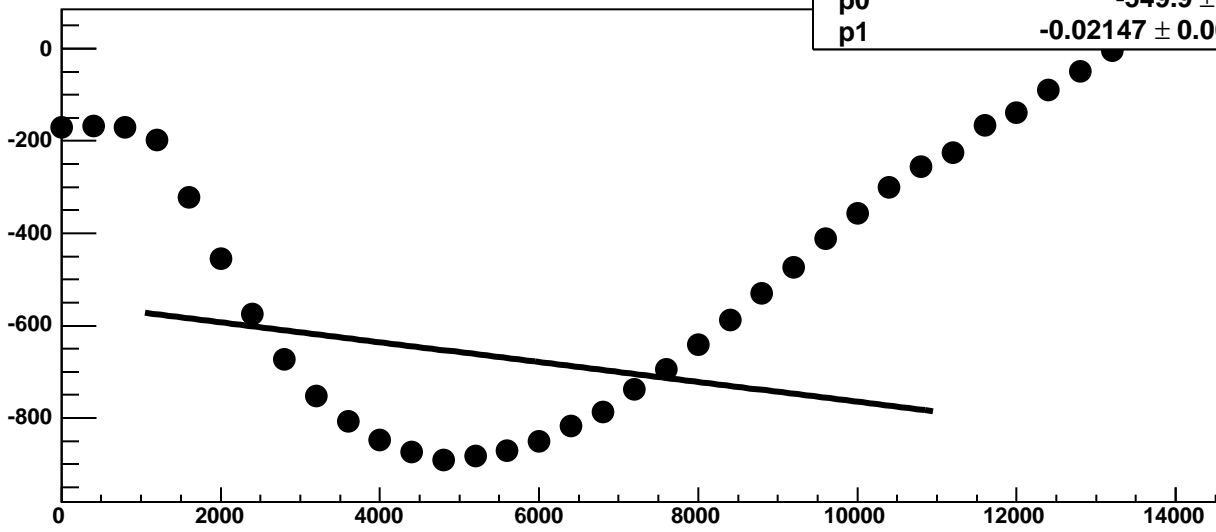
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



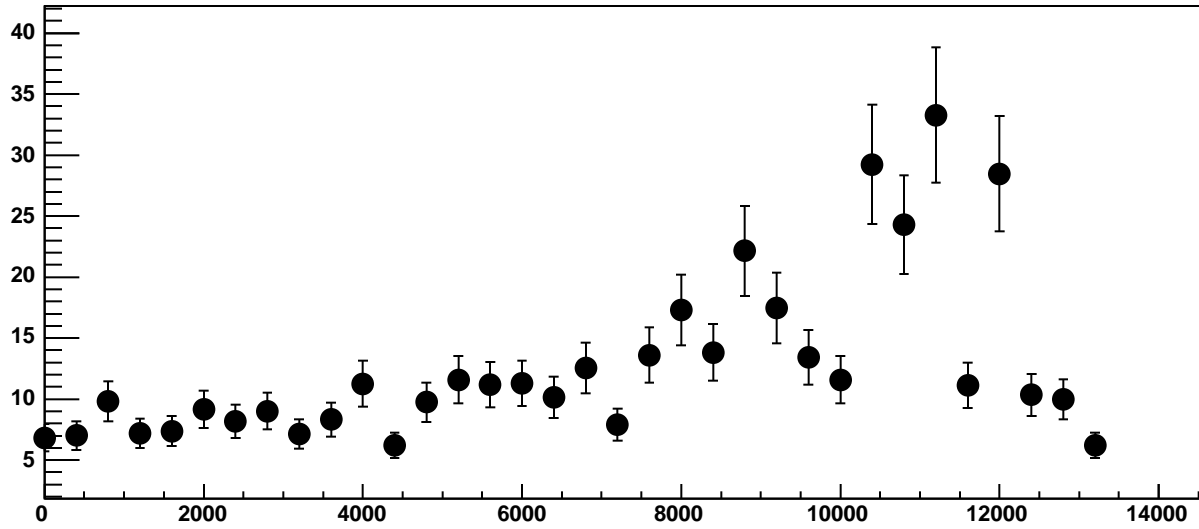
Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



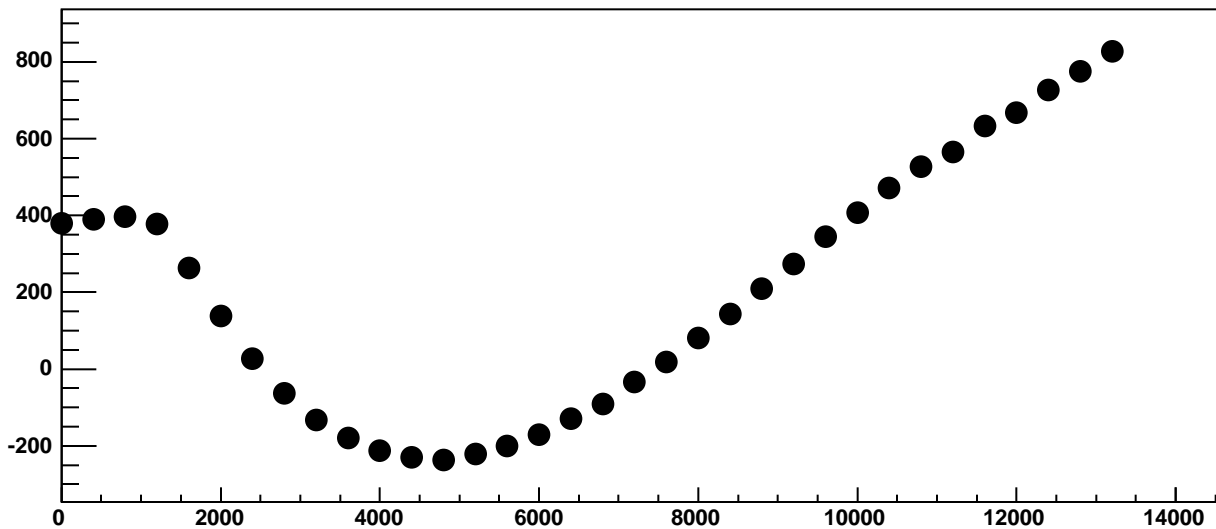
Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

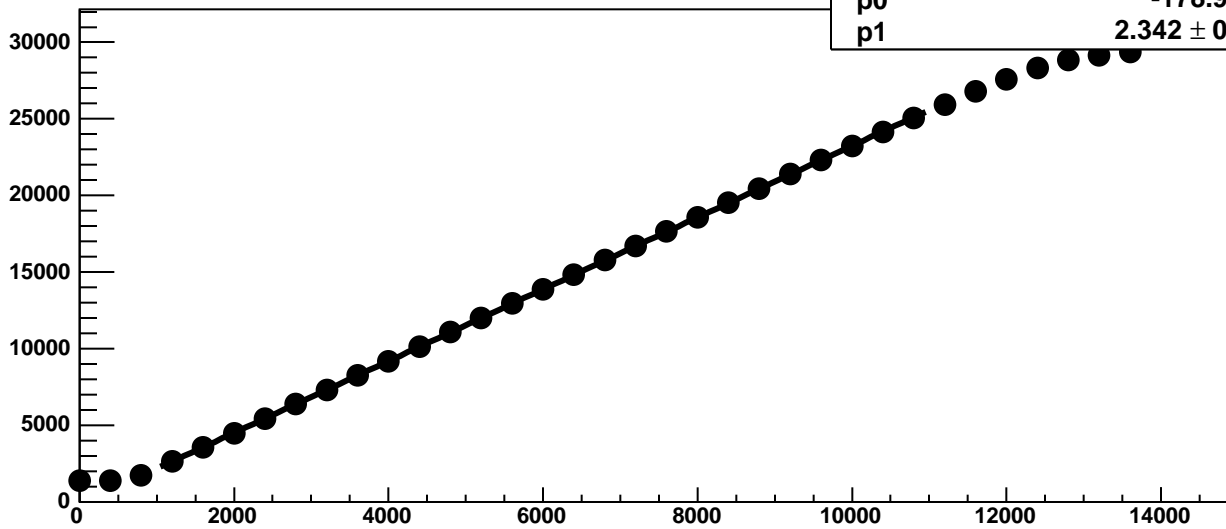


Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

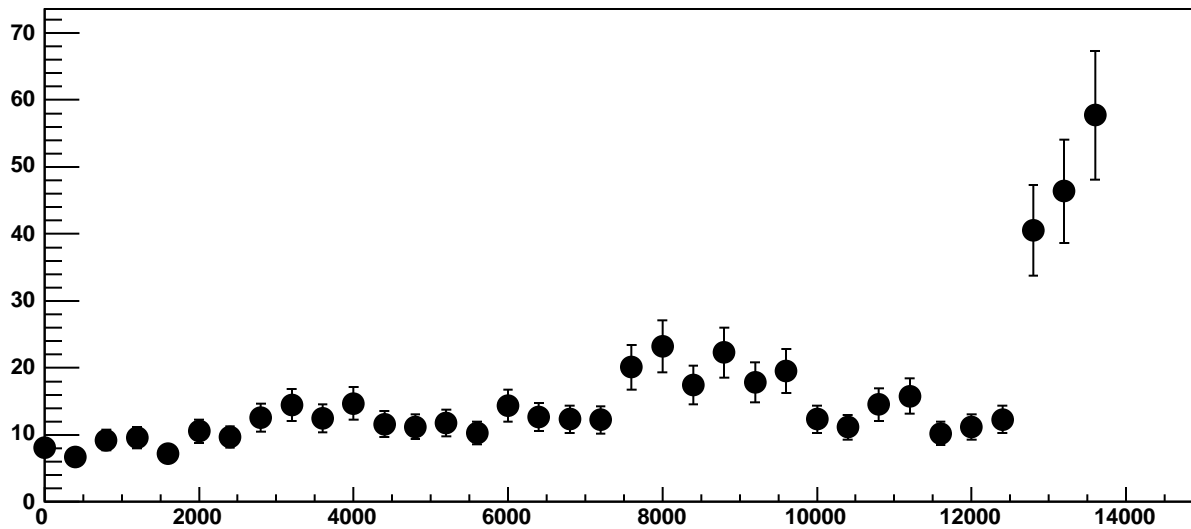


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

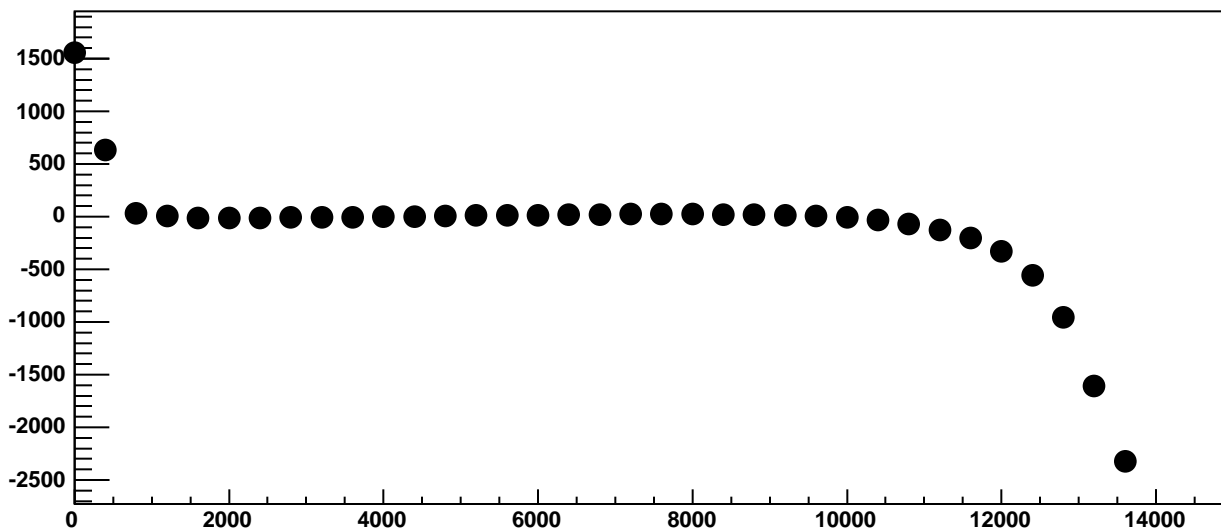
$\chi^2 / \text{ndf}$  1099 / 23  
p0  $-178.9 \pm 1.115$   
p1  $2.342 \pm 0.000191$



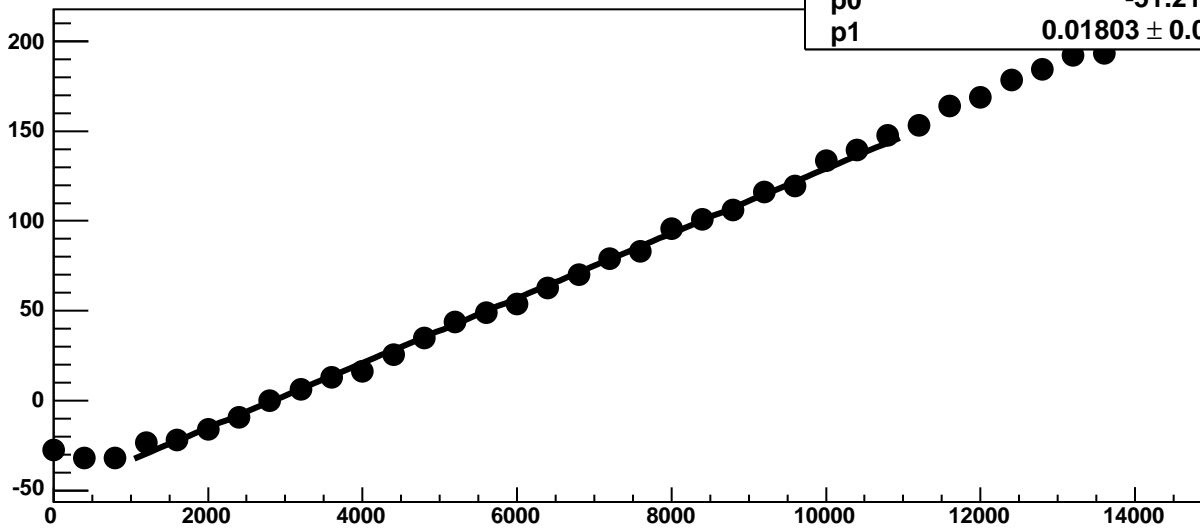
Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

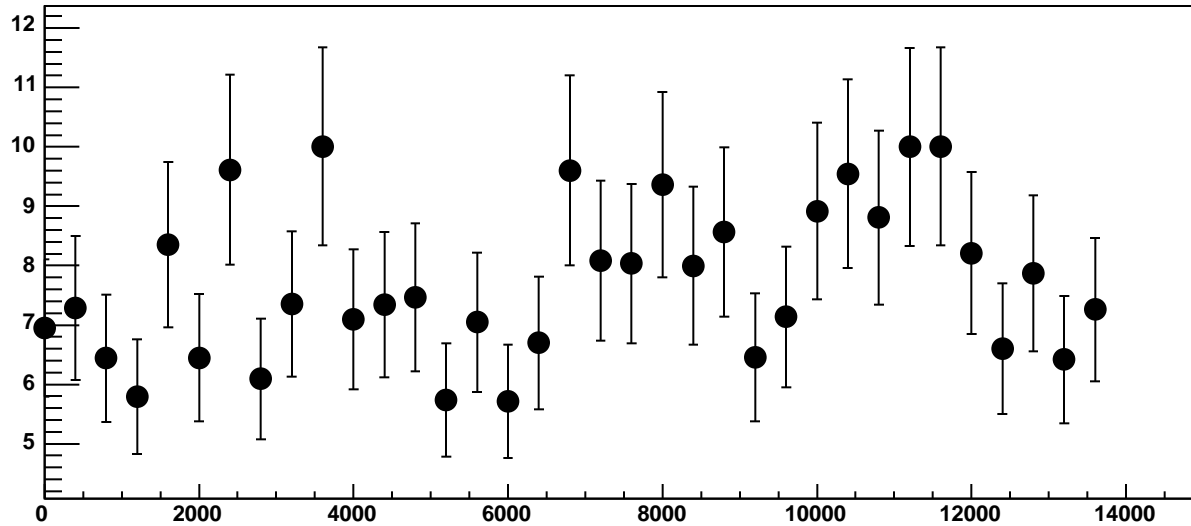


Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

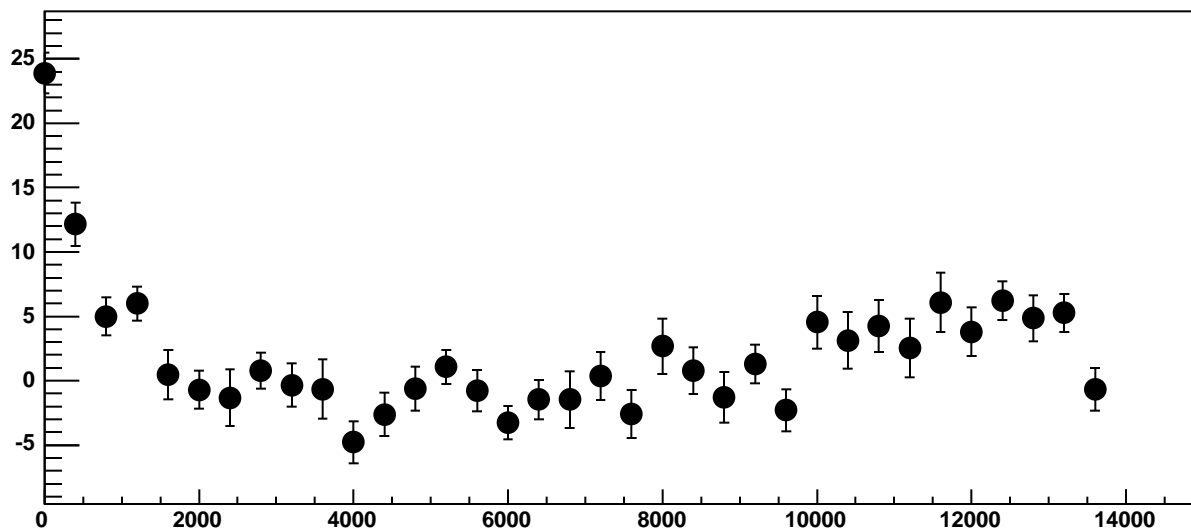


$\chi^2 / \text{ndf}$  59.16 / 23  
p0 -51.21 ± 0.762  
p1 0.01803 ± 0.0001207

Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

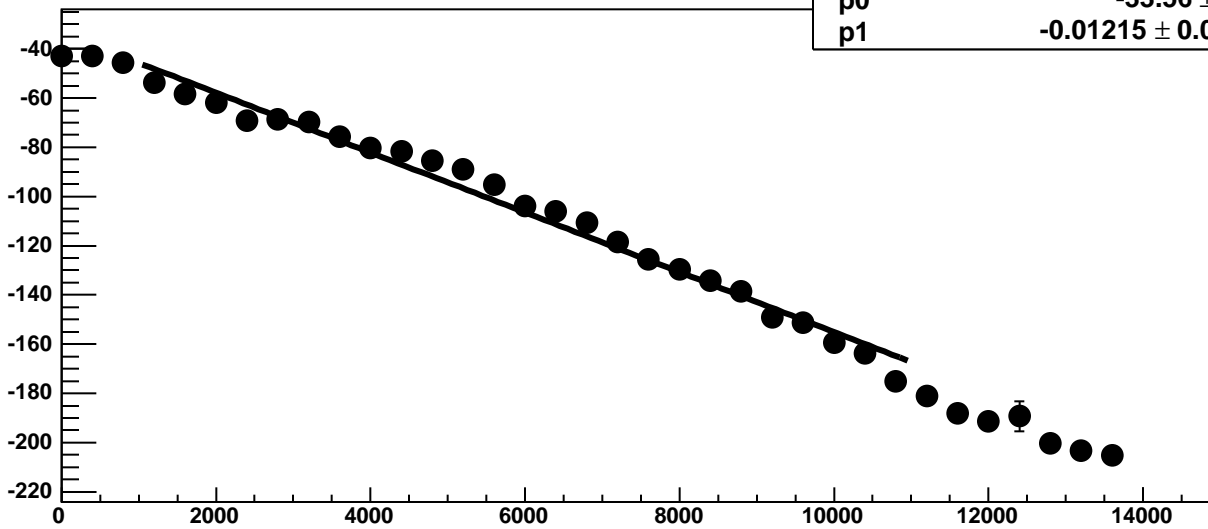


Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

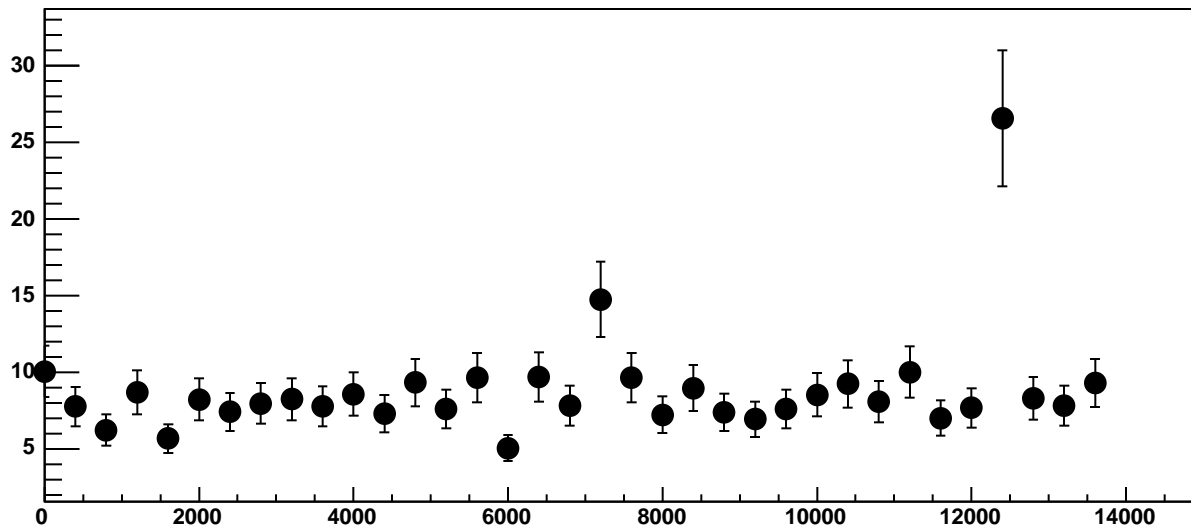


Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

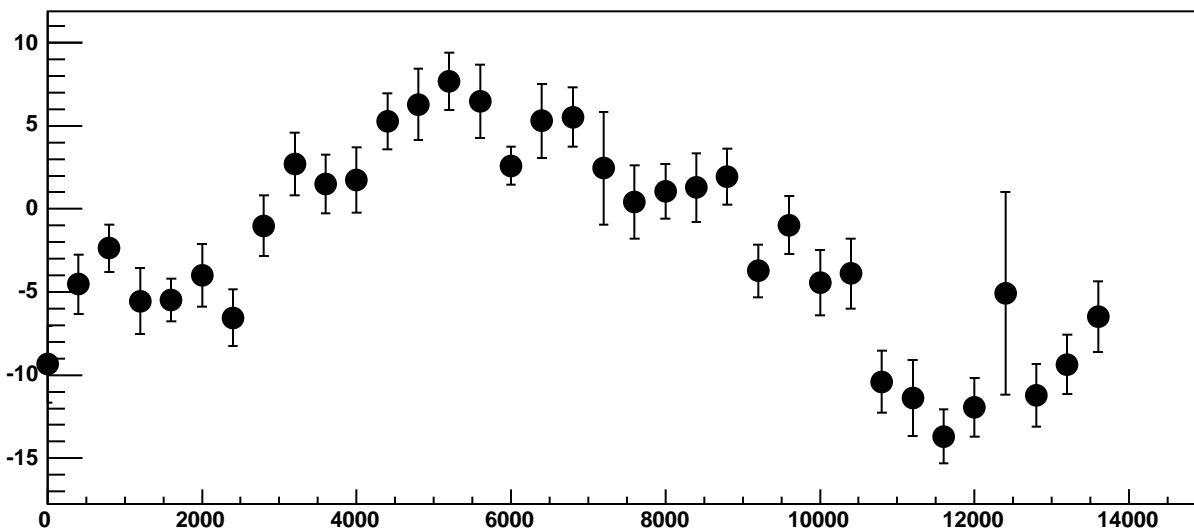
$\chi^2 / \text{ndf}$  163.9 / 23  
p0  $-33.56 \pm 0.8028$   
p1  $-0.01215 \pm 0.0001236$



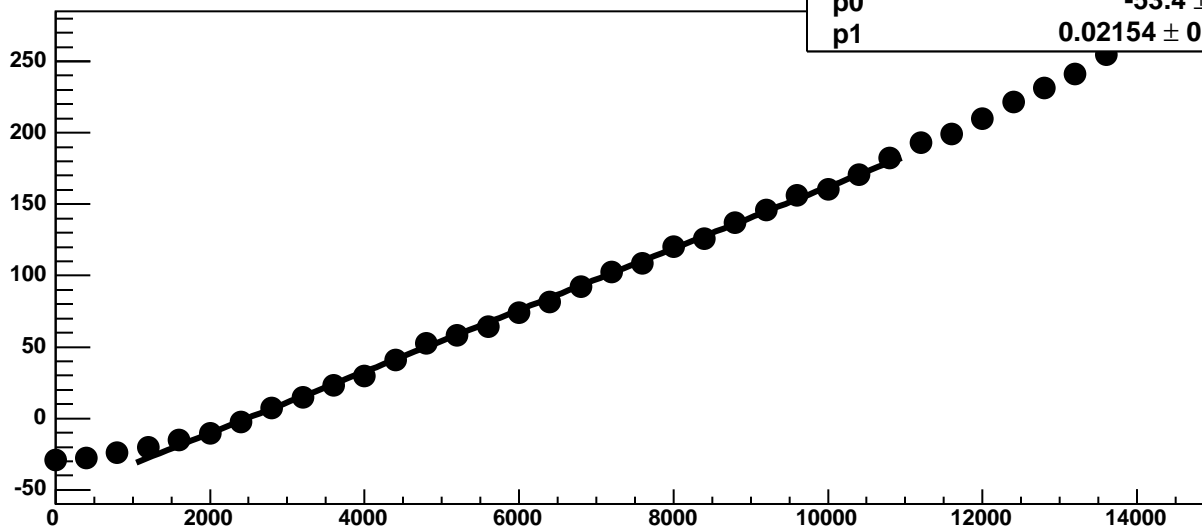
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

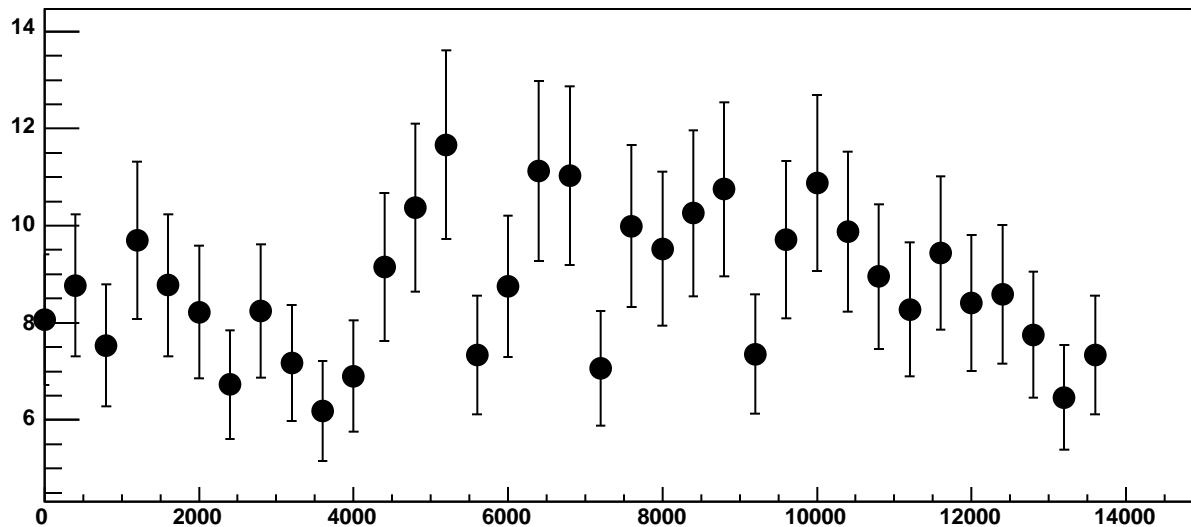


Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

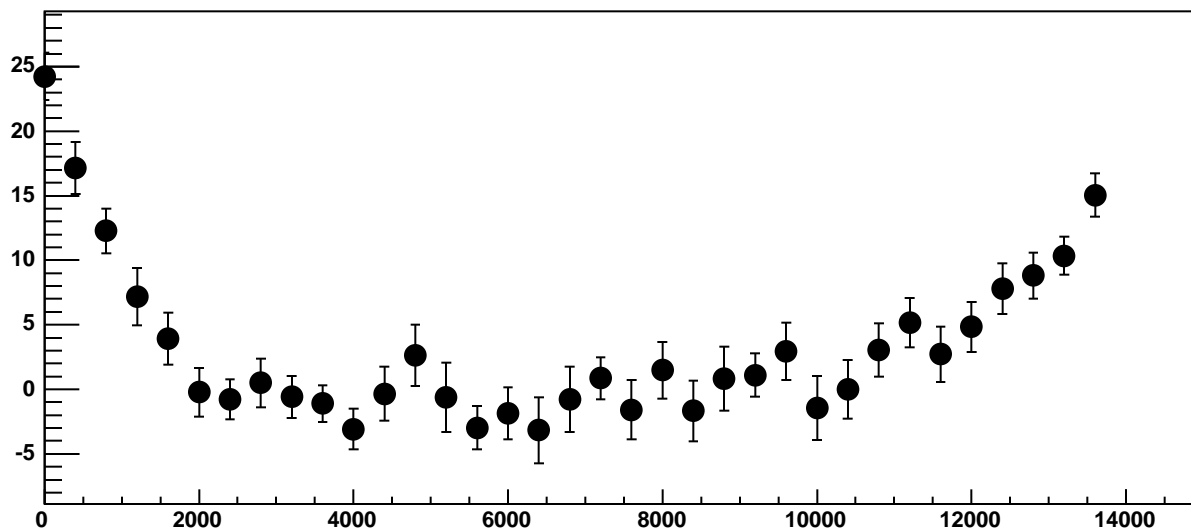


$\chi^2 / \text{ndf}$  32.57 / 23  
p0  $-53.4 \pm 0.8703$   
p1  $0.02154 \pm 0.000139$

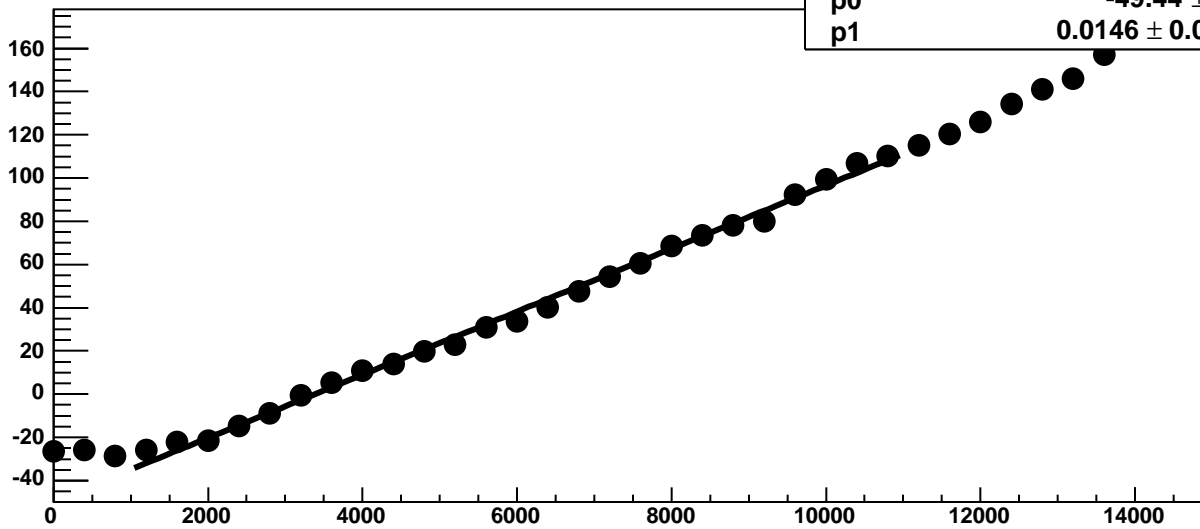
Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

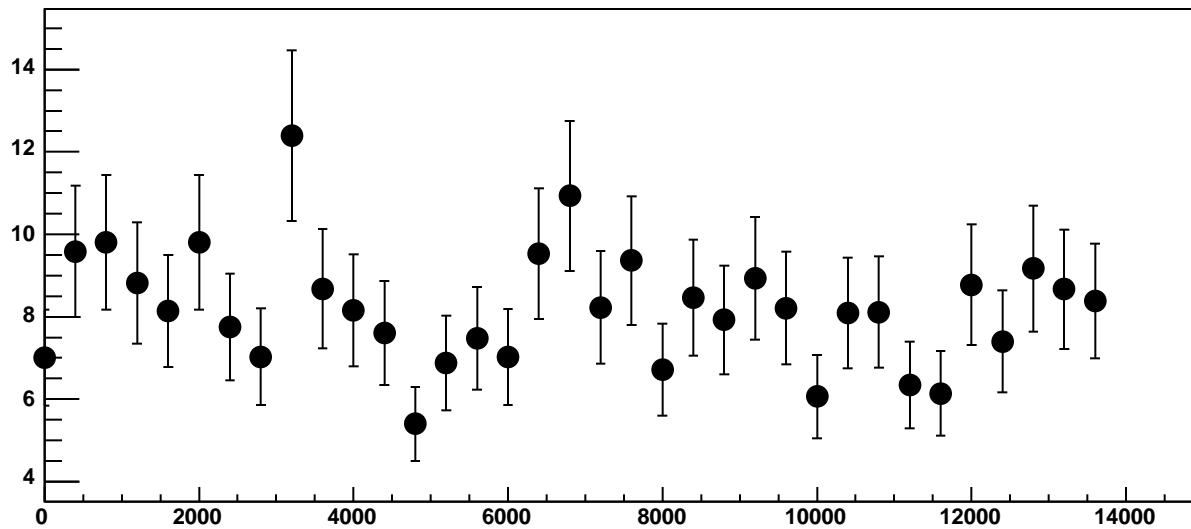


Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

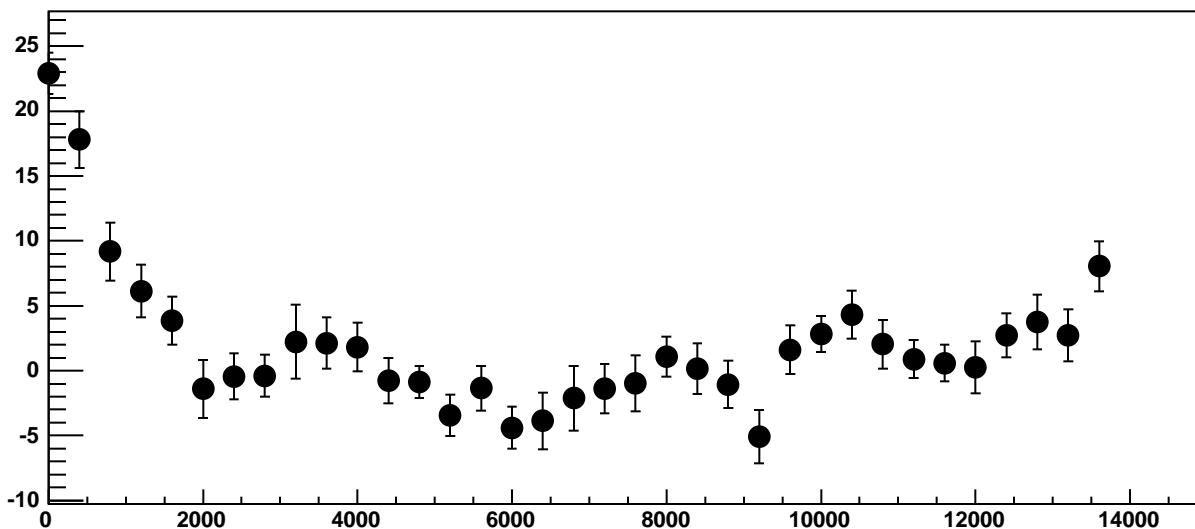


$\chi^2 / \text{ndf}$  53.15 / 23  
p0  $-49.44 \pm 0.8602$   
p1  $0.0146 \pm 0.0001278$

Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



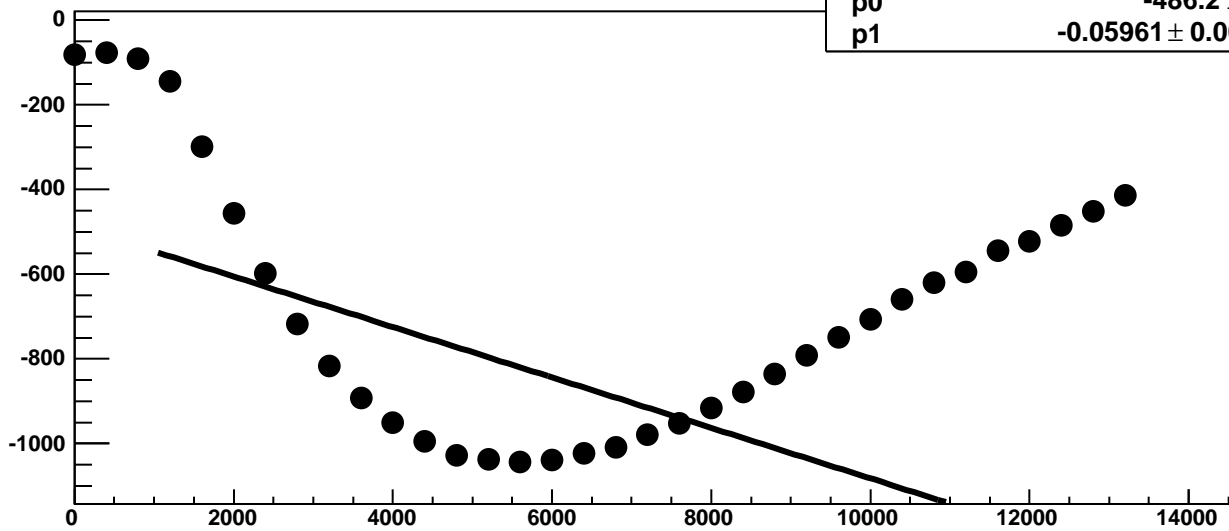
Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



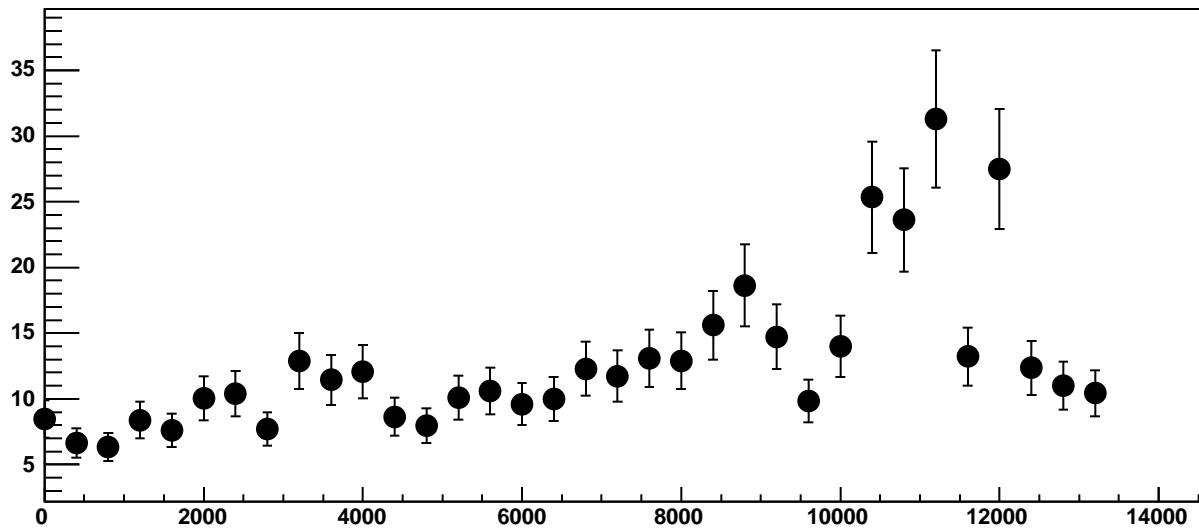


Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

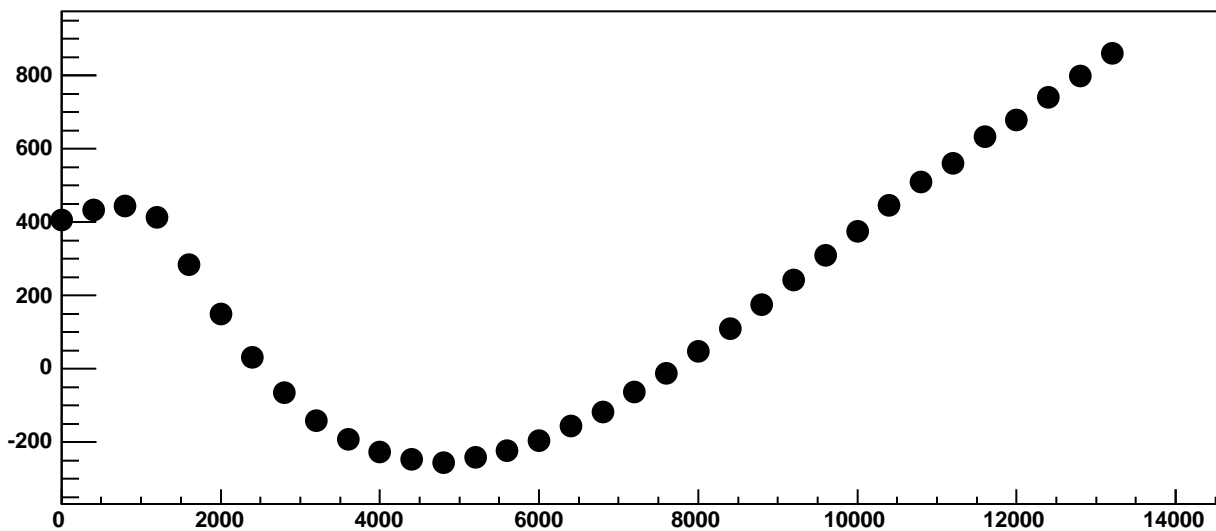
$\chi^2 / \text{ndf}$  2.169e+05 / 23  
p0 -486.2 ± 1.053  
p1 -0.05961 ± 0.0001879



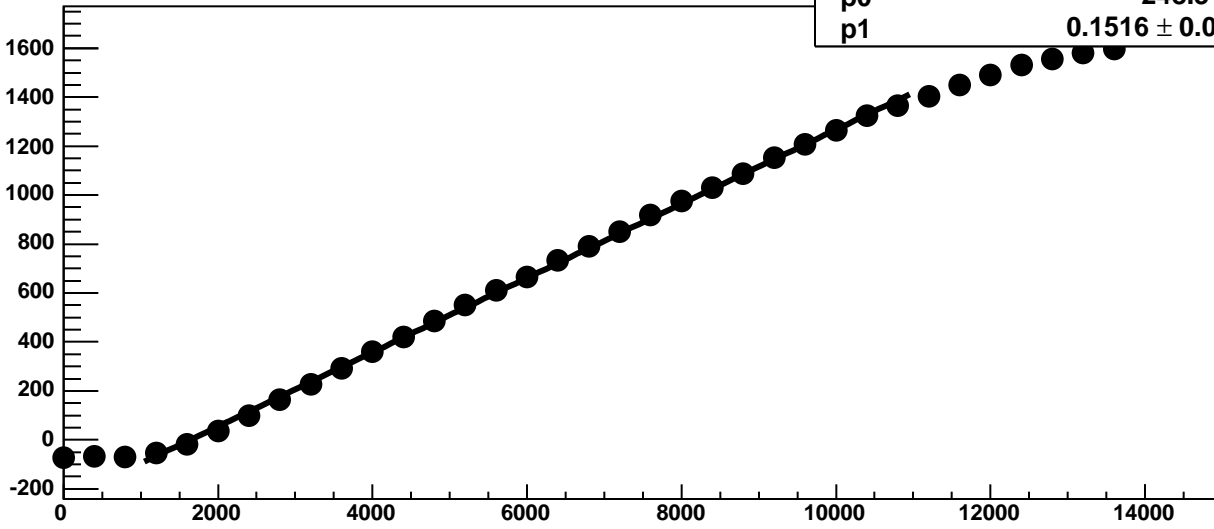
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



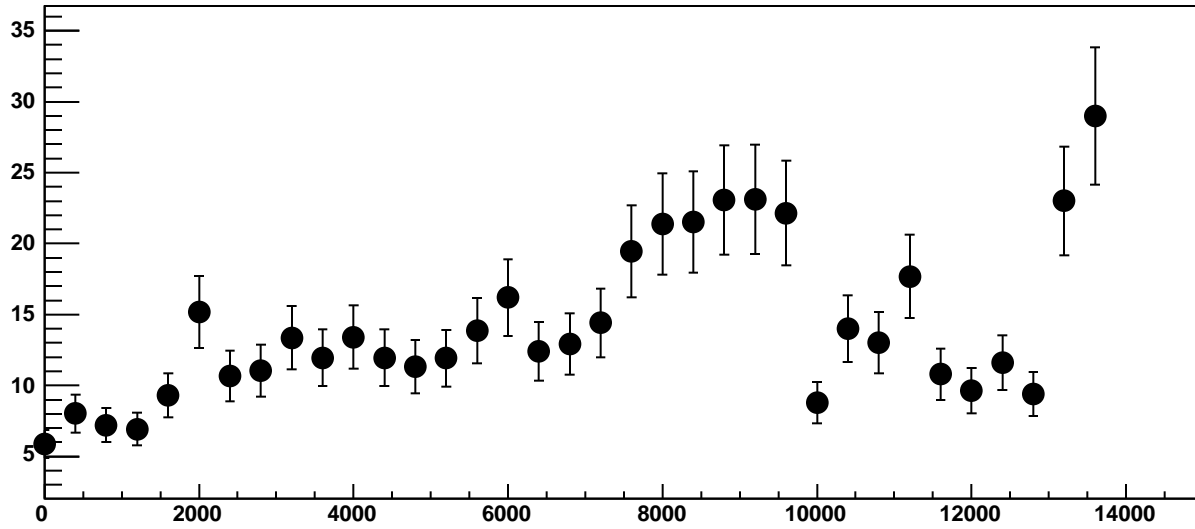
Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



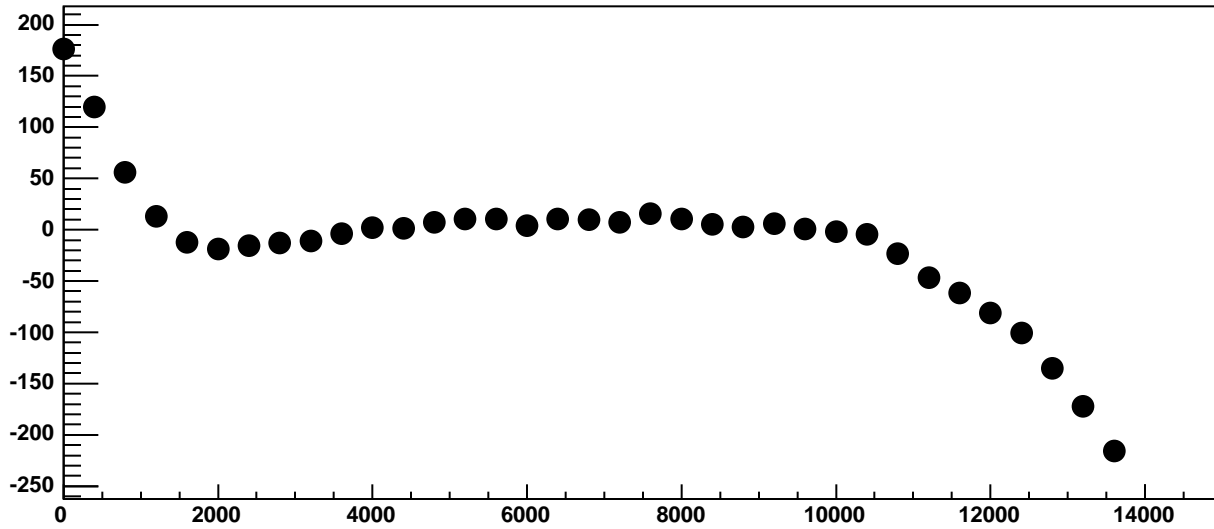
Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



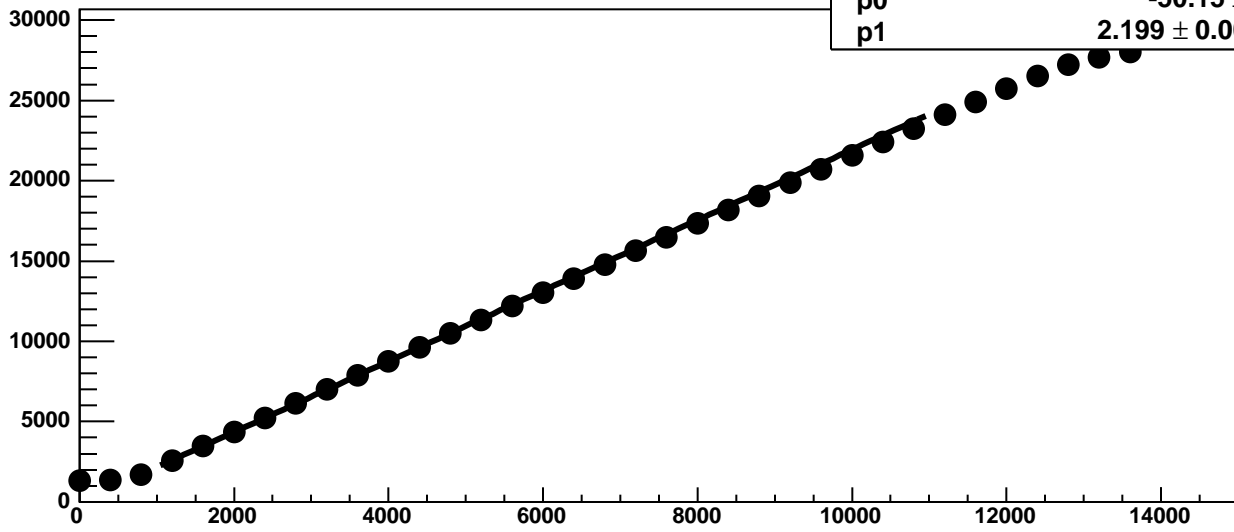
Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



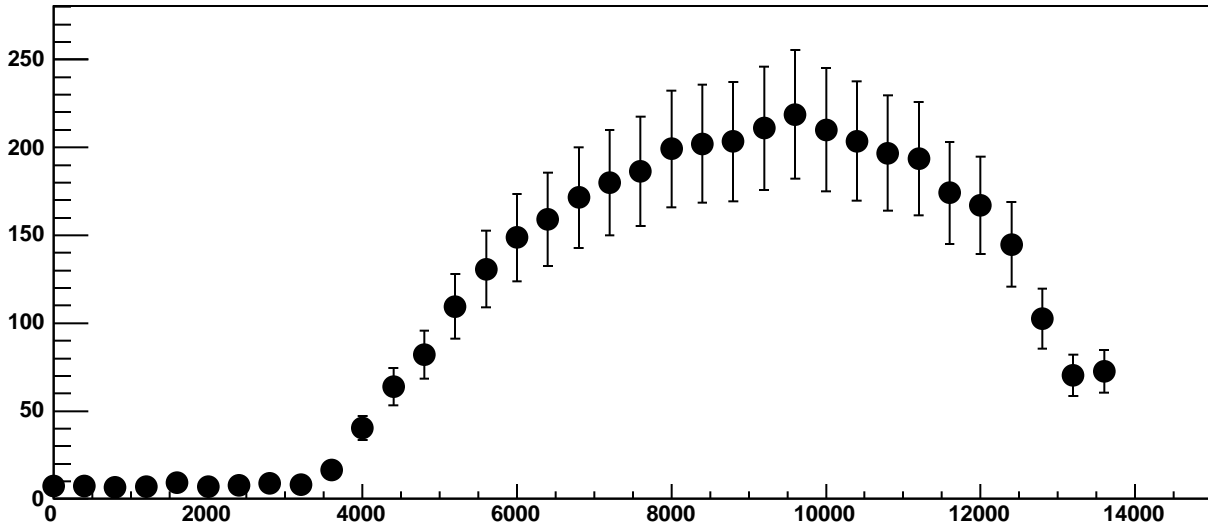
Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



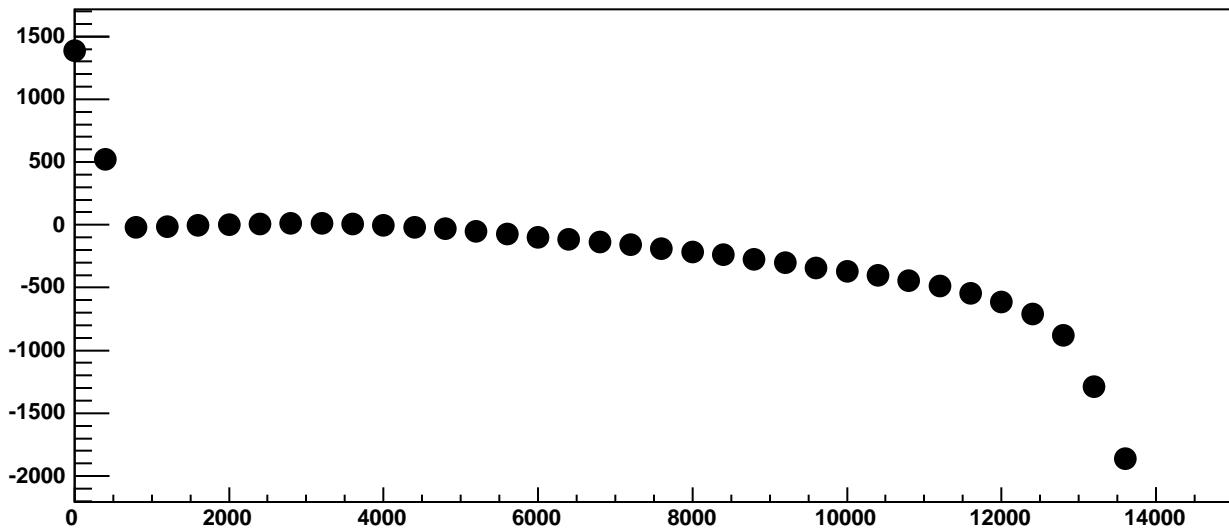
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



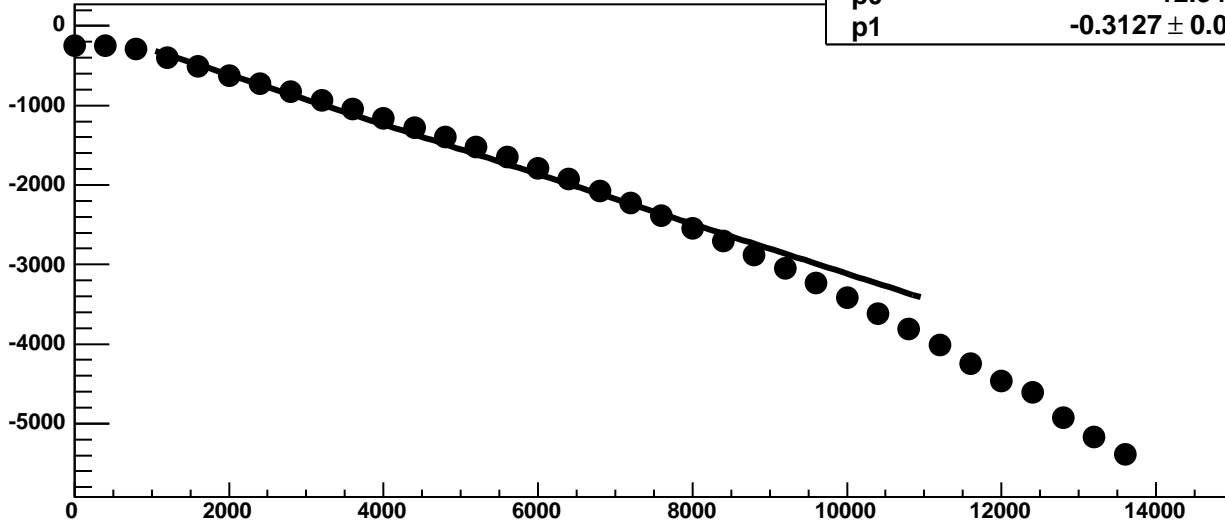
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

5055 / 23

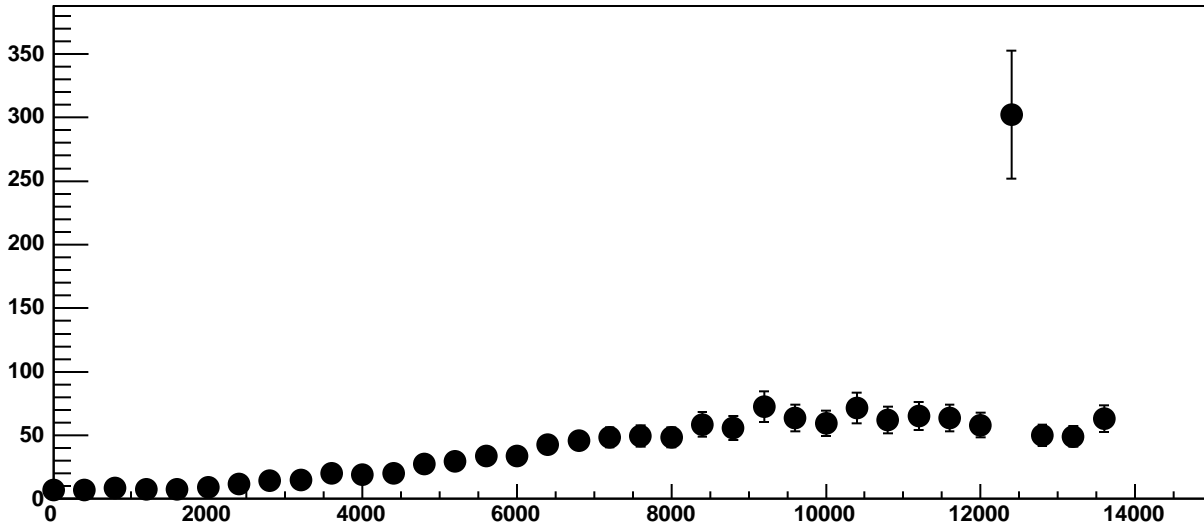
p0

$12.34 \pm 1.47$

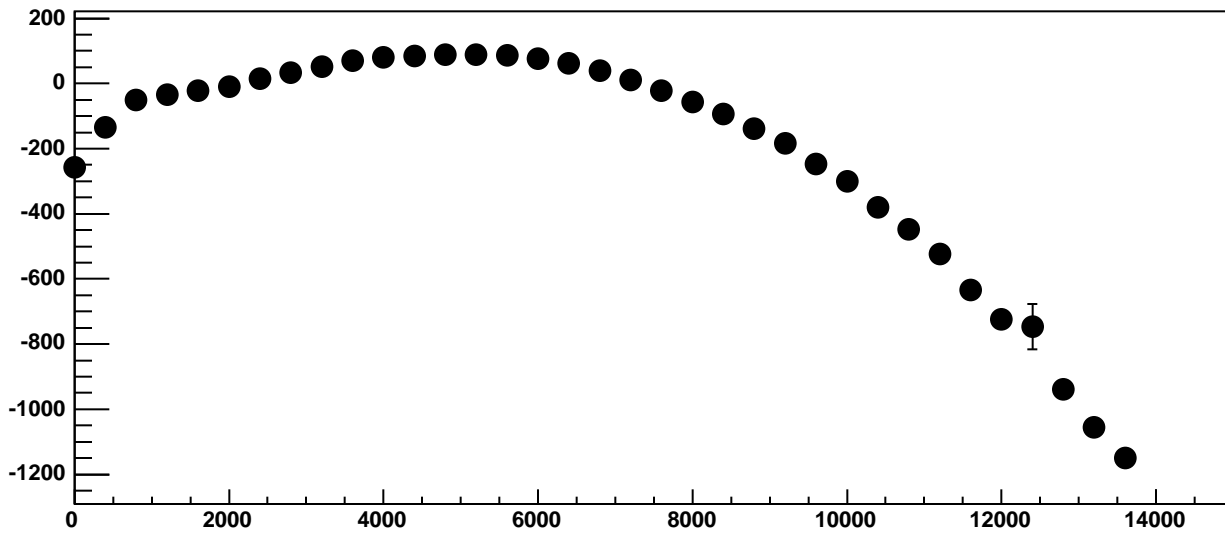
p1

$-0.3127 \pm 0.0004694$

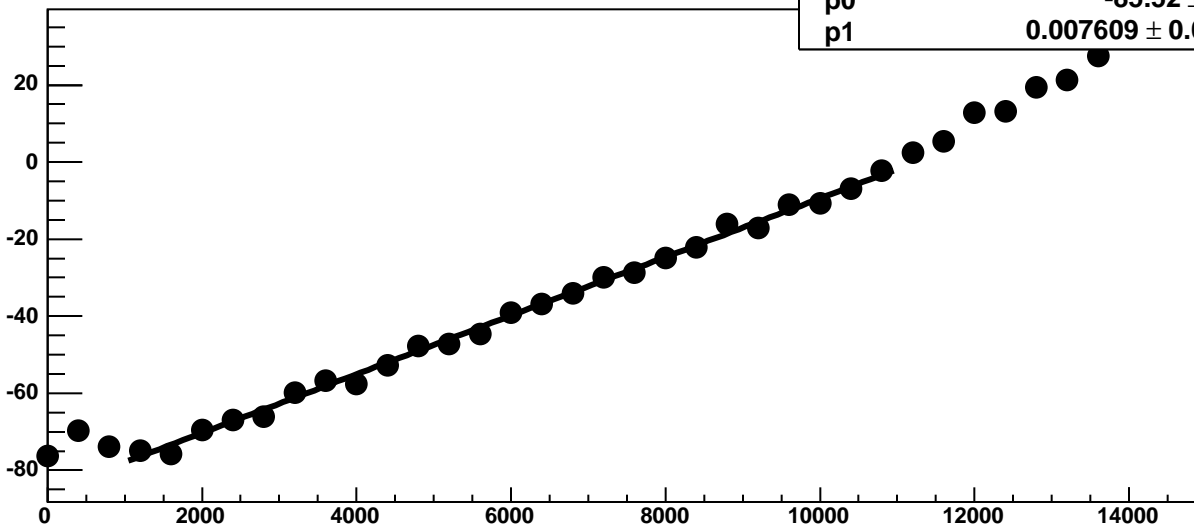
Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



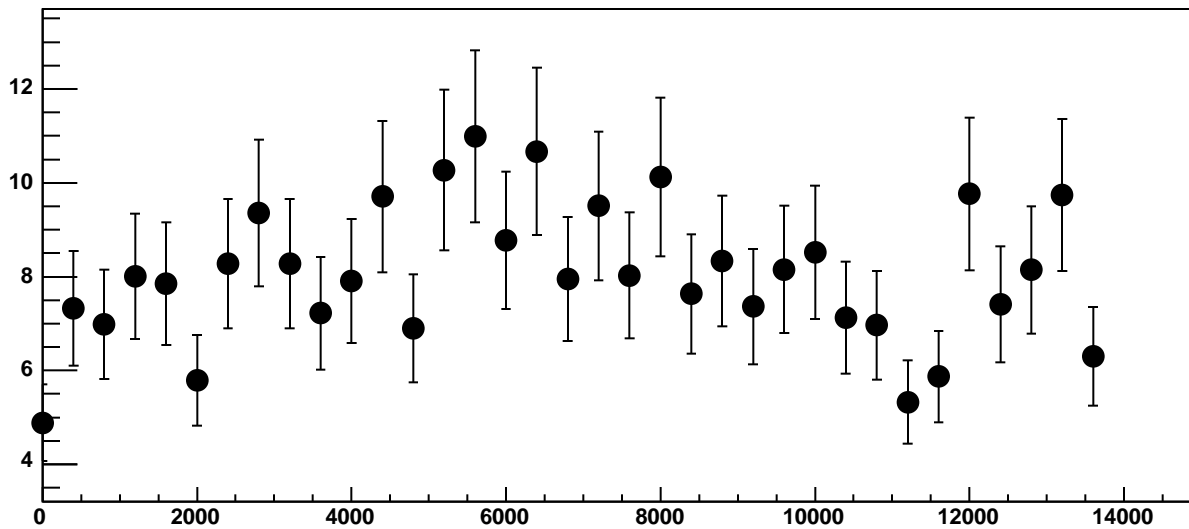
Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



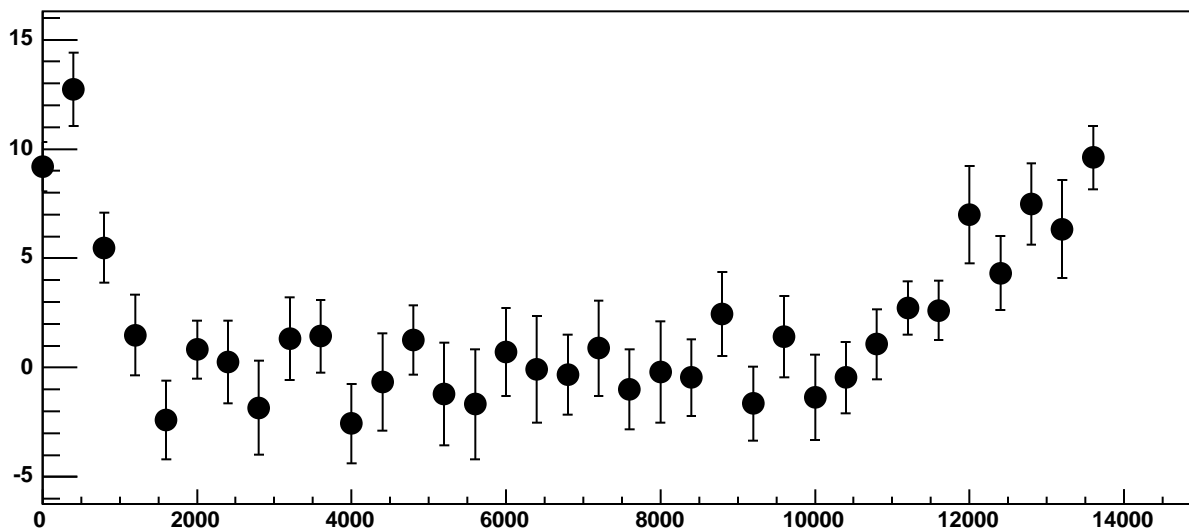
Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



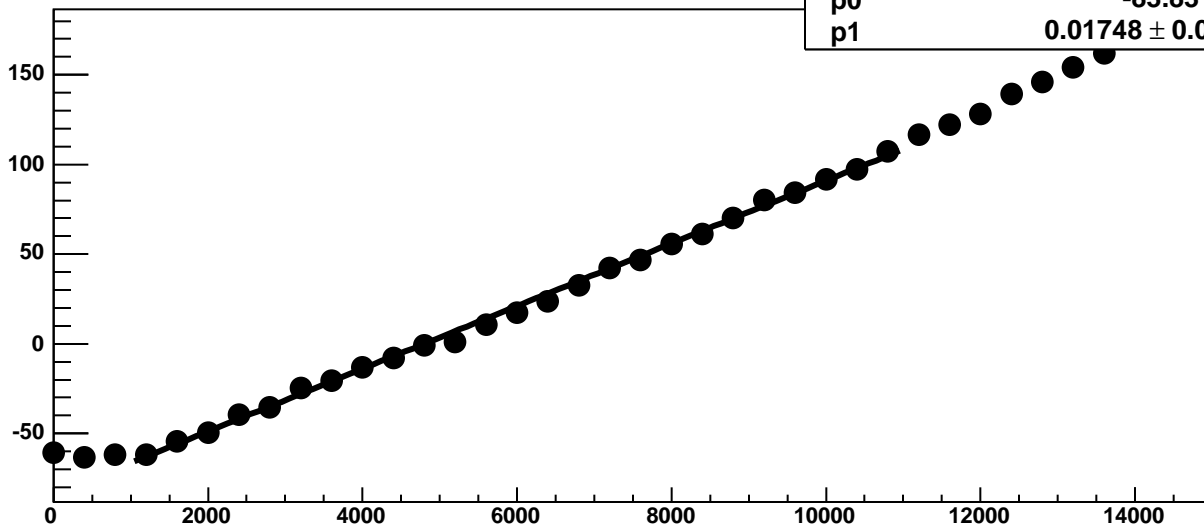
Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

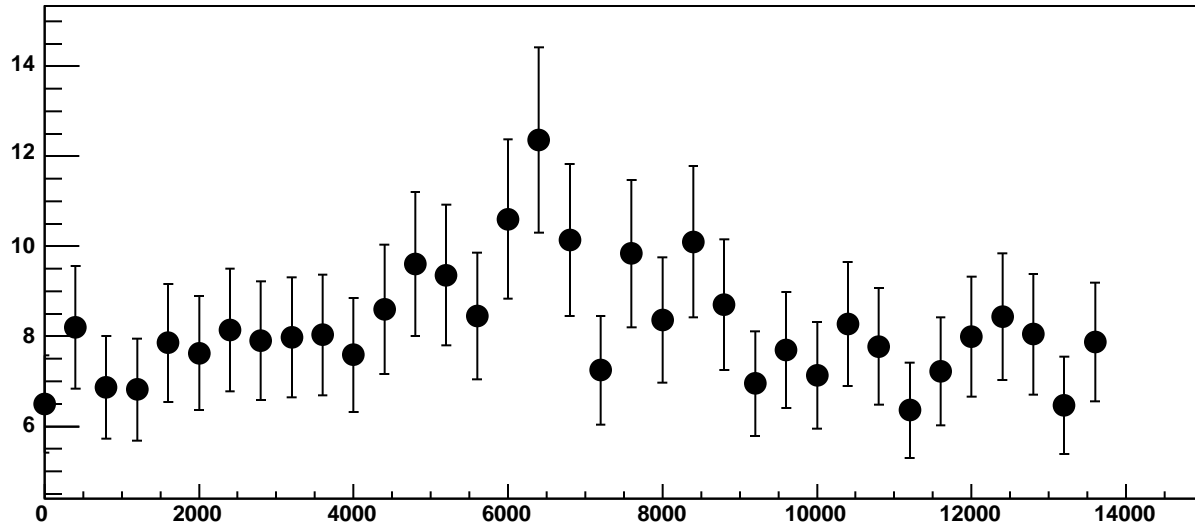


Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

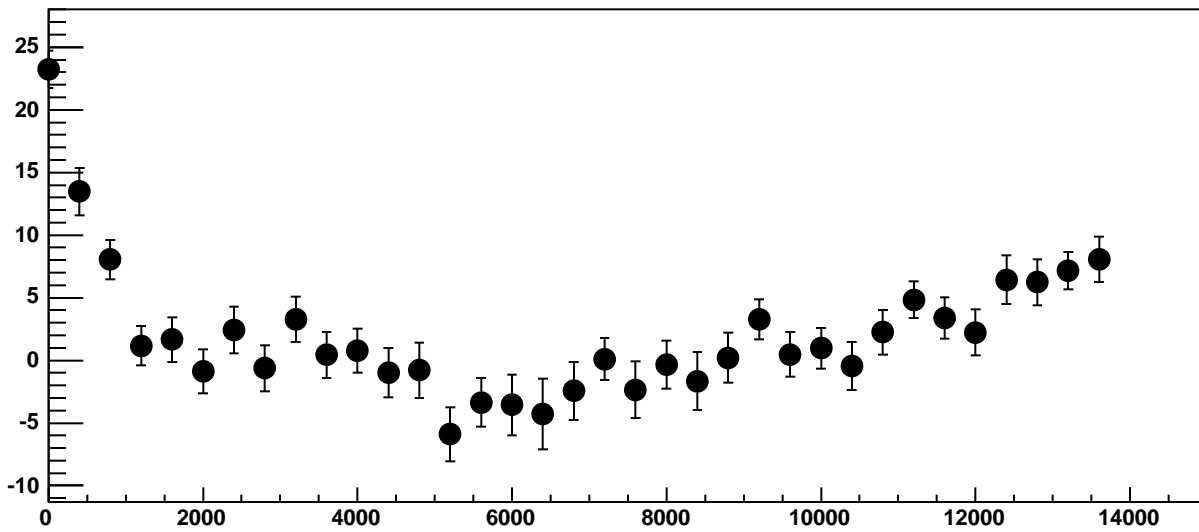


$\chi^2 / \text{ndf}$  31.16 / 23  
p0  $-83.83 \pm 0.823$   
p1  $0.01748 \pm 0.0001231$

Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

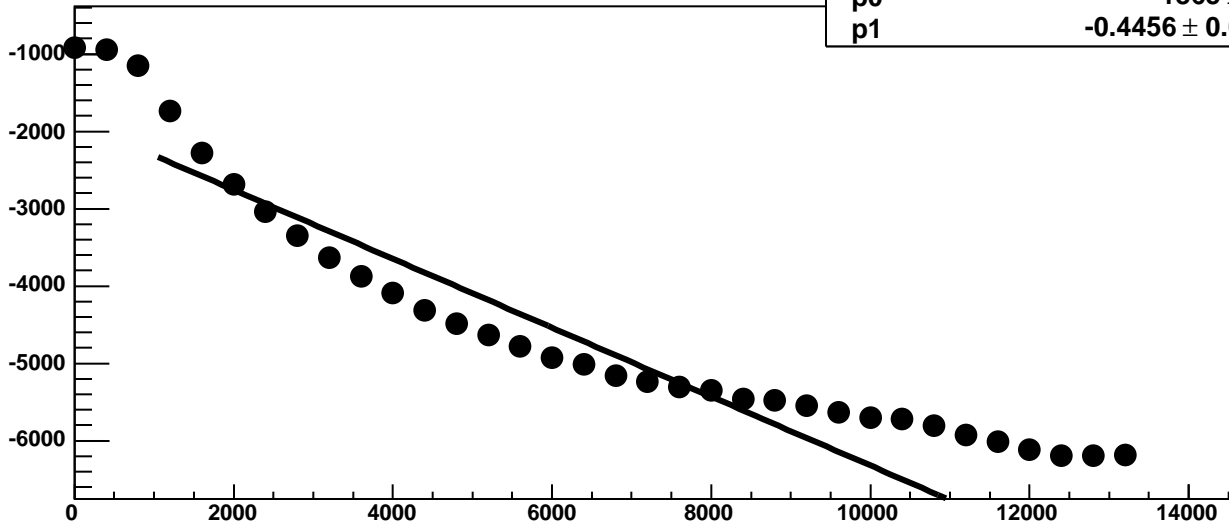
2.701e+04 / 23

p0

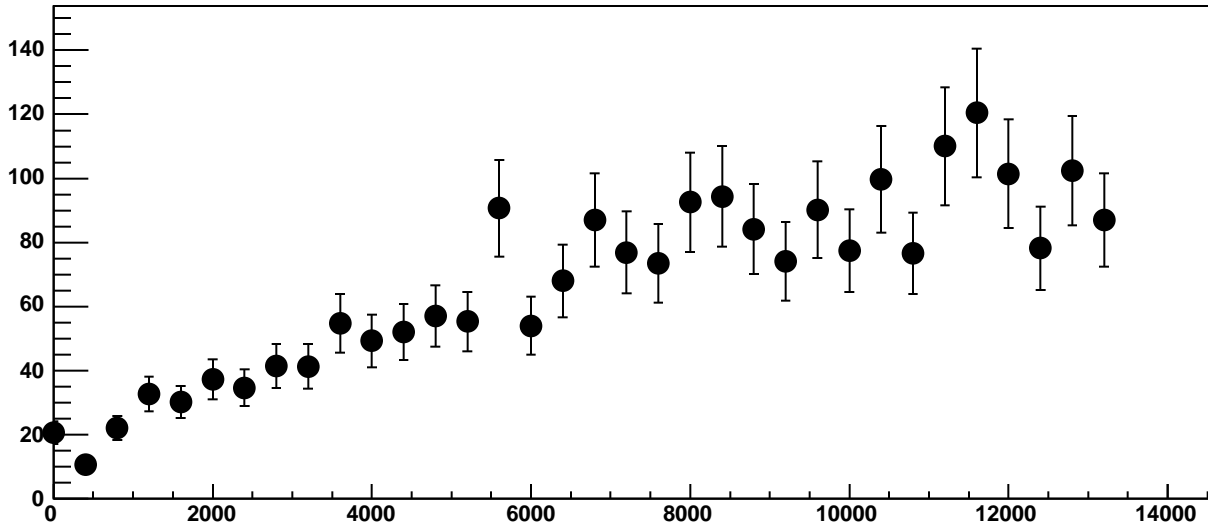
-1865 ± 4.454

p1

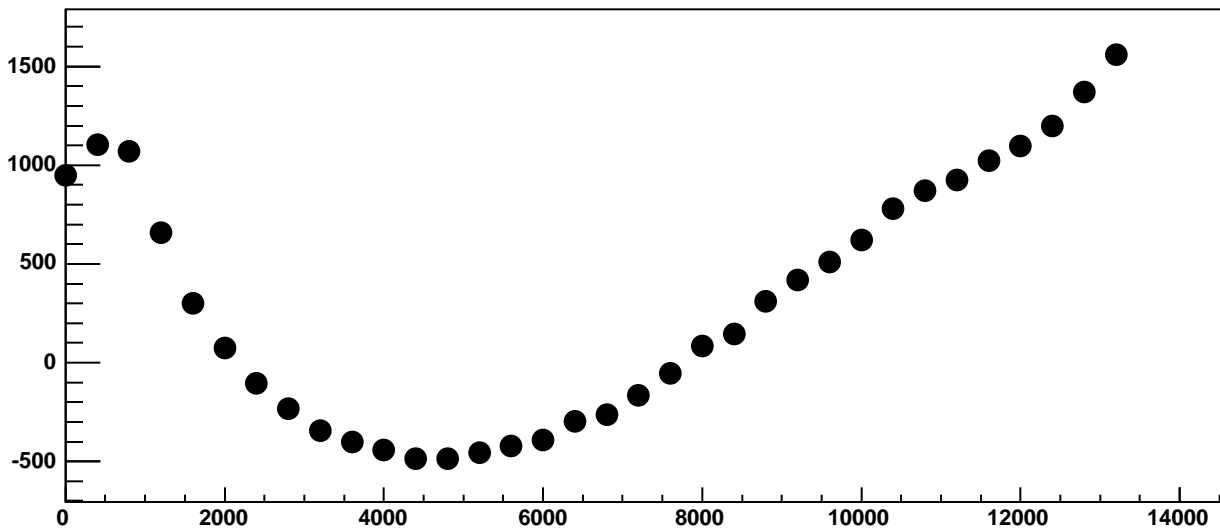
-0.4456 ± 0.000916



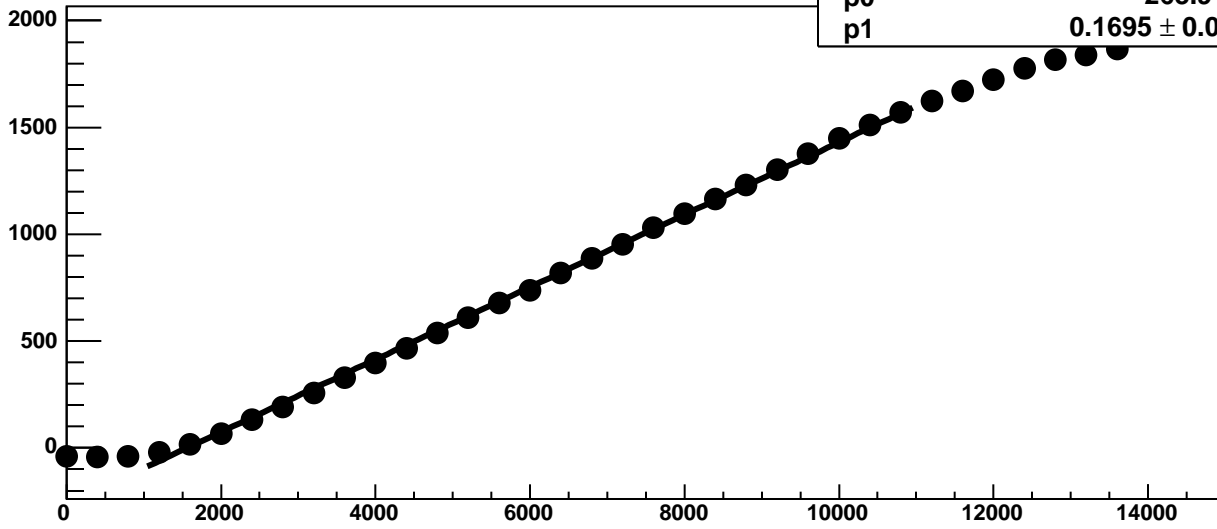
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

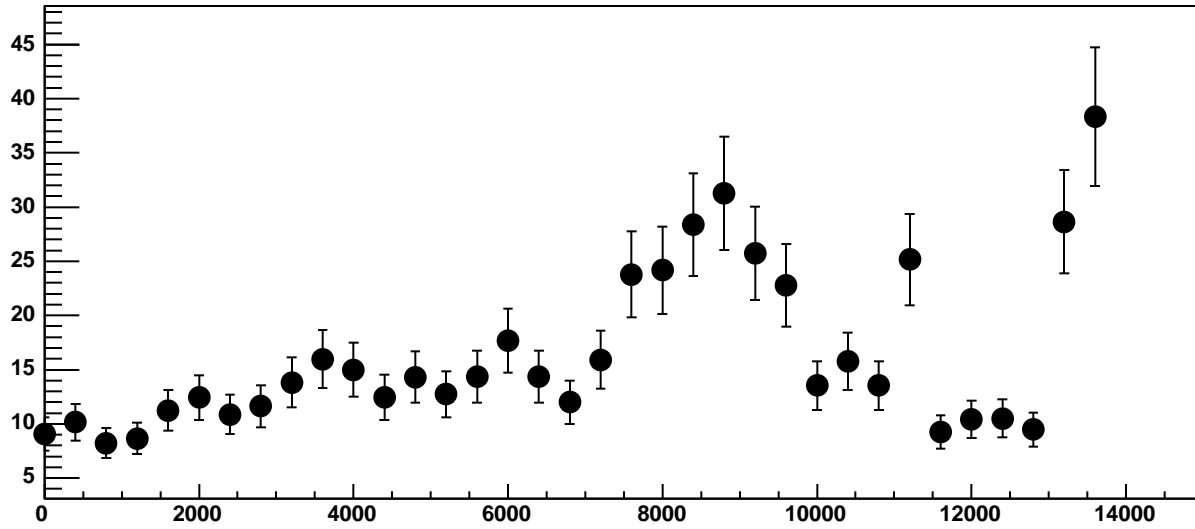


Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

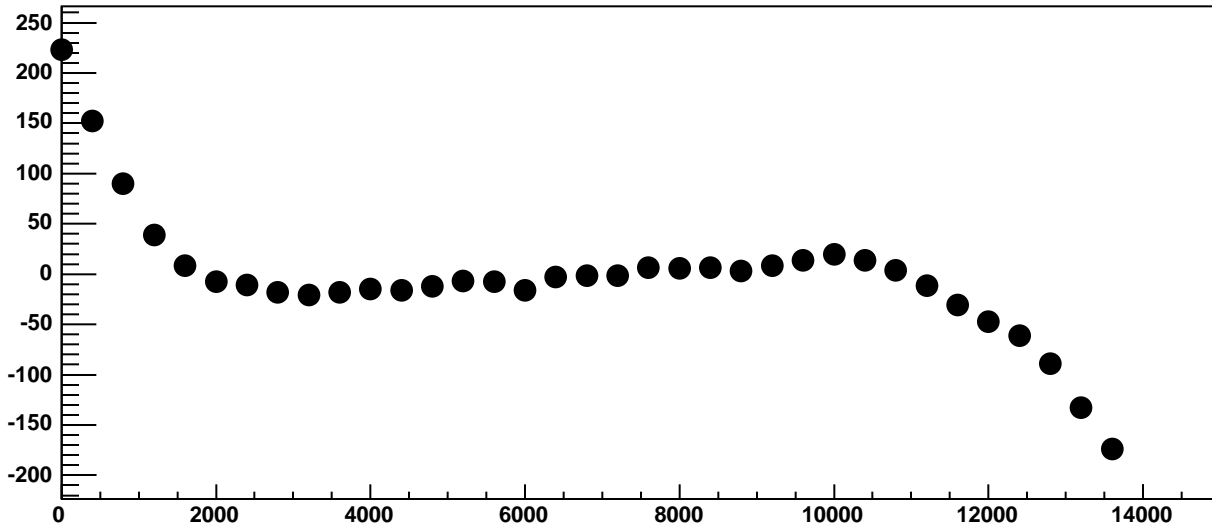


$\chi^2 / \text{ndf}$  693.9 / 23  
p0  $-263.9 \pm 1.266$   
p1  $0.1695 \pm 0.0002204$

Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

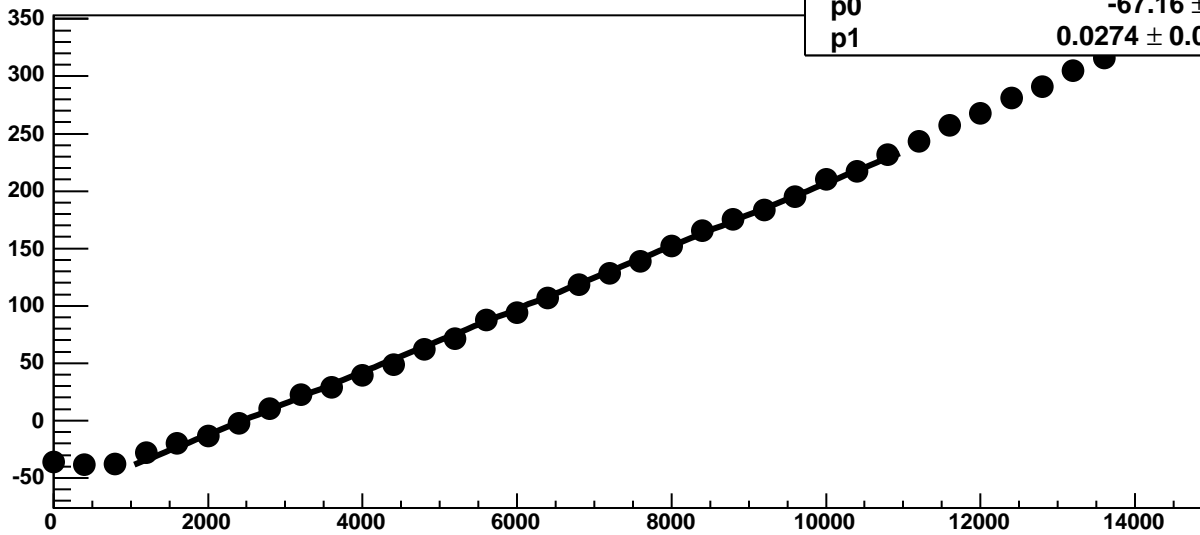


Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC





Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

44.37 / 23

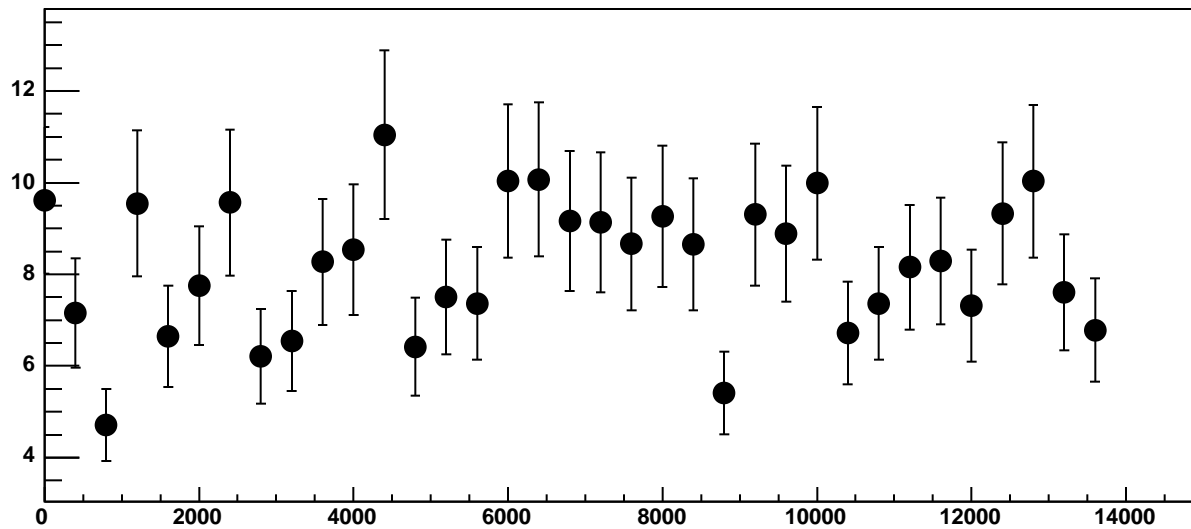
p0

$-67.16 \pm 0.8134$

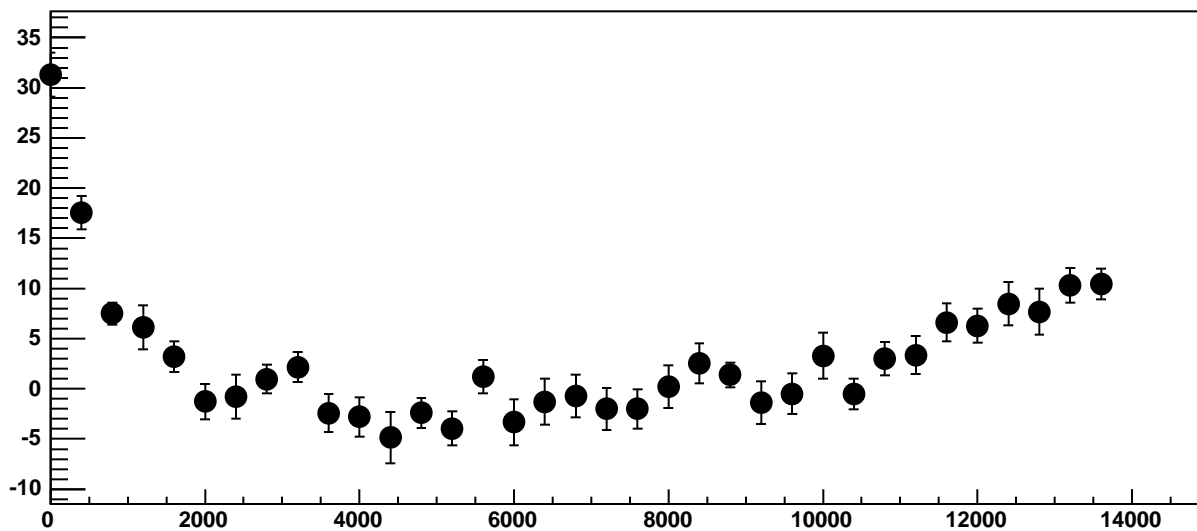
p1

$0.0274 \pm 0.0001225$

Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

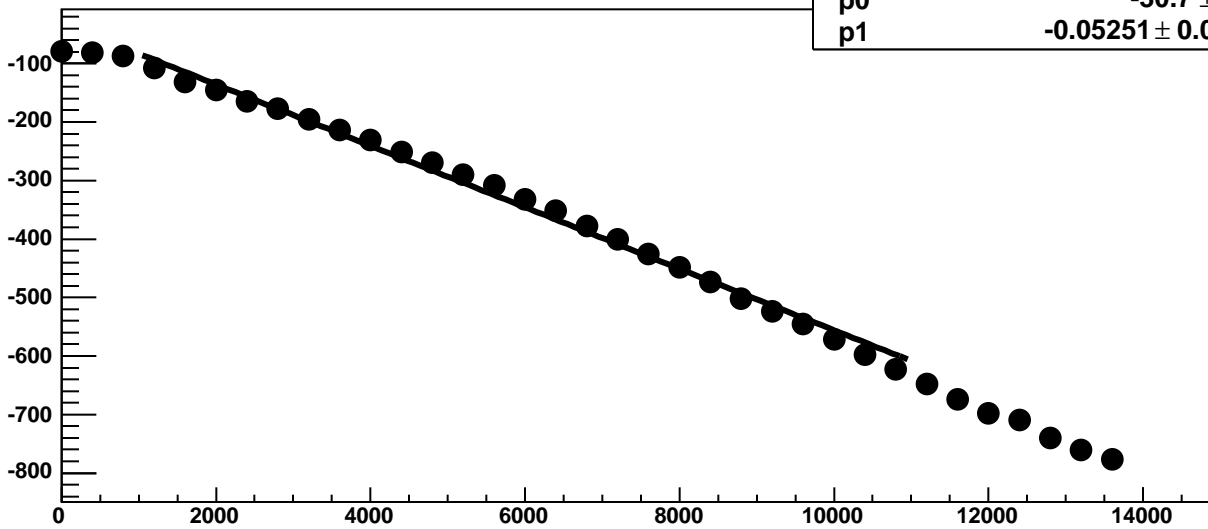


Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

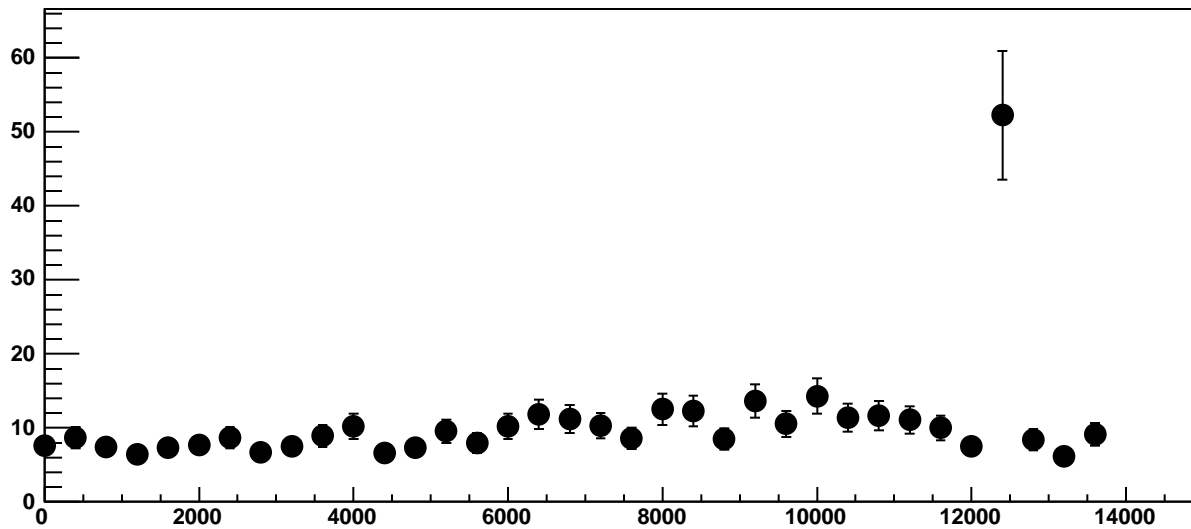


Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

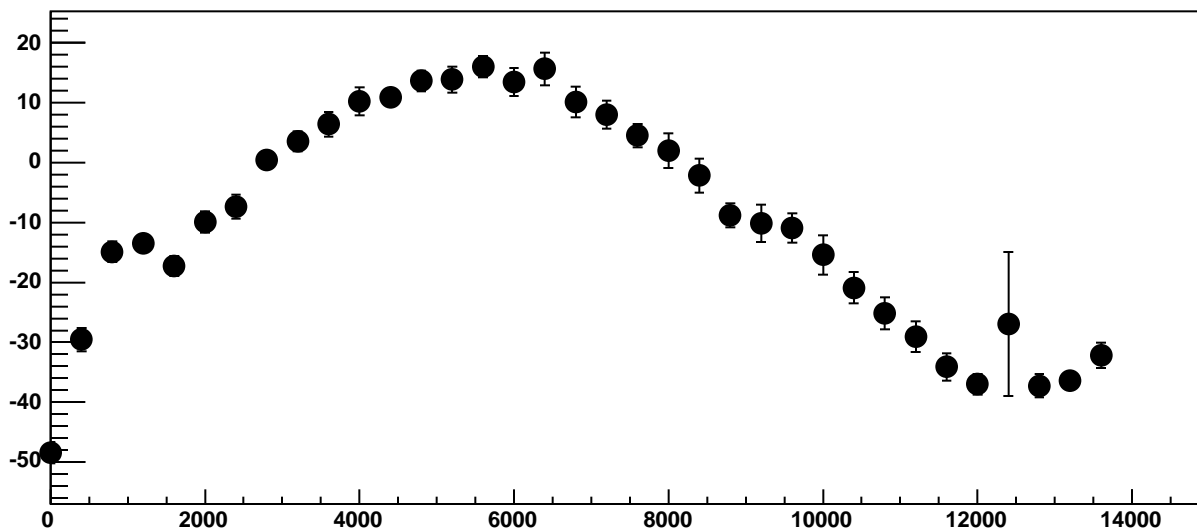
$\chi^2 / \text{ndf}$  824.1 / 23  
p0  $-30.7 \pm 0.8446$   
p1  $-0.05251 \pm 0.0001475$



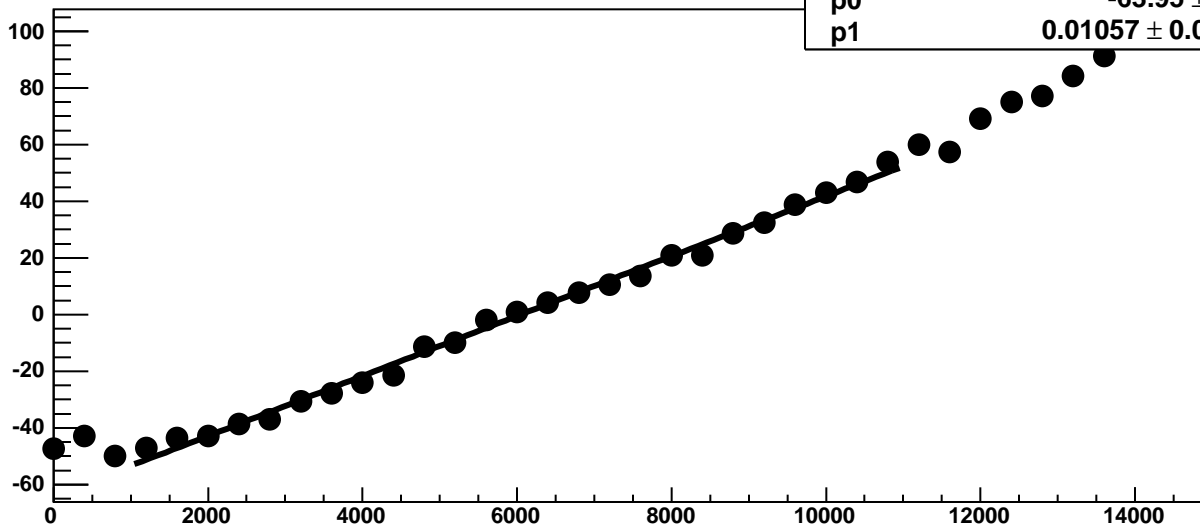
Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



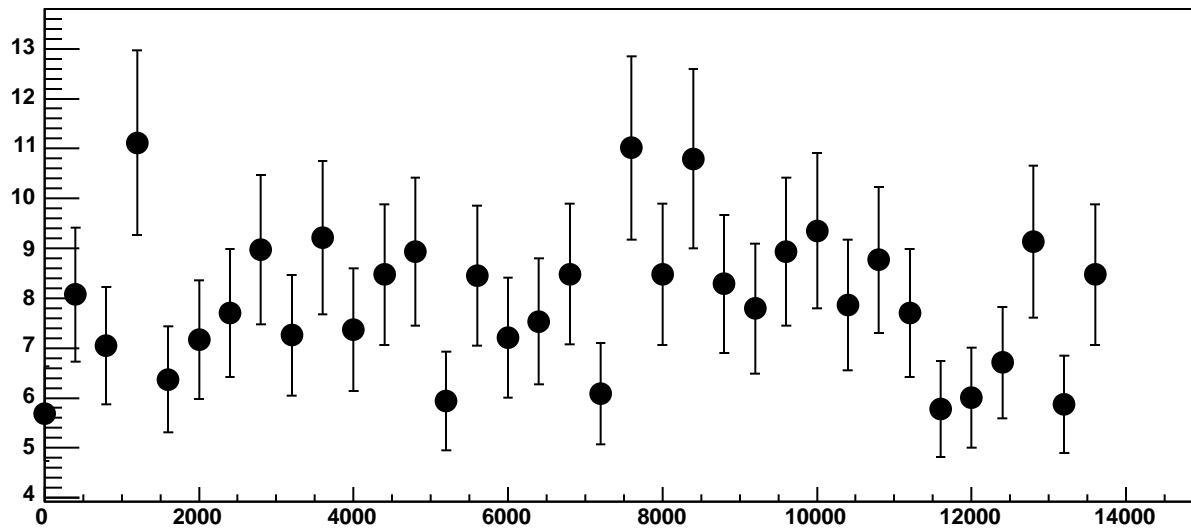
Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



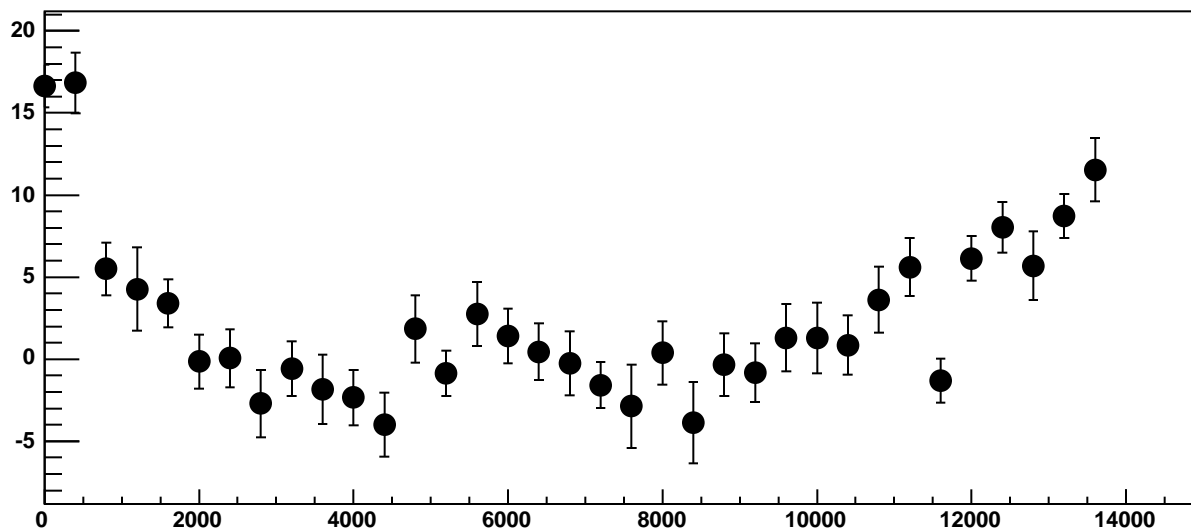
Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



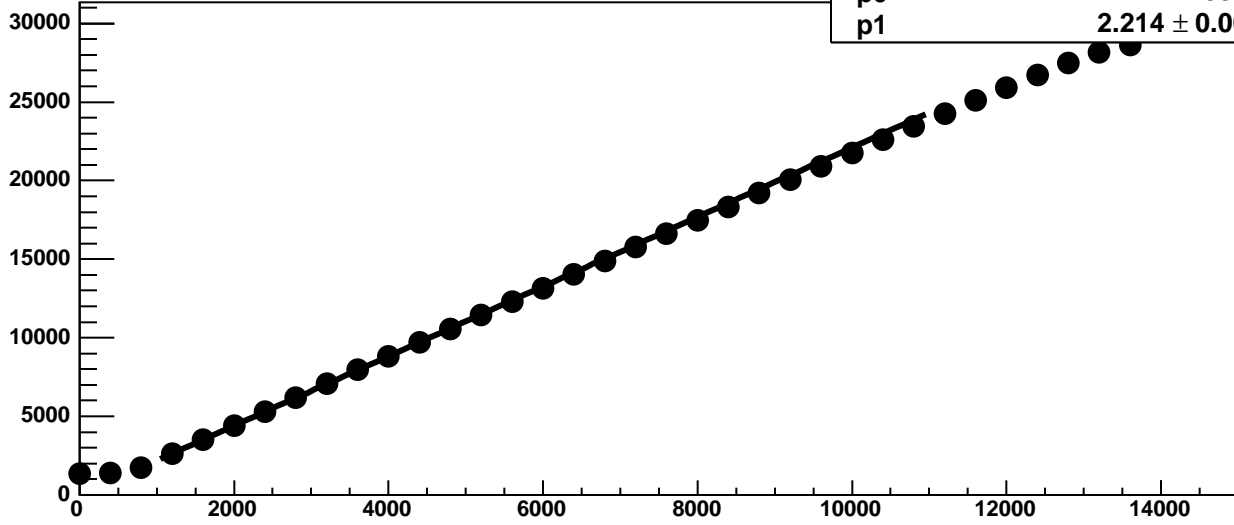
Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



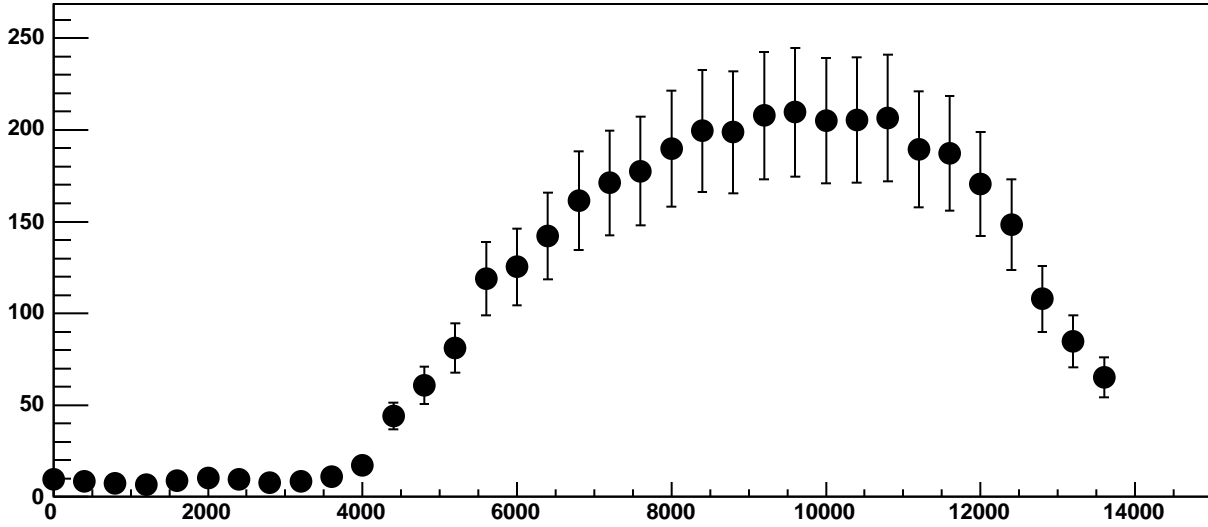
Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



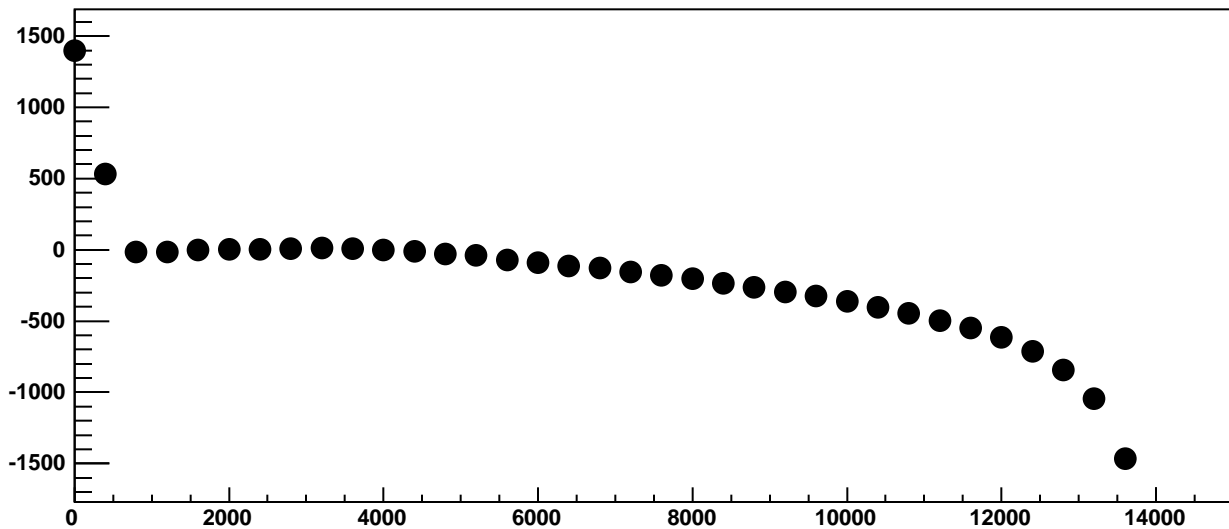
Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

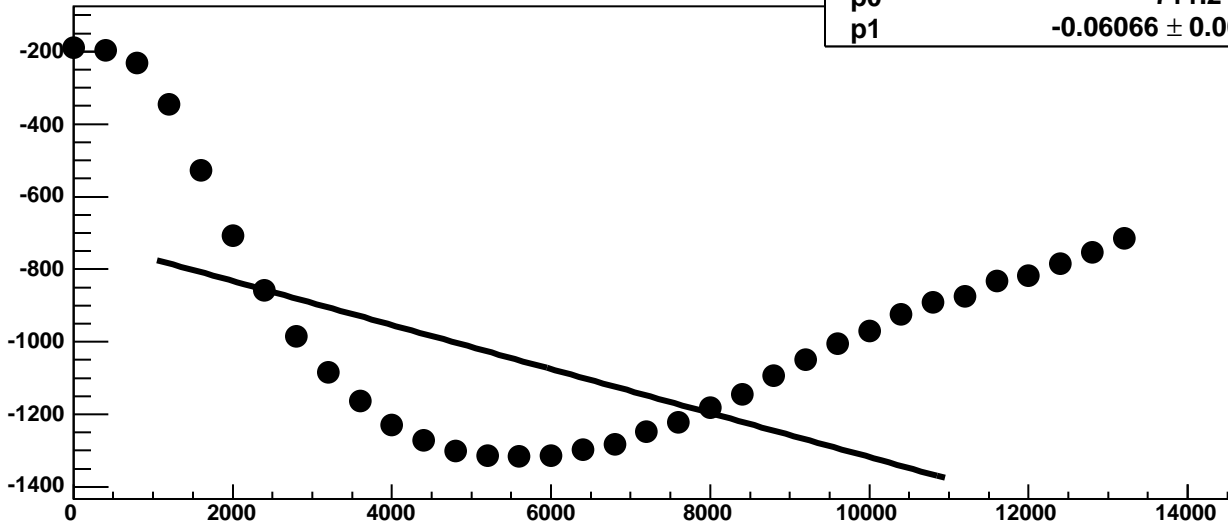


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

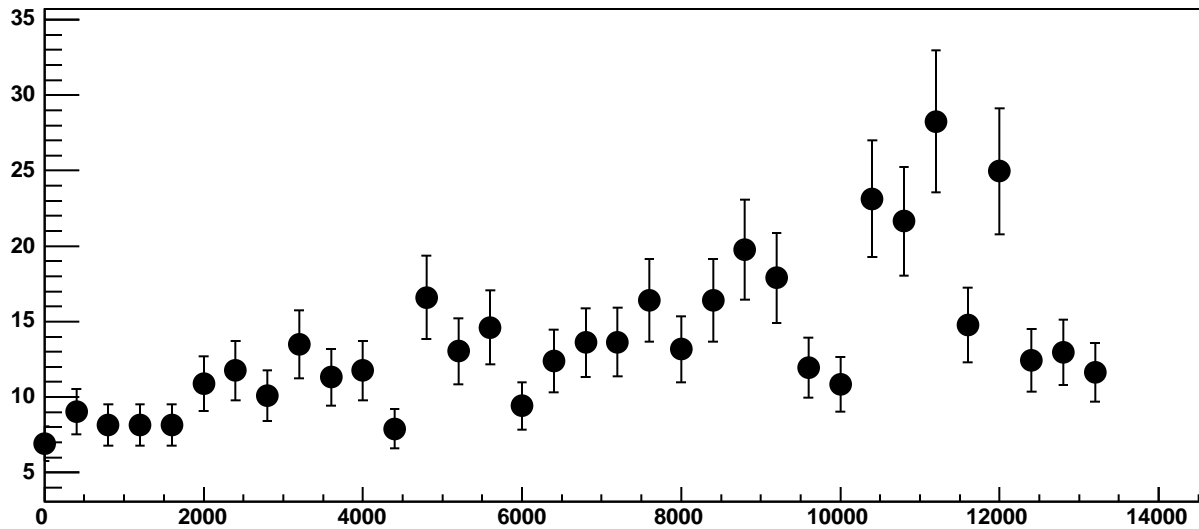


Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

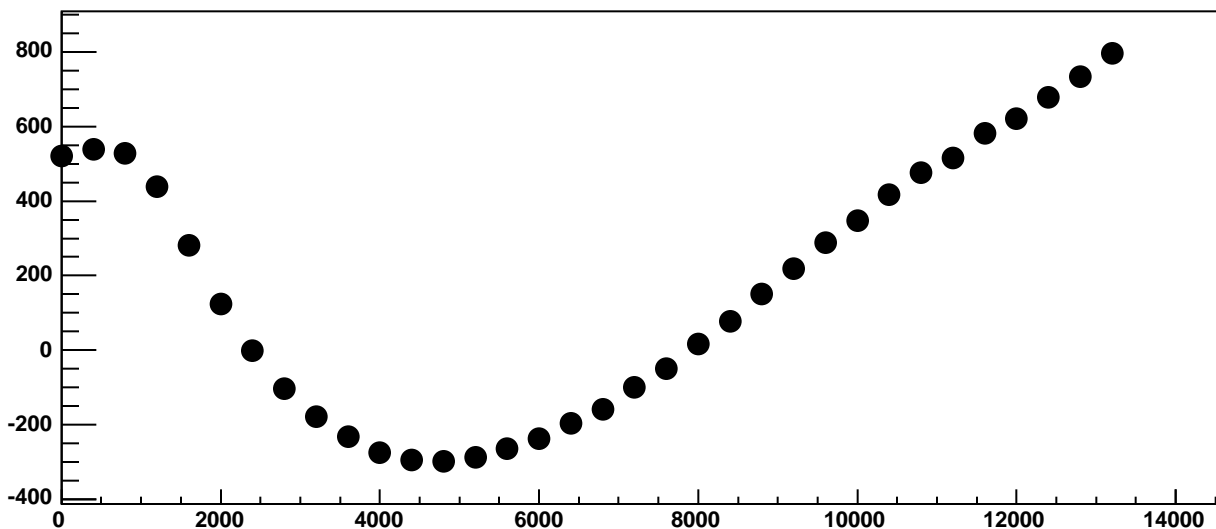
$\chi^2 / \text{ndf}$  2.234e+05 / 23  
p0 -711.2 ± 1.115  
p1 -0.06066 ± 0.0001954



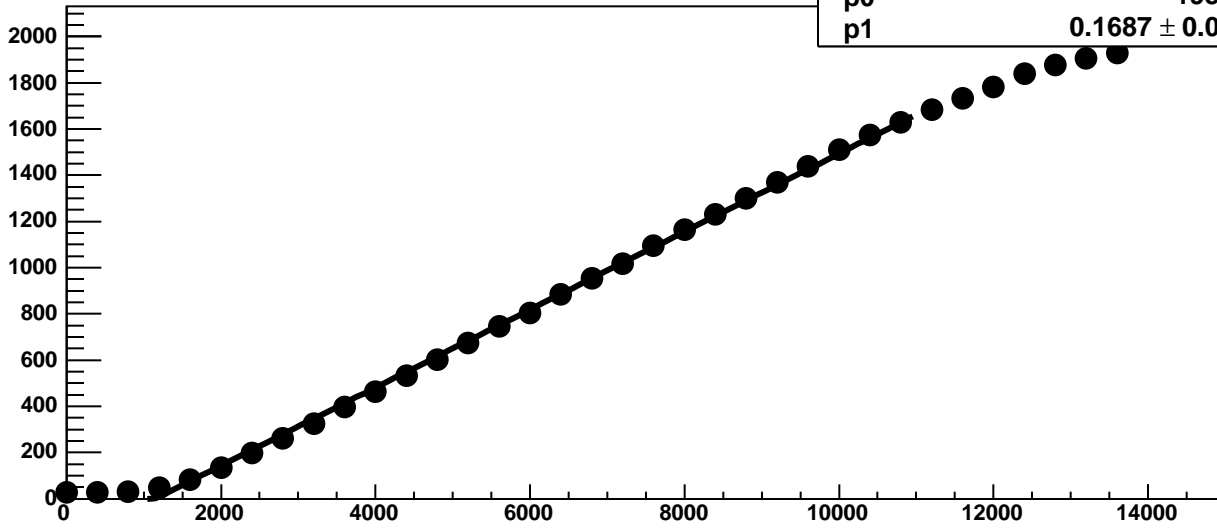
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

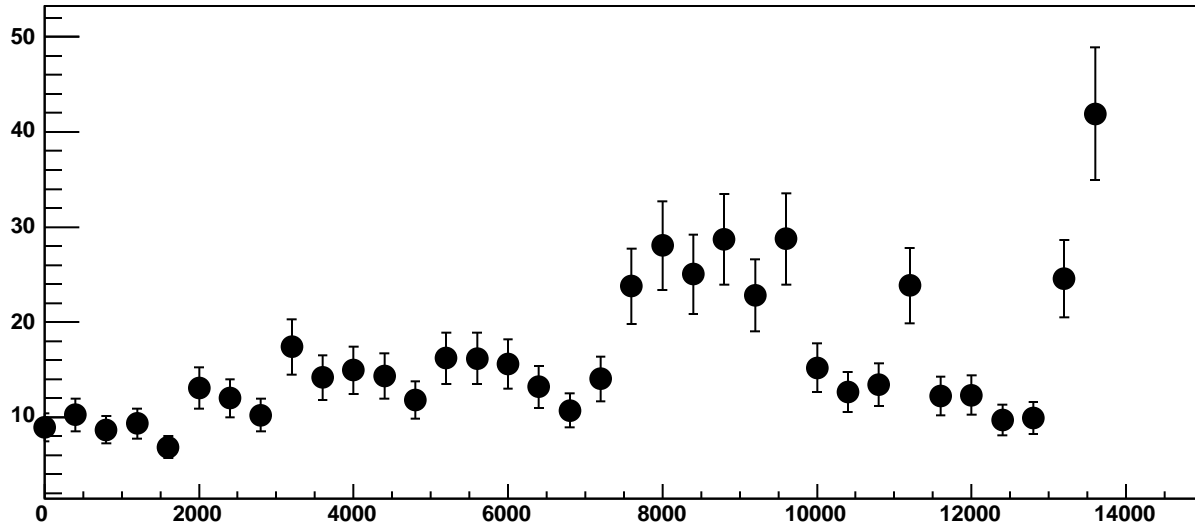


Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

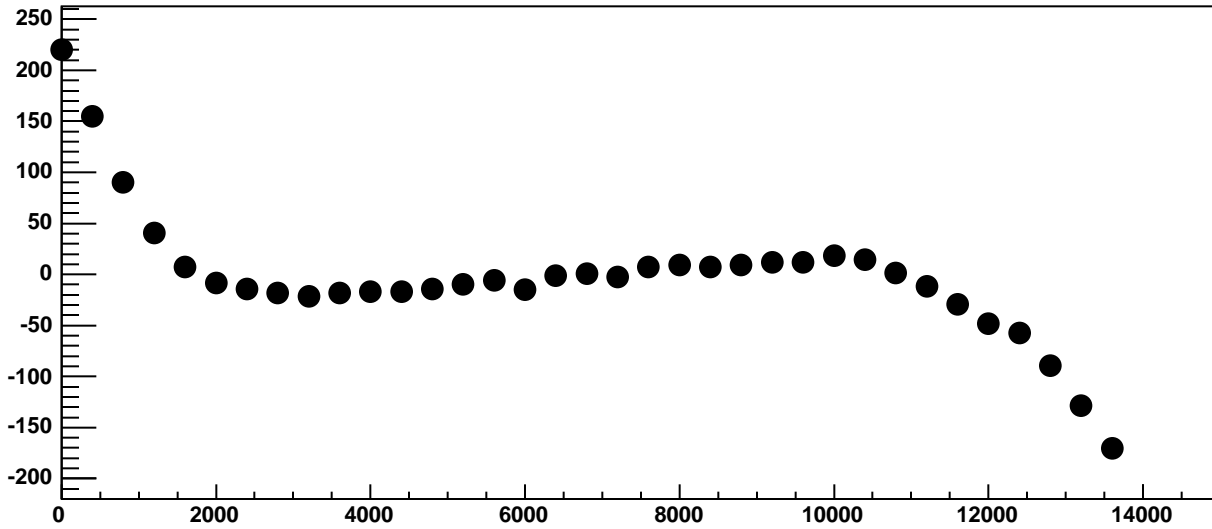


$\chi^2 / \text{ndf}$  706.9 / 23  
p0  $-193 \pm 1.16$   
p1  $0.1687 \pm 0.0002058$

Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

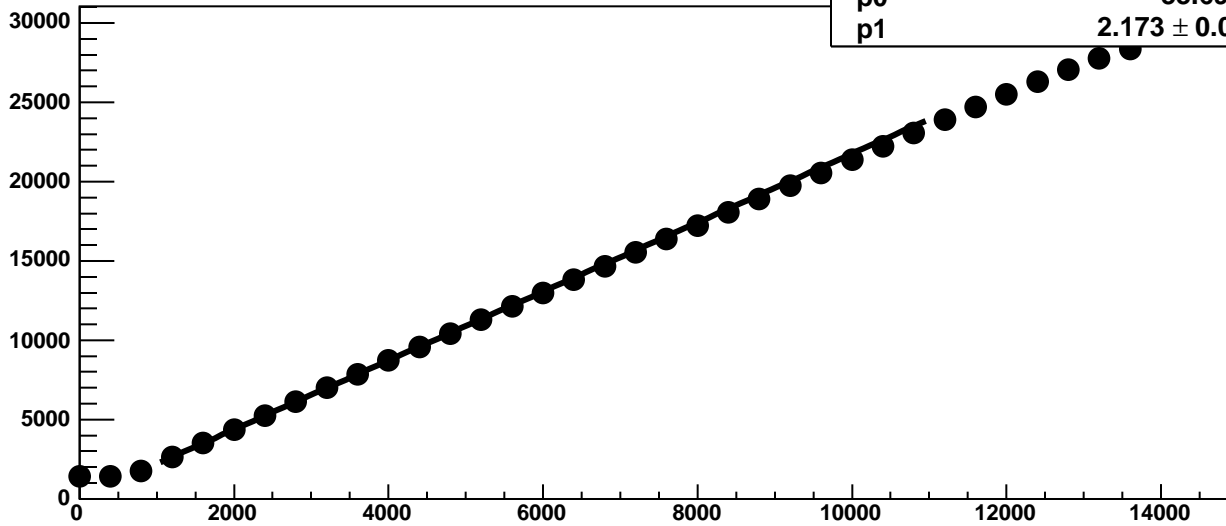


Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

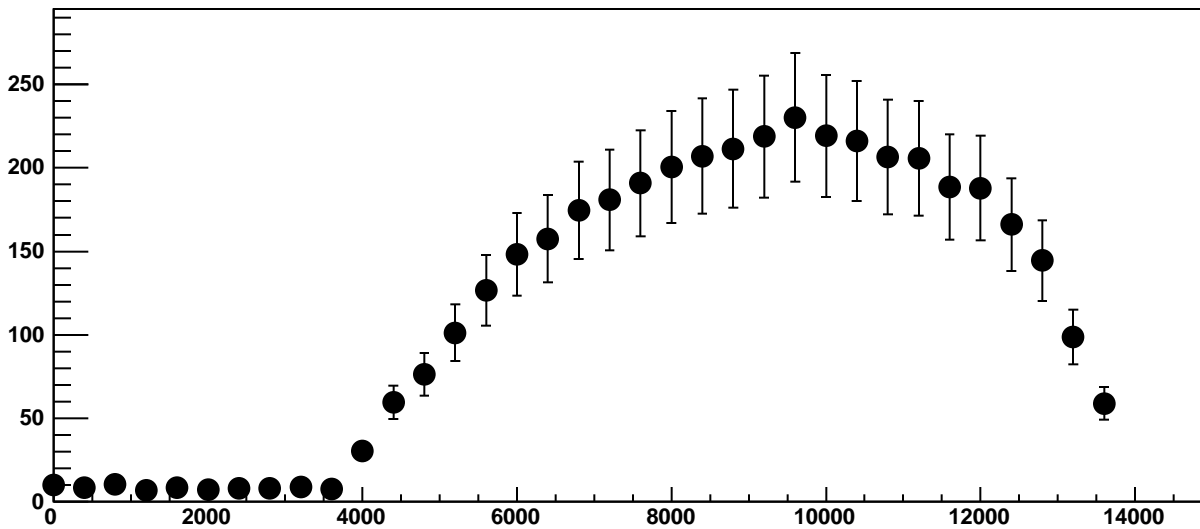


Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

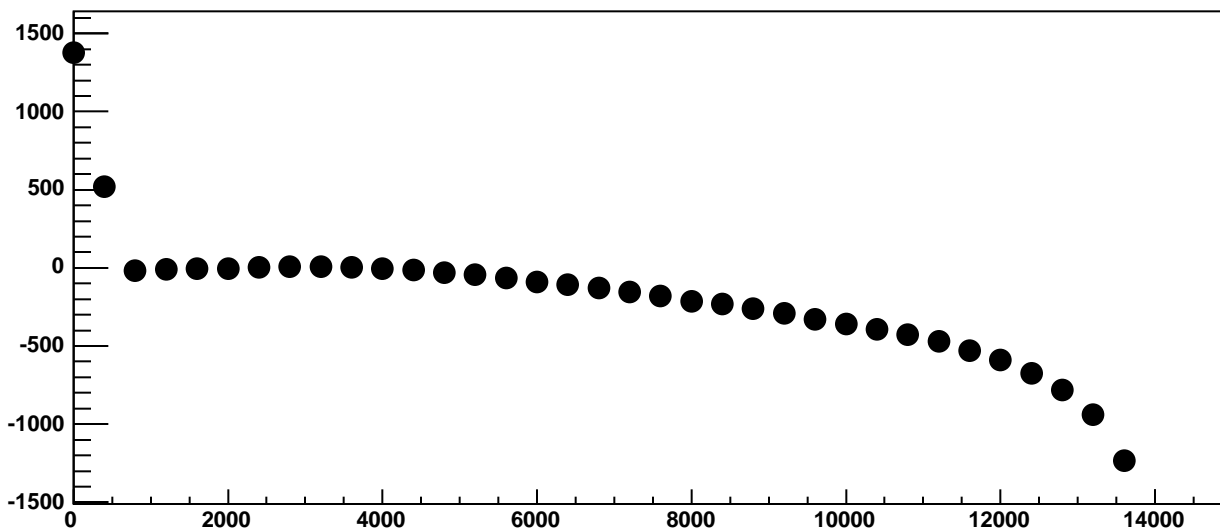
$\chi^2 / \text{ndf}$  512.4 / 23  
p0  $33.69 \pm 1.921$   
p1  $2.173 \pm 0.0007526$



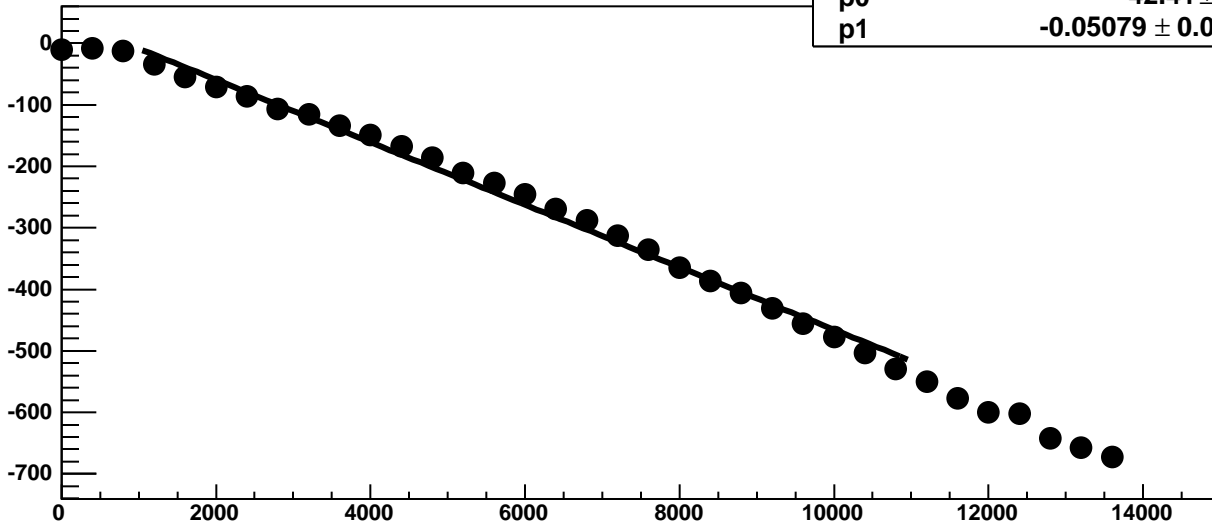
Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

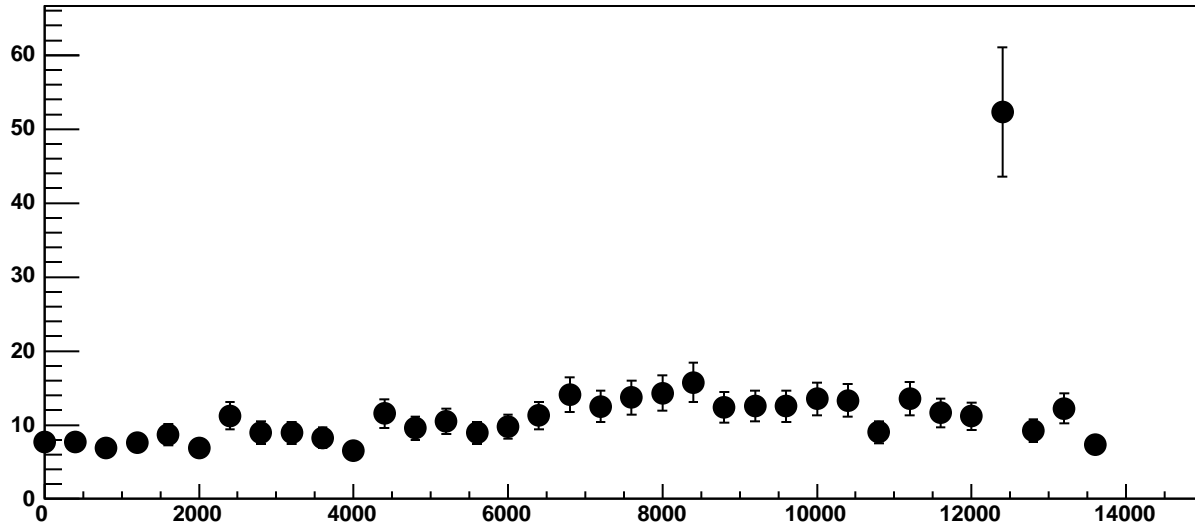


Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

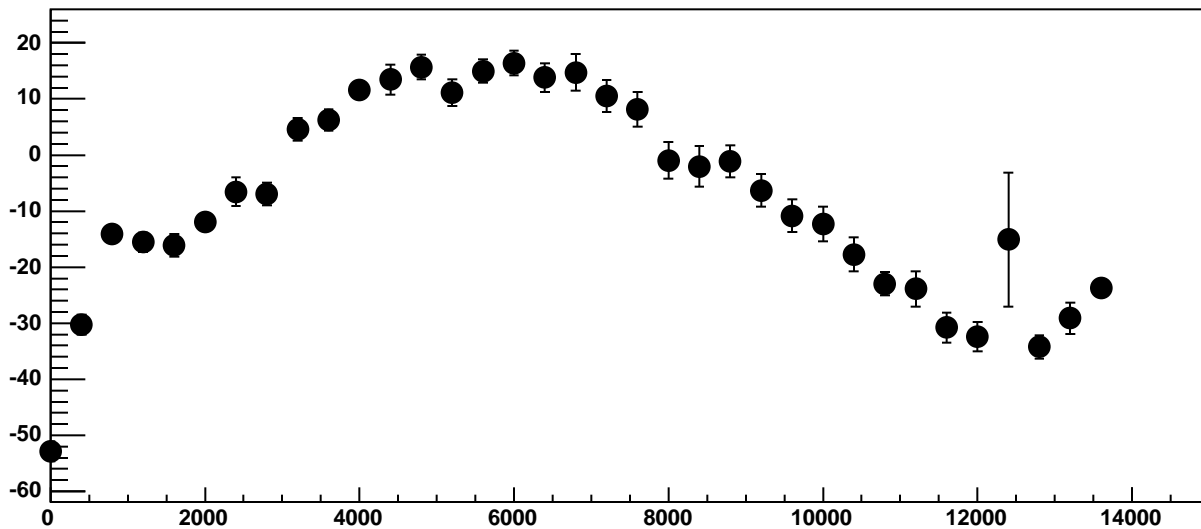


$\chi^2 / \text{ndf}$  762.7 / 23  
p0  $42.41 \pm 0.9283$   
p1  $-0.05079 \pm 0.0001599$

Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

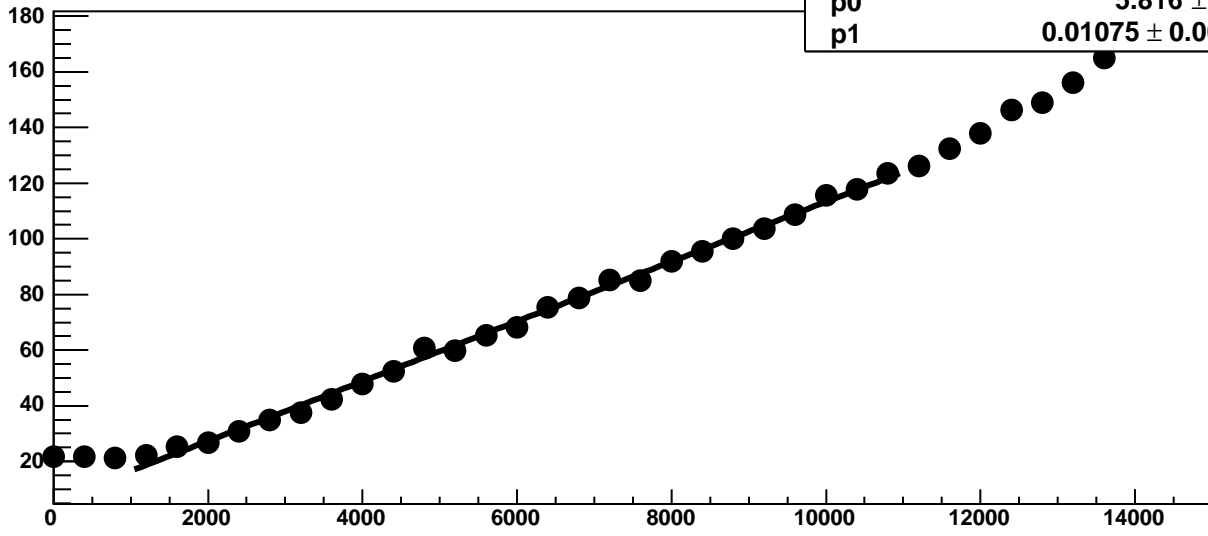


Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



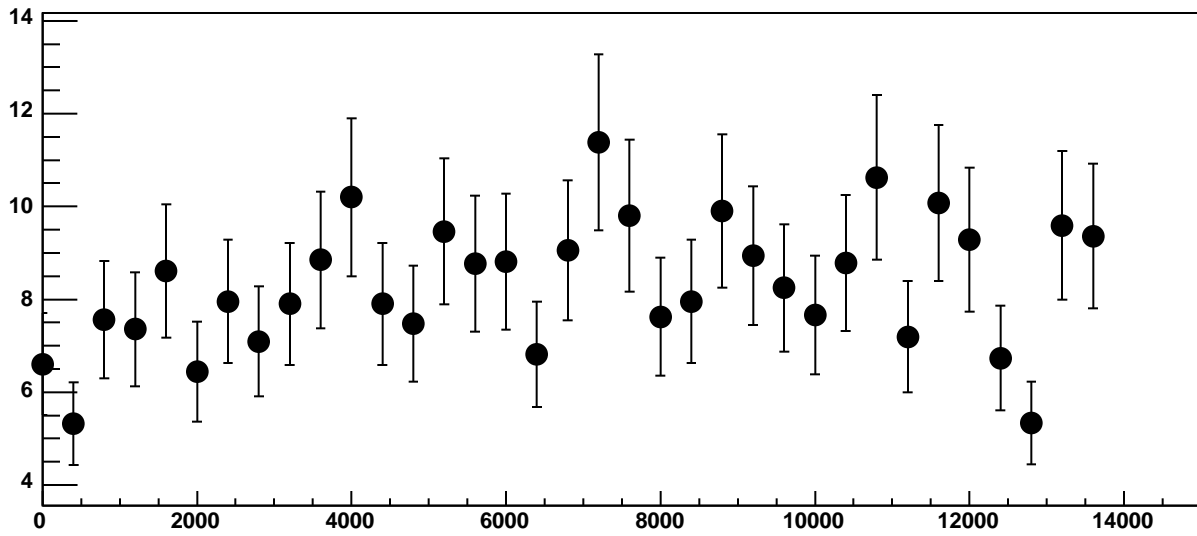


Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

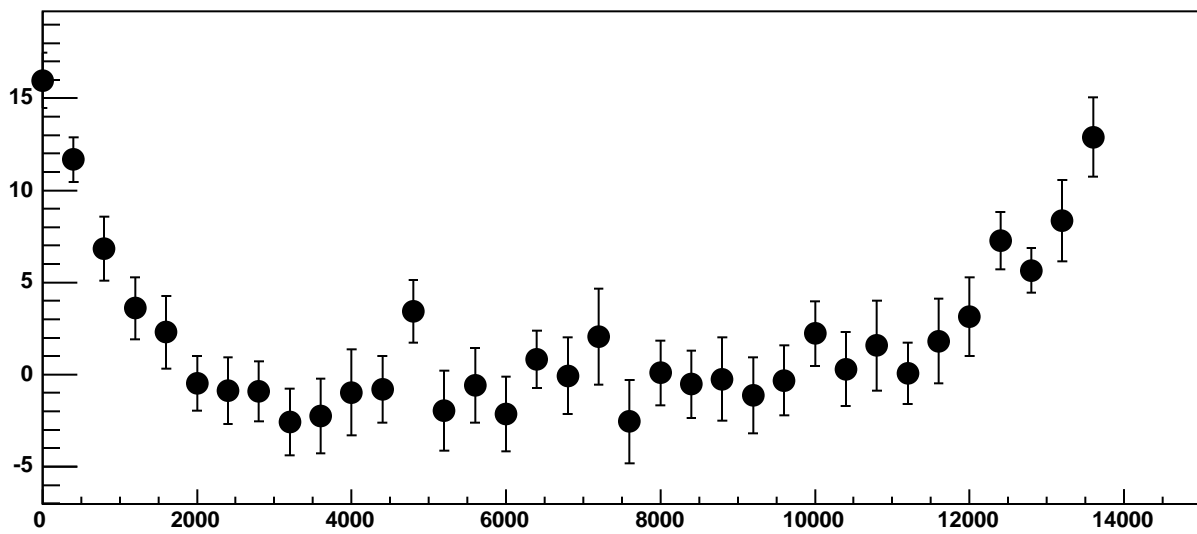


$\chi^2 / \text{ndf}$  20.87 / 23  
p0  $5.816 \pm 0.8336$   
p1  $0.01075 \pm 0.0001305$

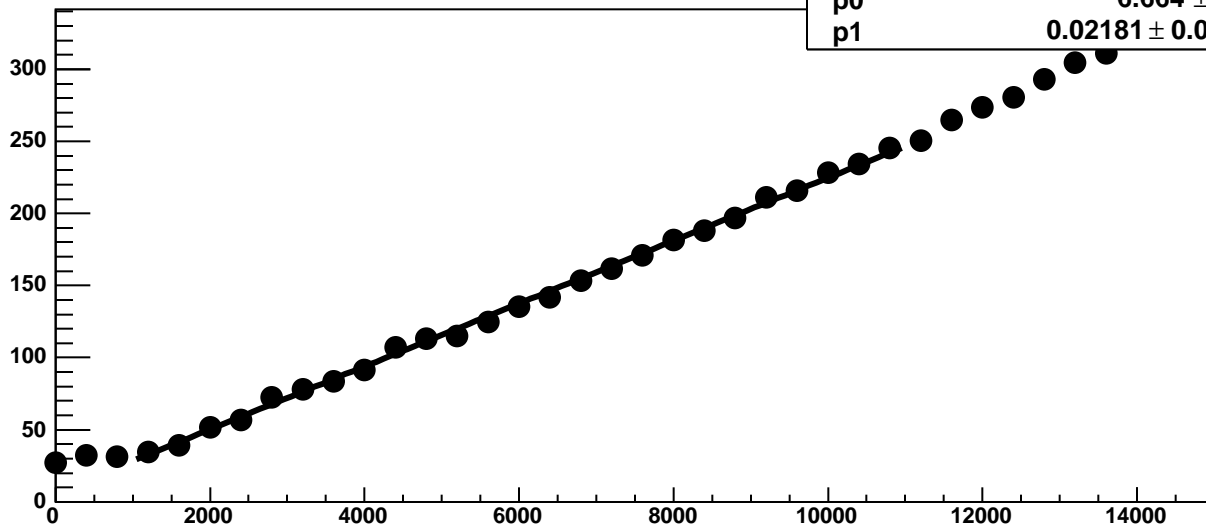
Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

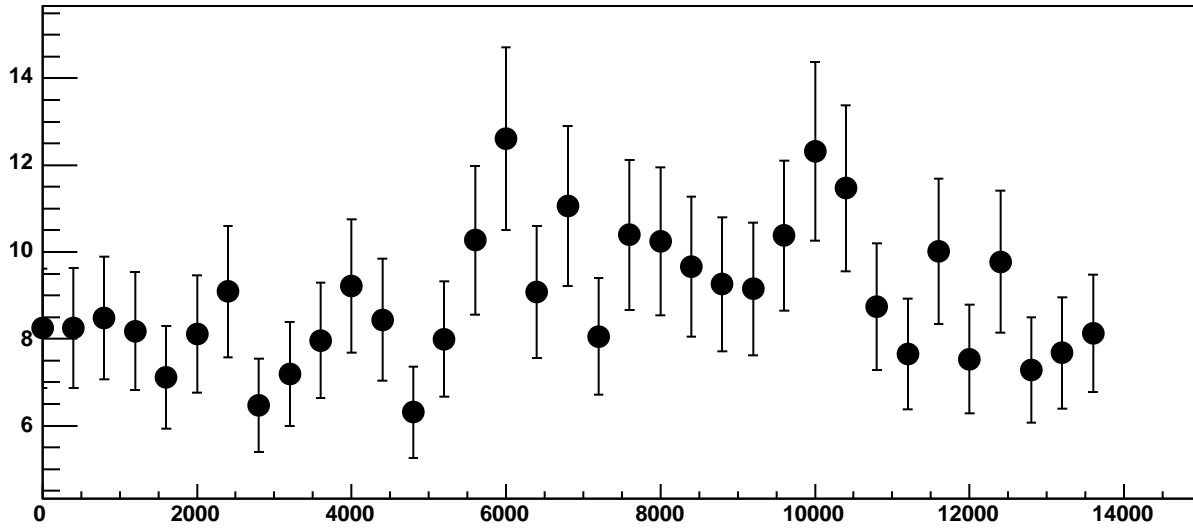


Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

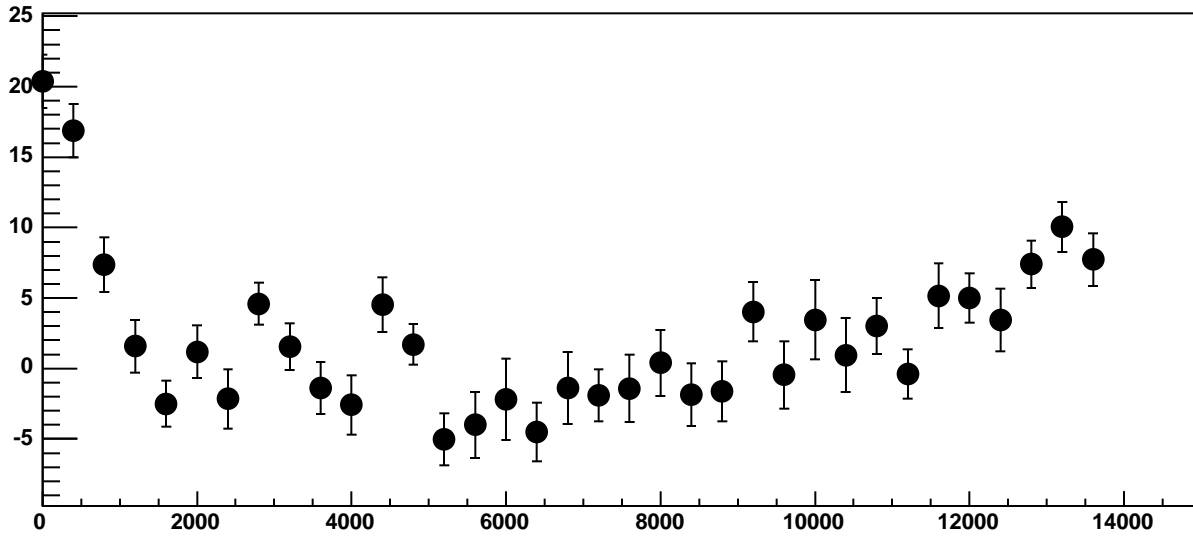


$\chi^2 / \text{ndf}$  50.13 / 23  
p0  $6.664 \pm 0.8553$   
p1  $0.02181 \pm 0.0001408$

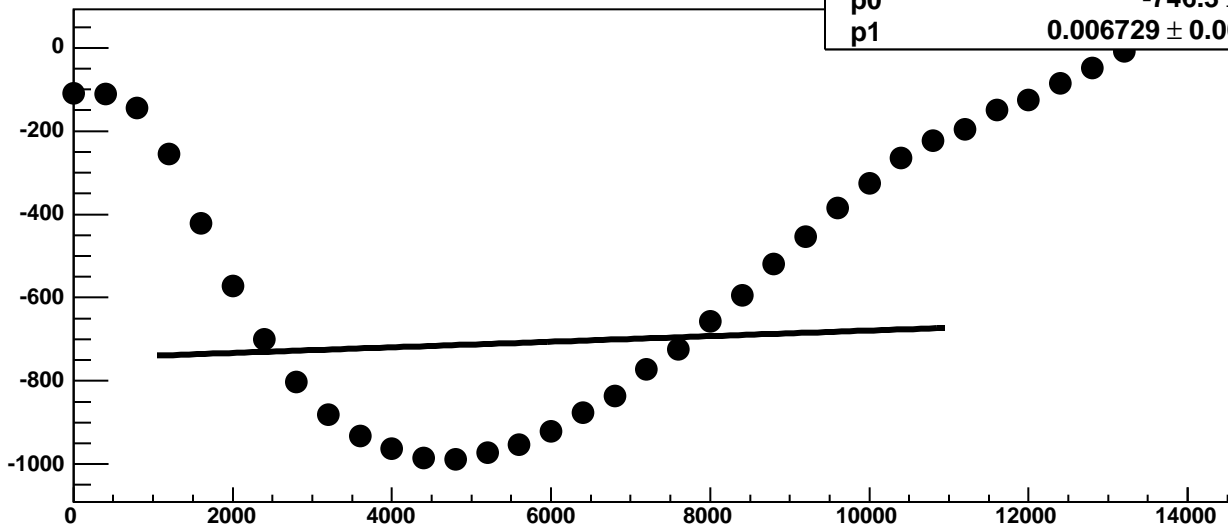
Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

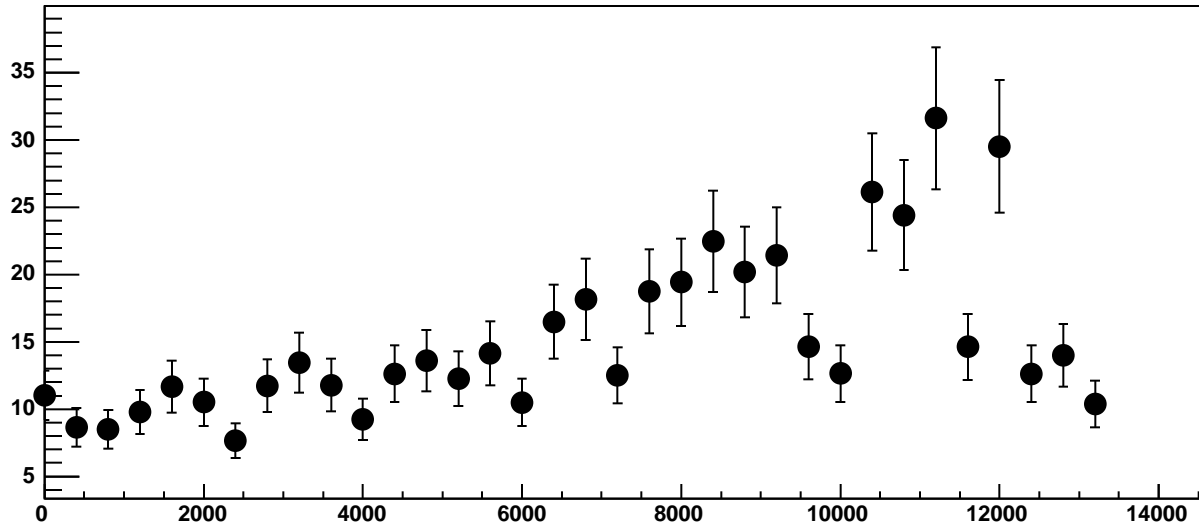


Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

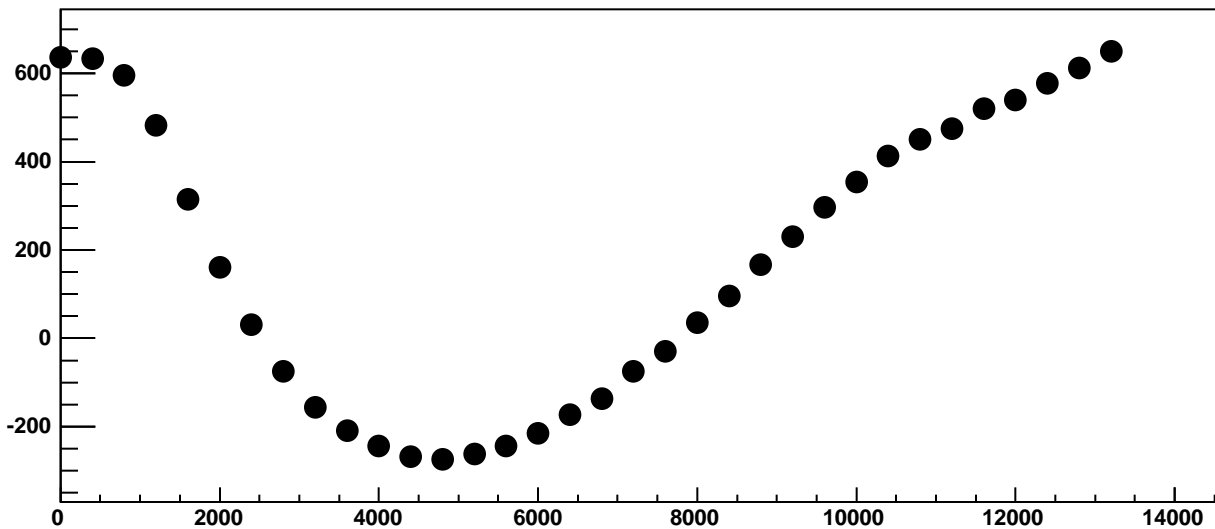


$\chi^2 / \text{ndf}$  1.676e+05 / 23  
p0 -746.3 ± 1.214  
p1 0.006729 ± 0.0002233

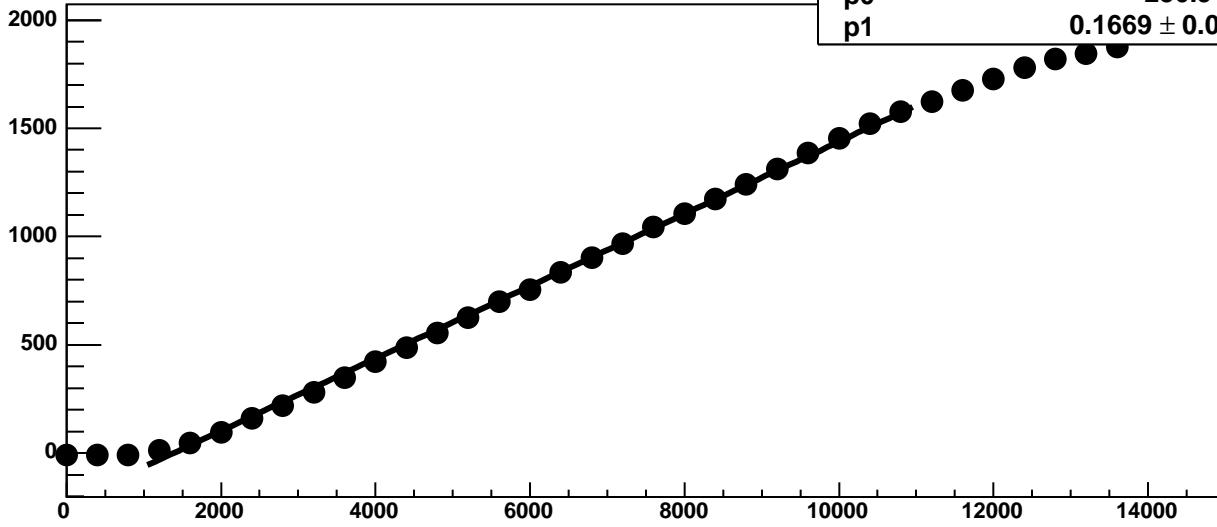
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



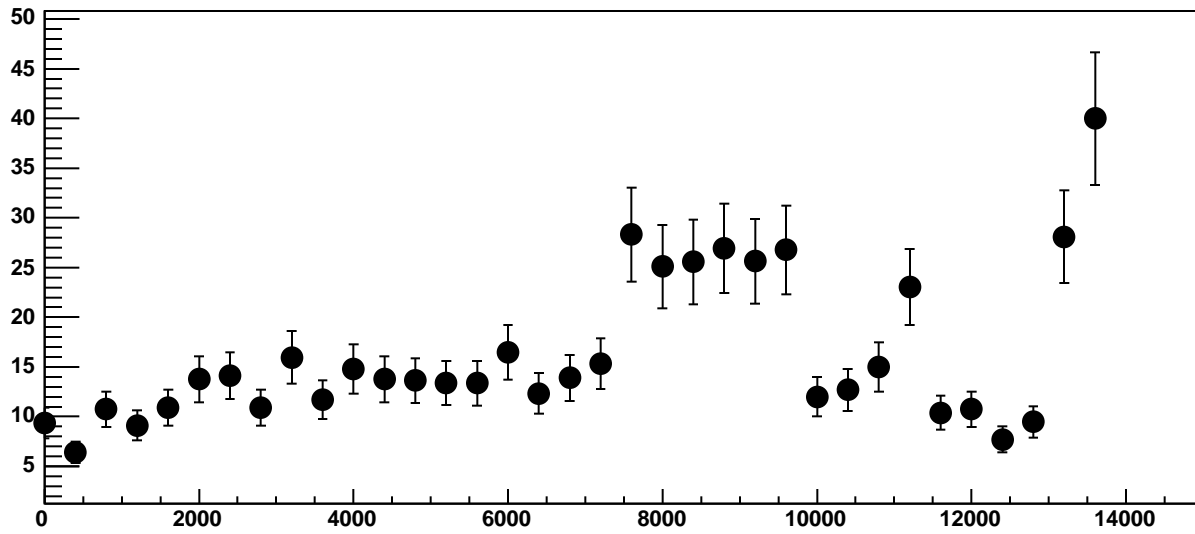
Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



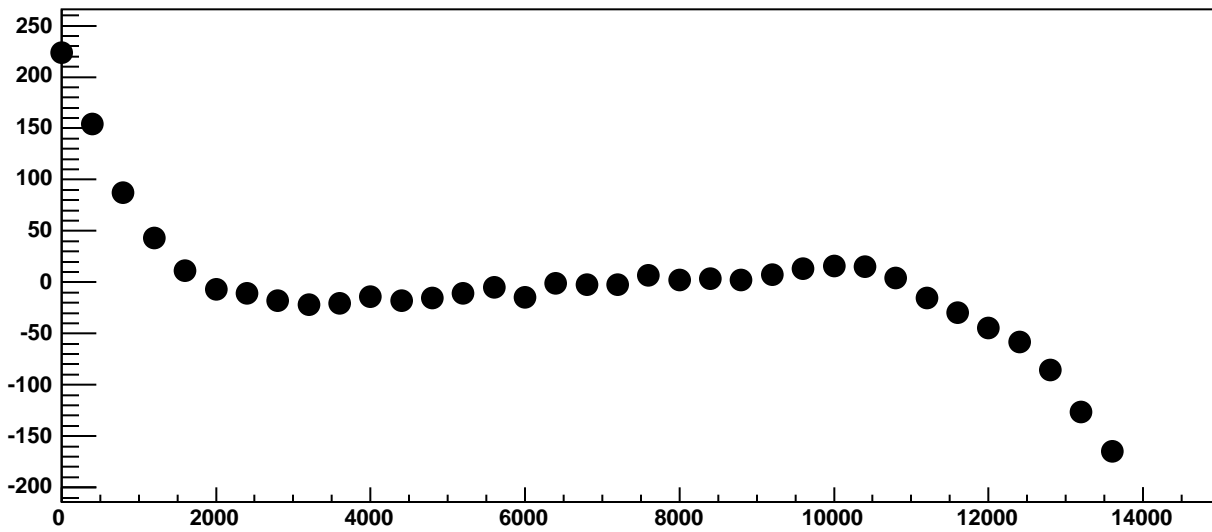
Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



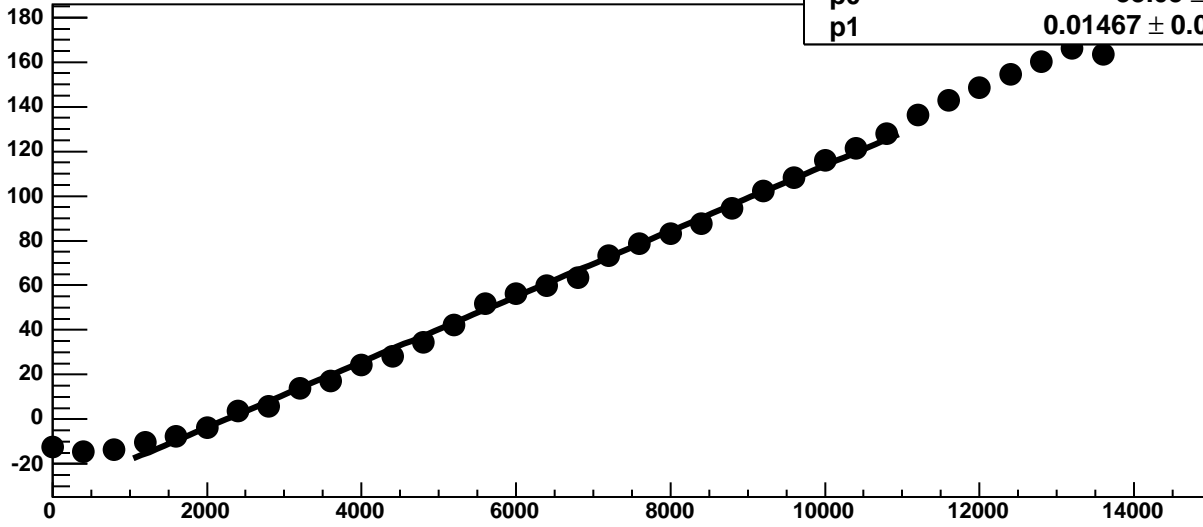
Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



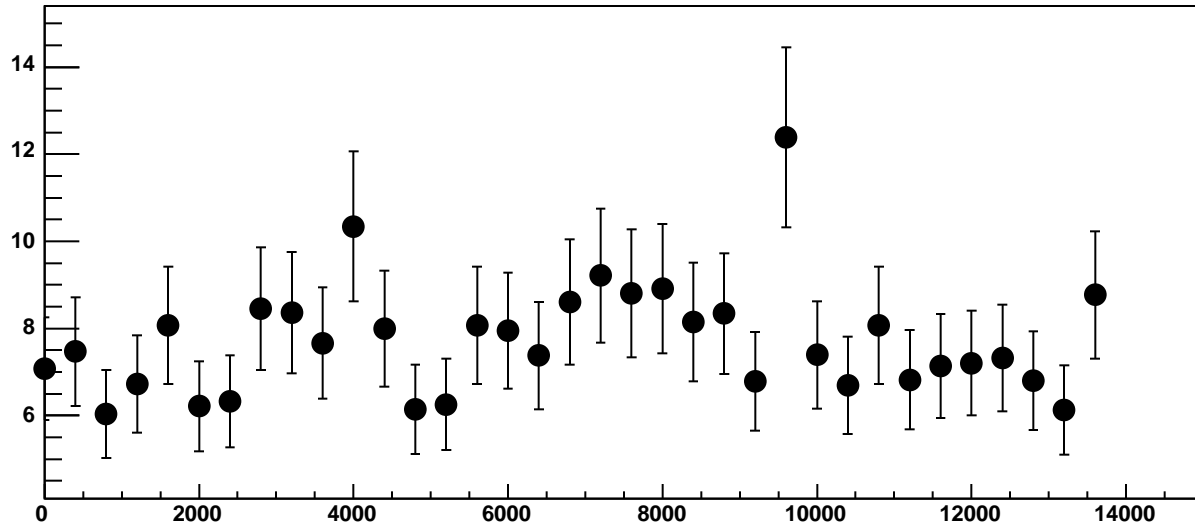
Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



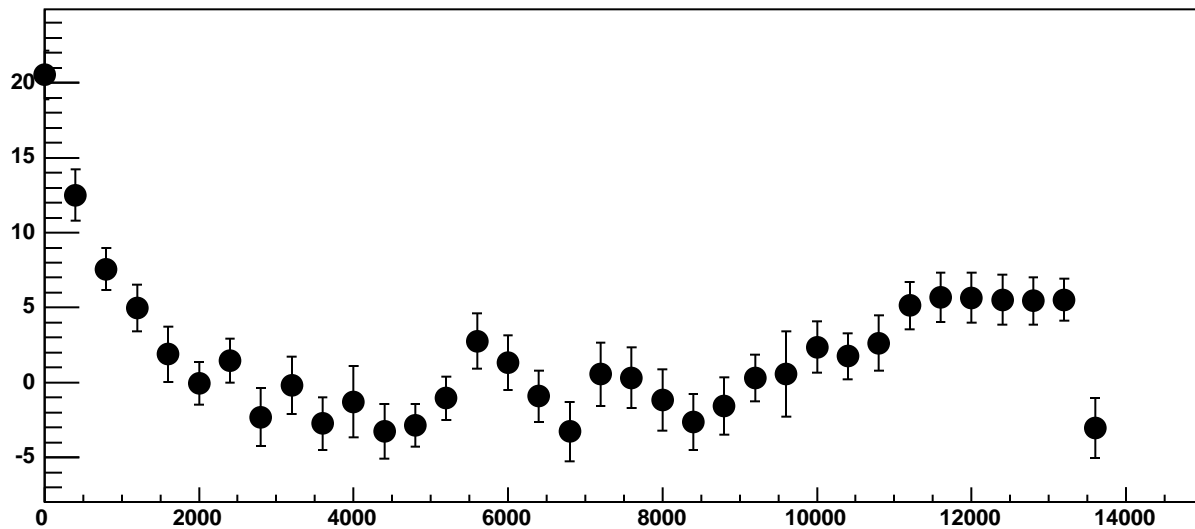
Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

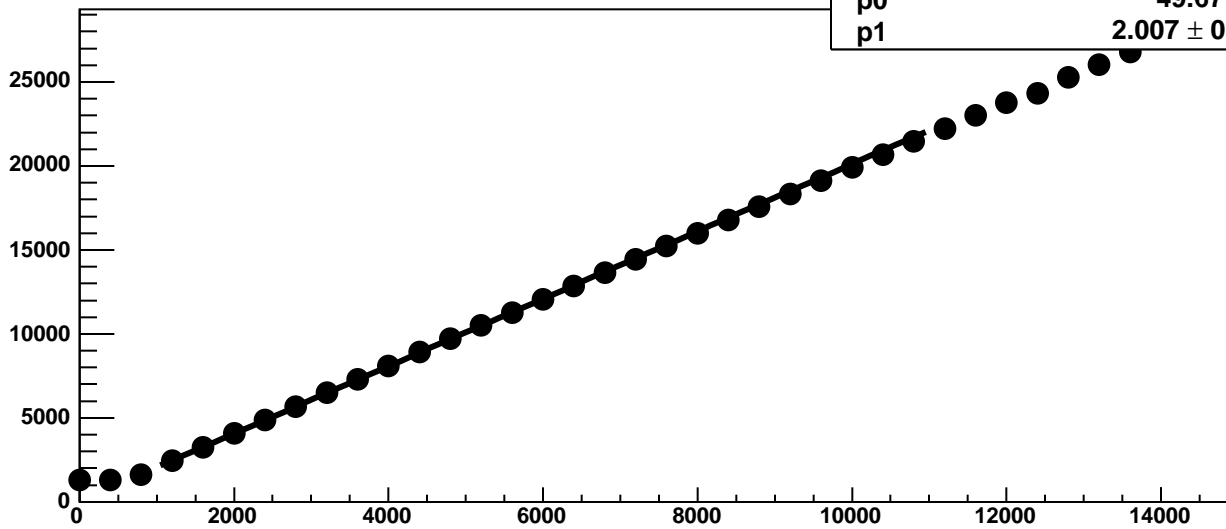


Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

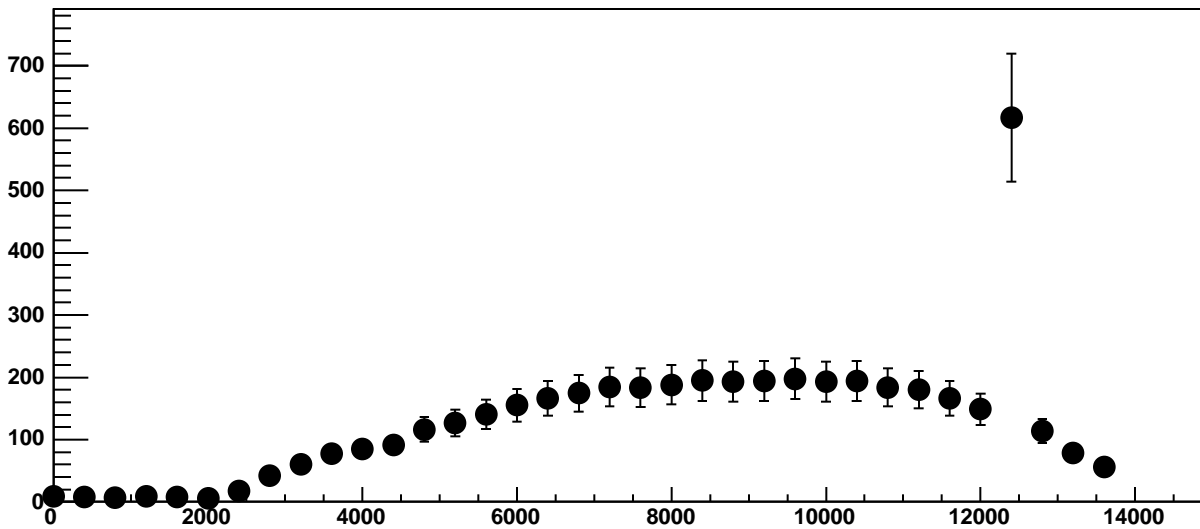


Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

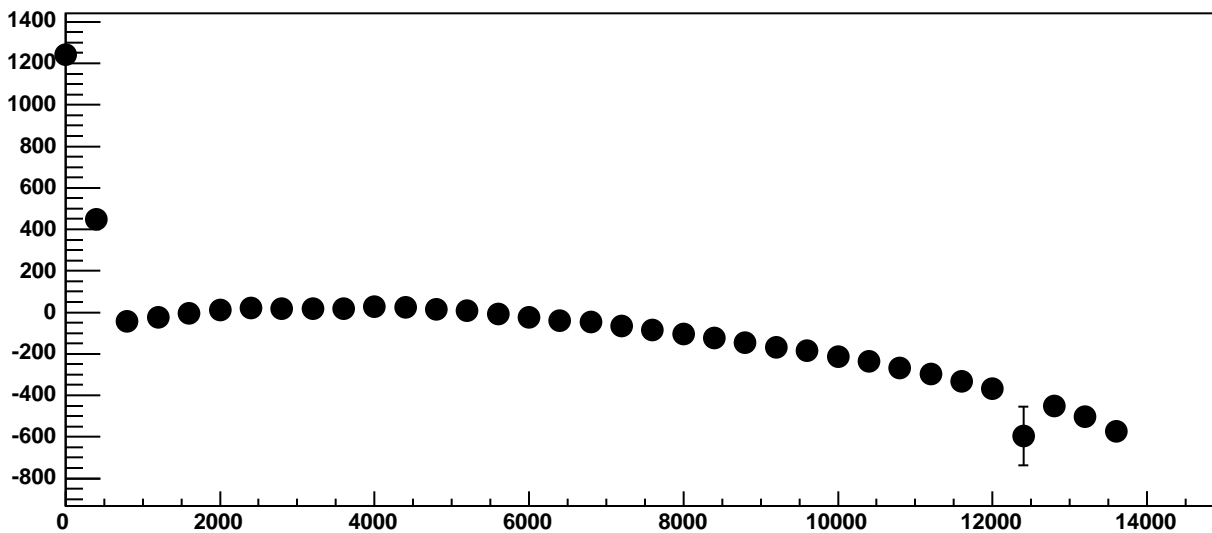
$\chi^2 / \text{ndf}$  416.7 / 23  
p0  $49.67 \pm 2.607$   
p1  $2.007 \pm 0.001333$



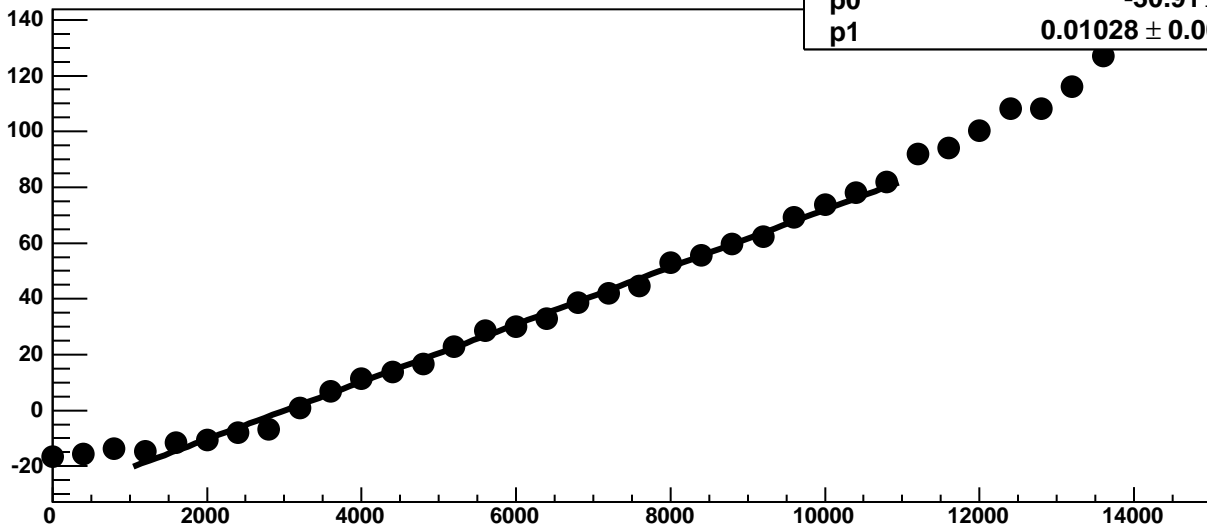
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



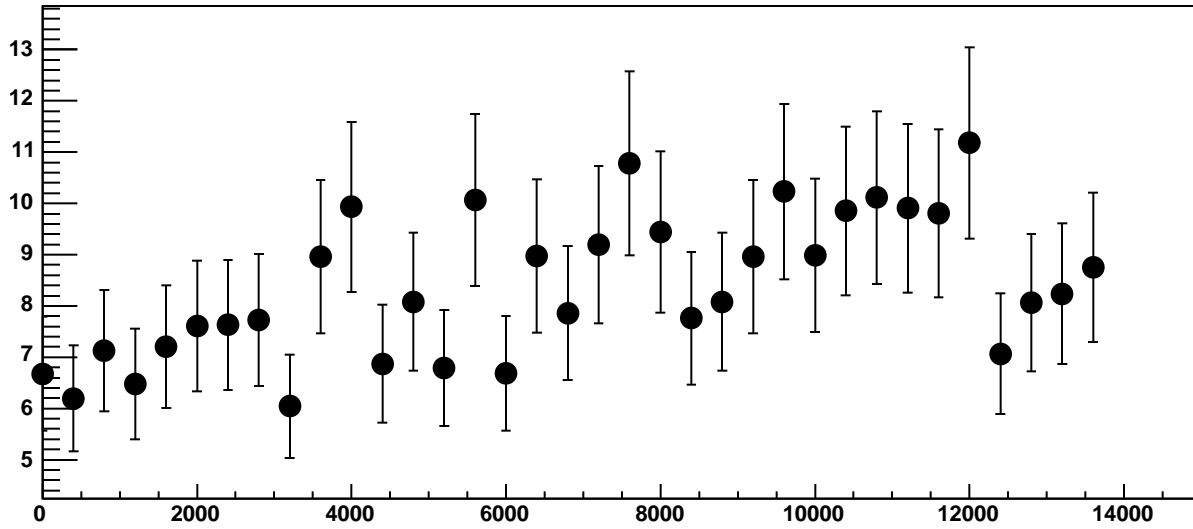
Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC



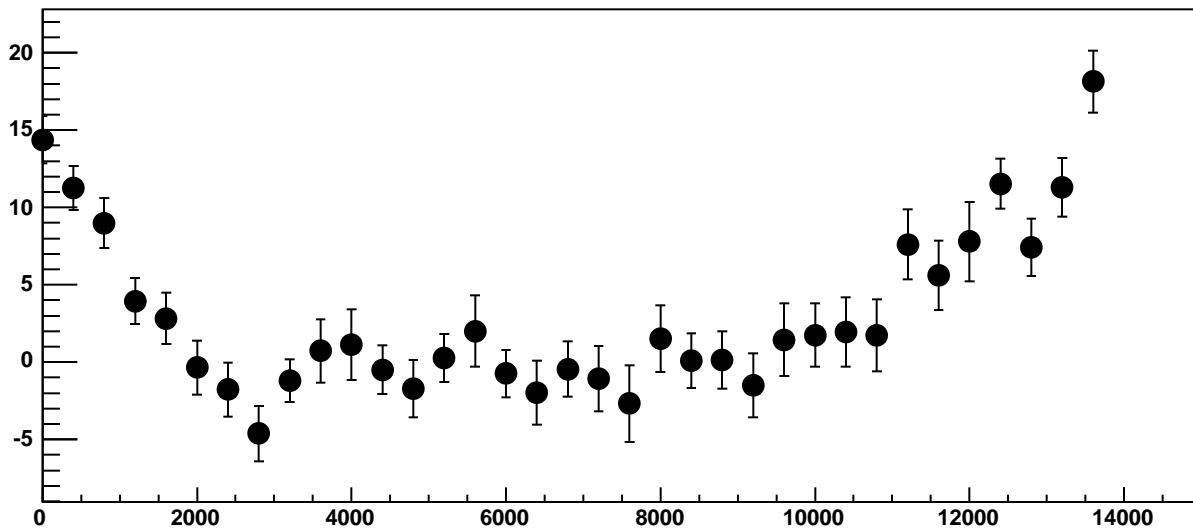
Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



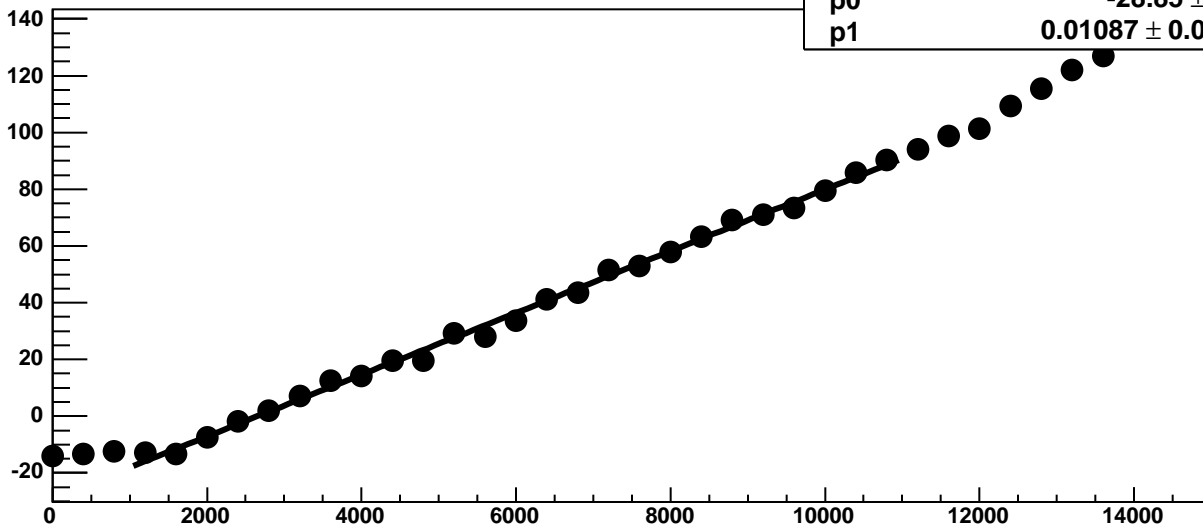
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

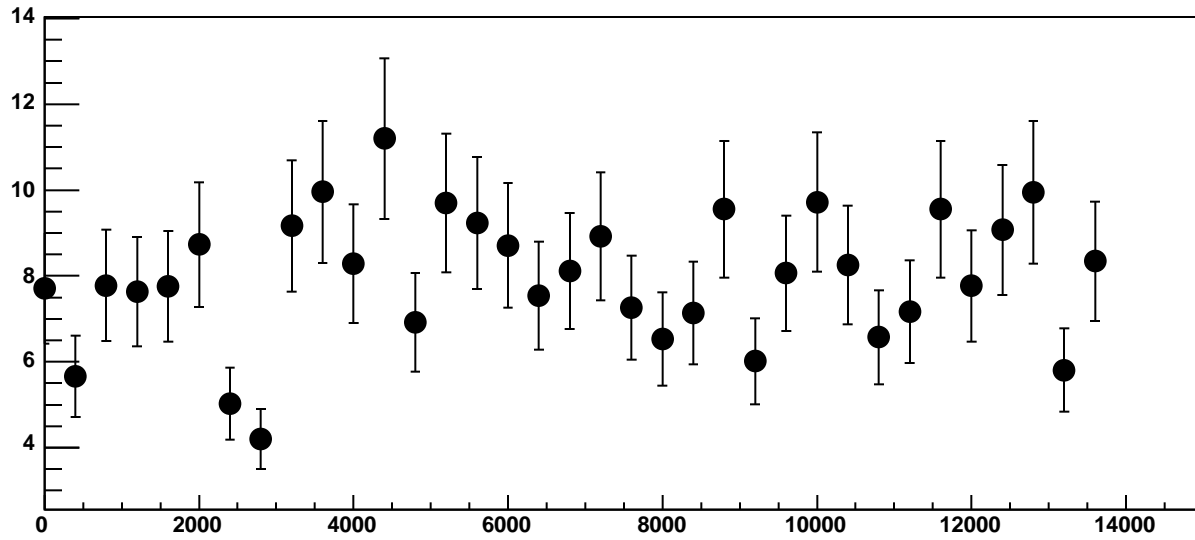


Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

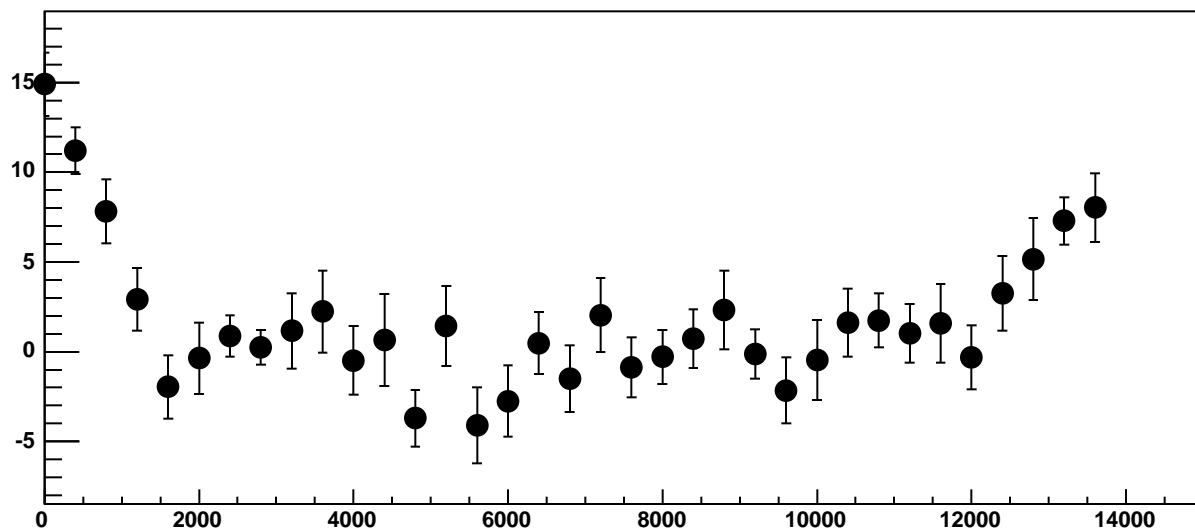


$\chi^2 / \text{ndf}$  24.35 / 23  
p0  $-28.85 \pm 0.7345$   
p1  $0.01087 \pm 0.0001134$

Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

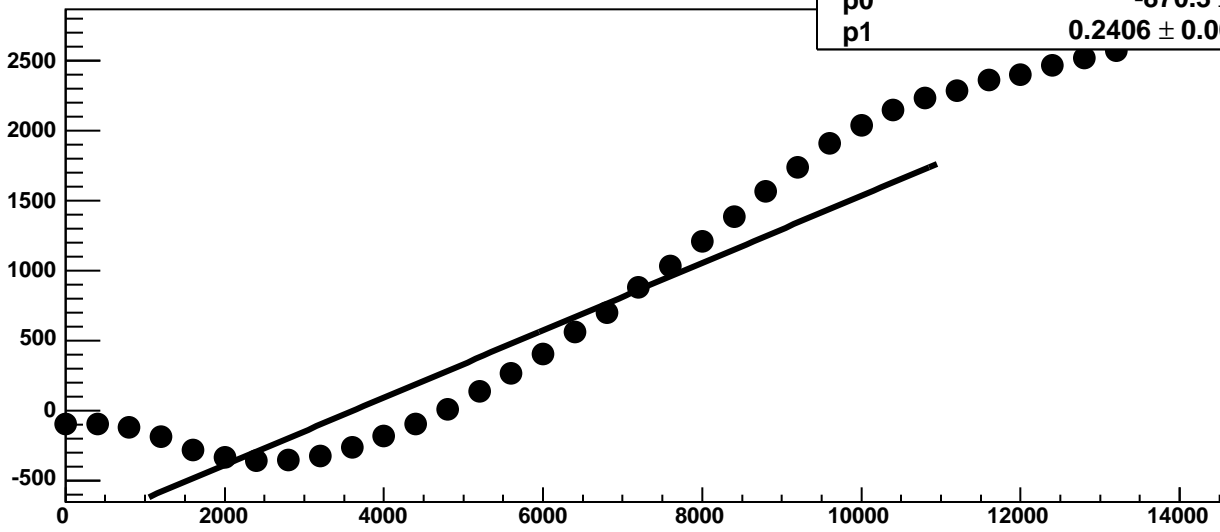


Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



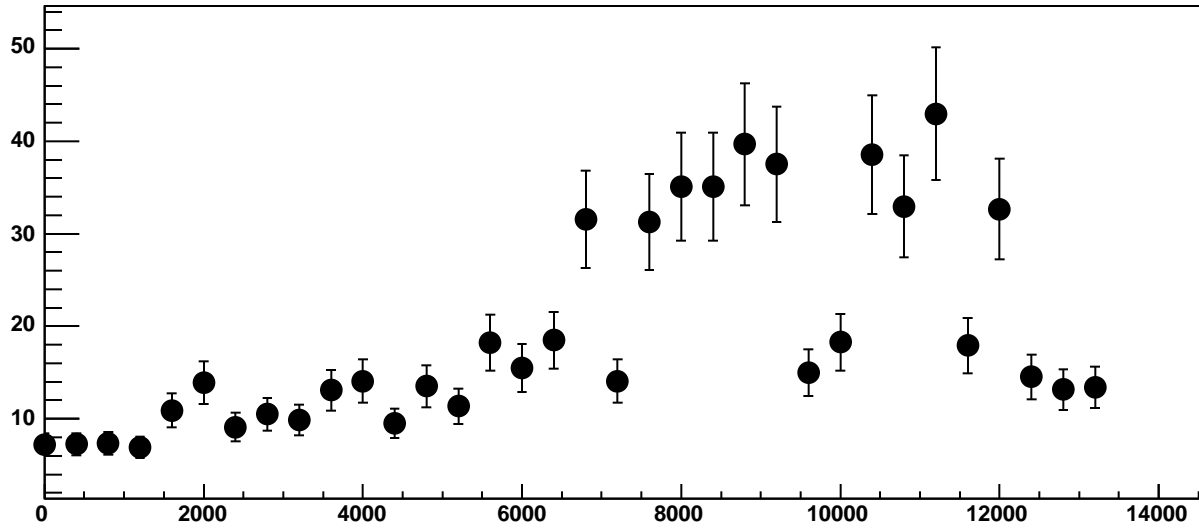


Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

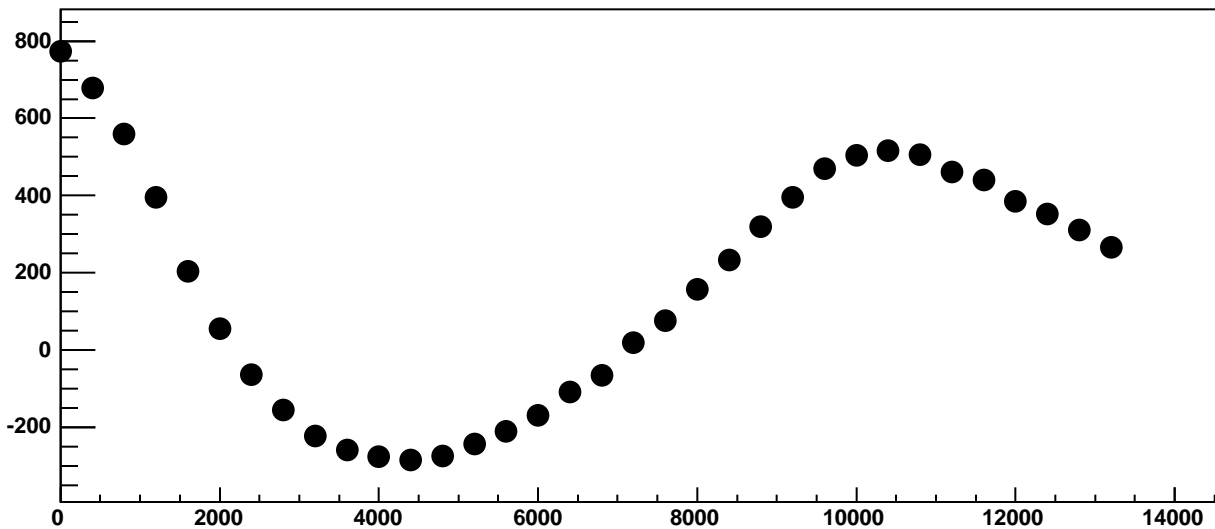


$\chi^2 / \text{ndf}$  1.835e+05 / 23  
p0 -870.3 ± 1.208  
p1 0.2406 ± 0.0002575

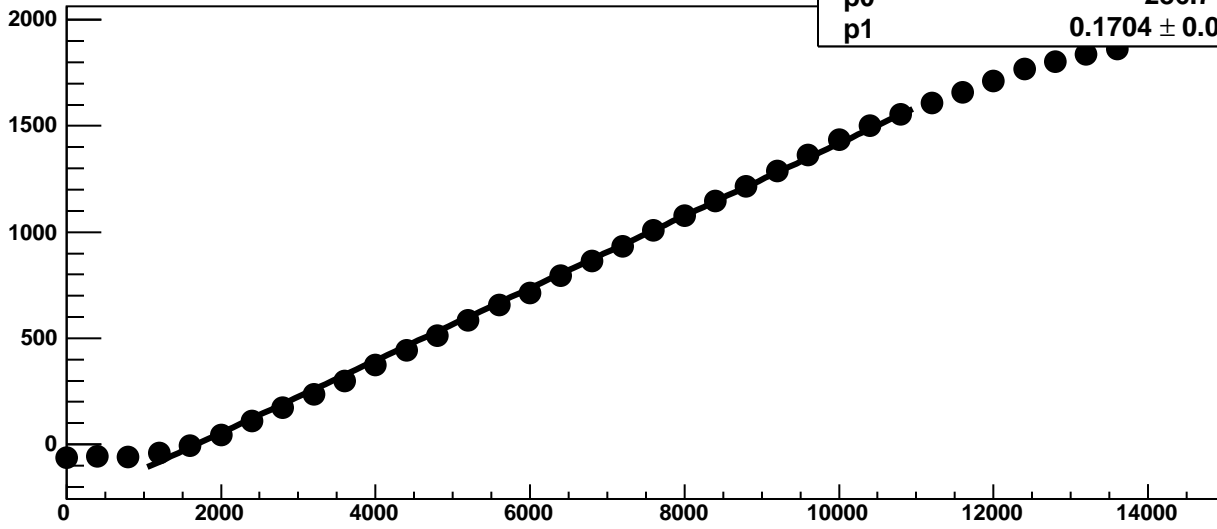
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



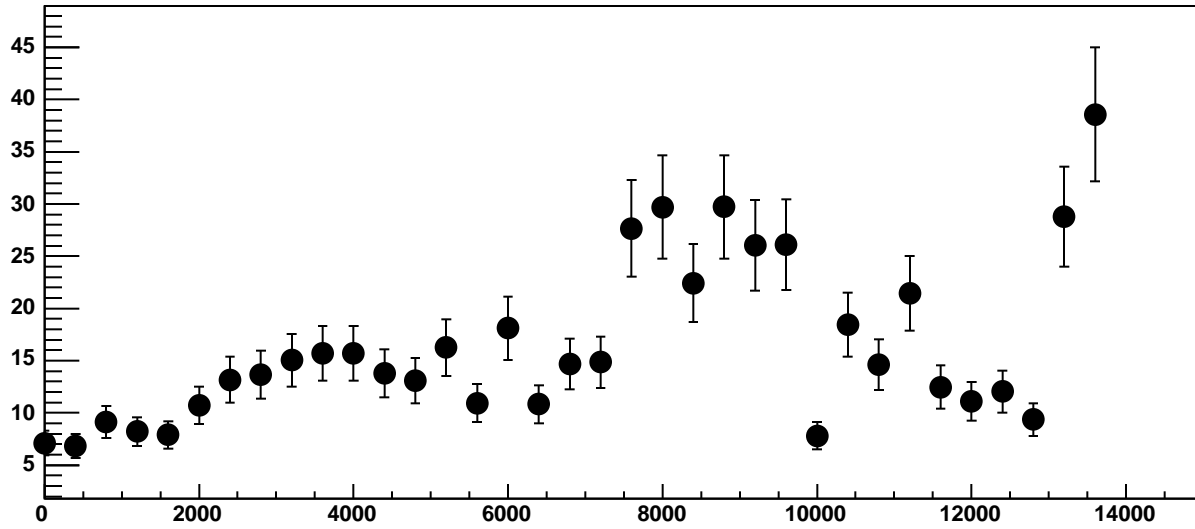
Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



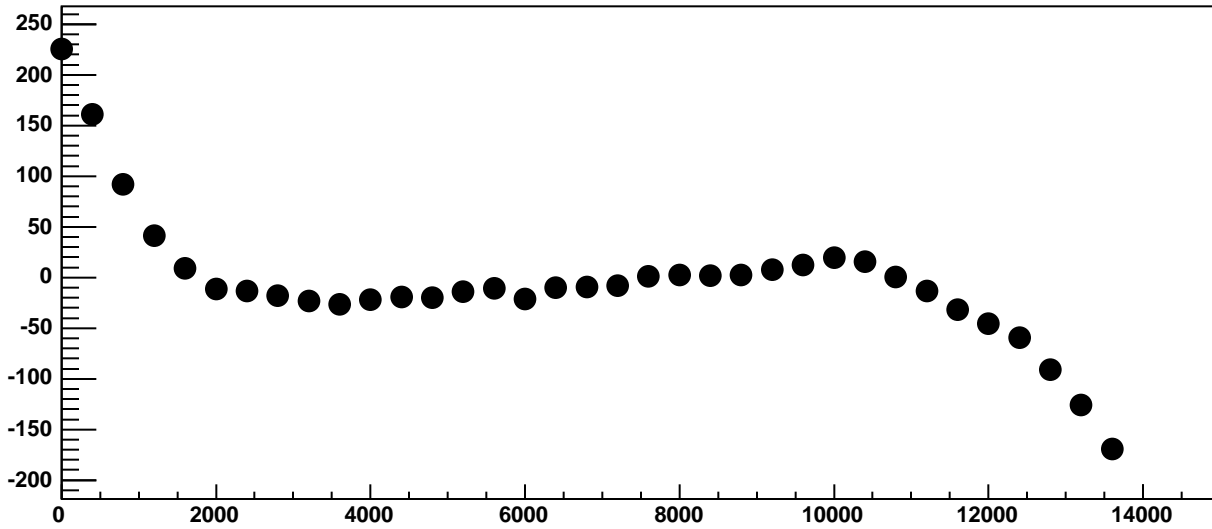
Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



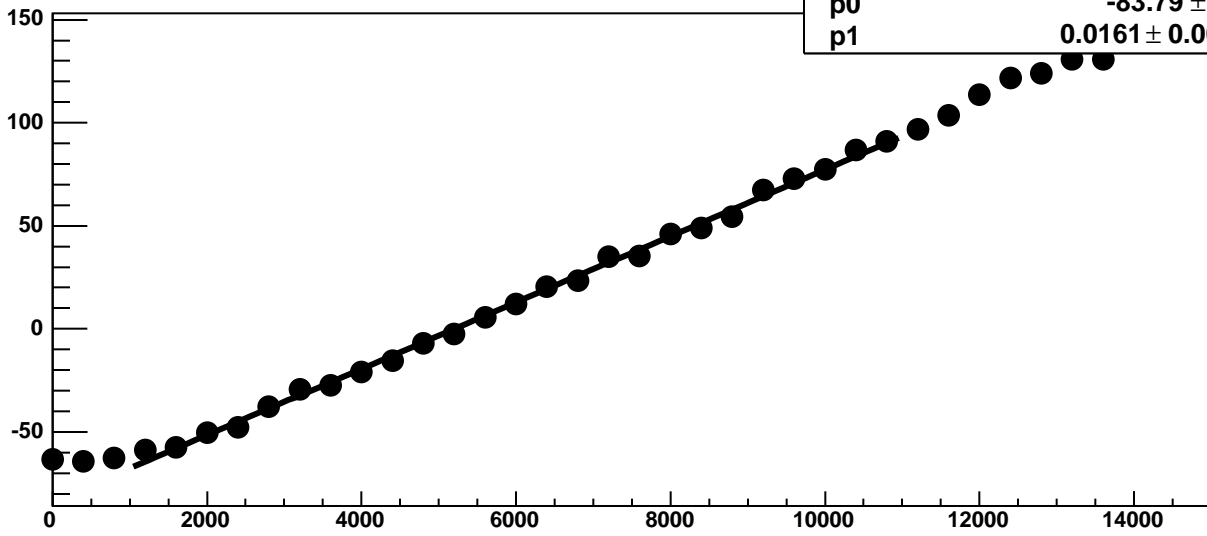
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



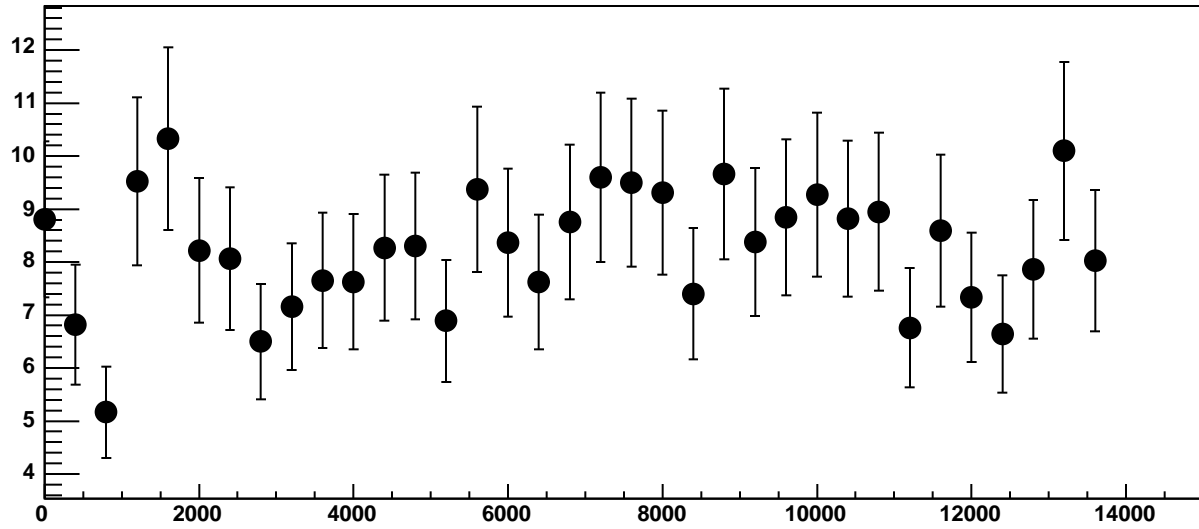
Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



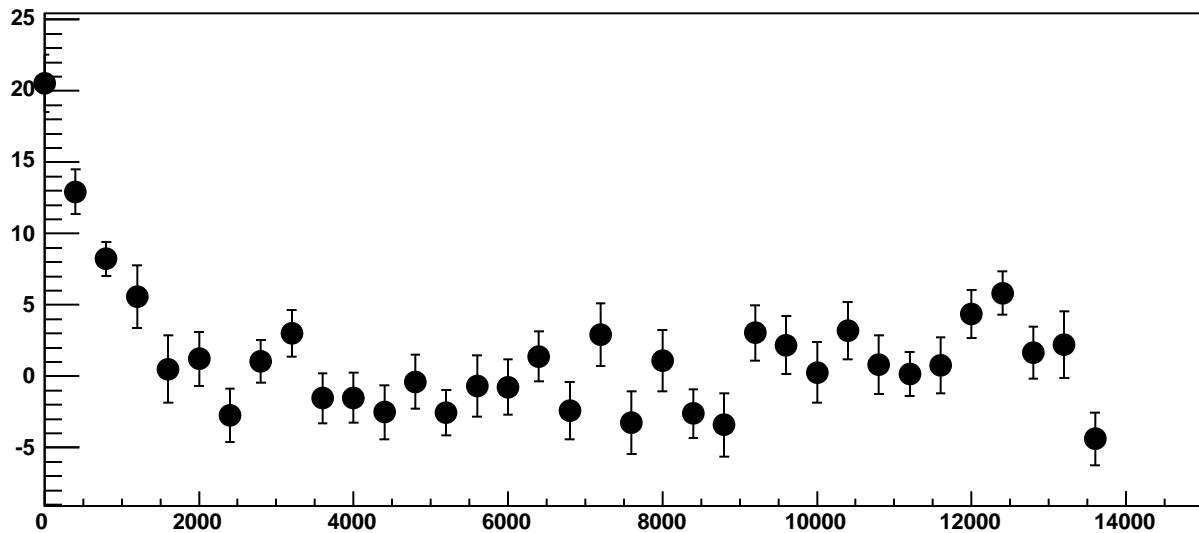
Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



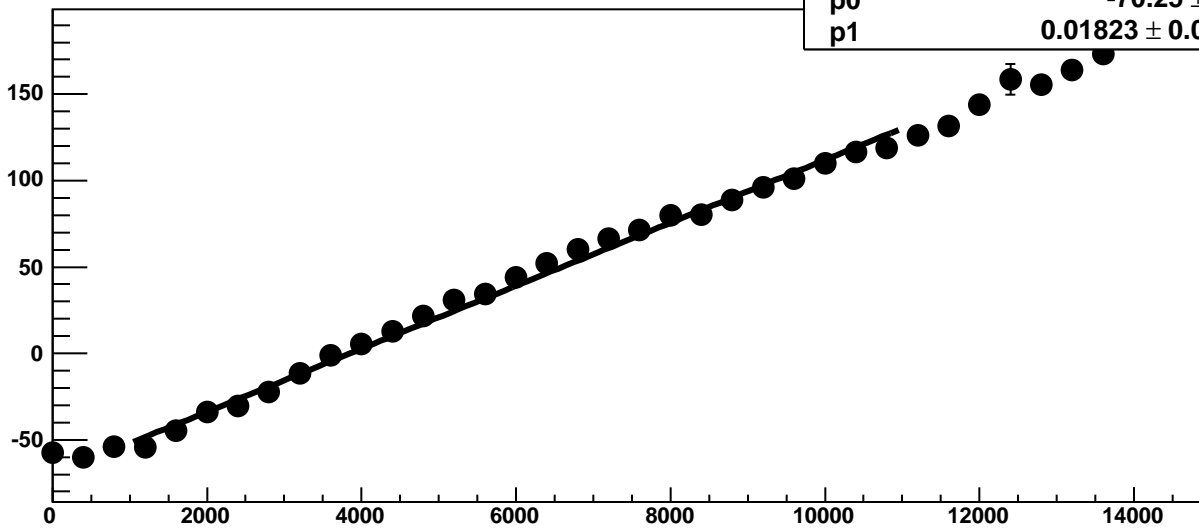
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



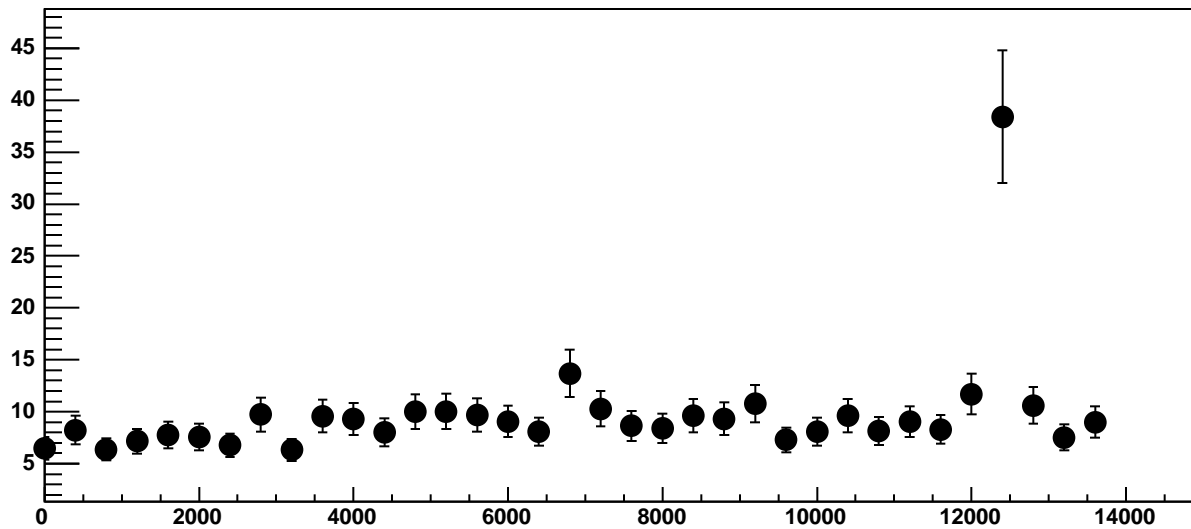
Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



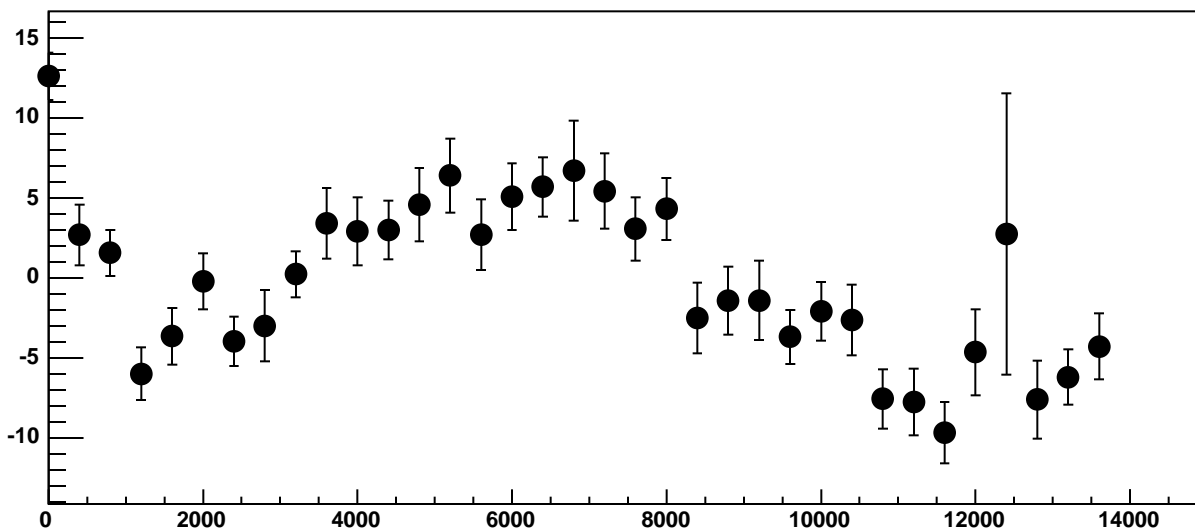
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



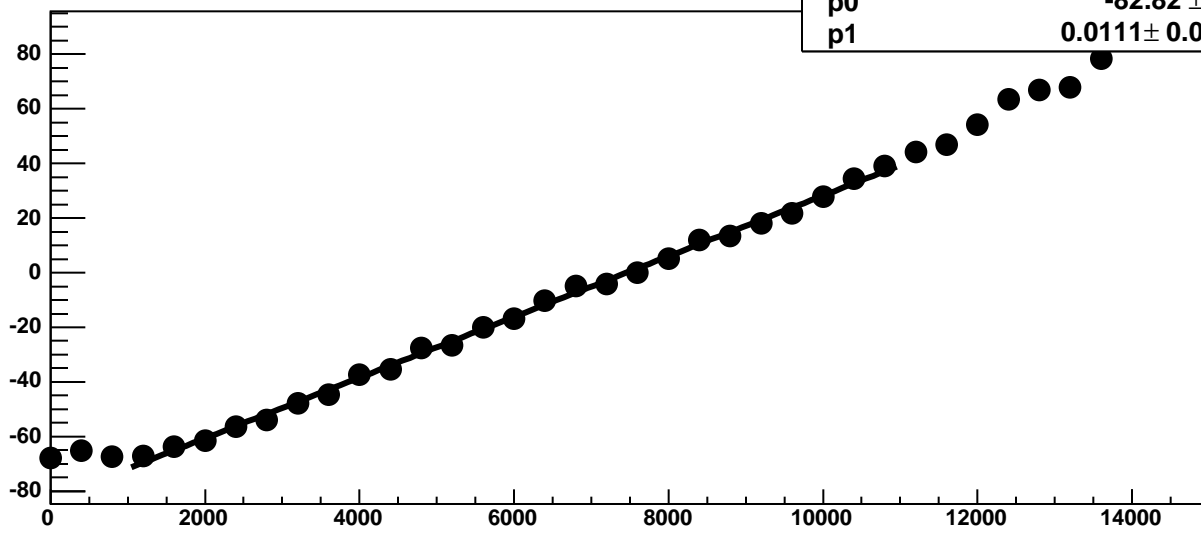
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

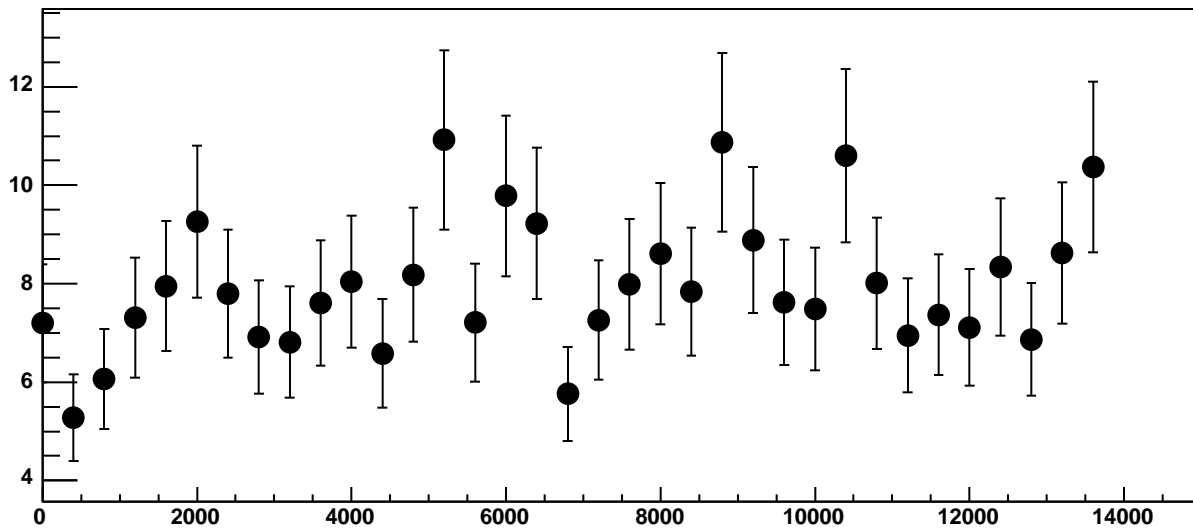


Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

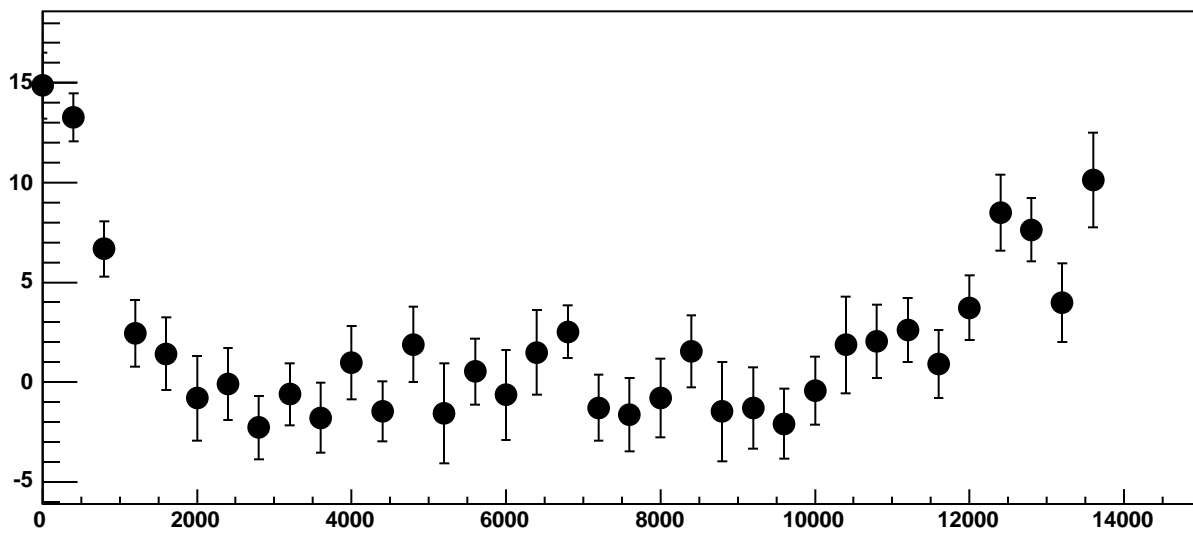


$\chi^2 / \text{ndf}$  19.43 / 23  
p0  $-82.82 \pm 0.8249$   
p1  $0.0111 \pm 0.0001277$

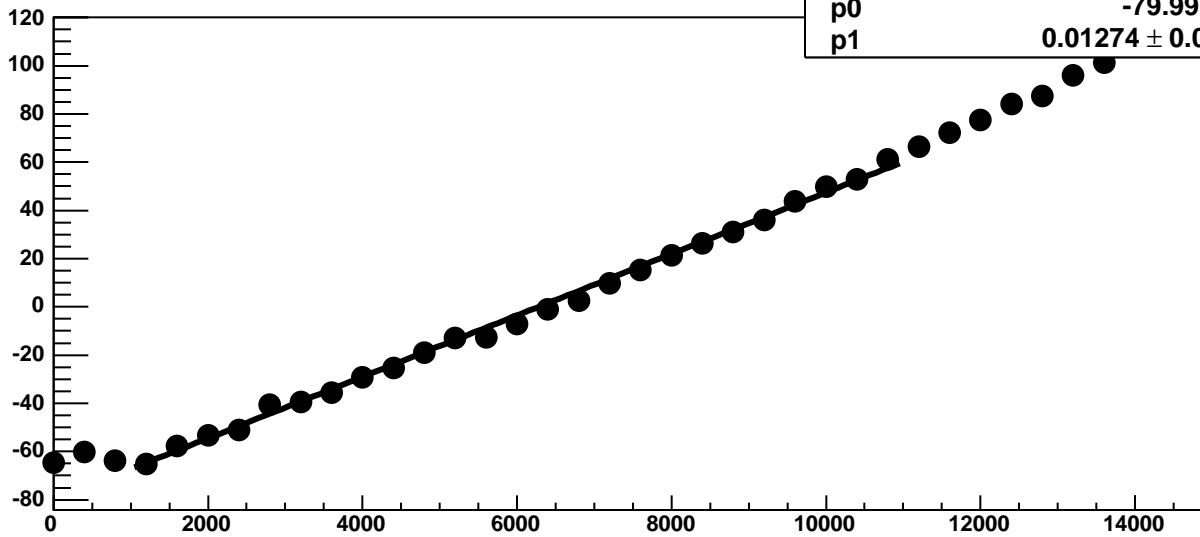
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

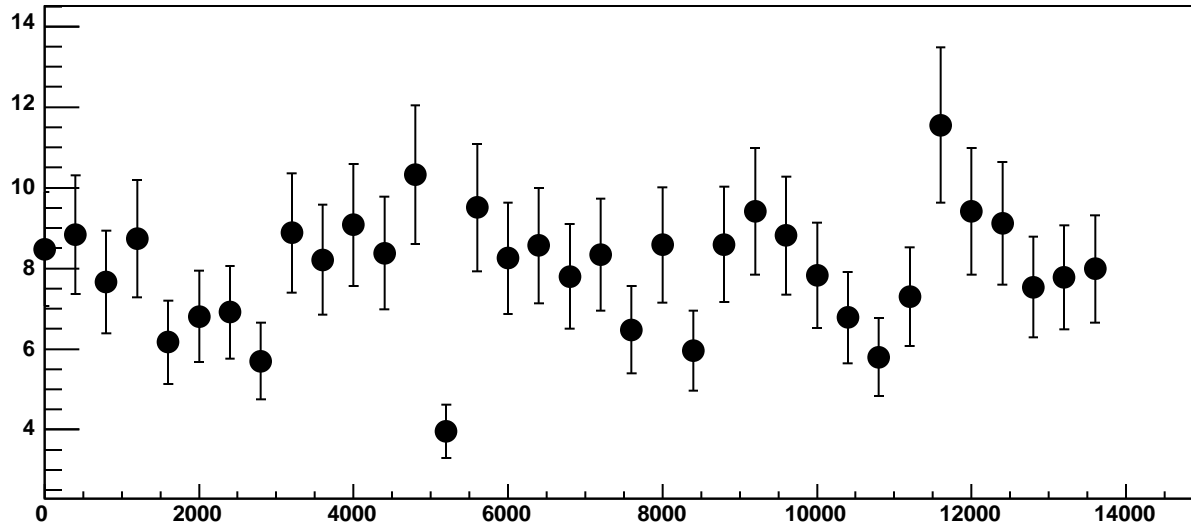


Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

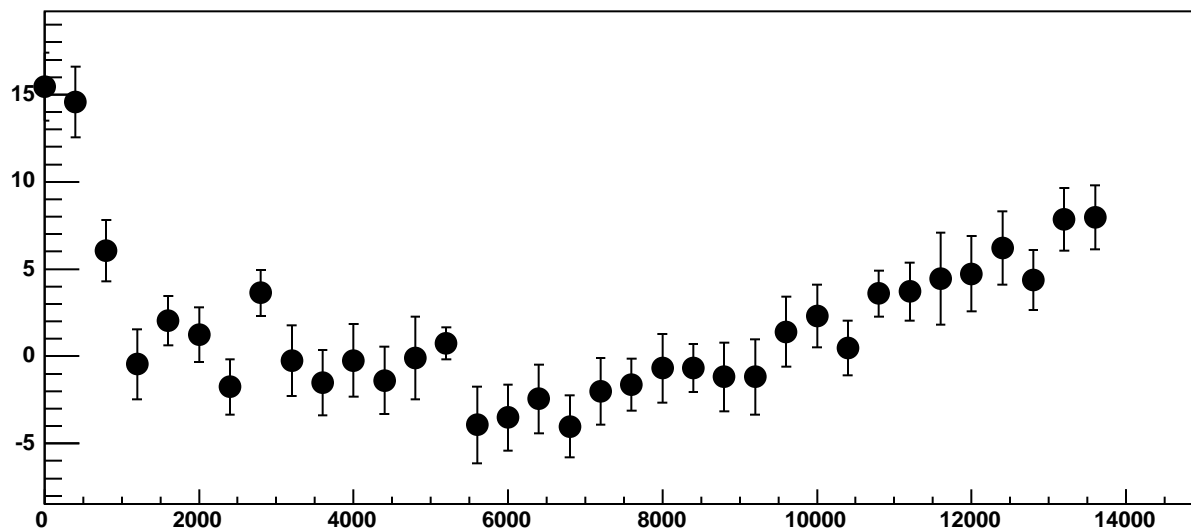


$\chi^2 / \text{ndf}$  39.69 / 23  
p0  $-79.99 \pm 0.747$   
p1  $0.01274 \pm 0.0001128$

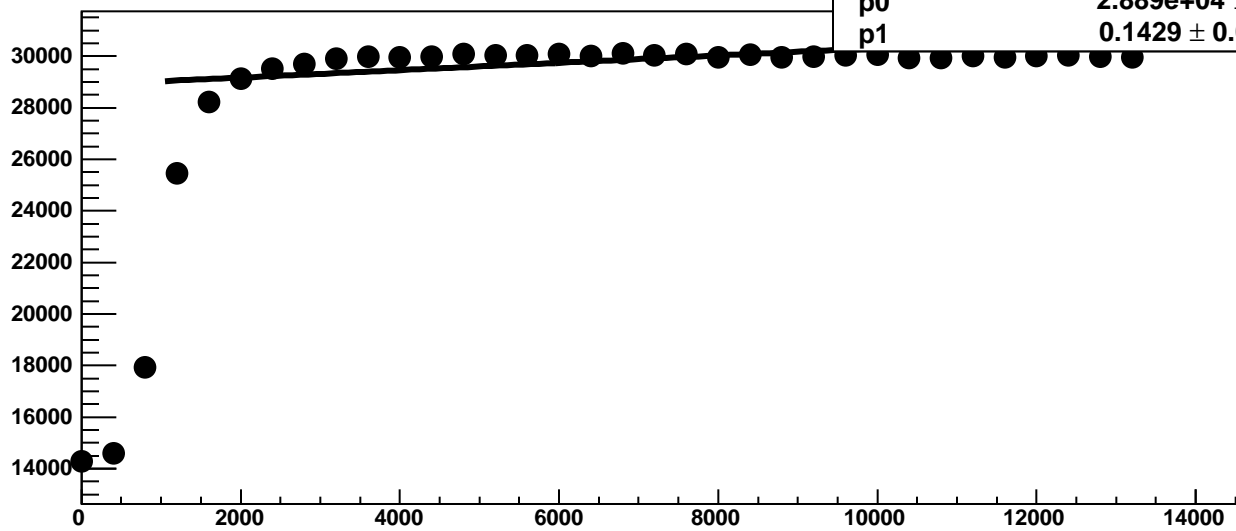
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



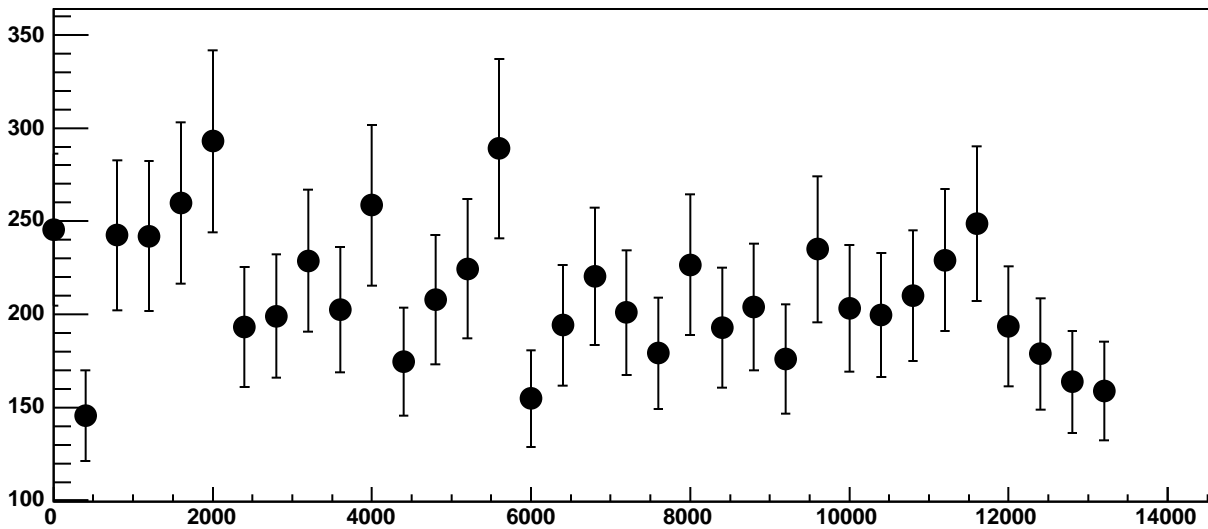
Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



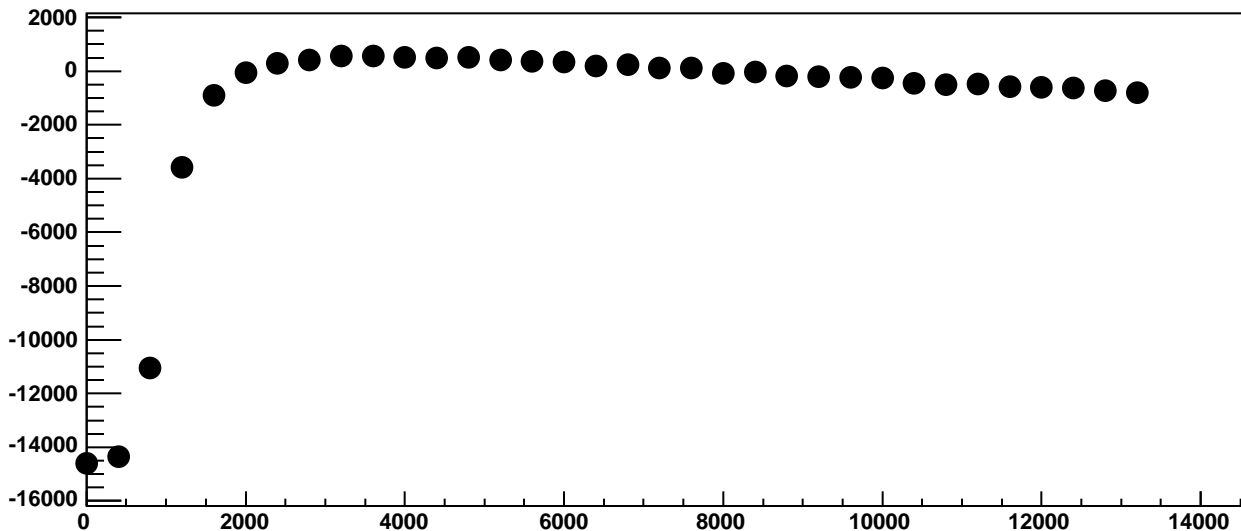
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC

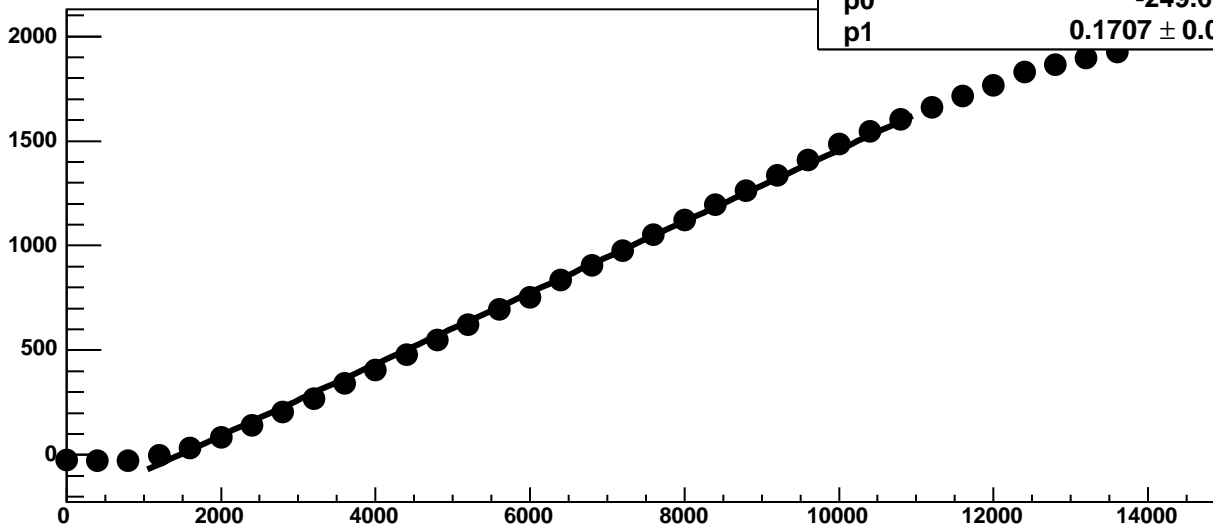


Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

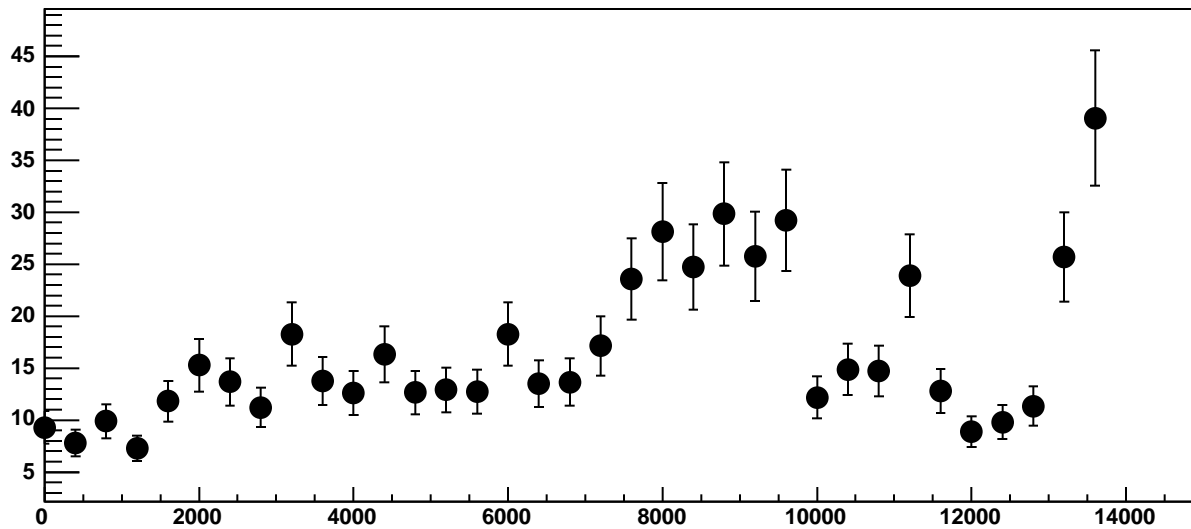


Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

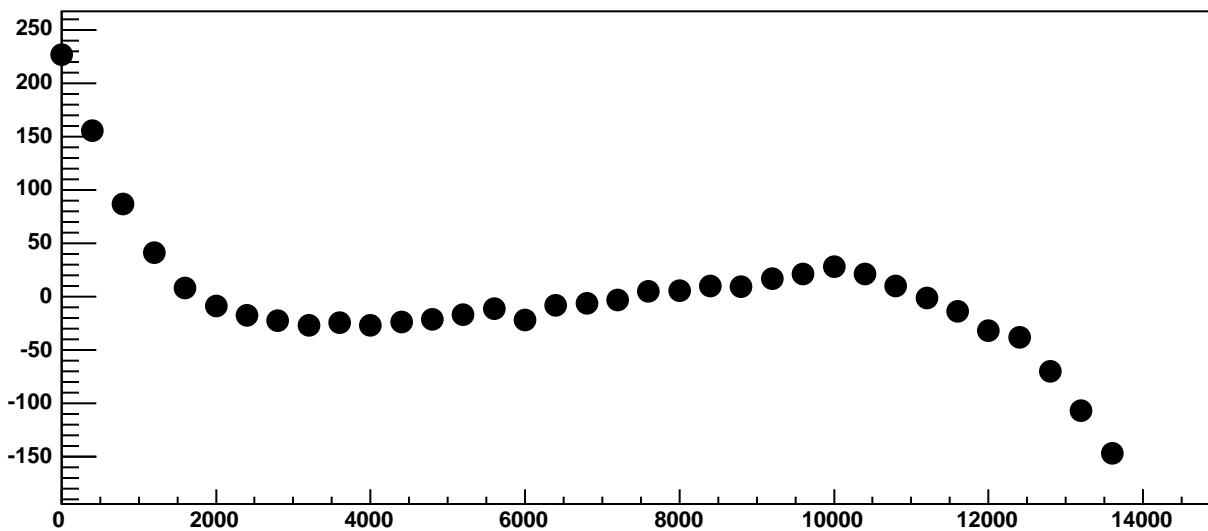
$\chi^2 / \text{ndf}$  1270 / 23  
p0  $-249.6 \pm 1.249$   
p1  $0.1707 \pm 0.0002178$



Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

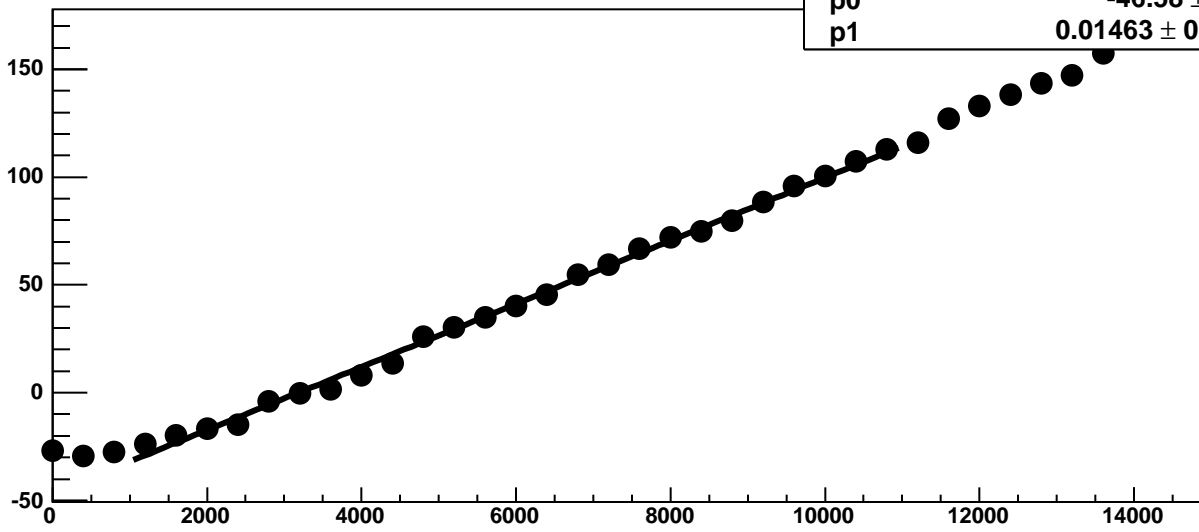


Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



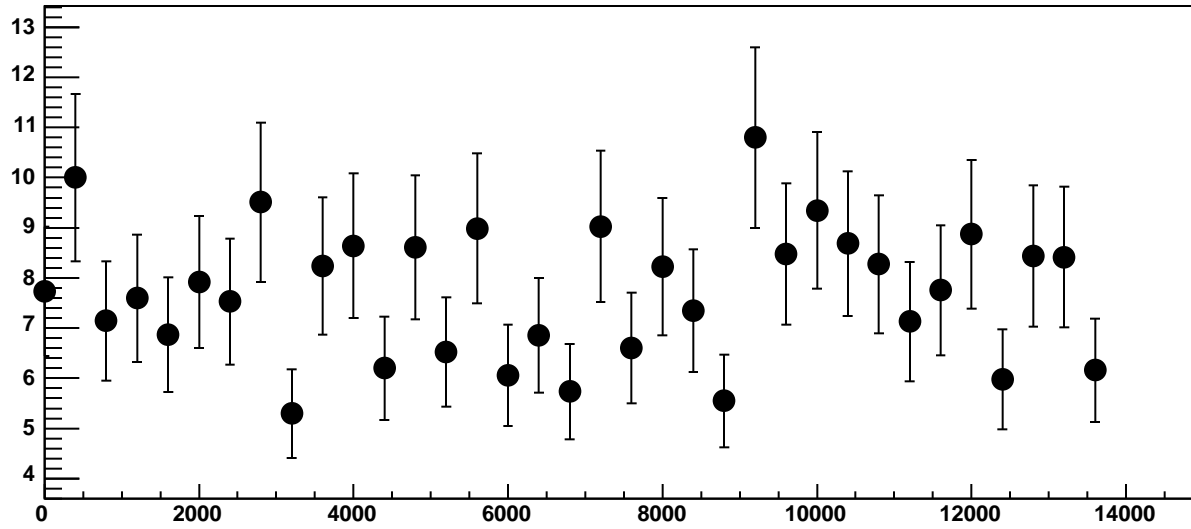


Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

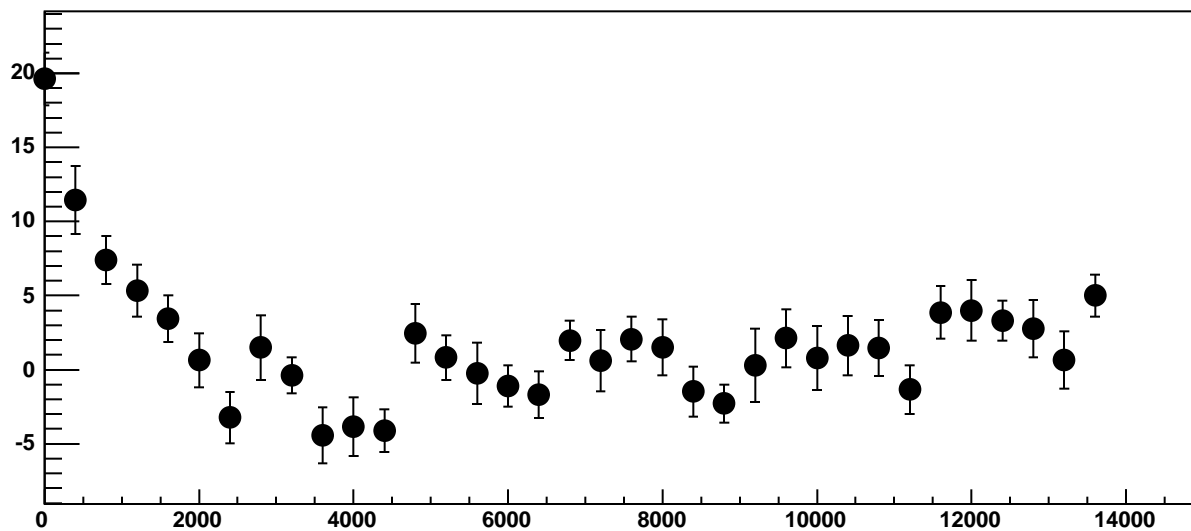


$\chi^2 / \text{ndf}$  50.96 / 23  
p0  $-46.58 \pm 0.7916$   
p1  $0.01463 \pm 0.000123$

Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

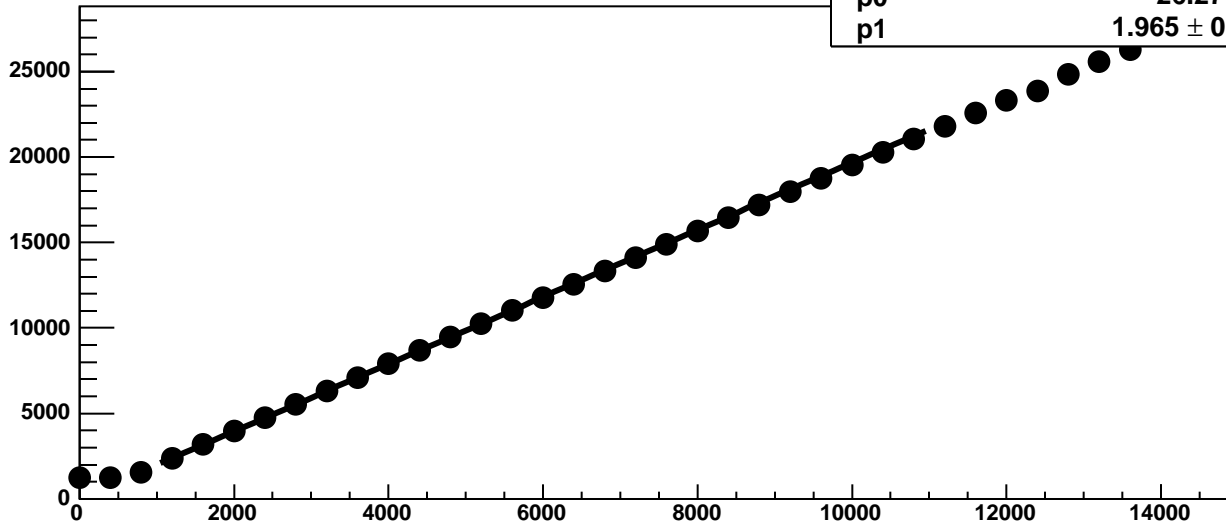


Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

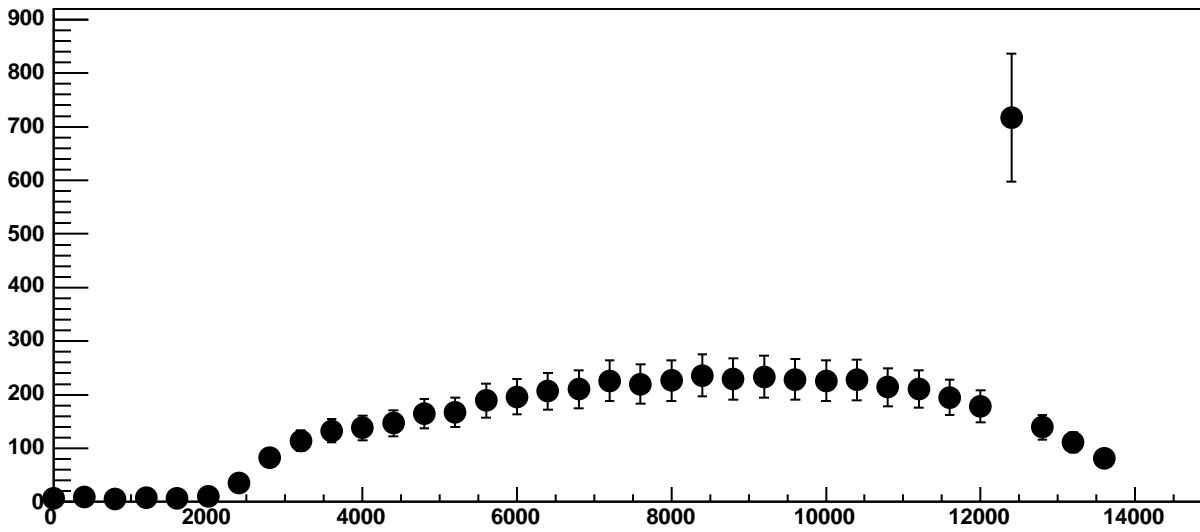


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

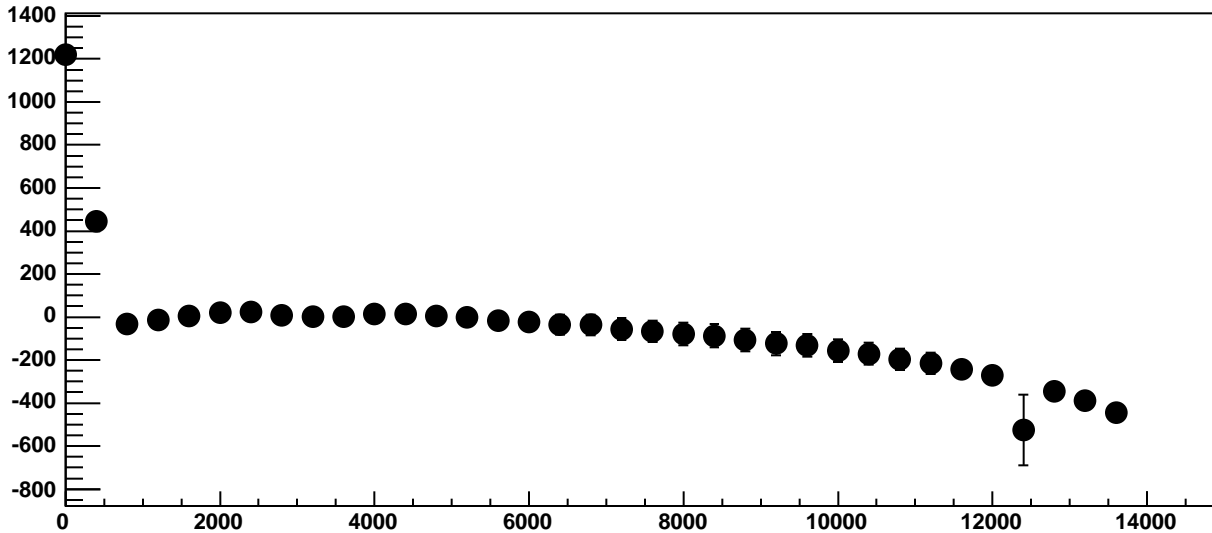
$\chi^2 / \text{ndf}$  205.5 / 23  
p0  $26.27 \pm 2.797$   
p1  $1.965 \pm 0.001617$



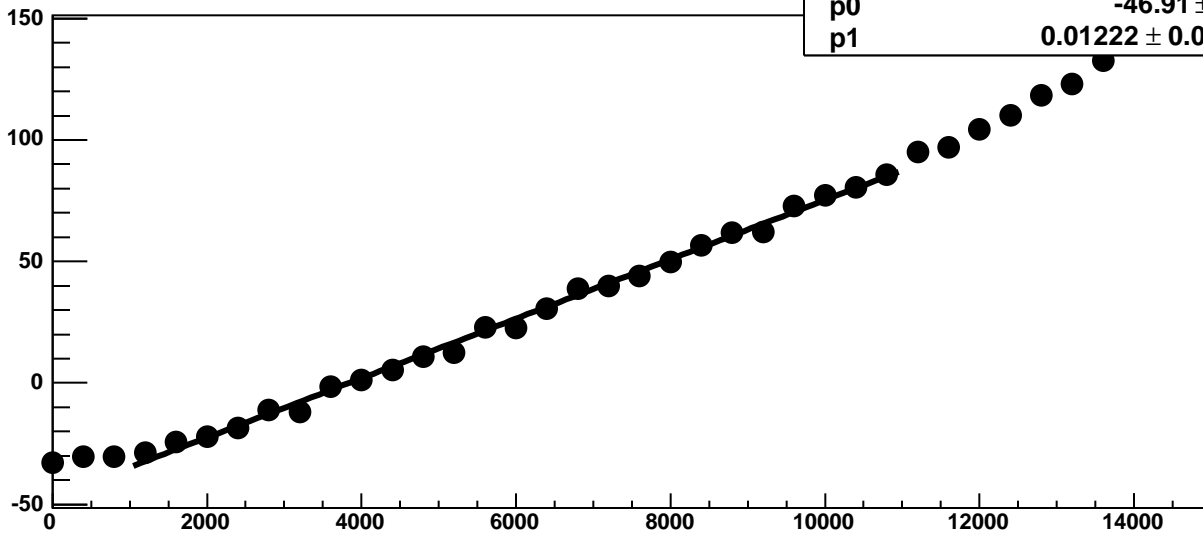
Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

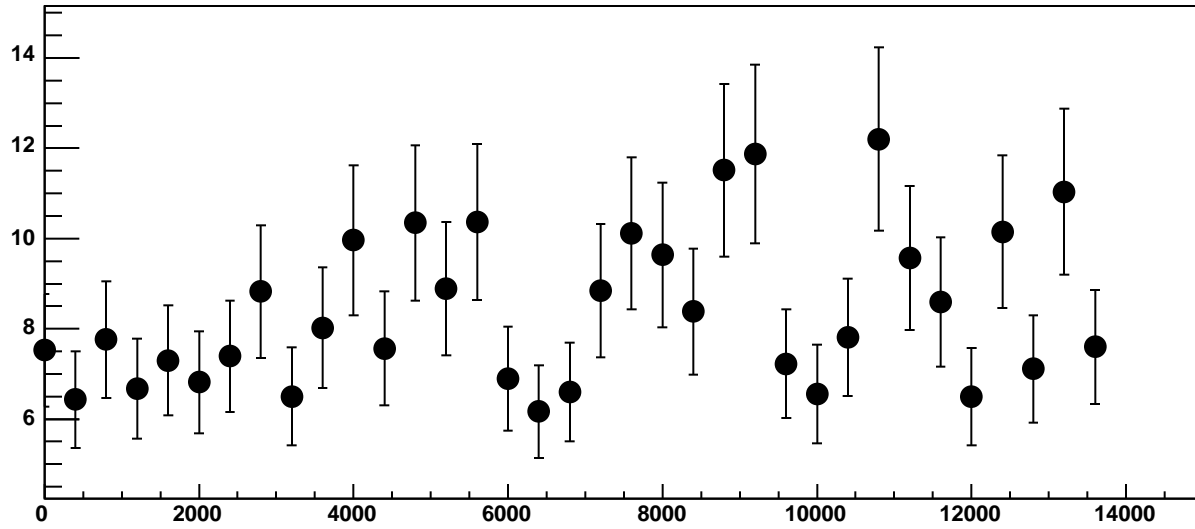


Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

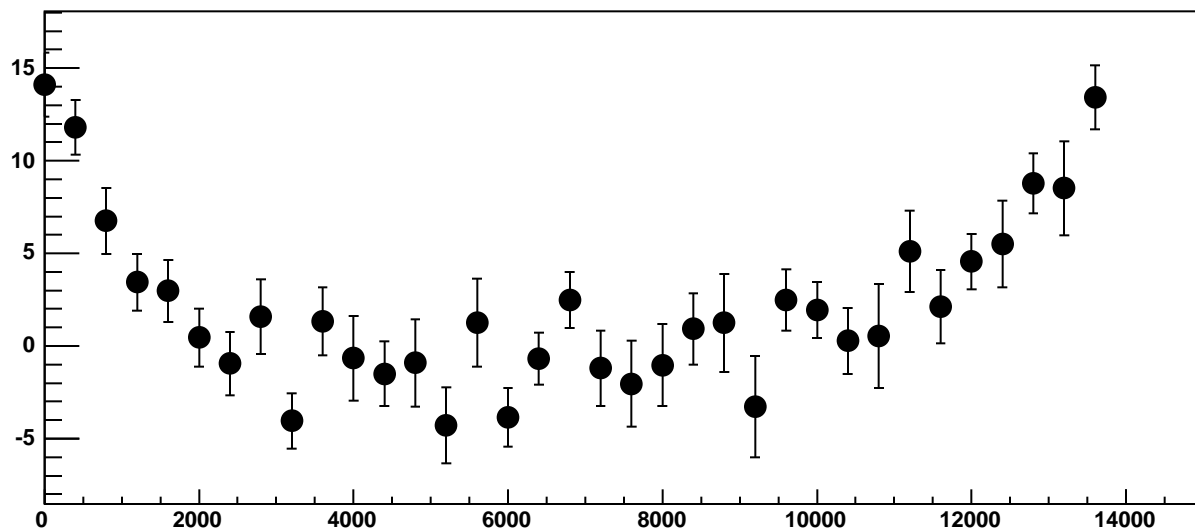


$\chi^2 / \text{ndf}$  38.83 / 23  
p0  $-46.91 \pm 0.7991$   
p1  $0.01222 \pm 0.0001255$

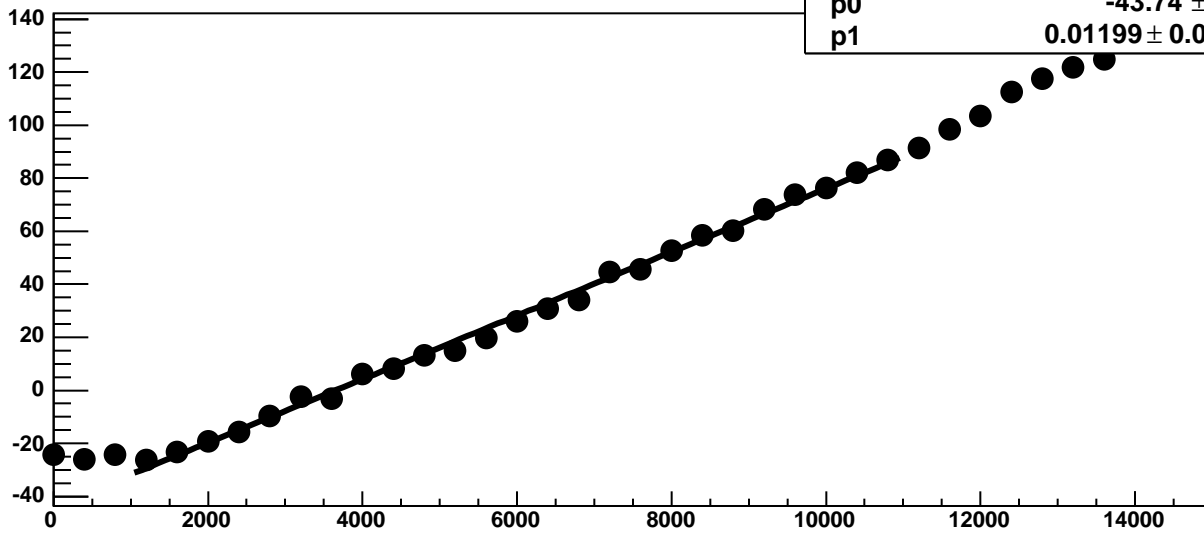
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



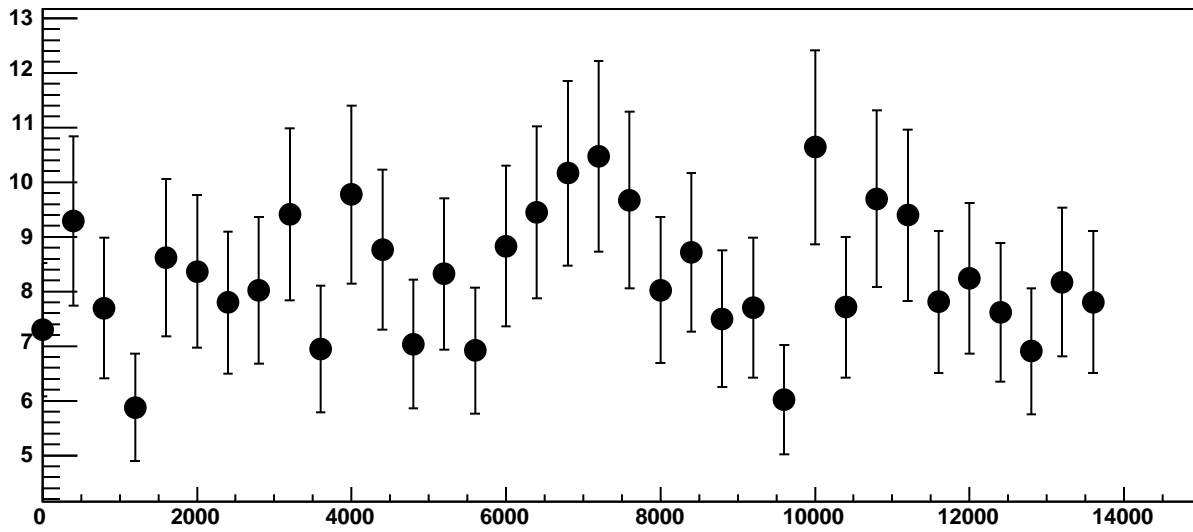
Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



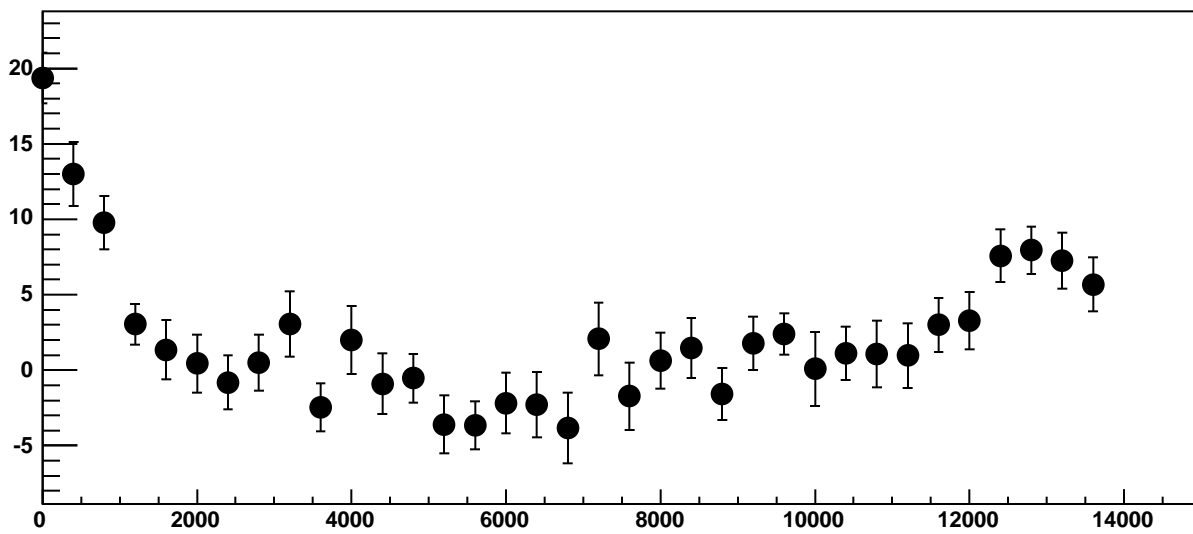
Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

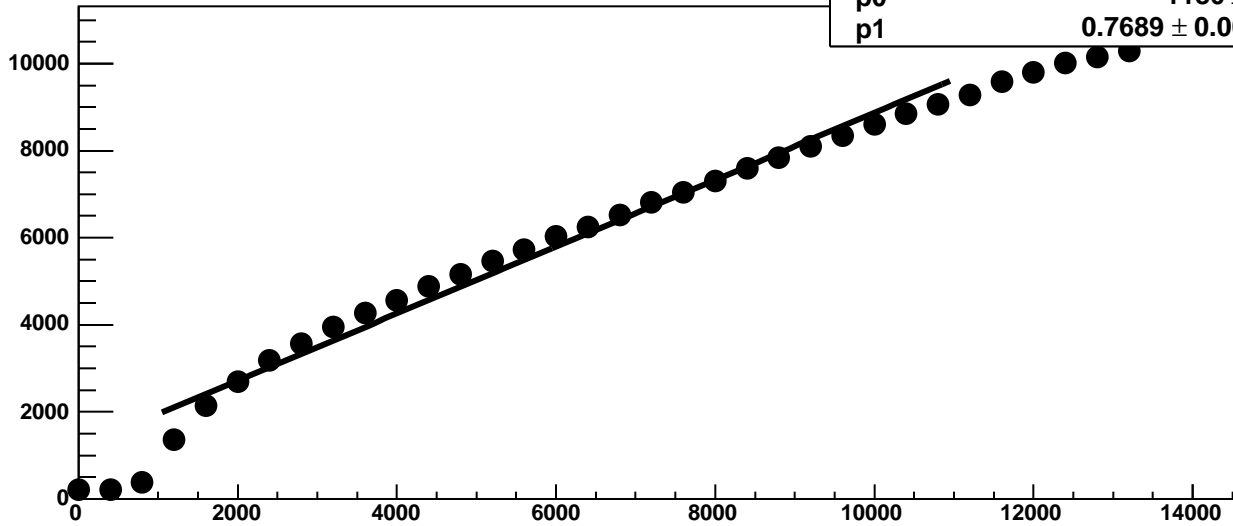


Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

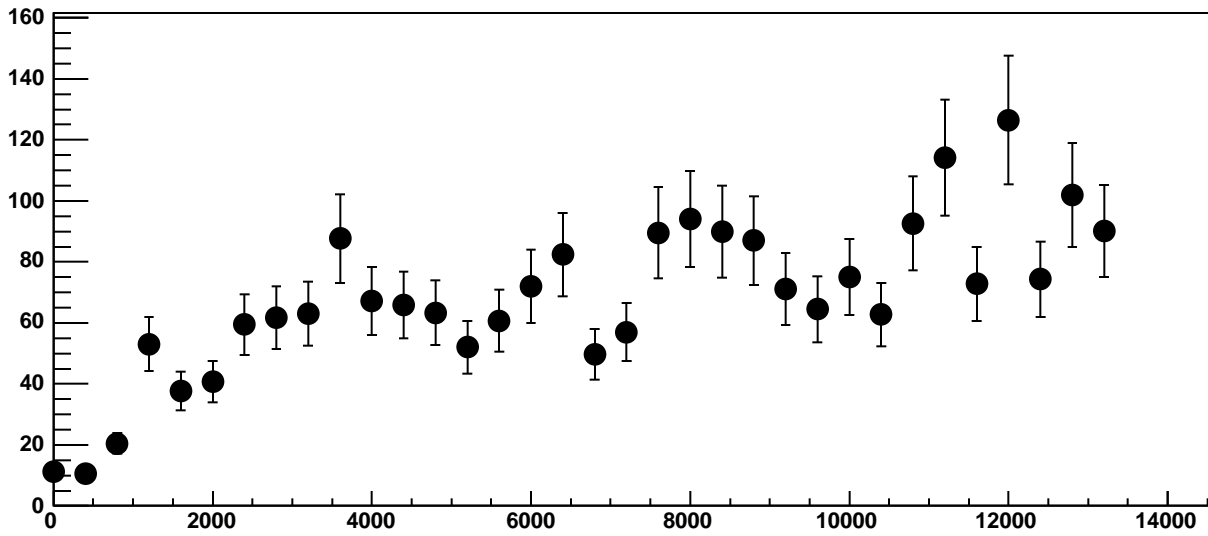


Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

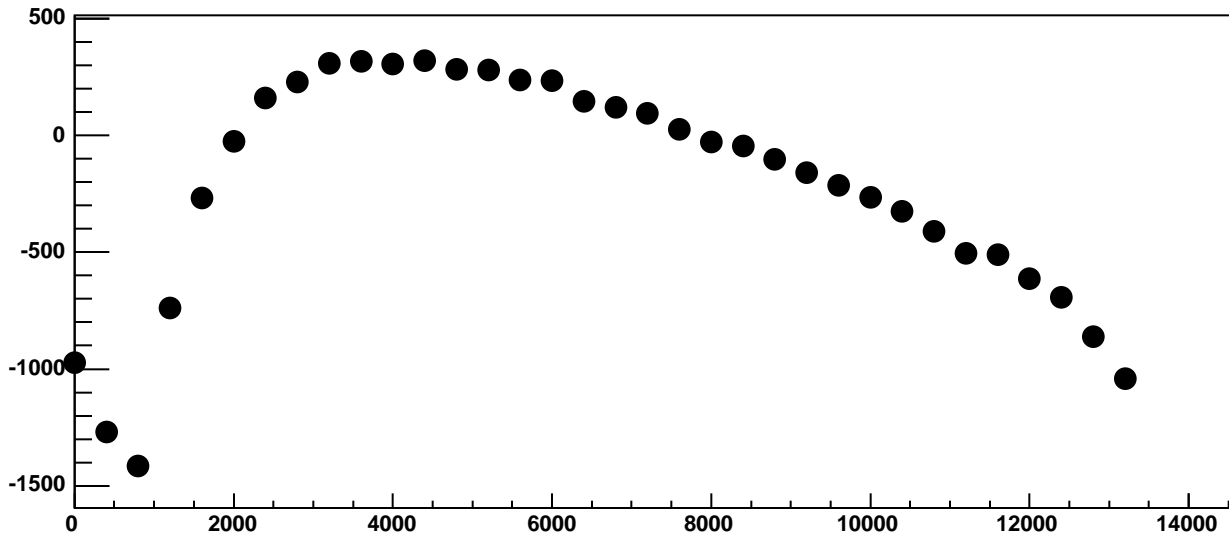
$\chi^2 / \text{ndf}$  9679 / 23  
p0  $1180 \pm 5.697$   
p1  $0.7689 \pm 0.0009684$



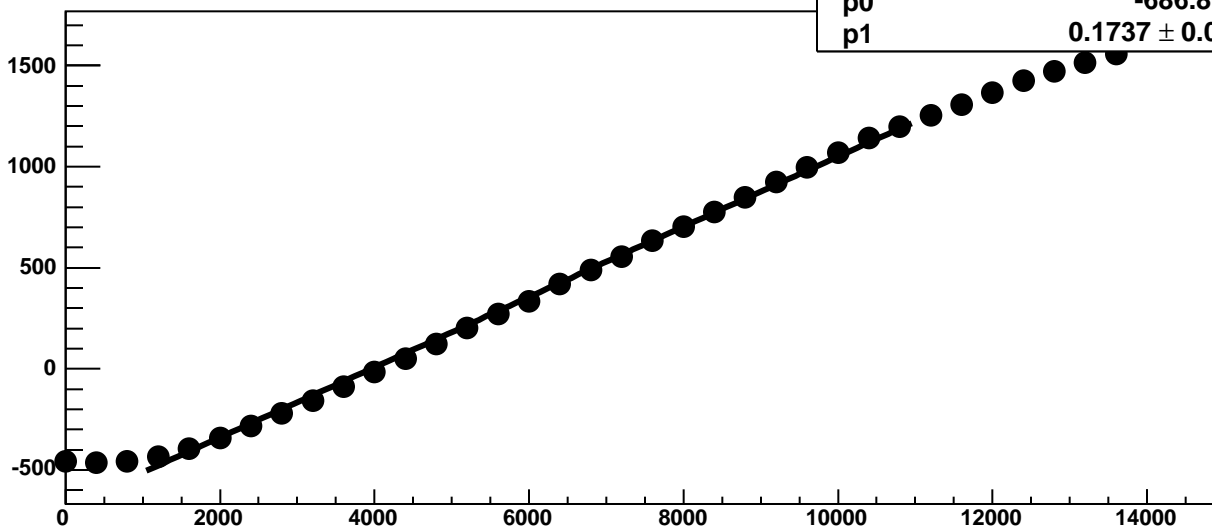
Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



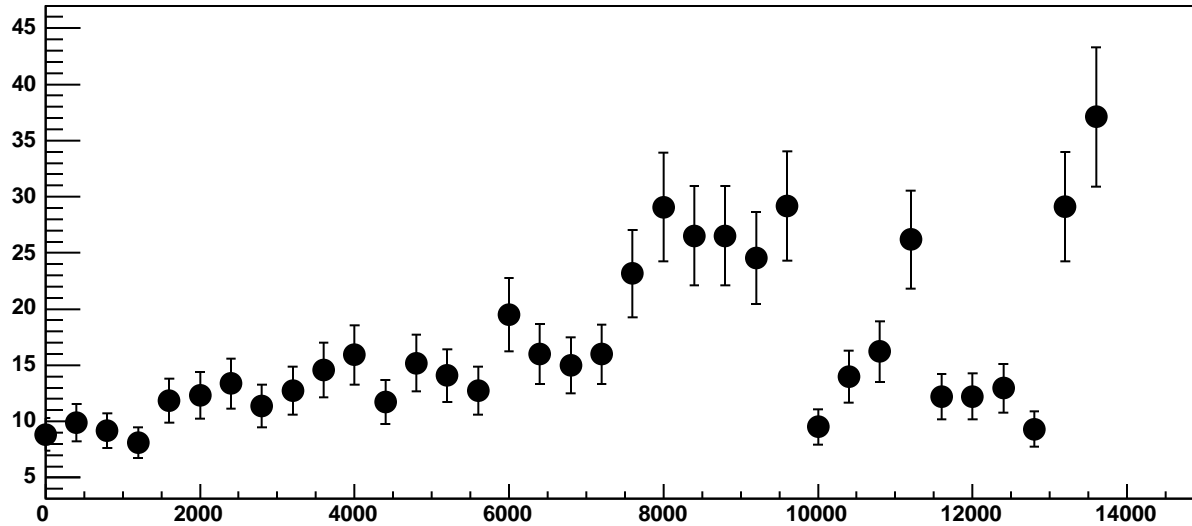
Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



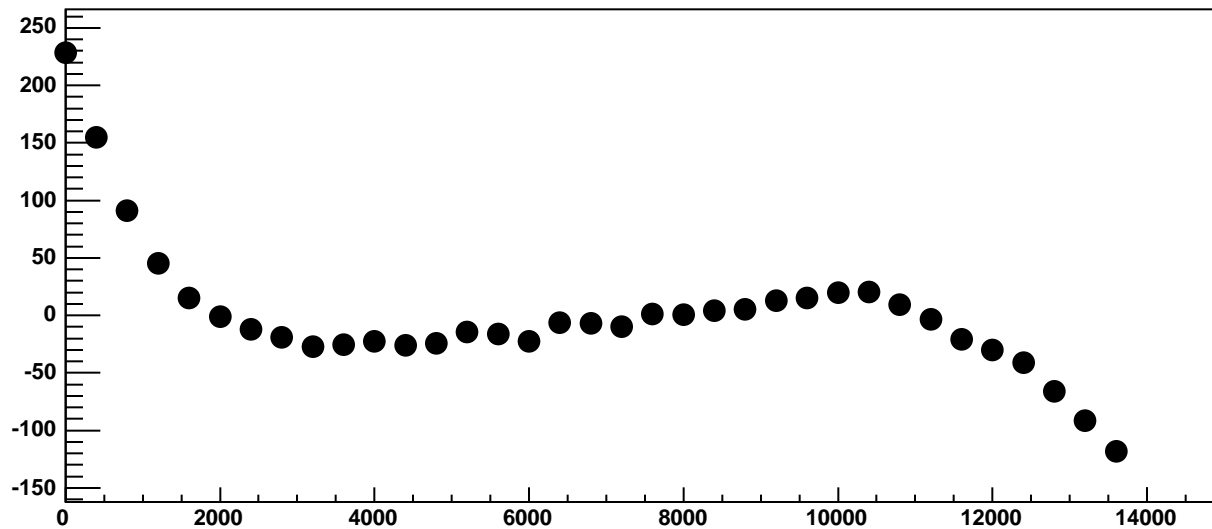
Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



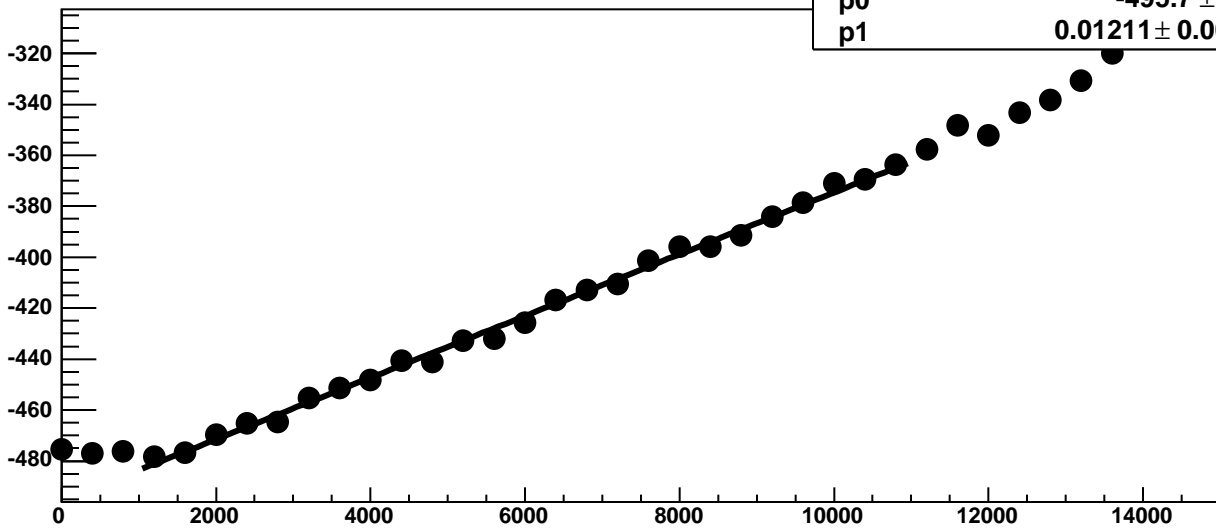
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

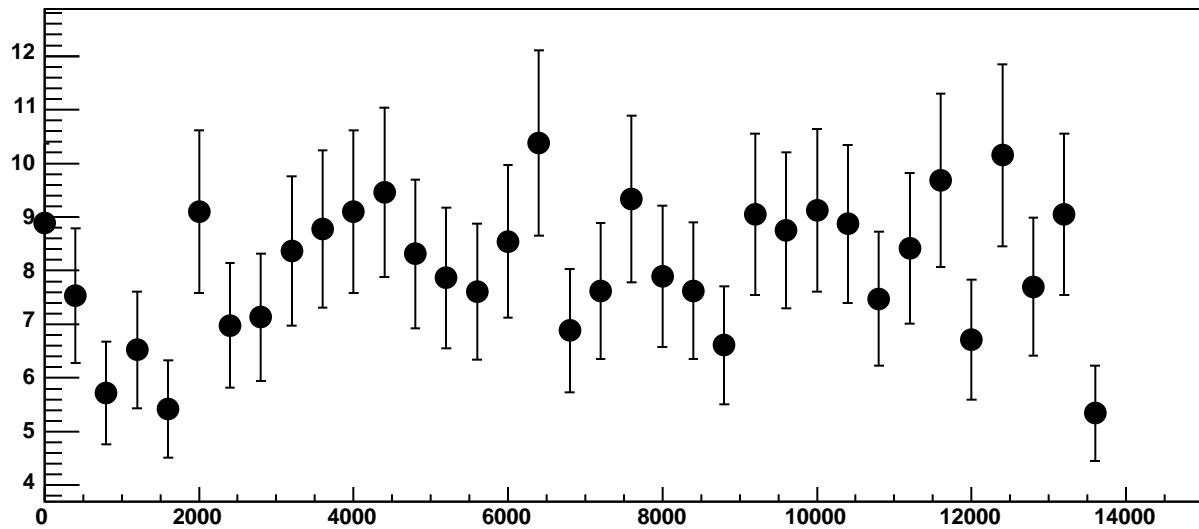


Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

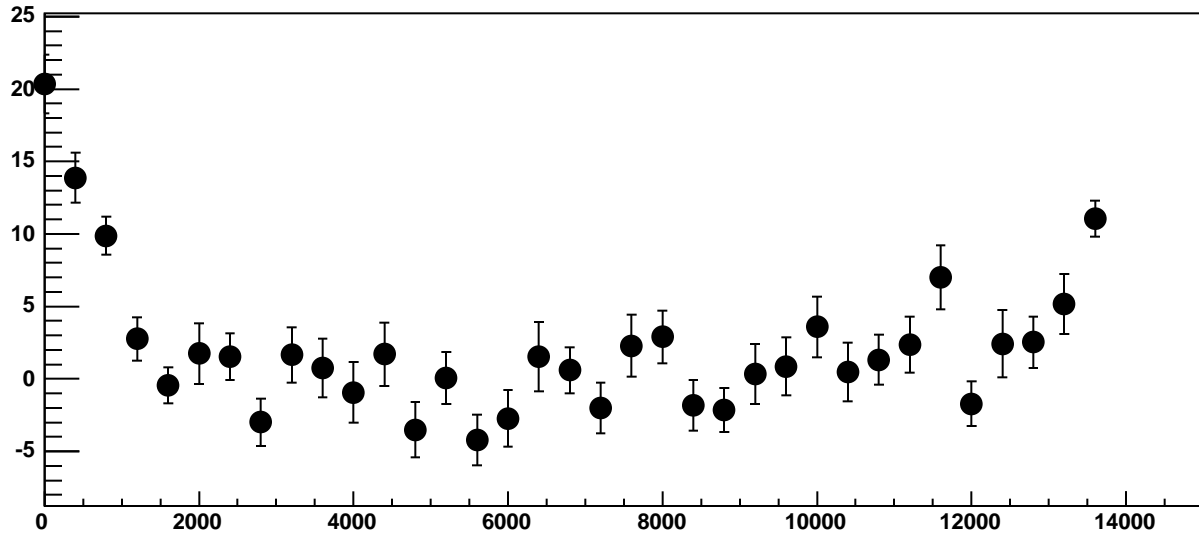


$\chi^2 / \text{ndf}$  33.73 / 23  
p0  $-495.7 \pm 0.7706$   
p1  $0.01211 \pm 0.0001197$

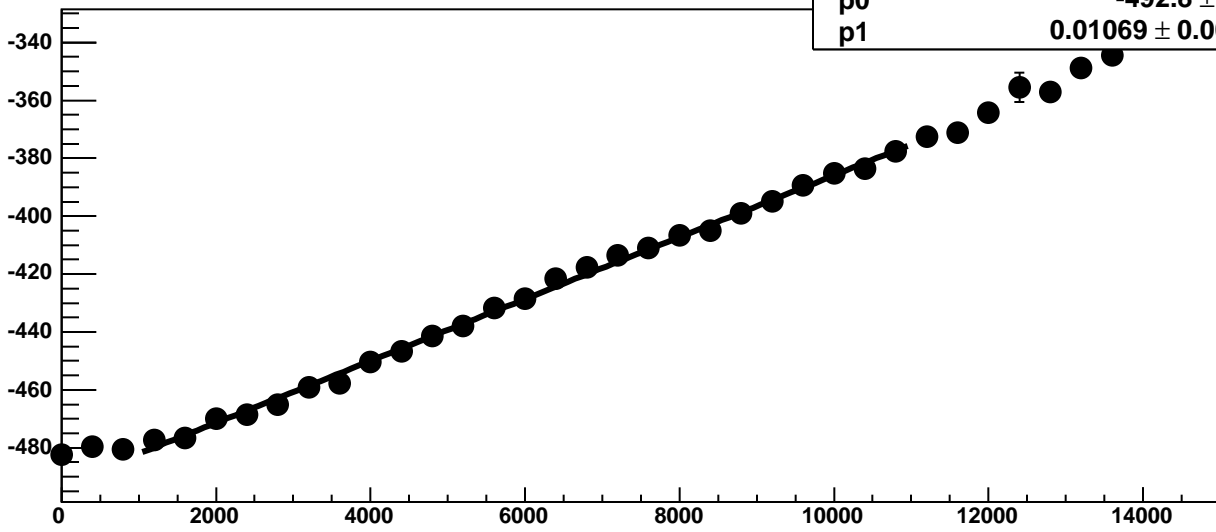
Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



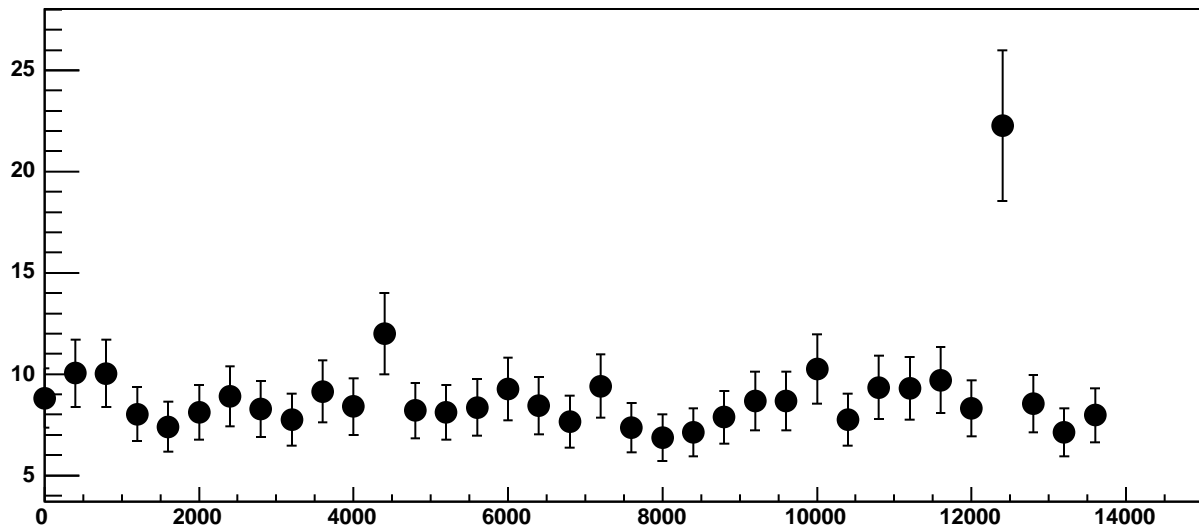
Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



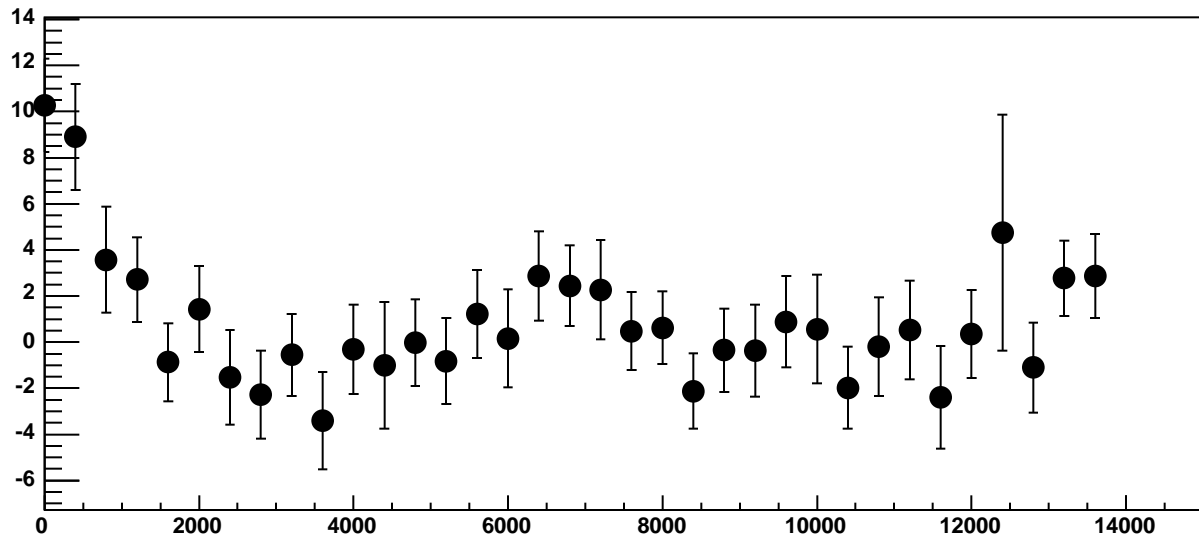
Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

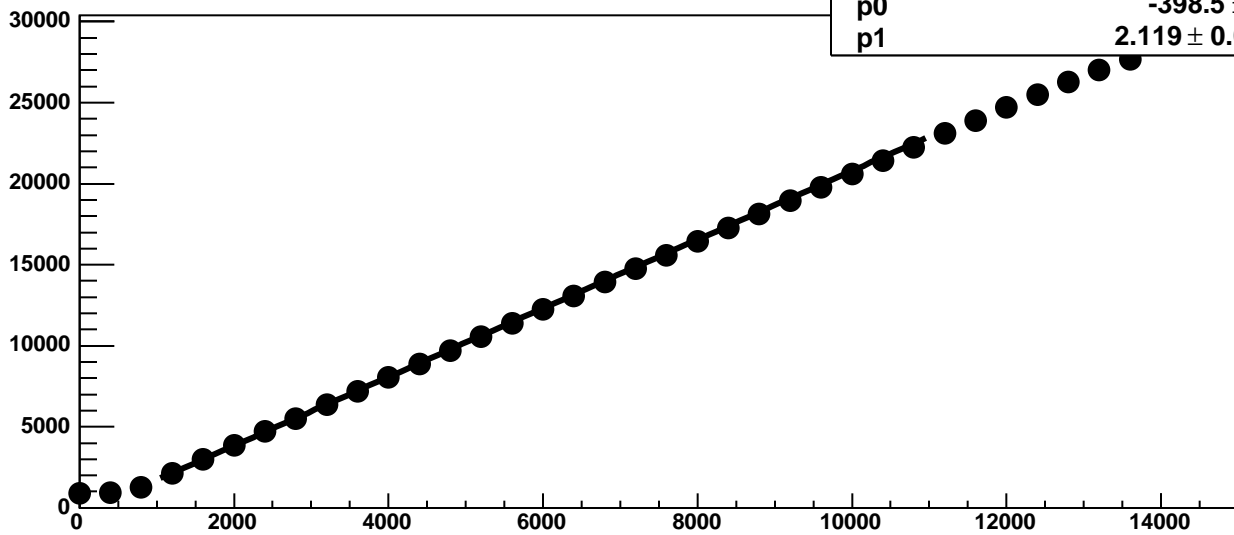


Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

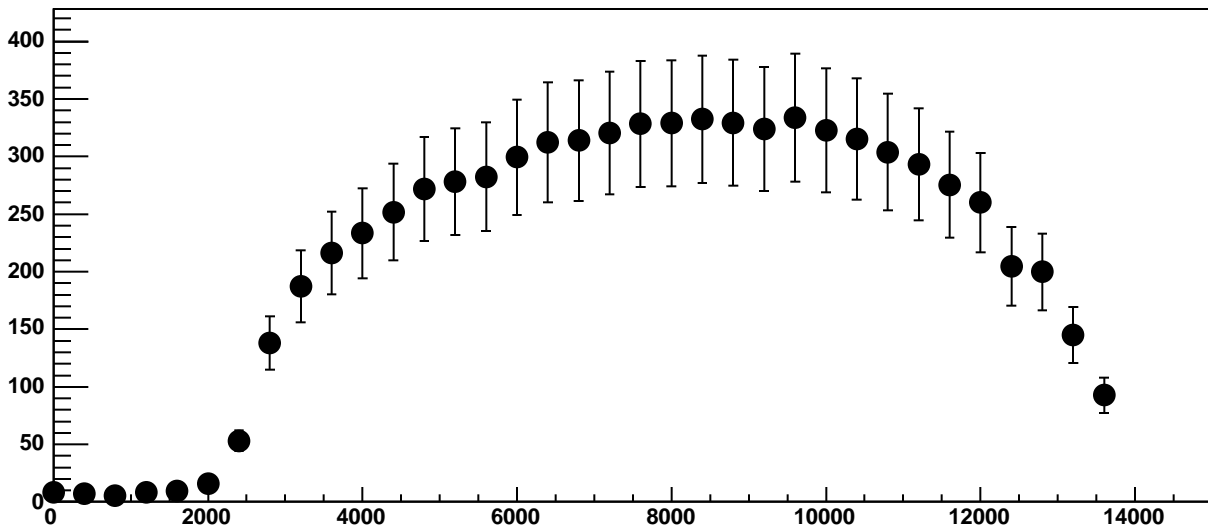




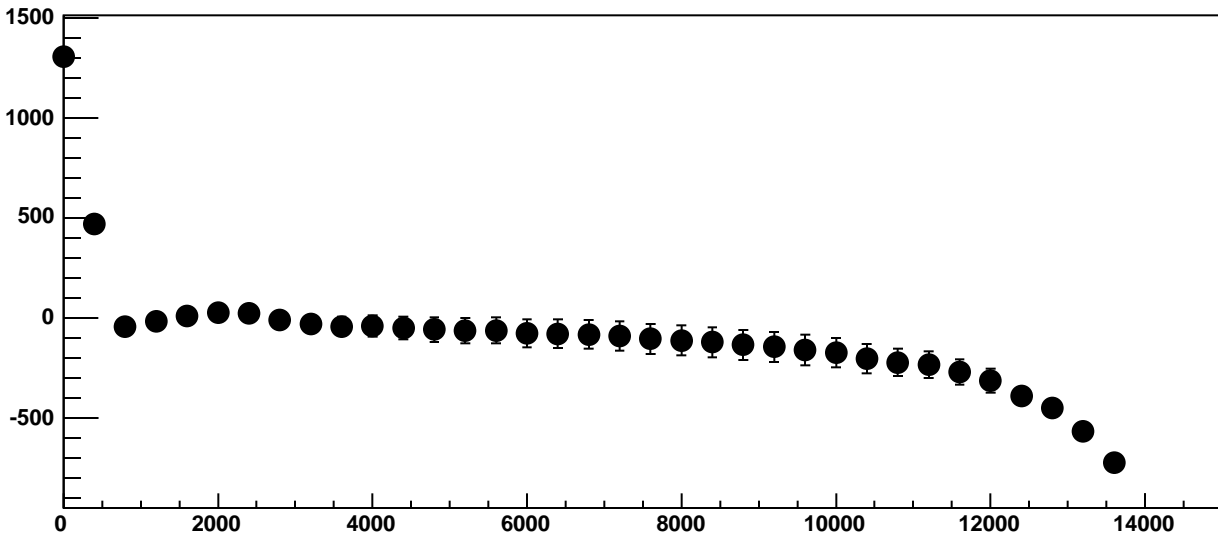
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



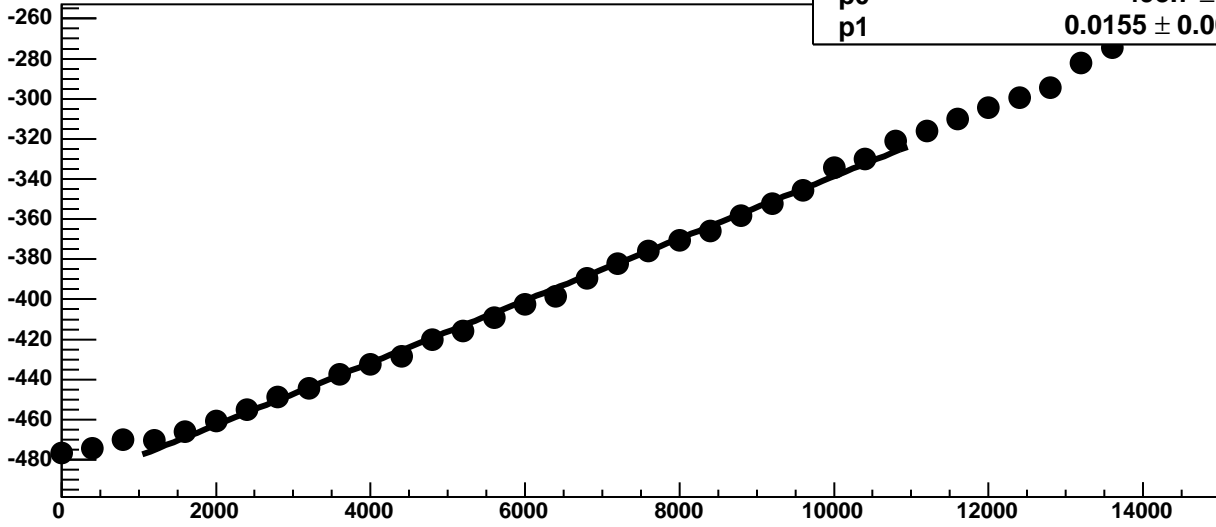
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



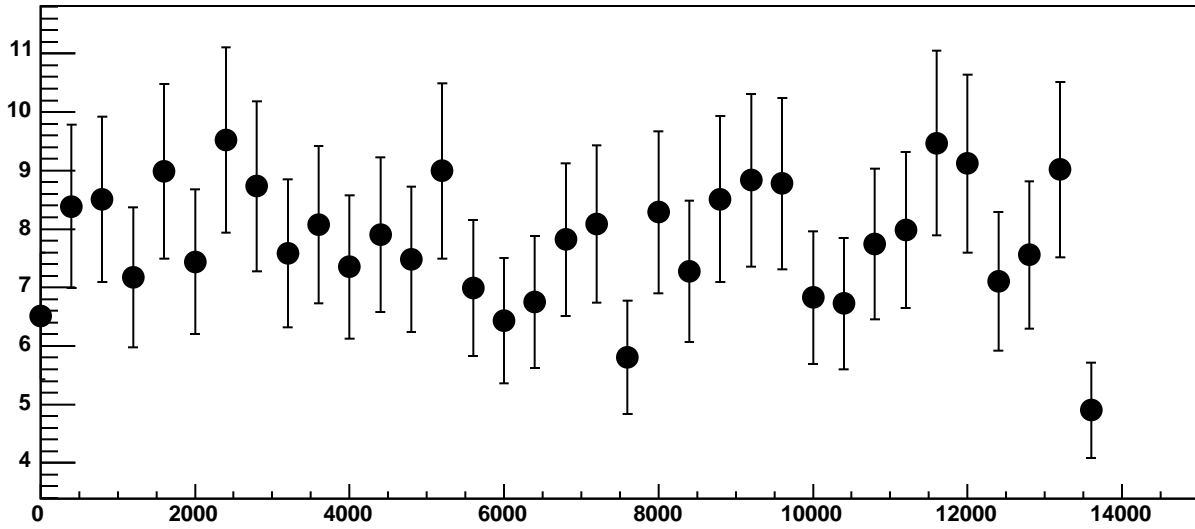
Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC



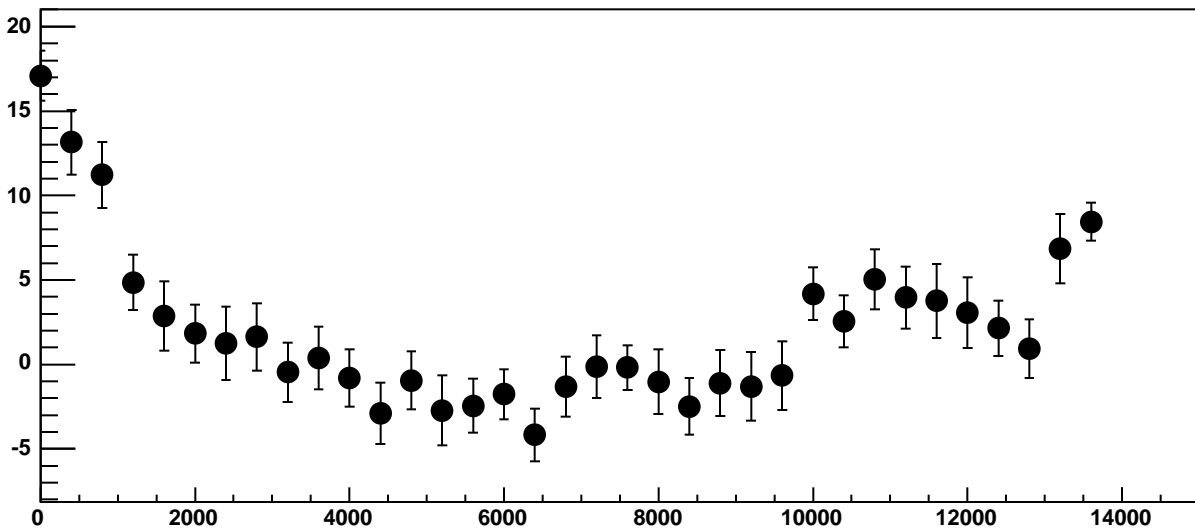
Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



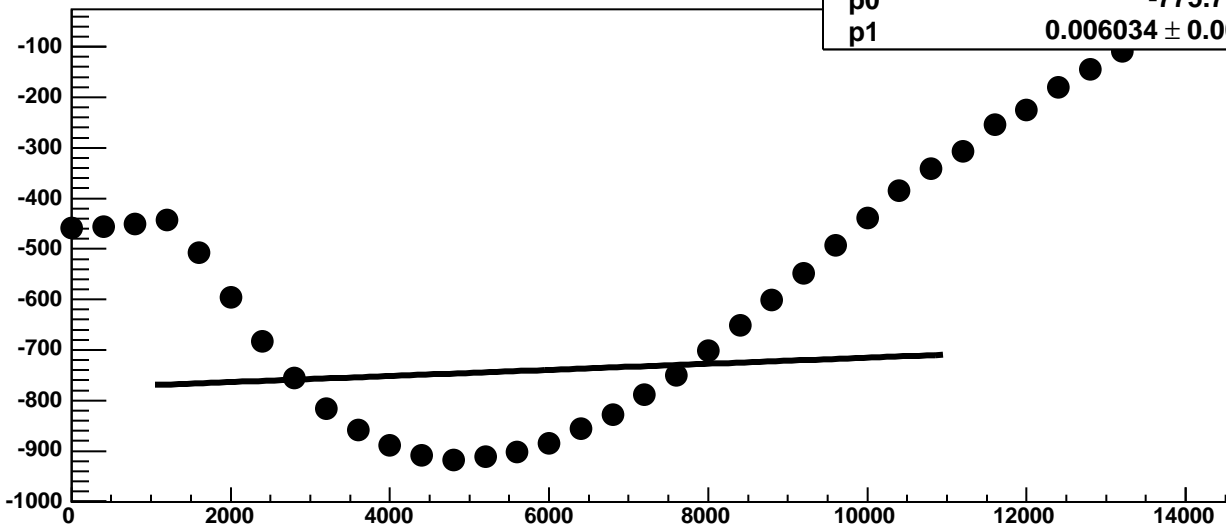
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

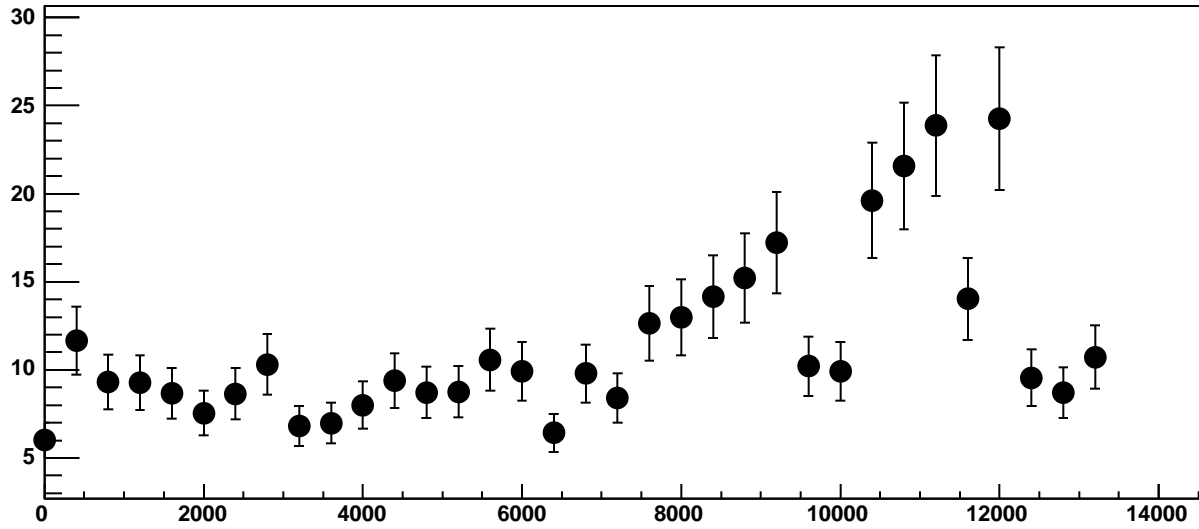


Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

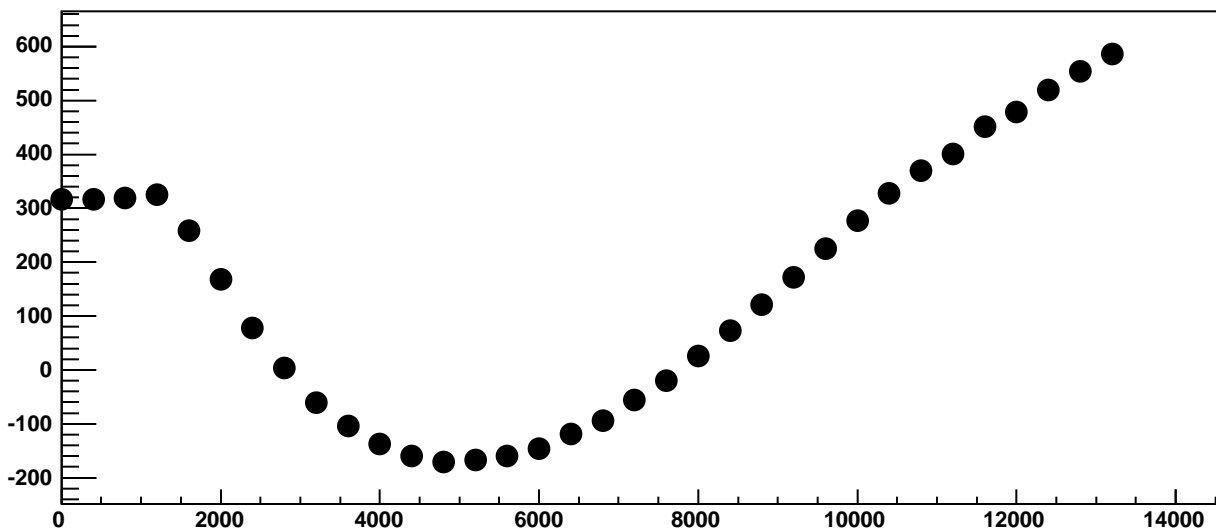


$\chi^2 / \text{ndf}$  1.384e+05 / 23  
p0 -775.7 ± 0.97  
p1 0.006034 ± 0.0001715

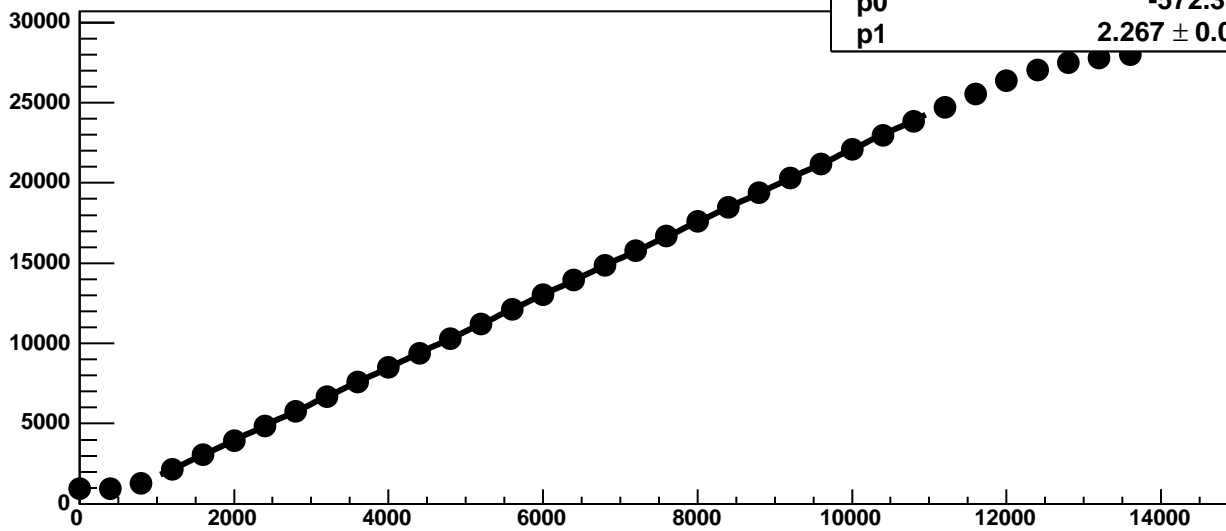
Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



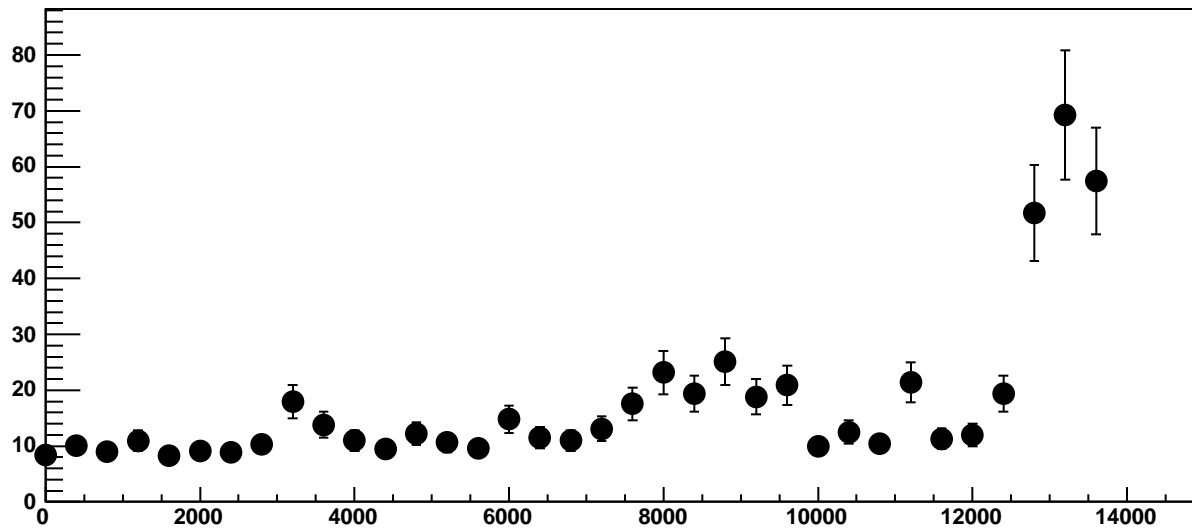
Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



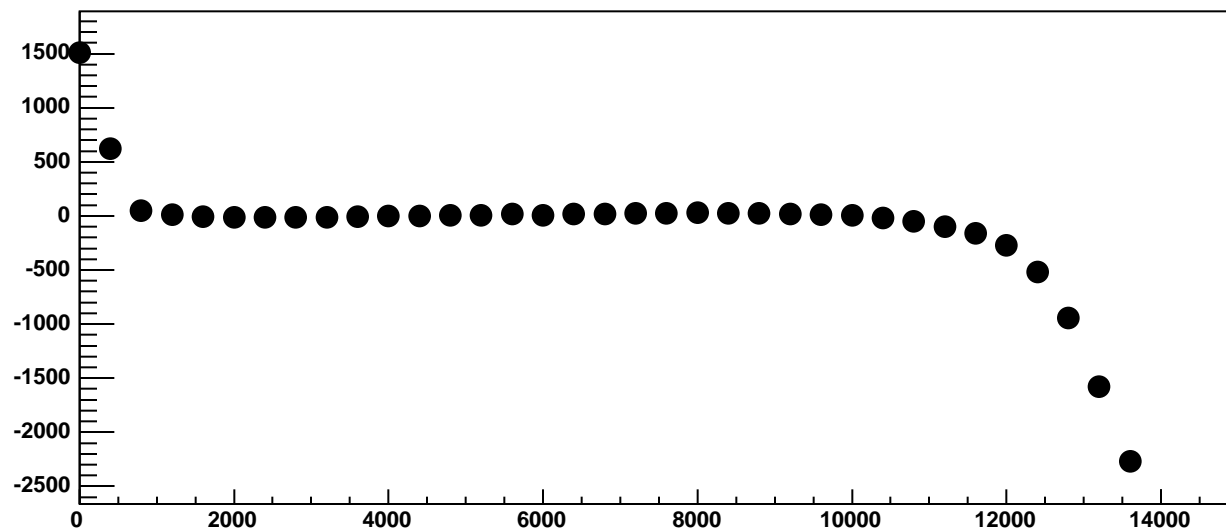
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



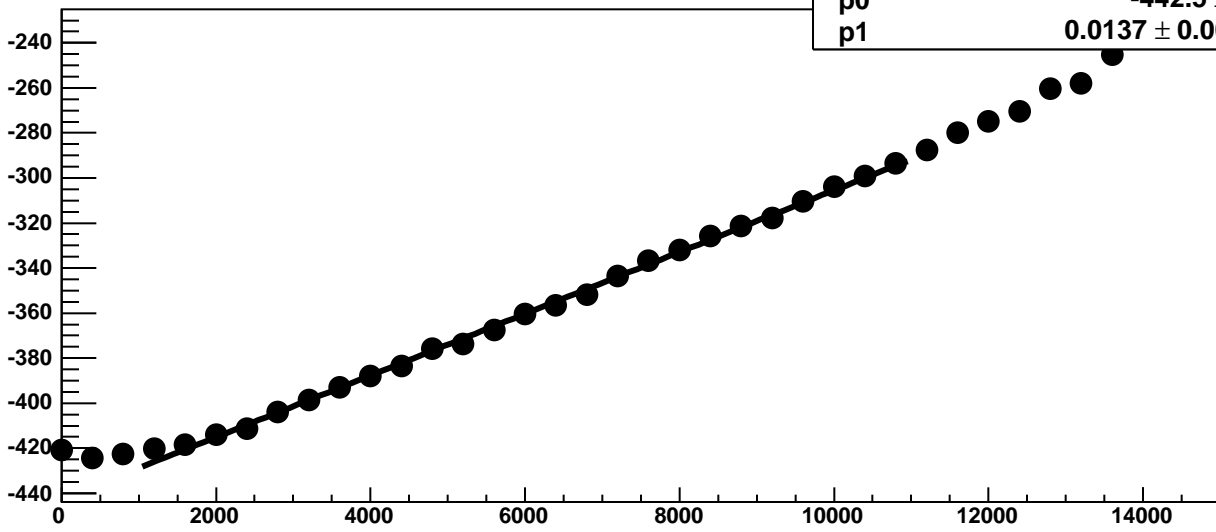
Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

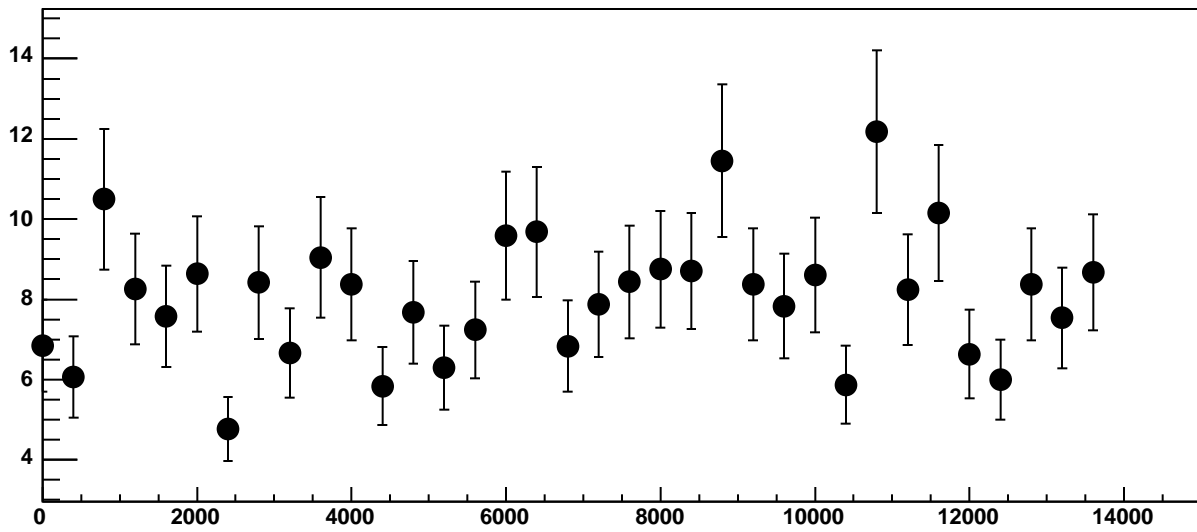


Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

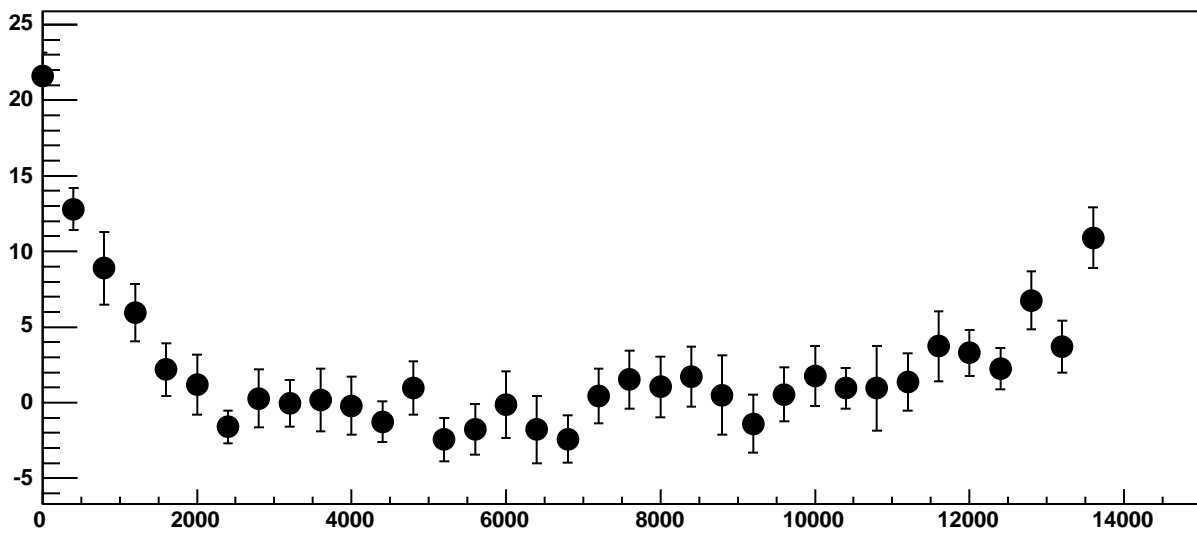


$\chi^2 / \text{ndf}$  25.92 / 23  
p0  $-442.5 \pm 0.772$   
p1  $0.0137 \pm 0.0001223$

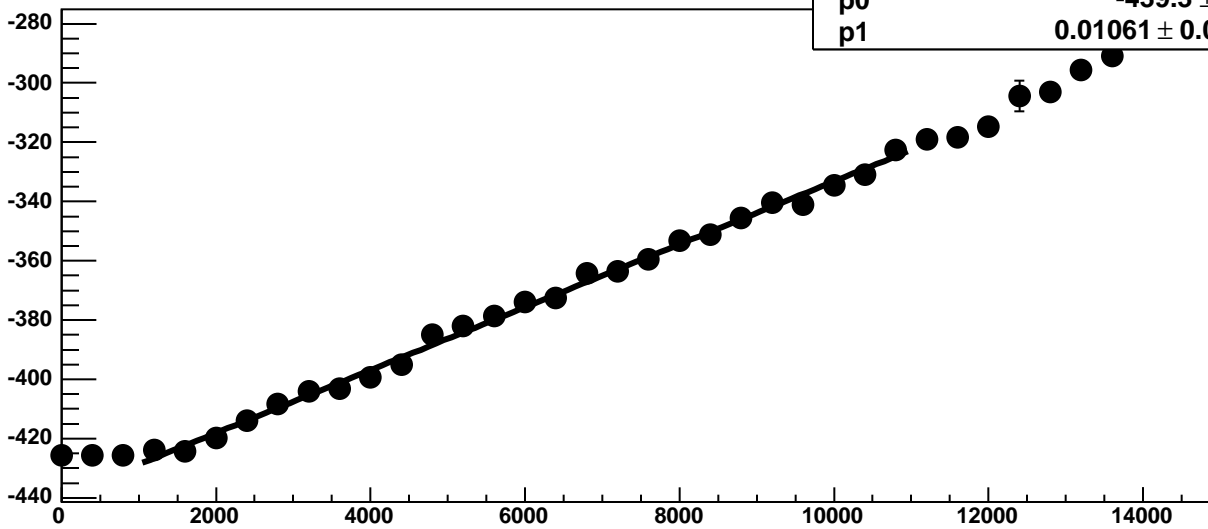
Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

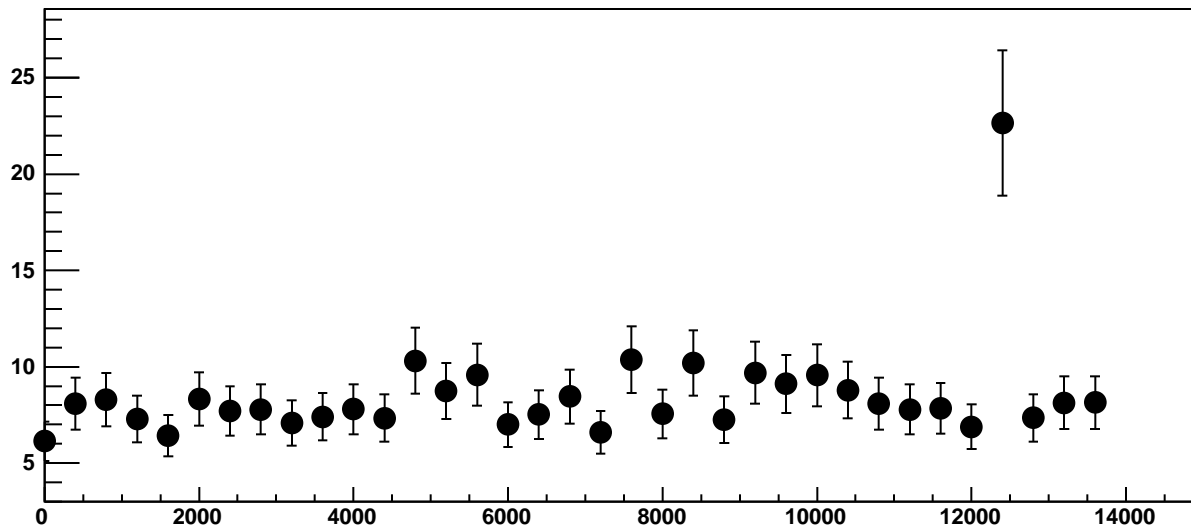


Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

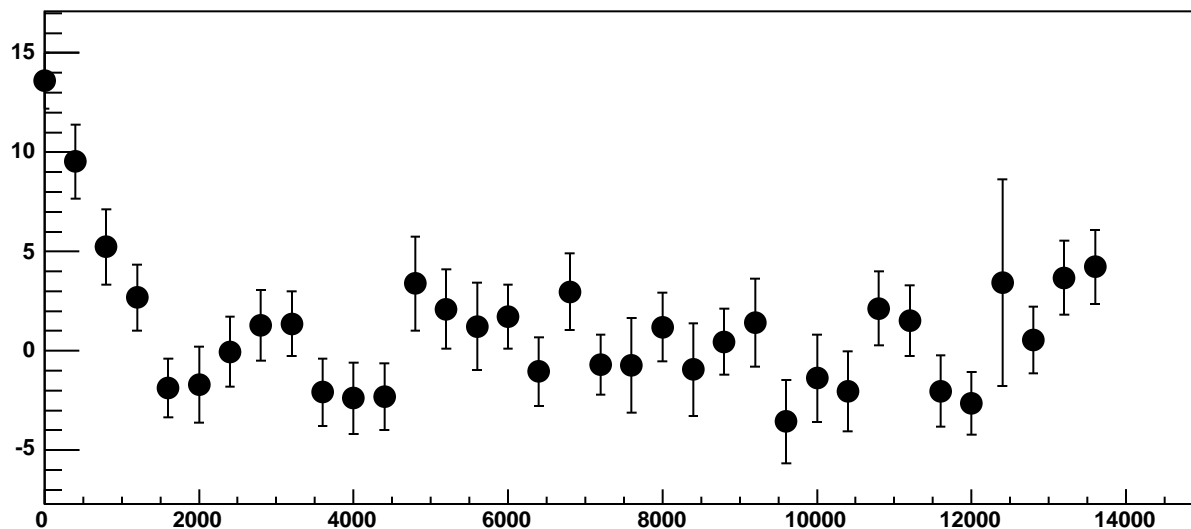


$\chi^2 / \text{ndf}$  25.79 / 23  
p0  $-439.3 \pm 0.8072$   
p1  $0.01061 \pm 0.0001271$

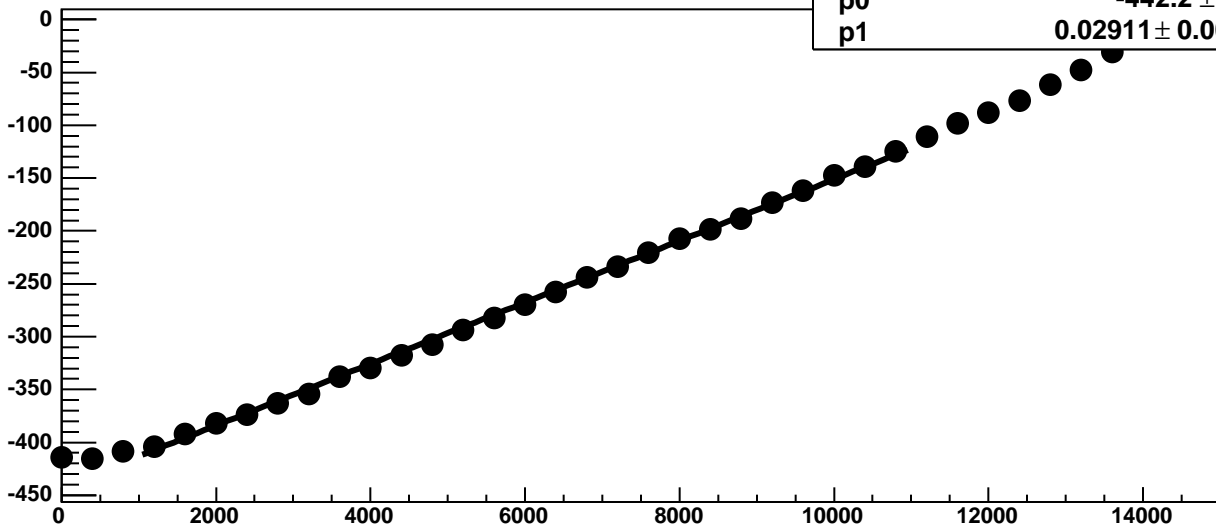
Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

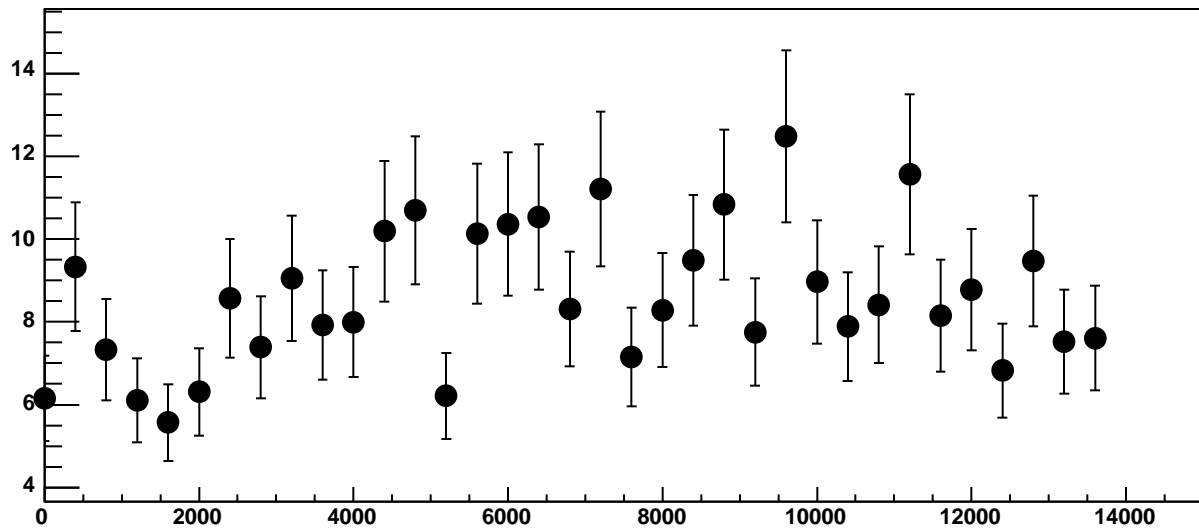


Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

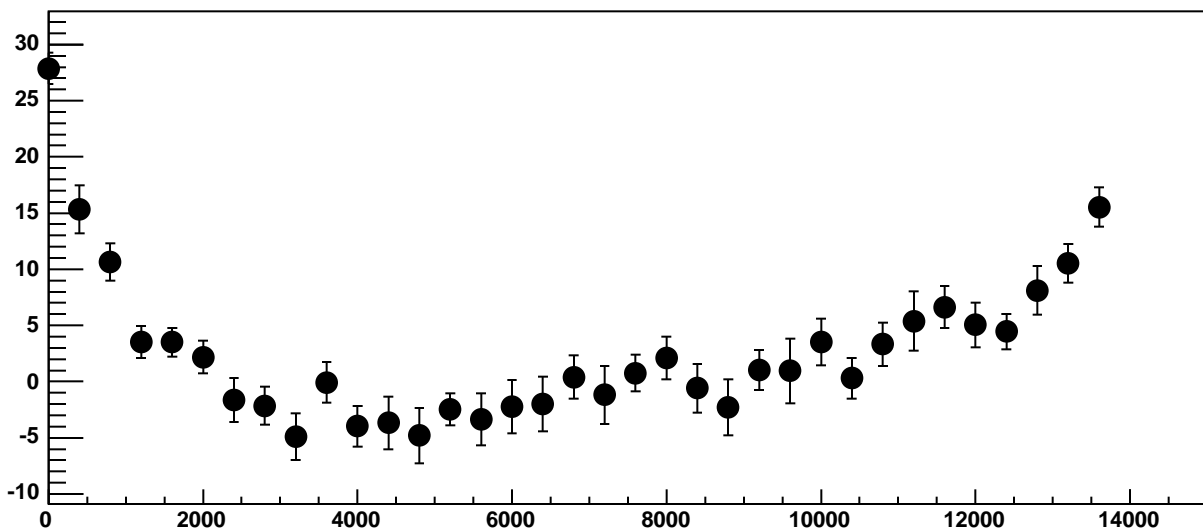


$\chi^2 / \text{ndf}$  50.75 / 23  
p0  $-442.2 \pm 0.7572$   
p1  $0.02911 \pm 0.0001223$

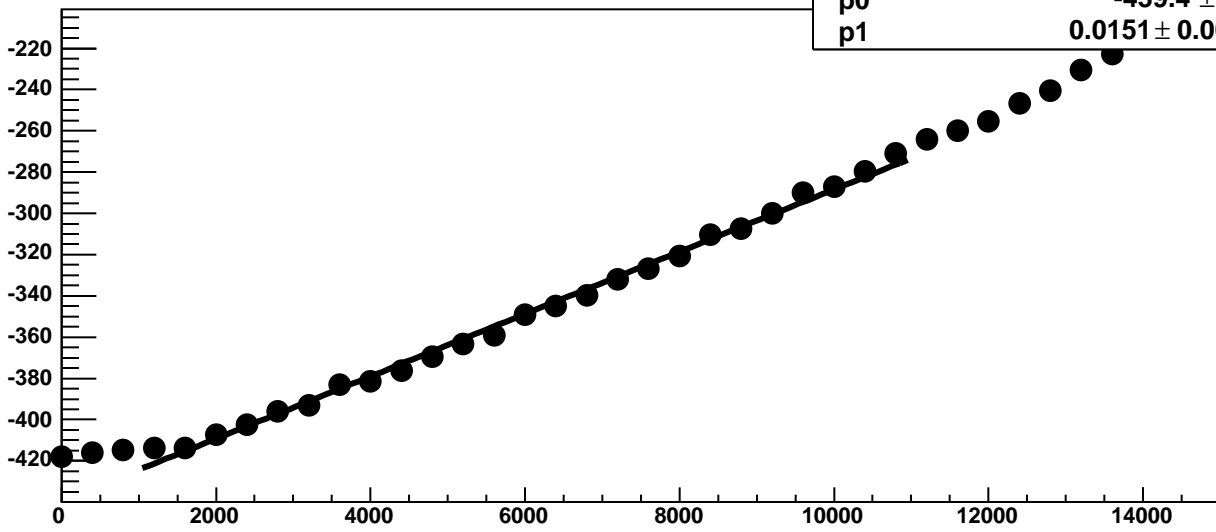
Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



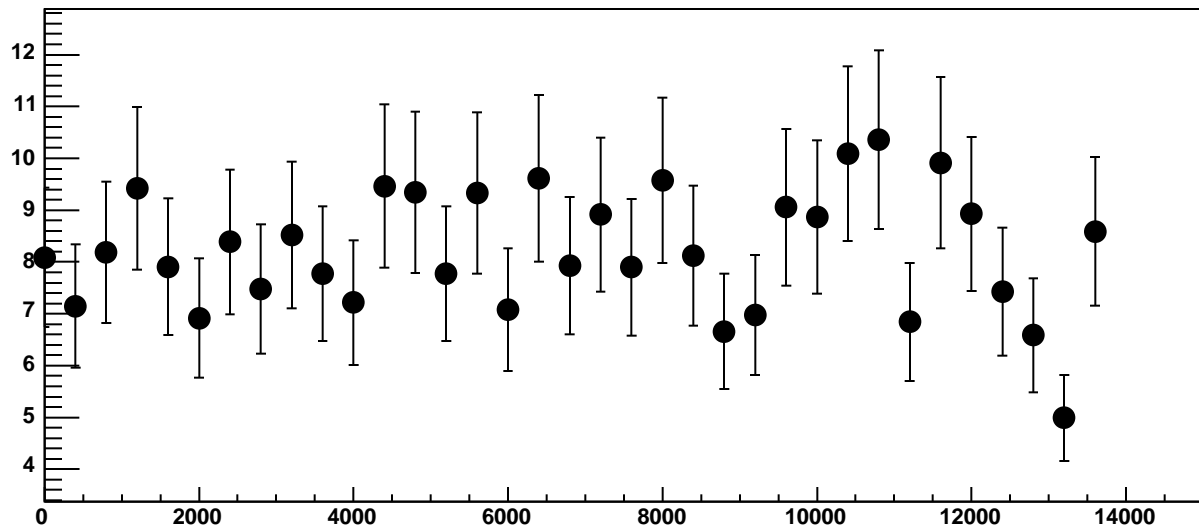
Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



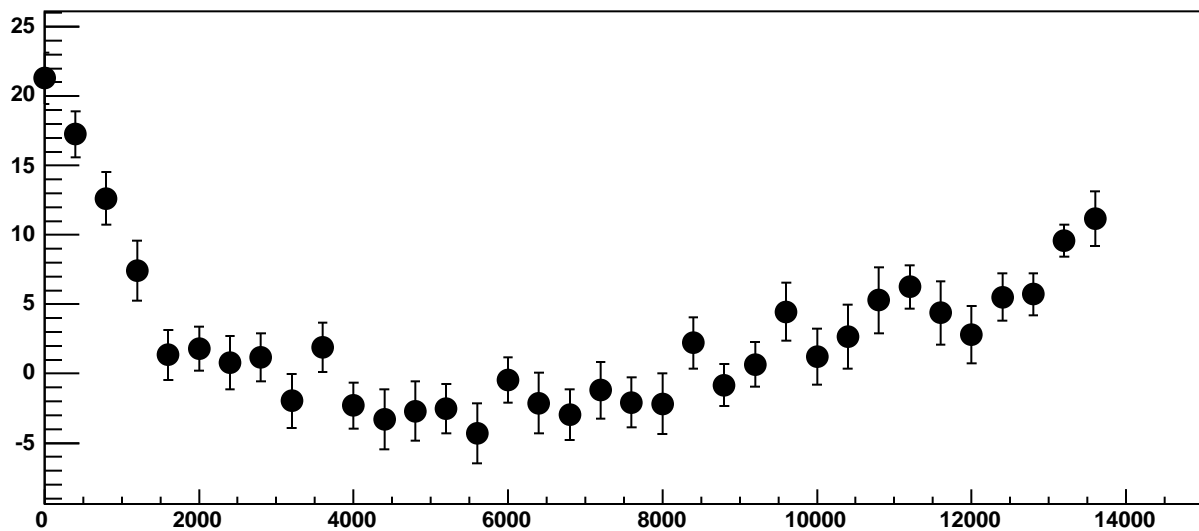
Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



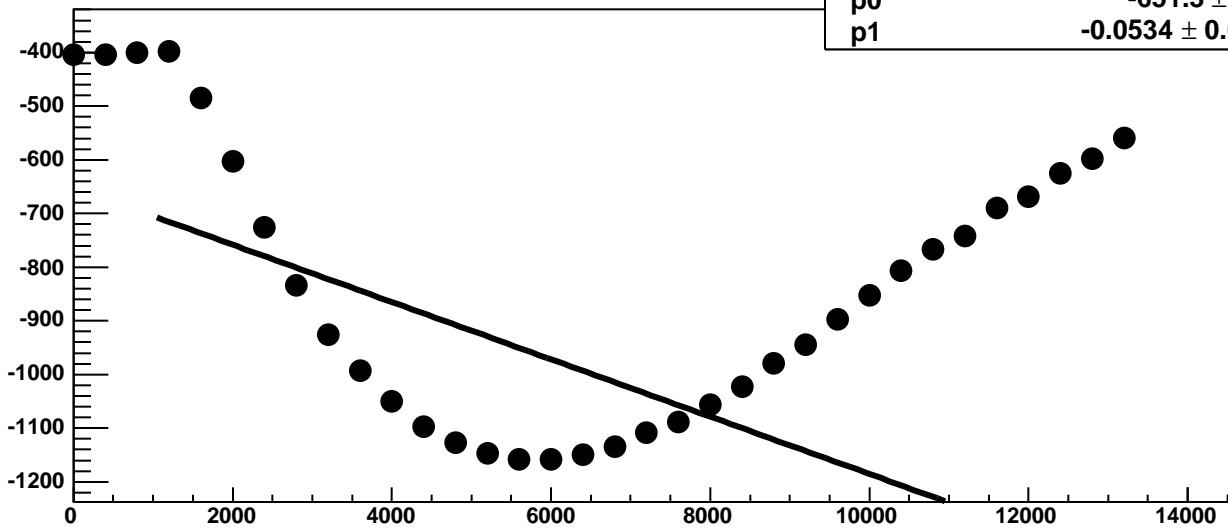
Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



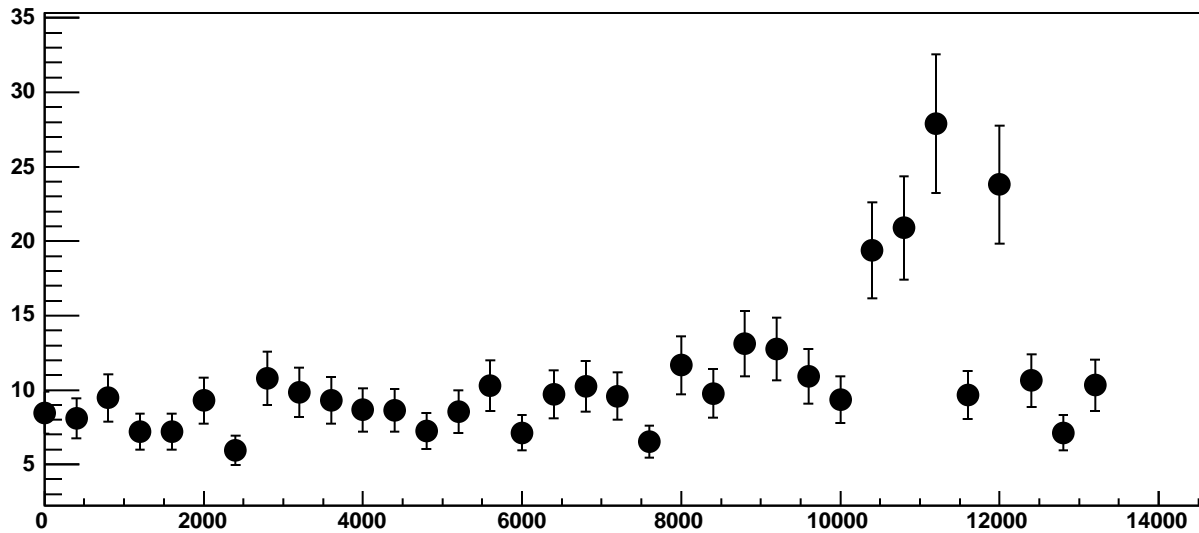


Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

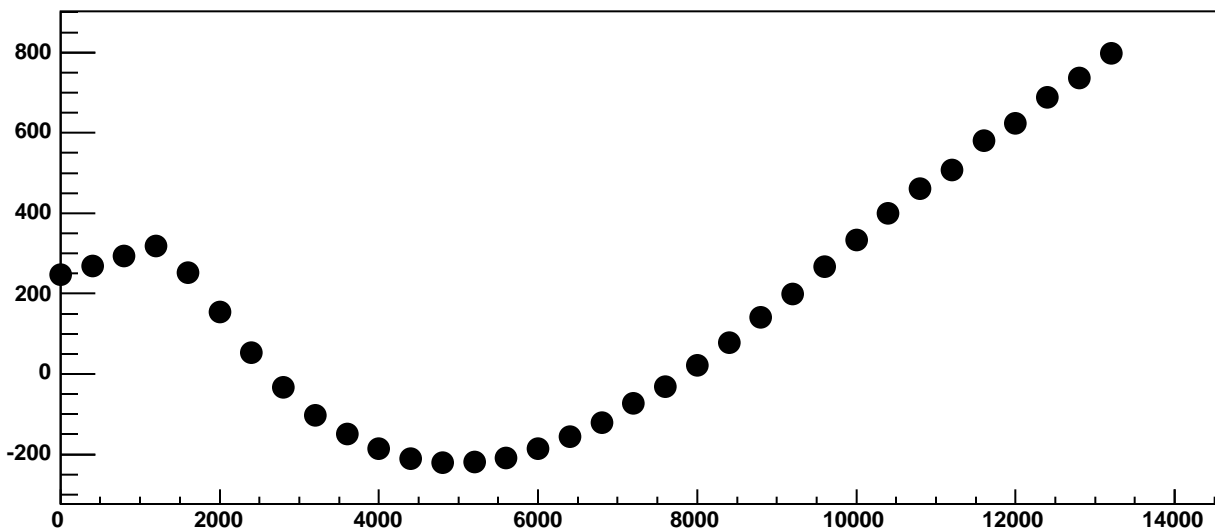
$\chi^2 / \text{ndf}$  2.149e+05 / 23  
p0 -651.3 ± 0.9006  
p1 -0.0534 ± 0.000155



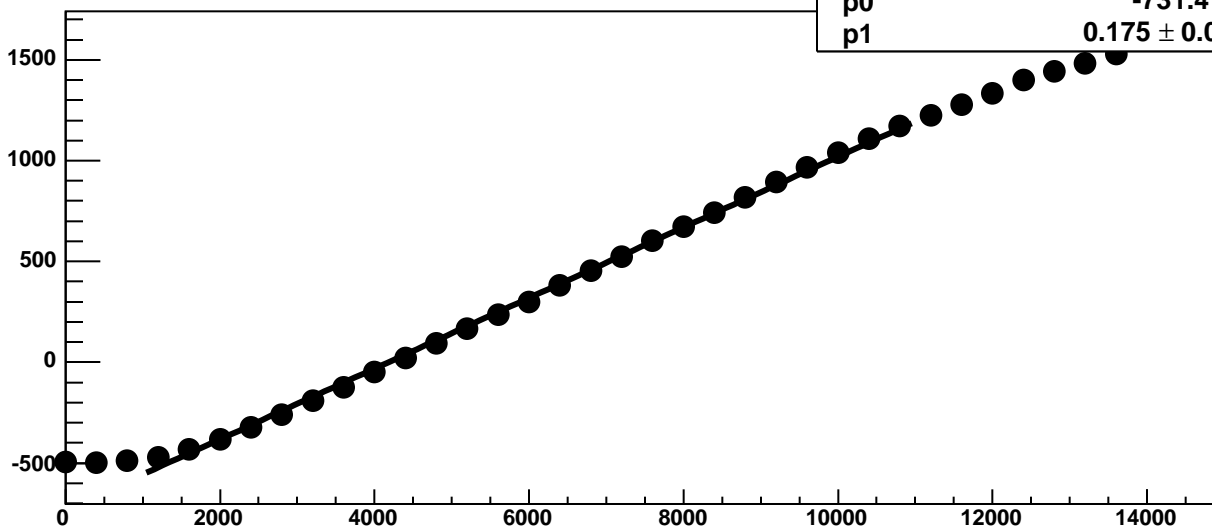
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



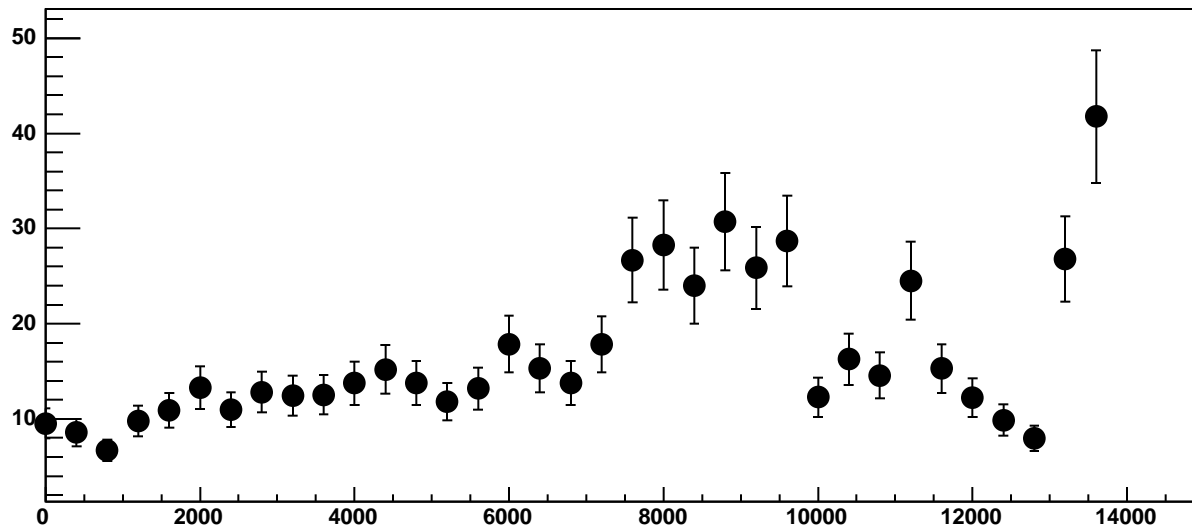
Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



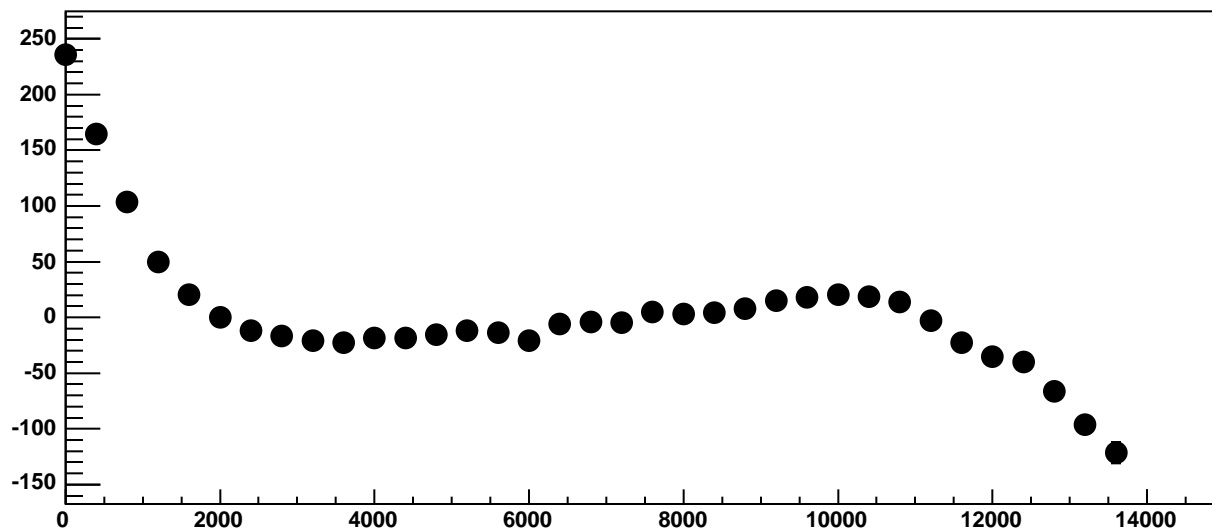
Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



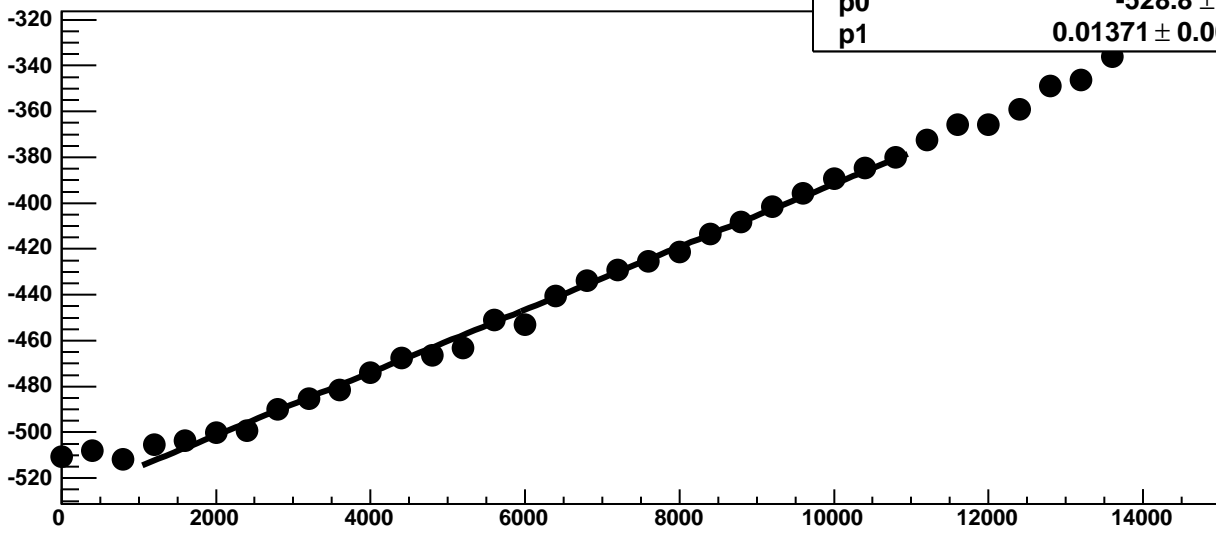
Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



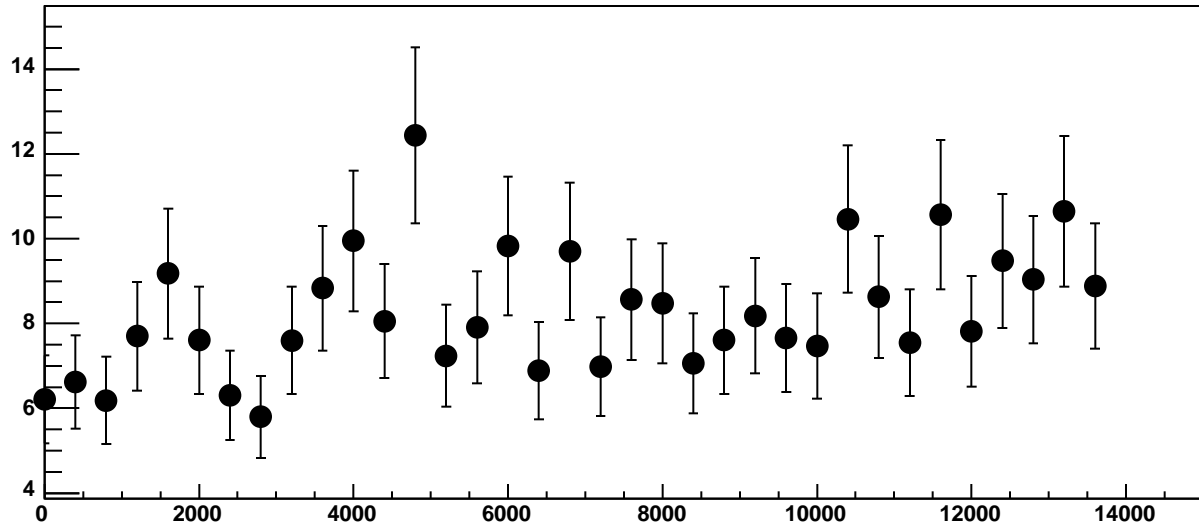
Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



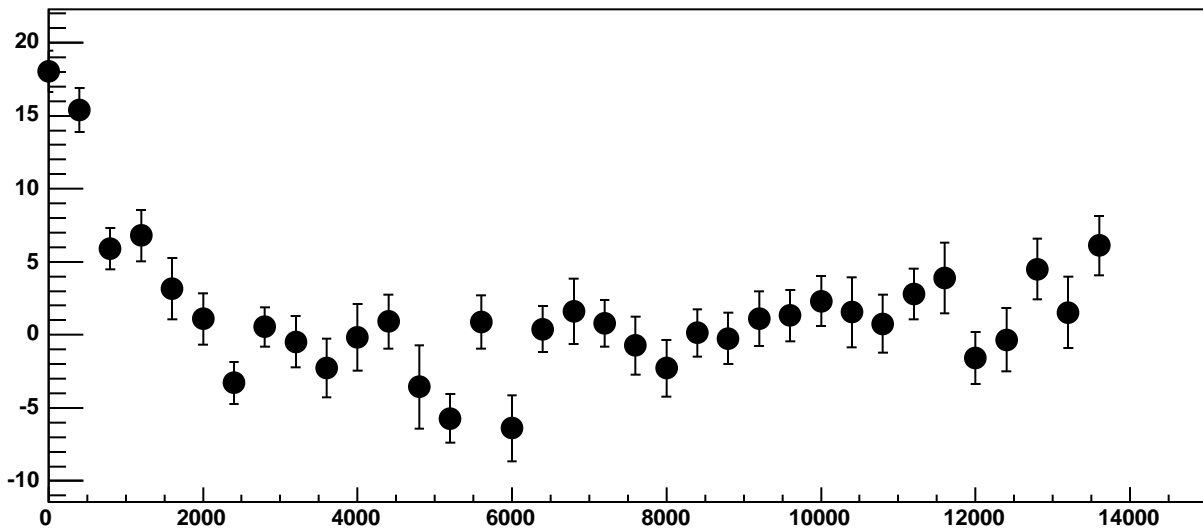
Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



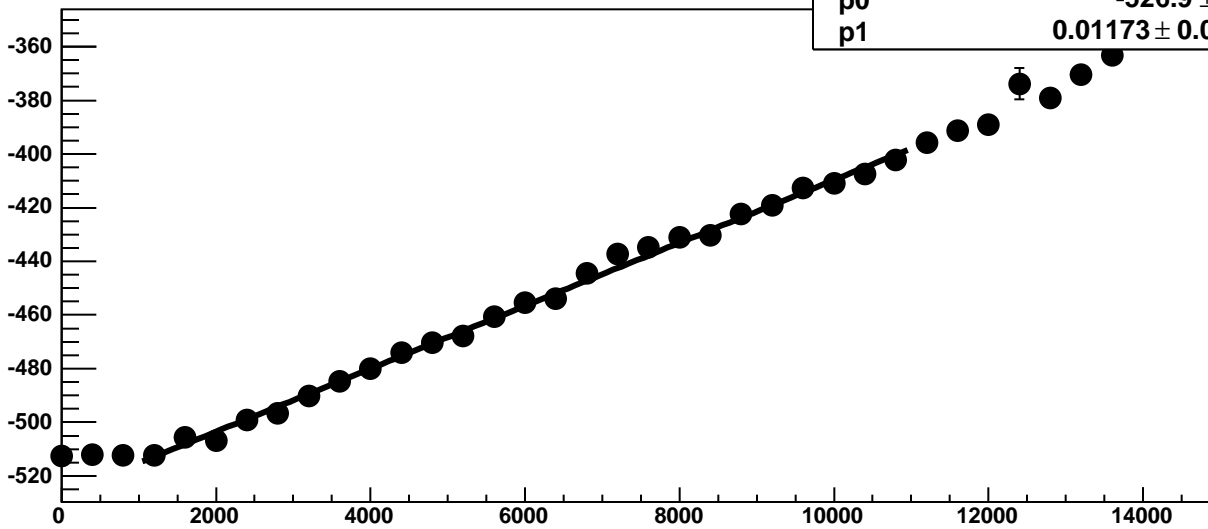
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



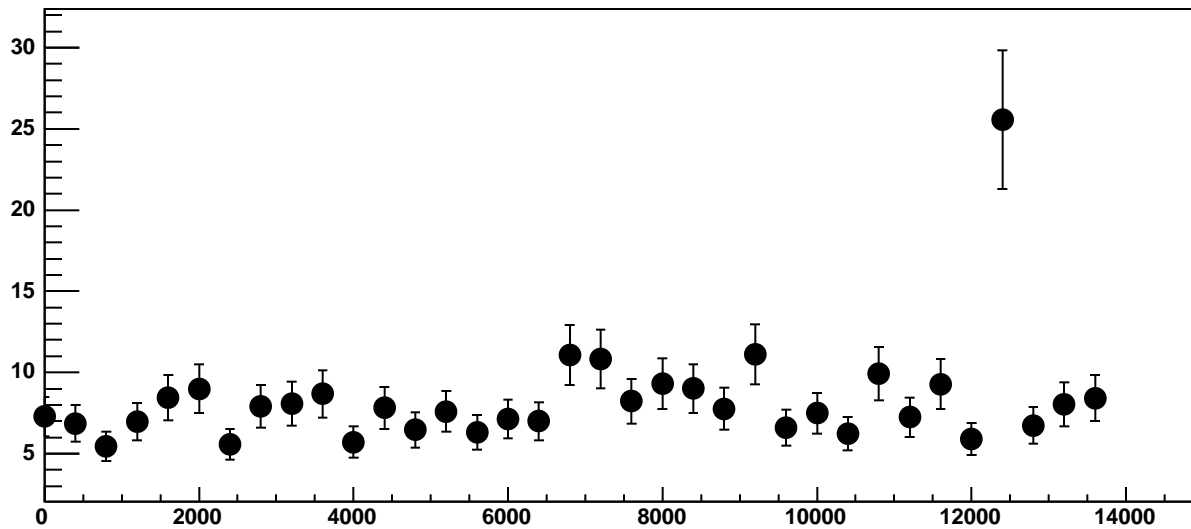
Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



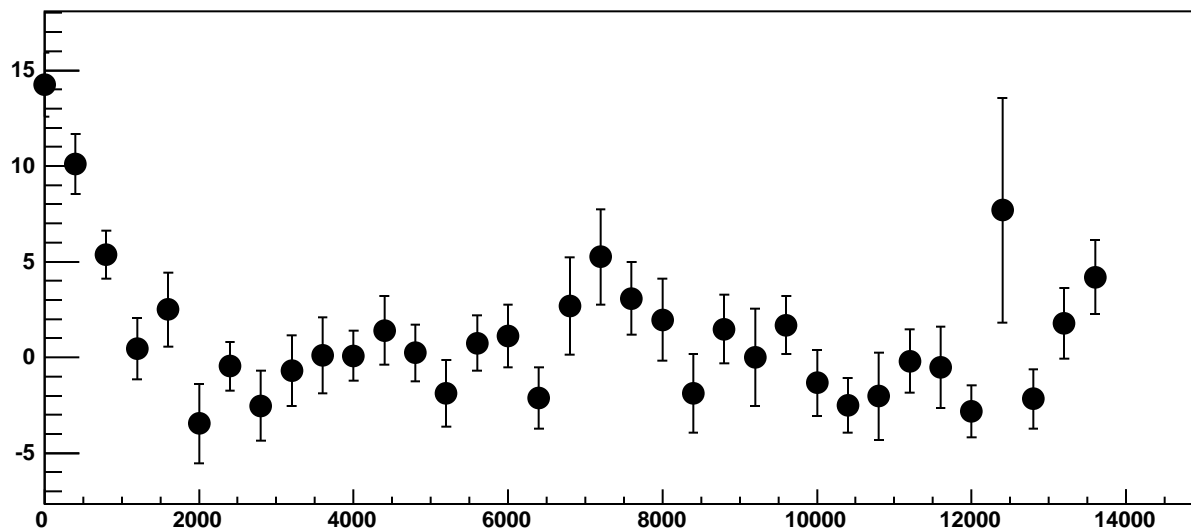
Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



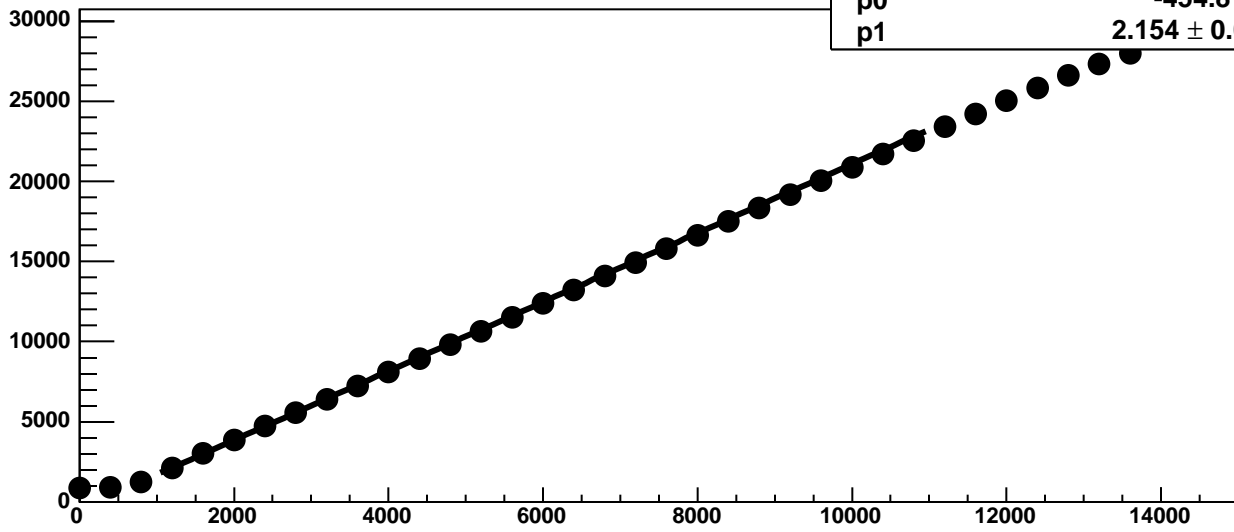
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



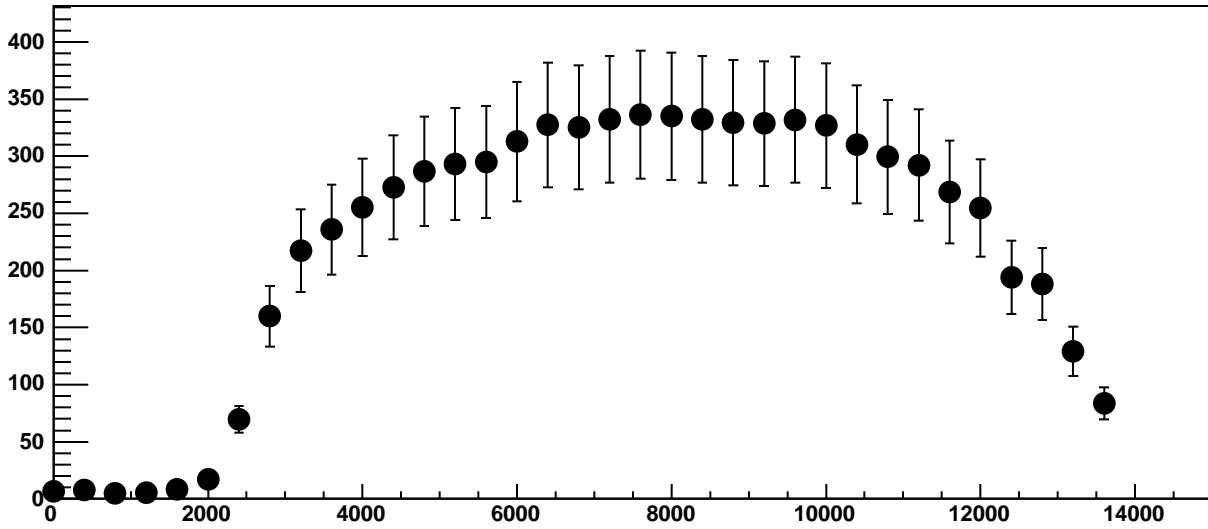
Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



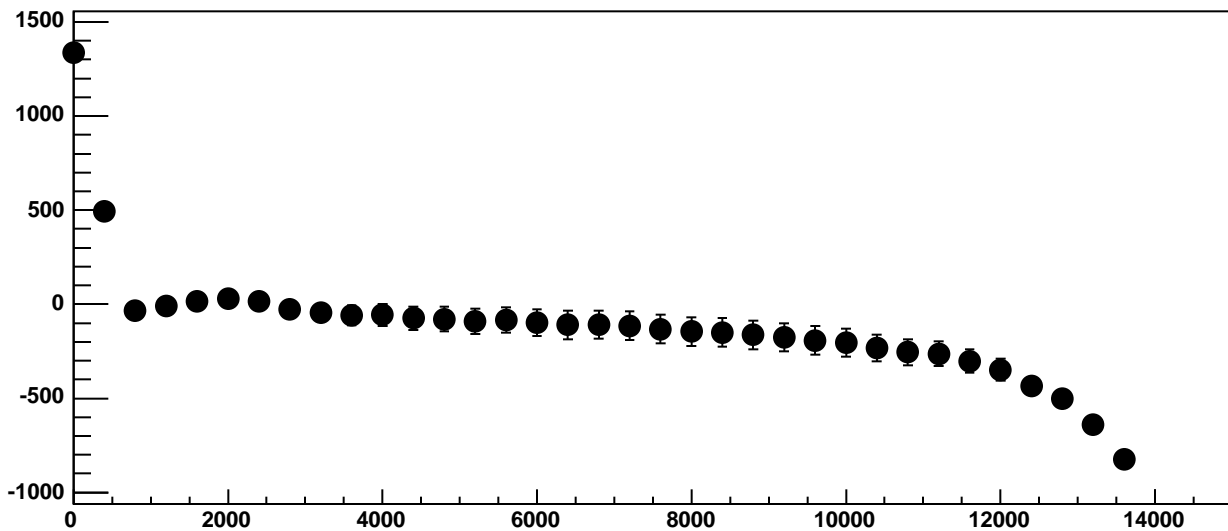
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



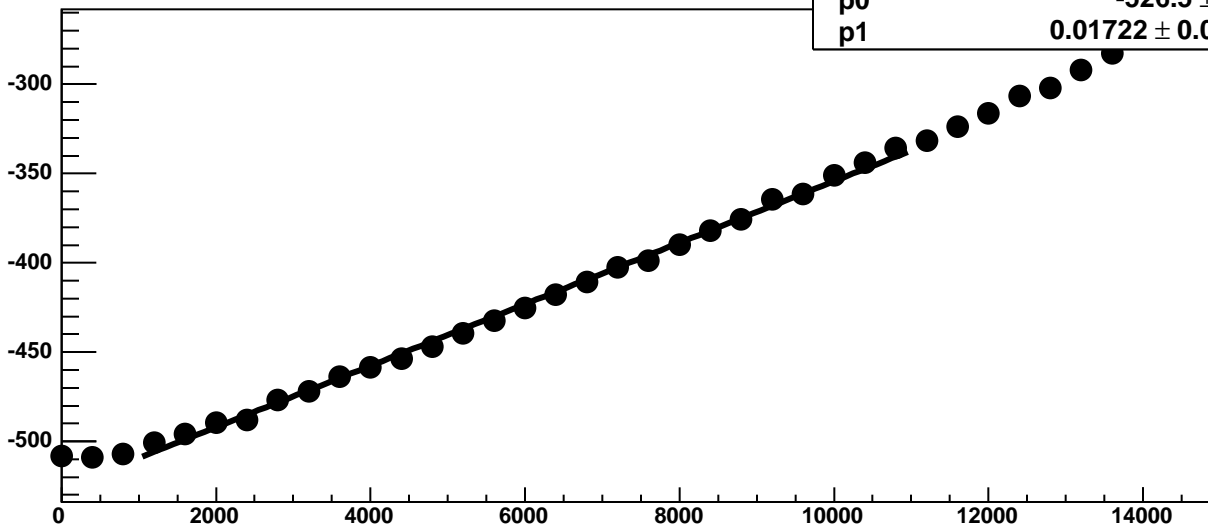
Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

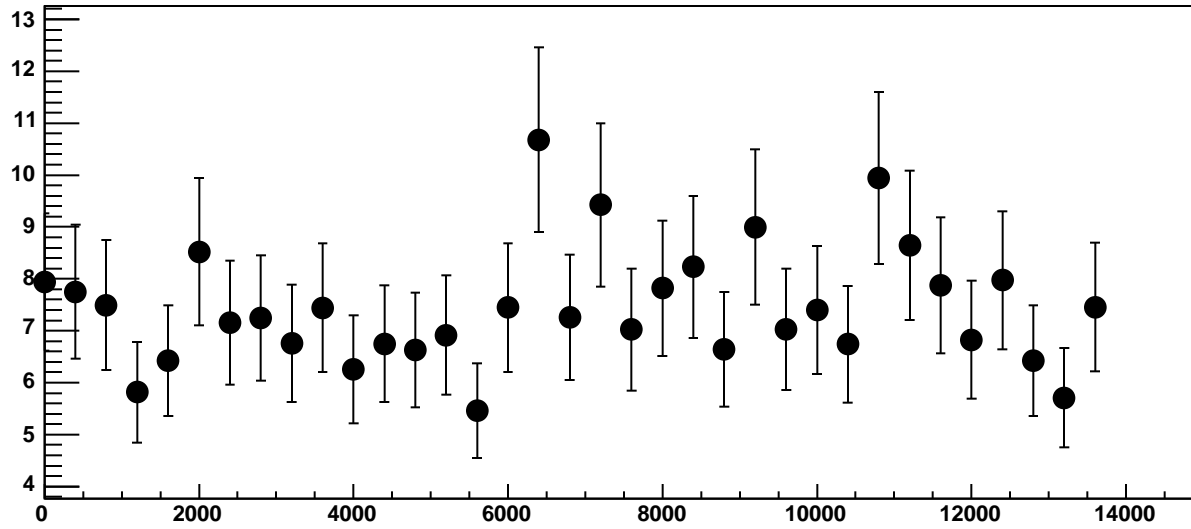


Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

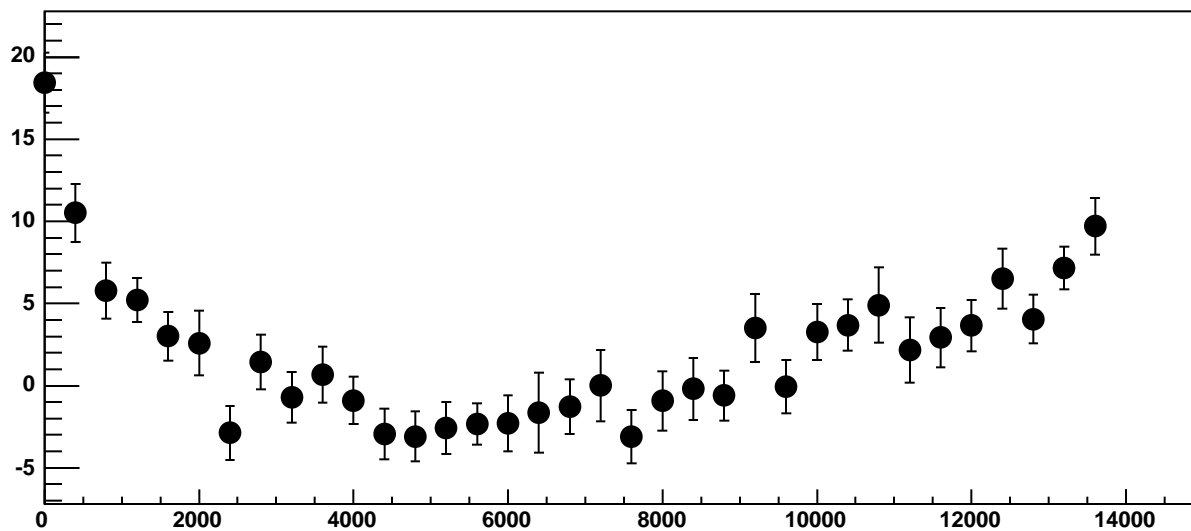


$\chi^2 / \text{ndf}$  63.47 / 23  
p0  $-526.5 \pm 0.7296$   
p1  $0.01722 \pm 0.0001146$

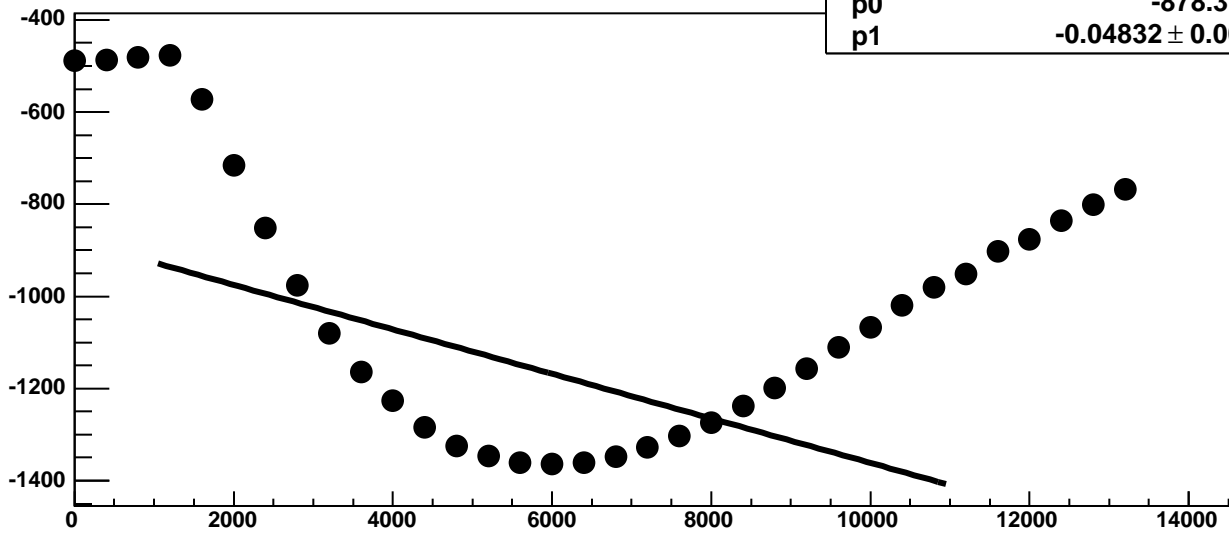
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

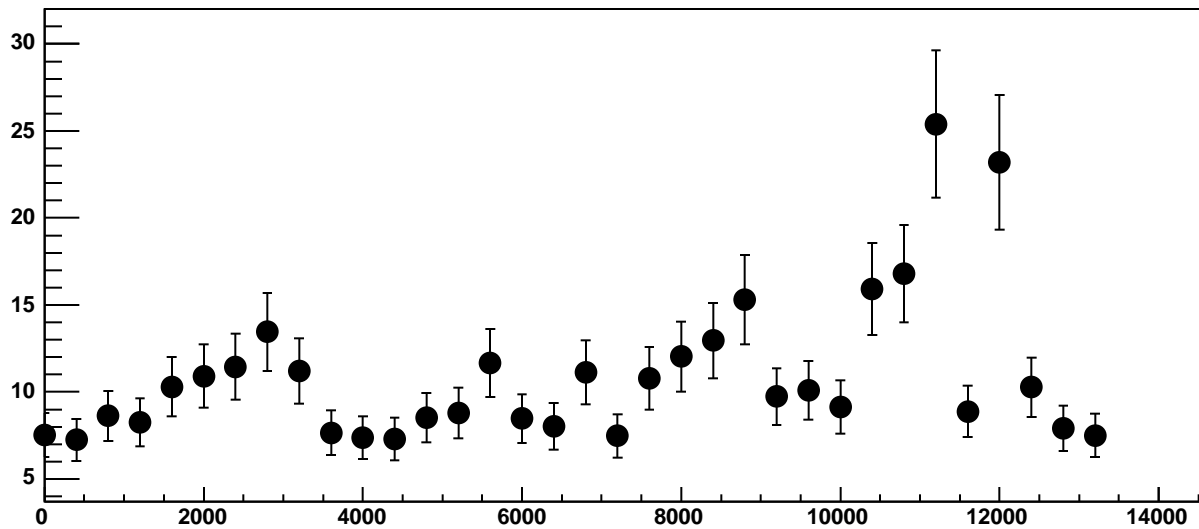


Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

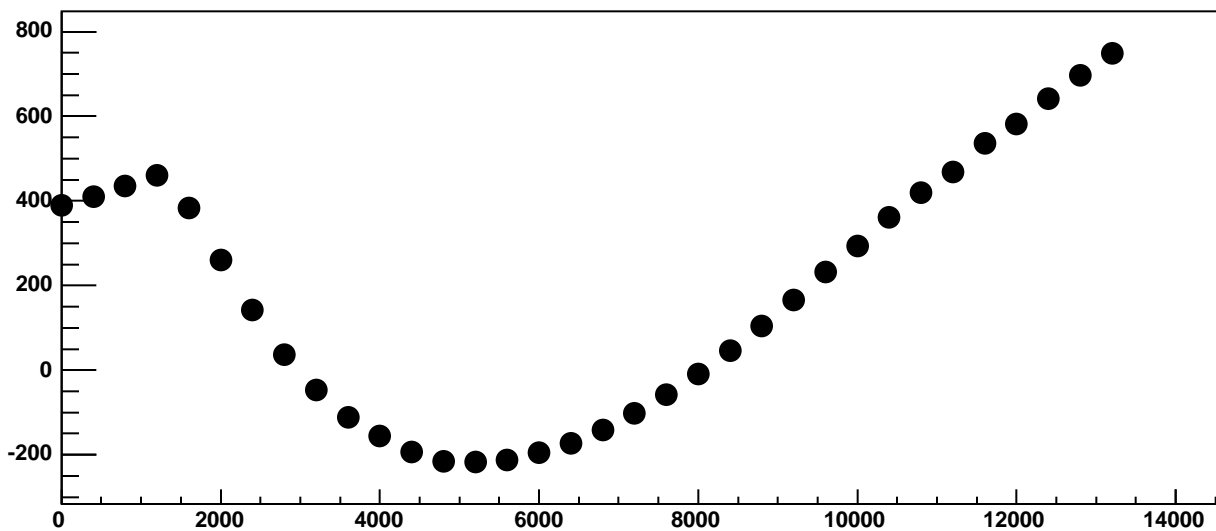


$\chi^2 / \text{ndf}$  2.394e+05 / 23  
p0 -878.3 ± 1.05  
p1 -0.04832 ± 0.0001705

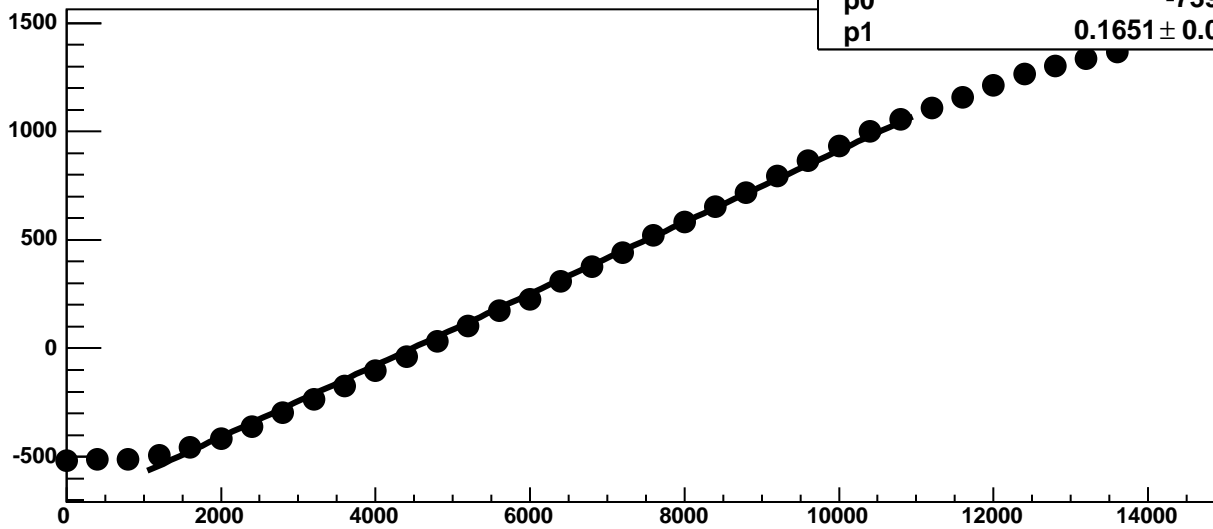
Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



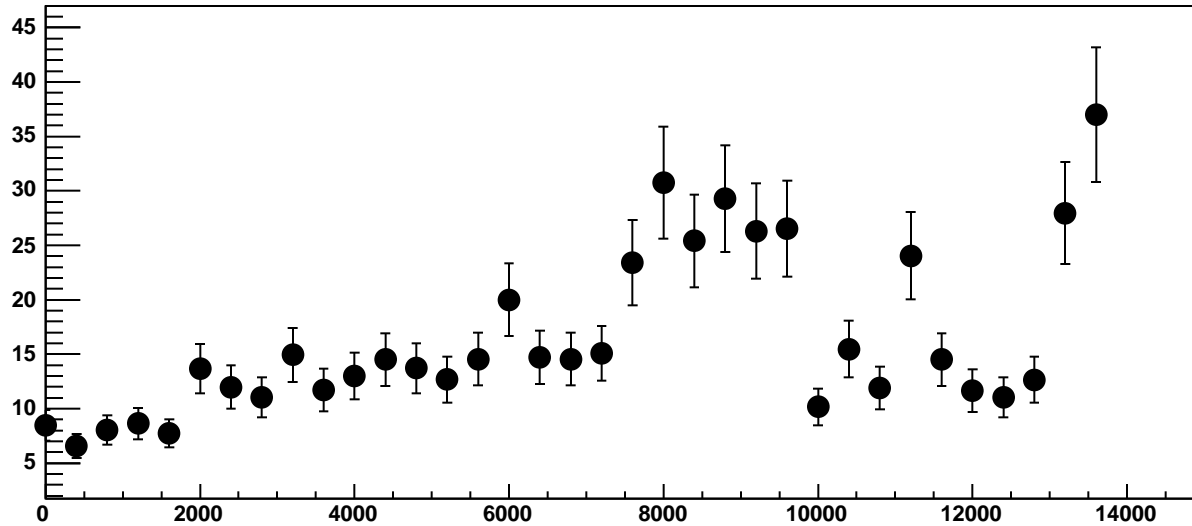
Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



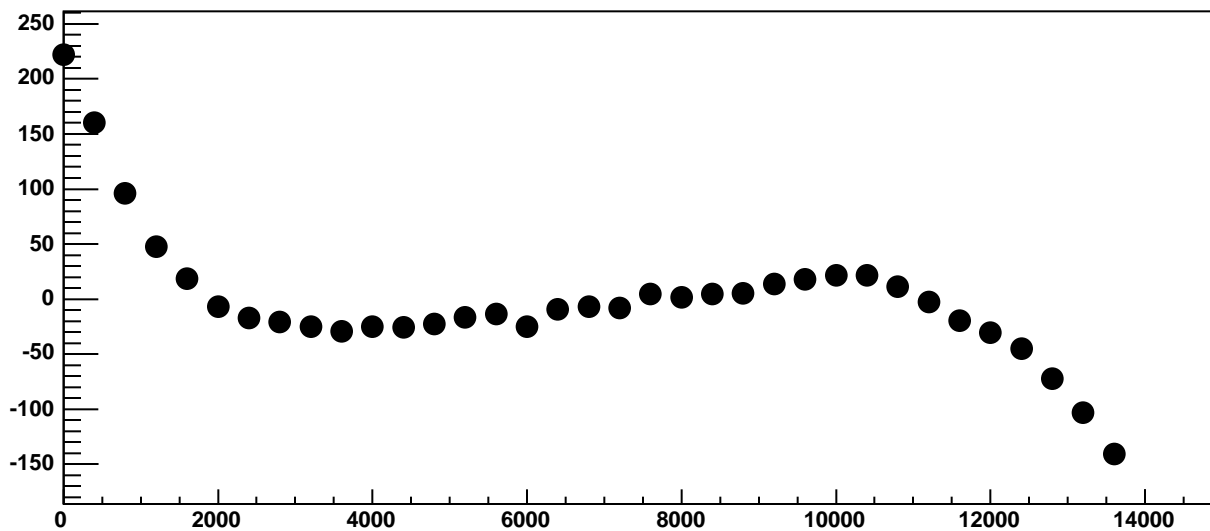
Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

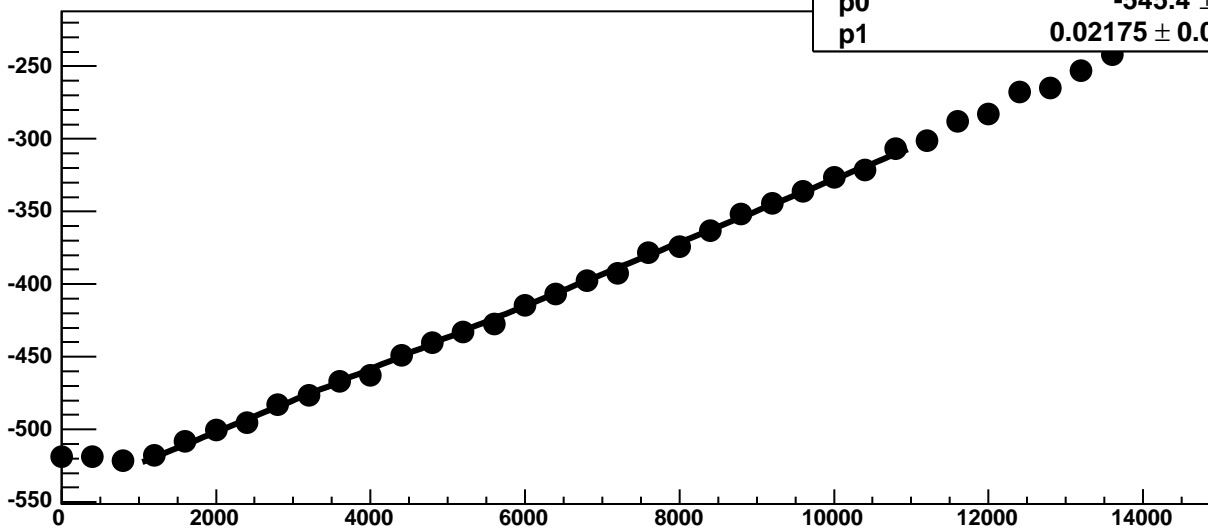


Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



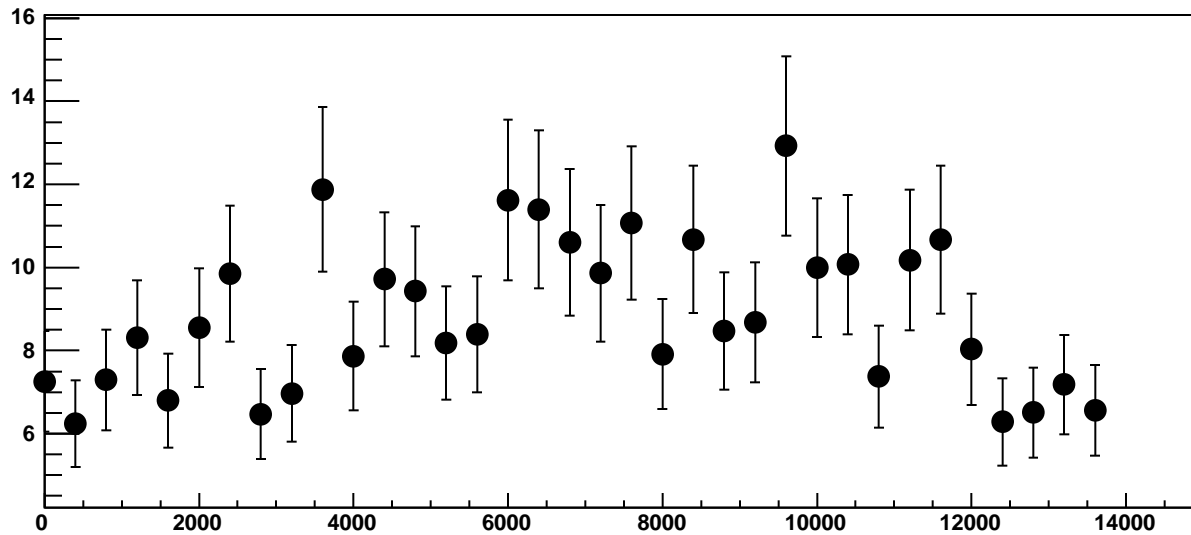


Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

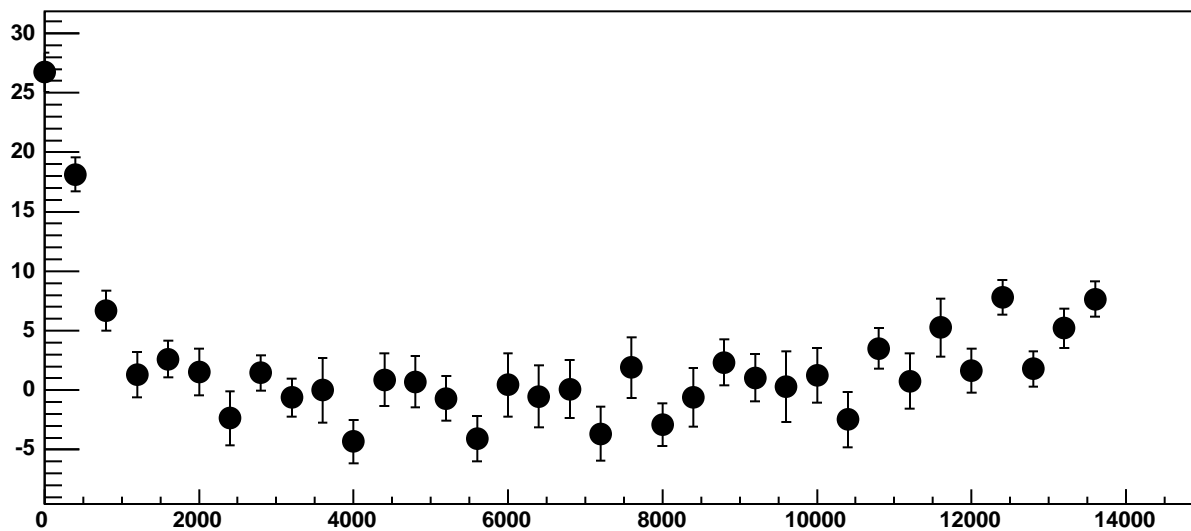


$\chi^2 / \text{ndf}$  30.05 / 23  
p0 -545.4 ± 0.8607  
p1 0.02175 ± 0.0001353

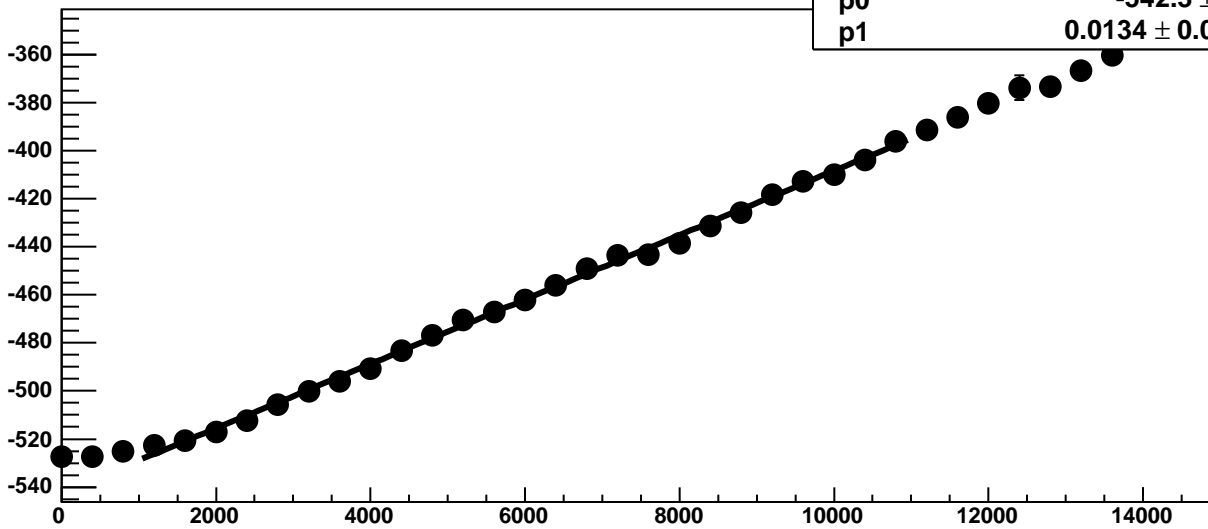
Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

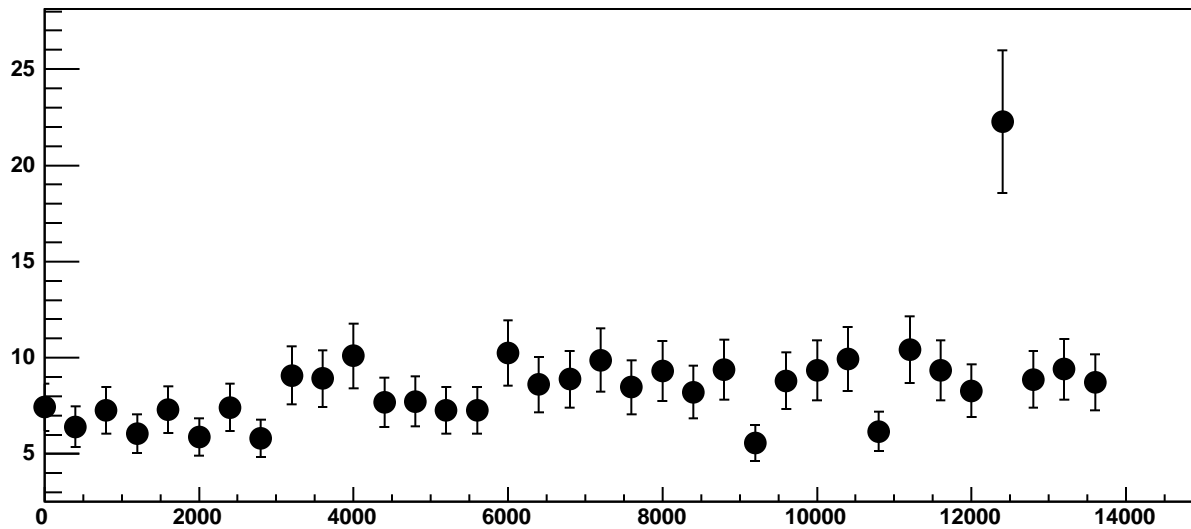


Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

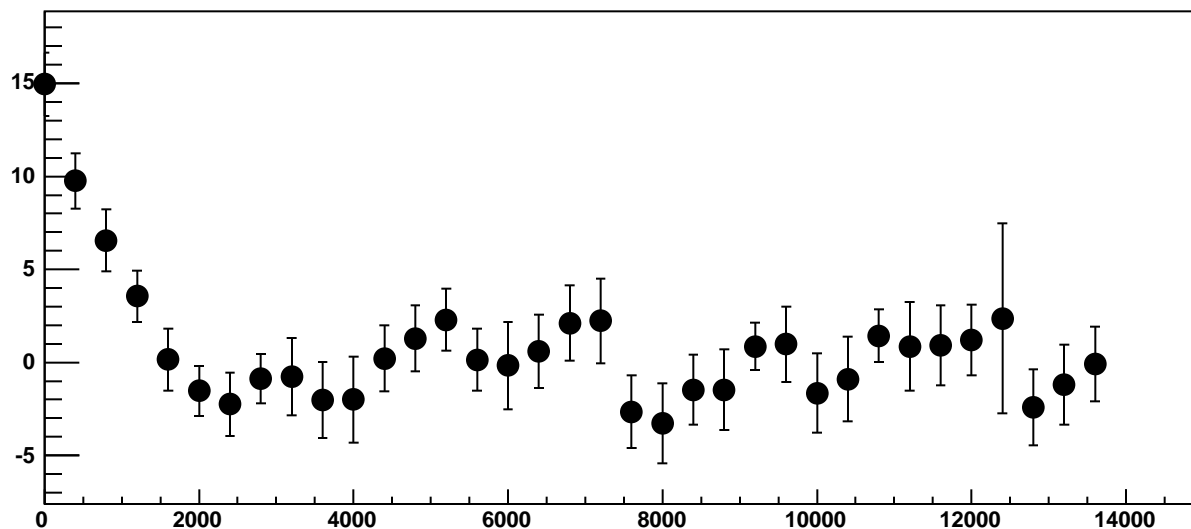


$\chi^2 / \text{ndf}$  24.25 / 23  
p0  $-542.3 \pm 0.7402$   
p1  $0.0134 \pm 0.0001146$

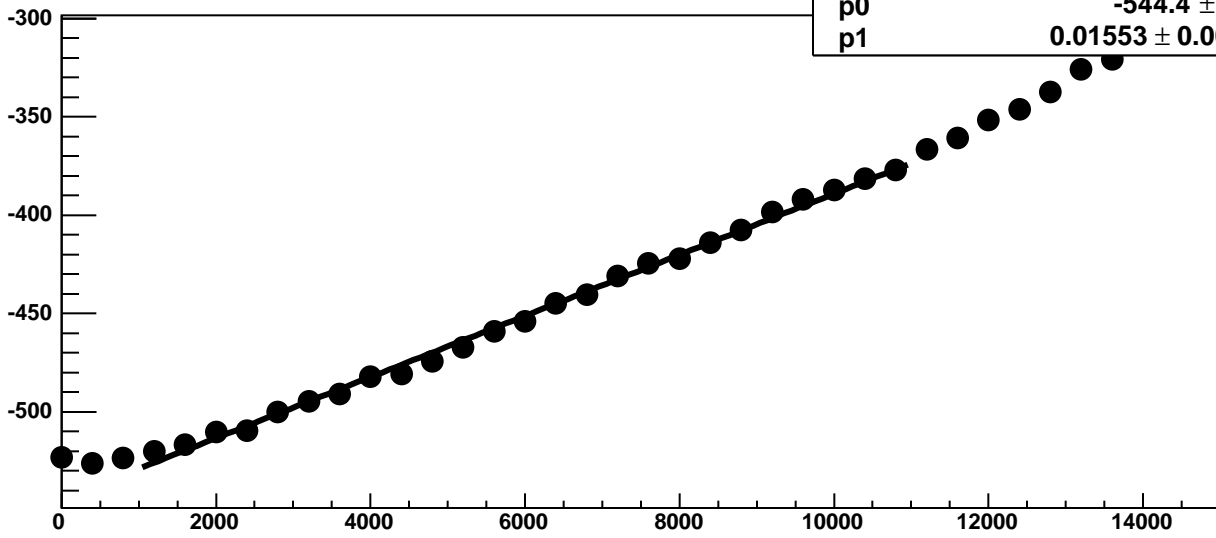
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



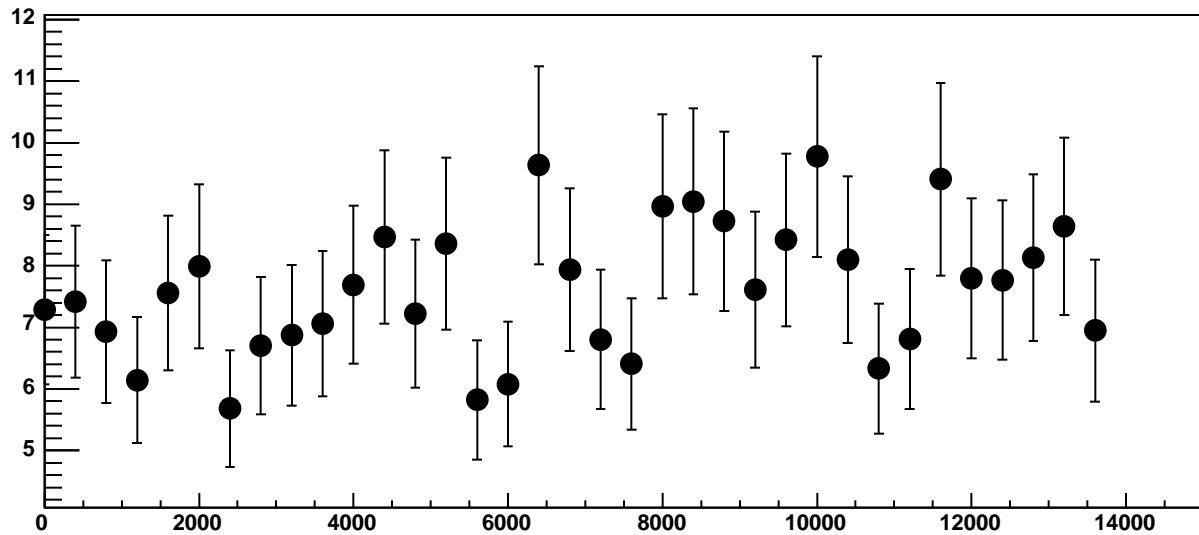
Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



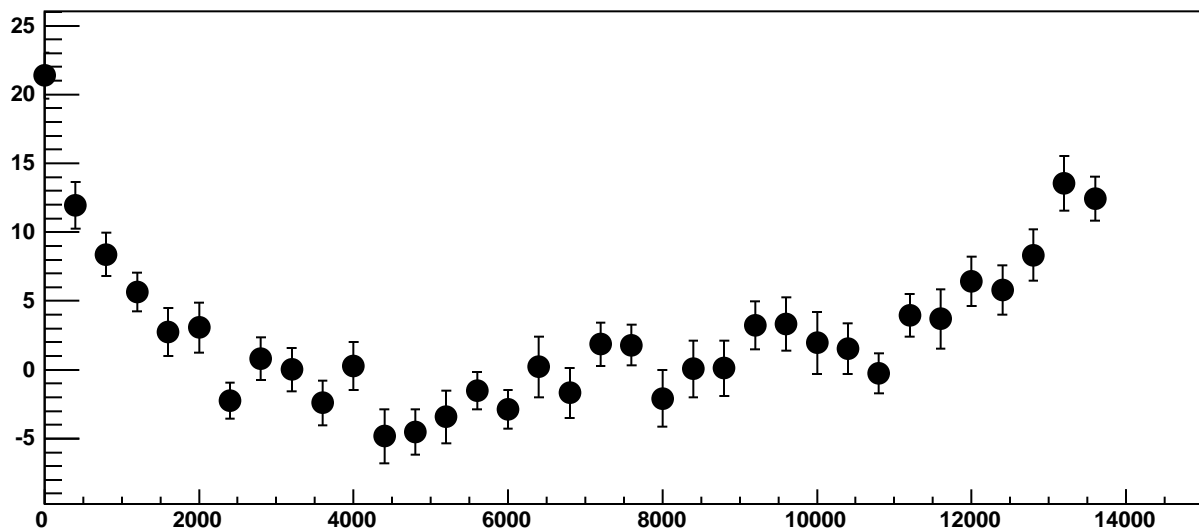
Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



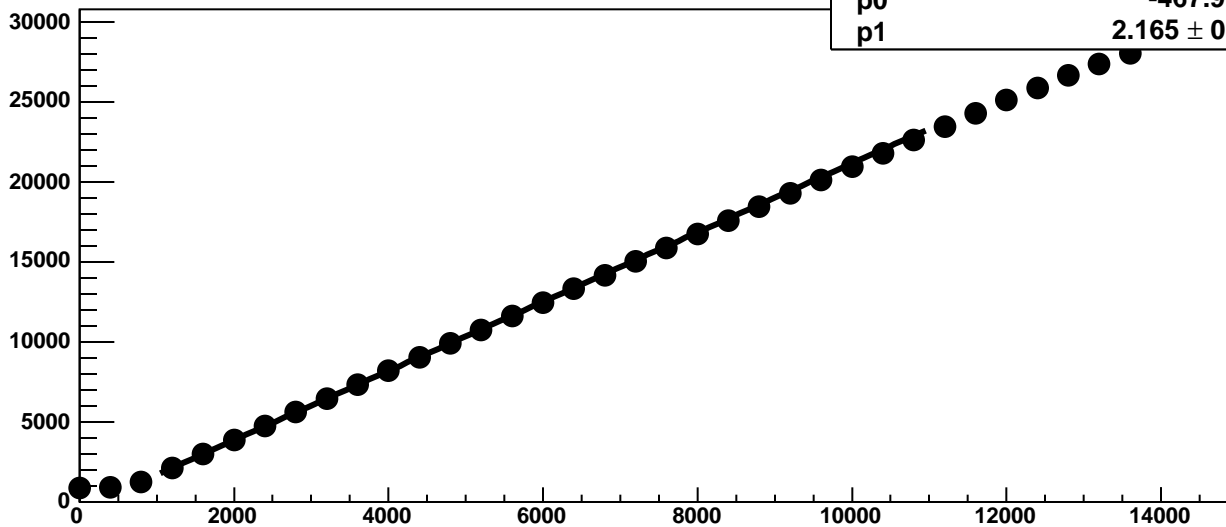
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

221 / 23

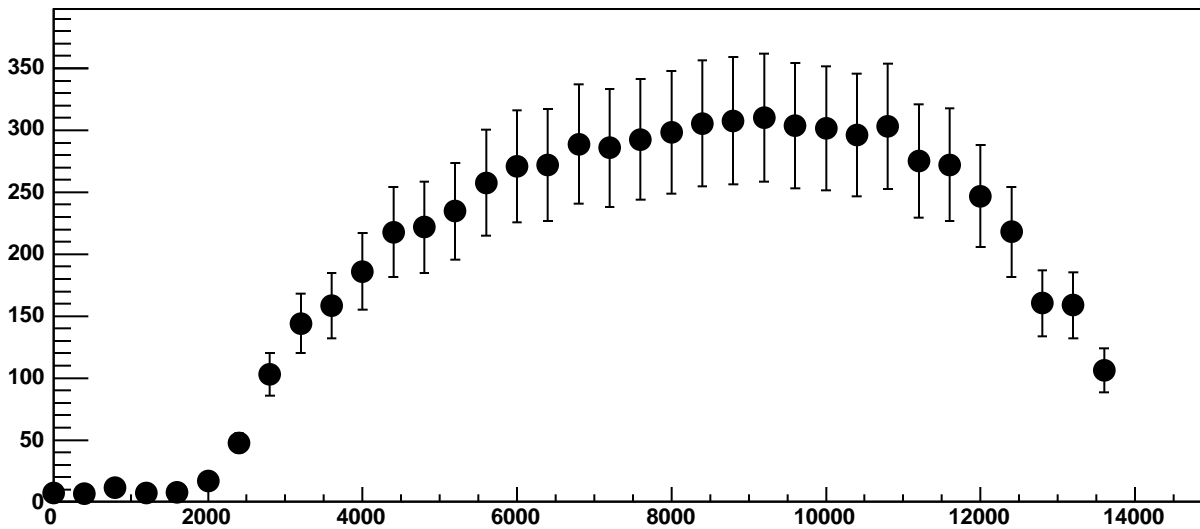
p0

$-467.9 \pm 3.313$

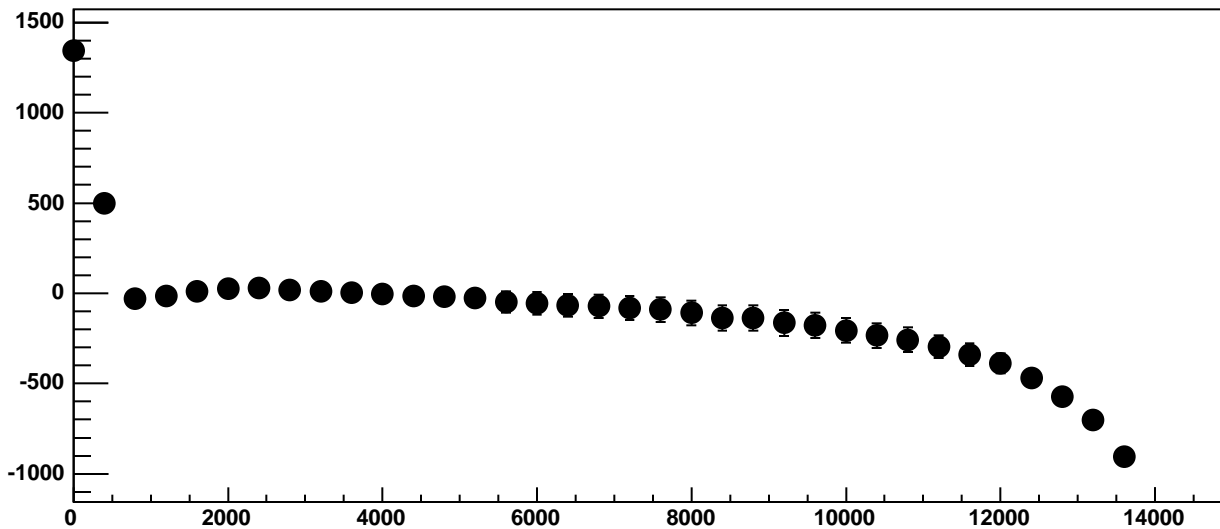
p1

$2.165 \pm 0.002092$

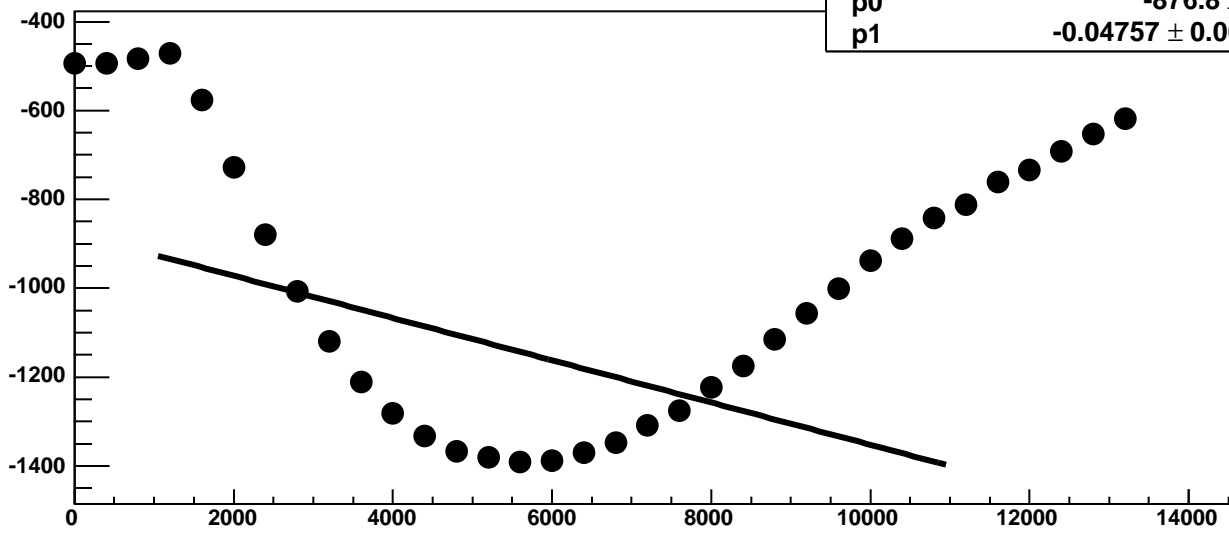
Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

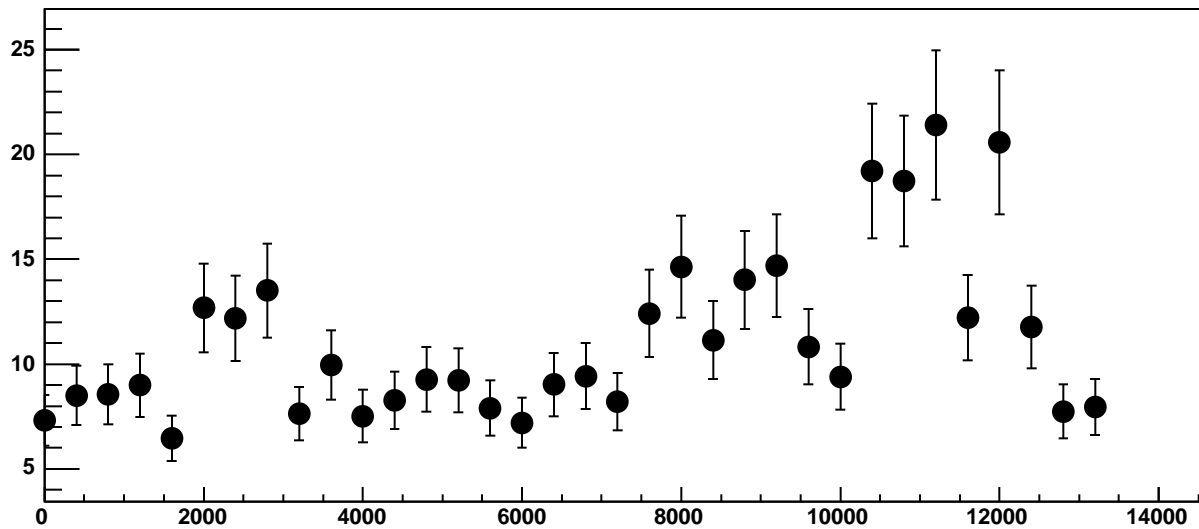


Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

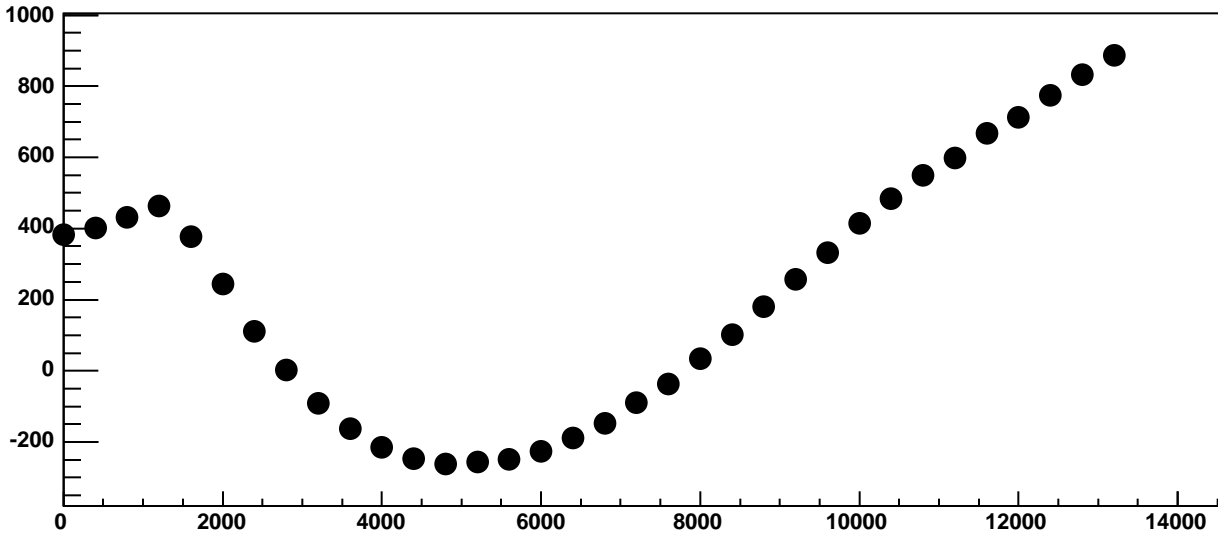


$\chi^2 / \text{ndf}$  3.403e+05 / 23  
p0 -876.8 ± 1.008  
p1 -0.04757 ± 0.0001714

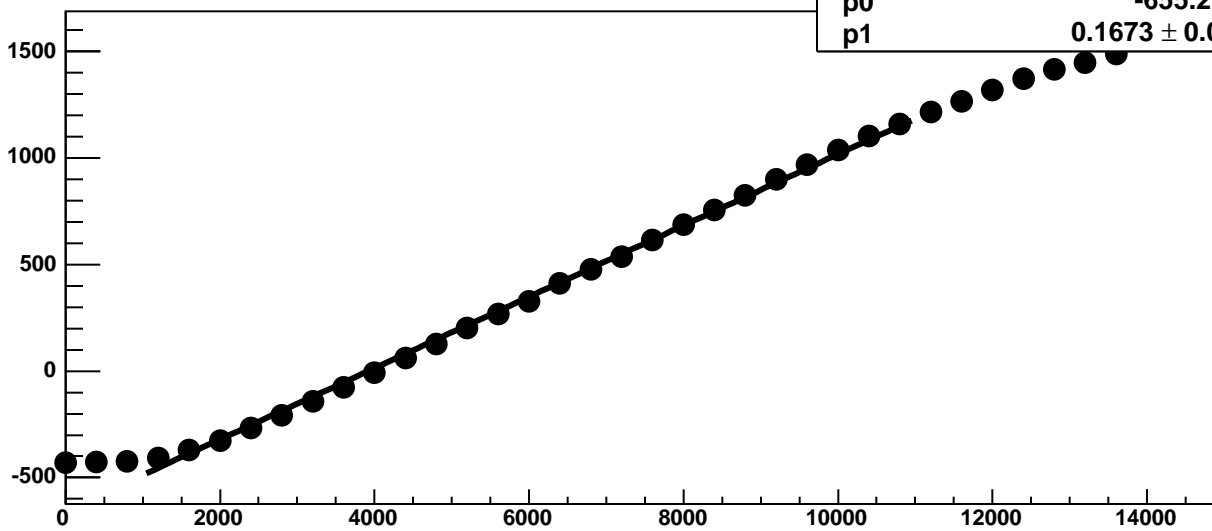
Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



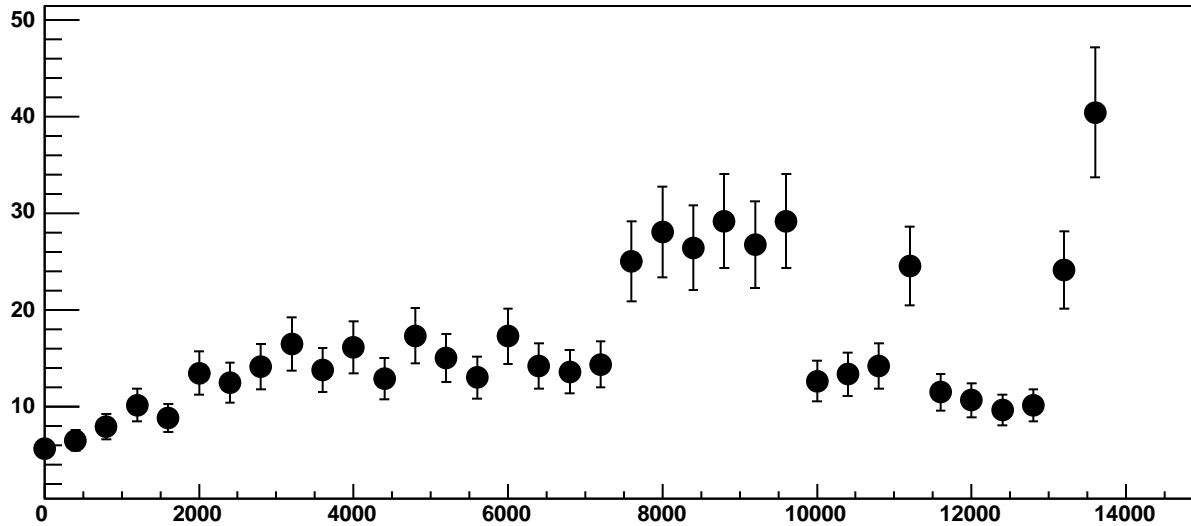
Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



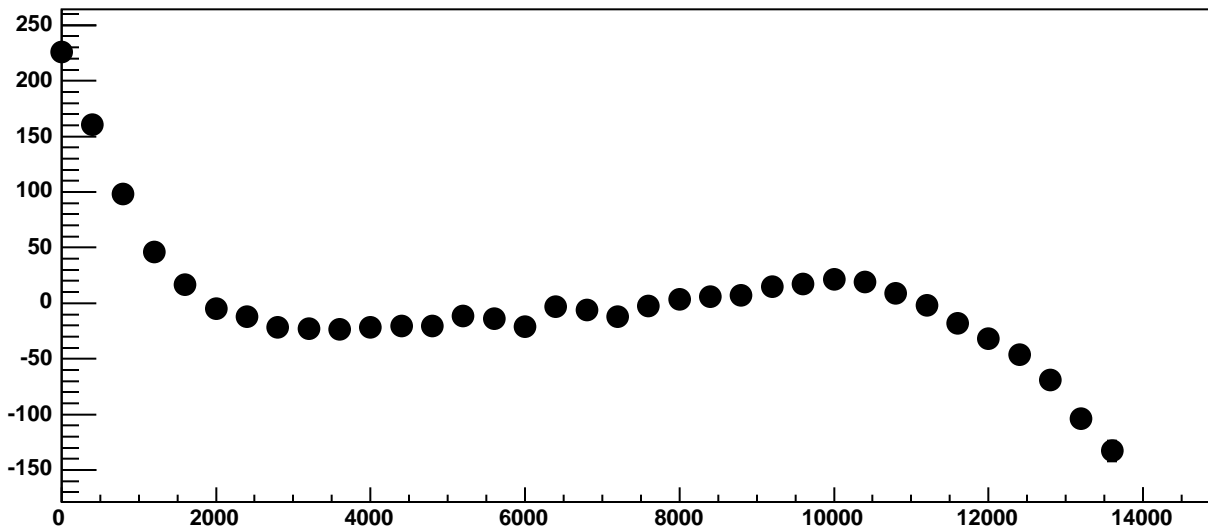
Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



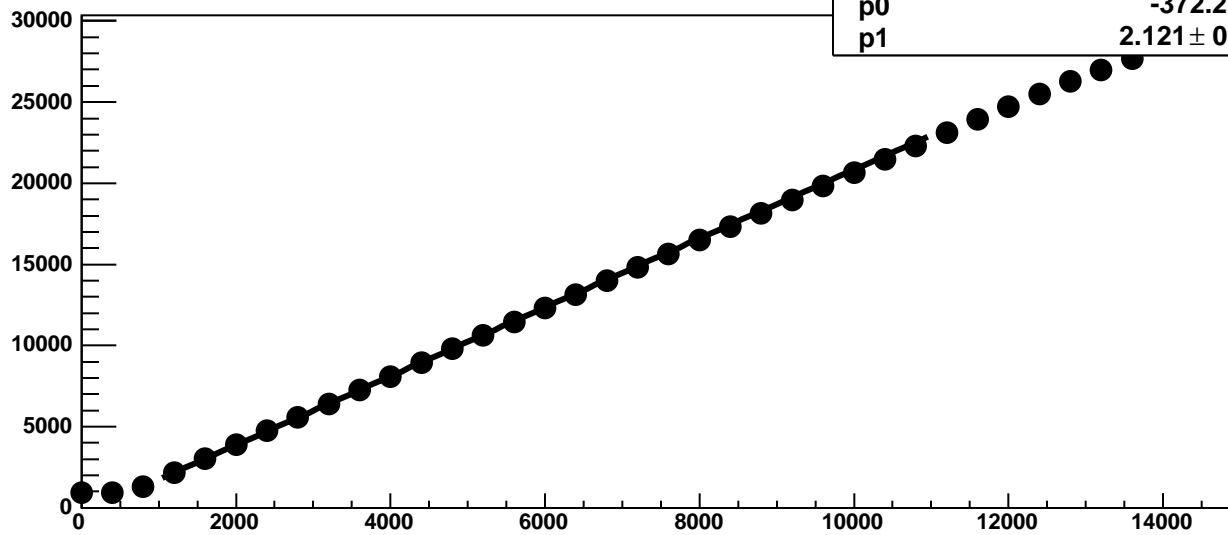
Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



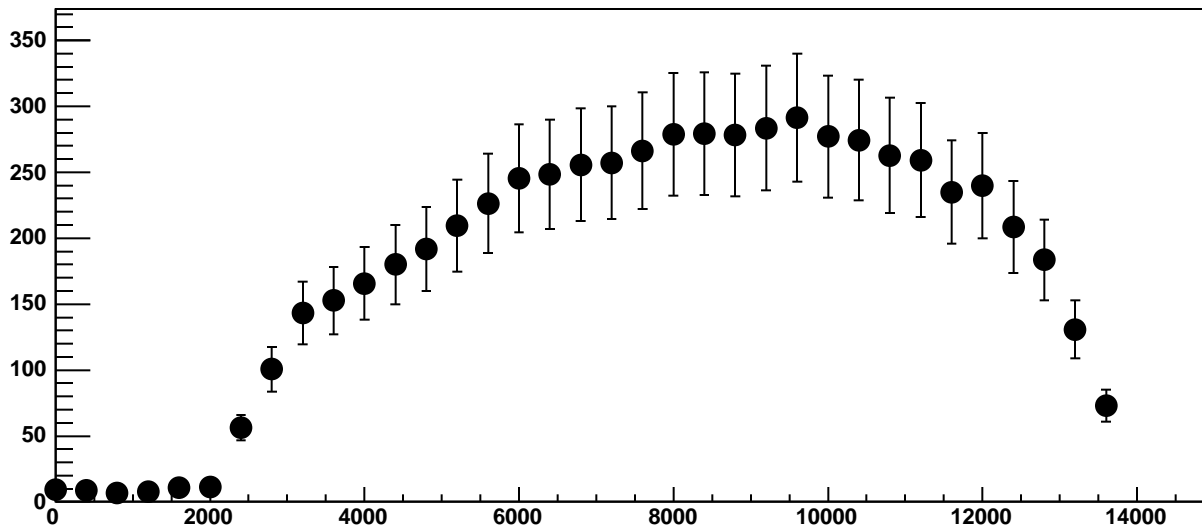
Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



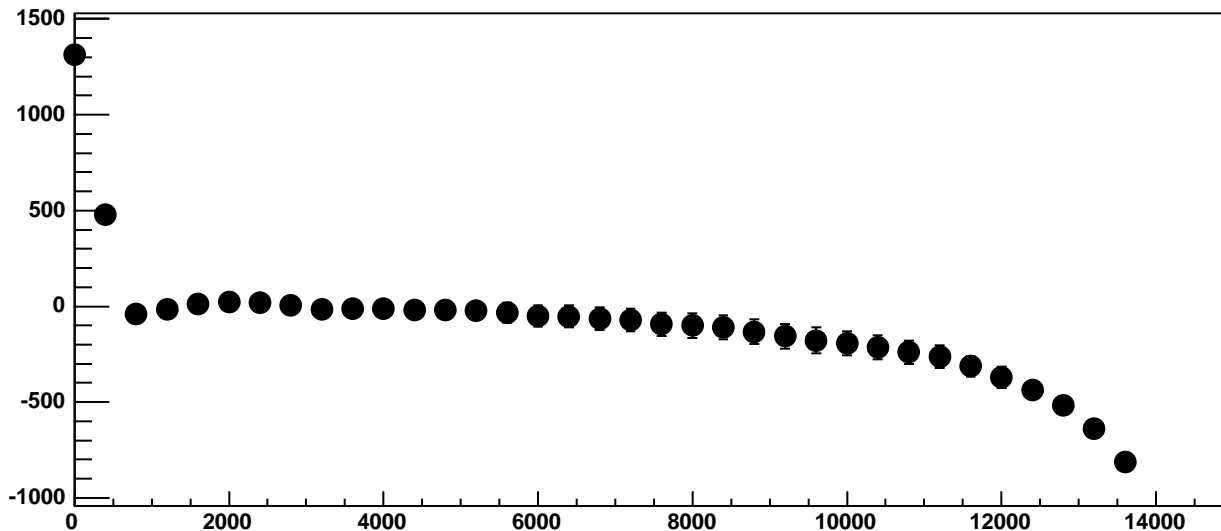
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



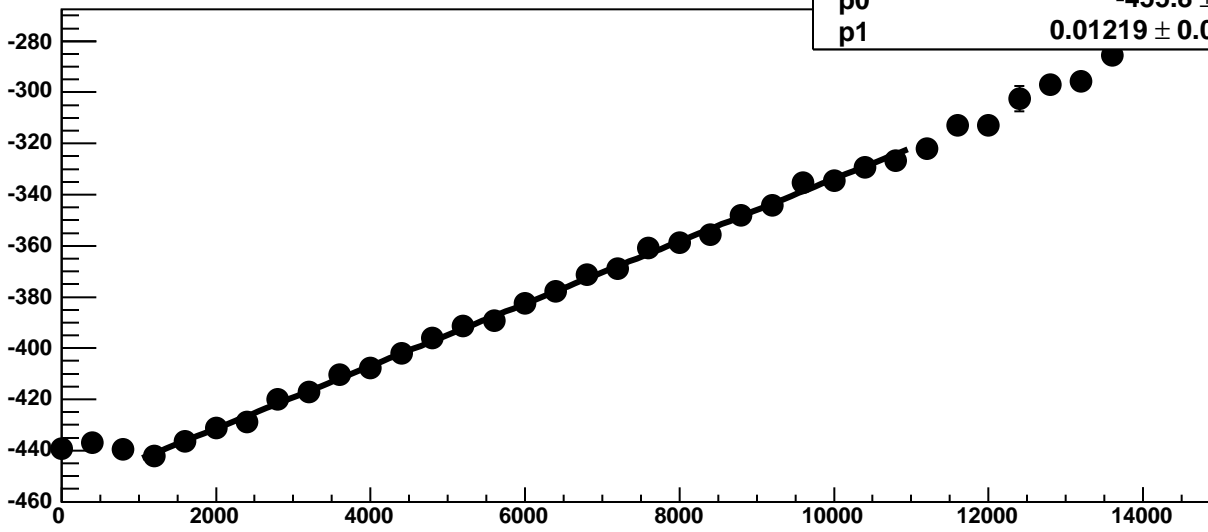
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



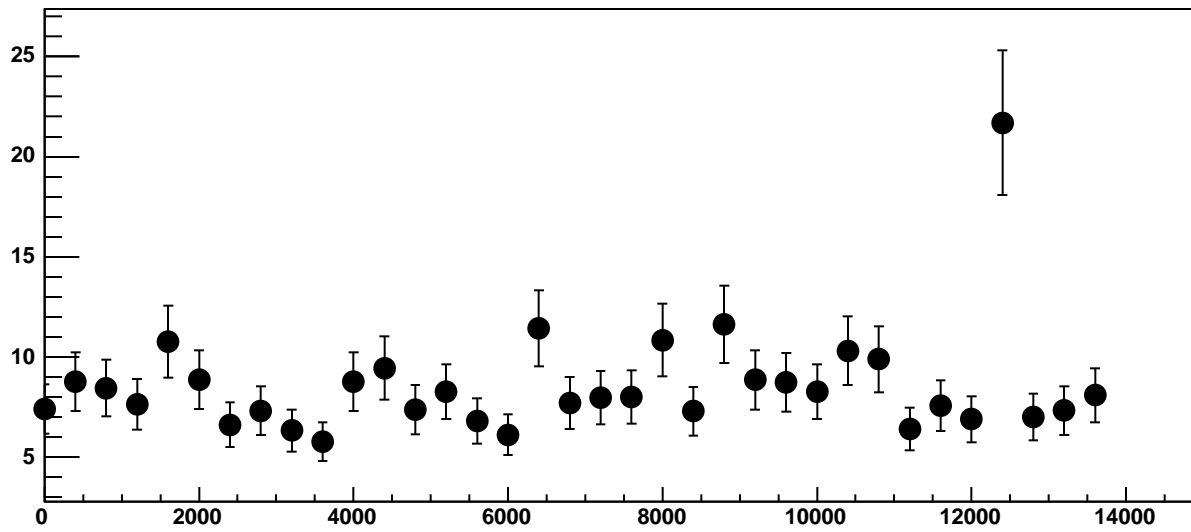
Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



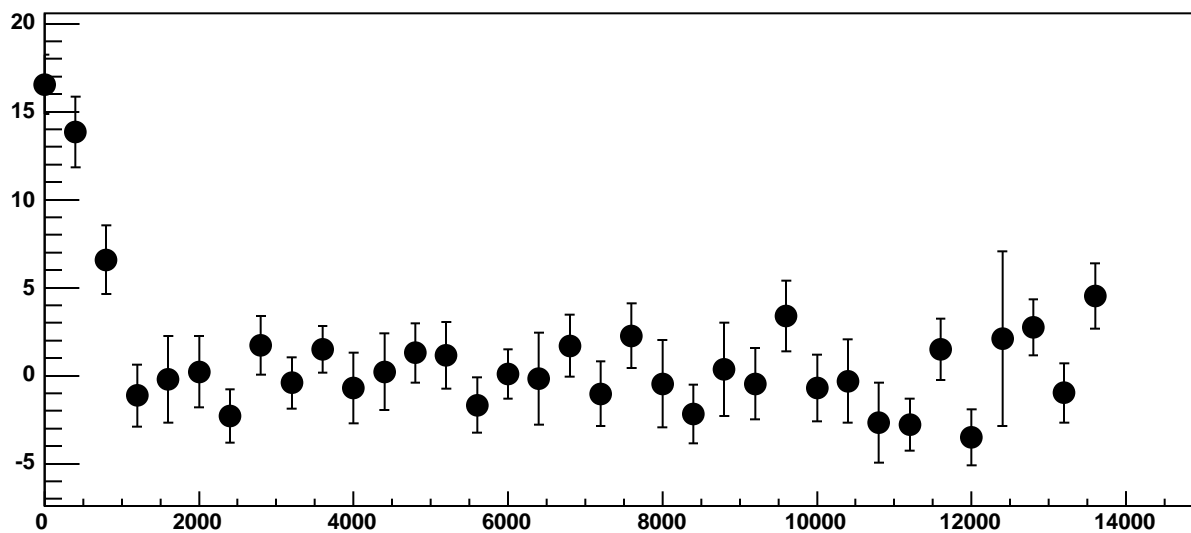
Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

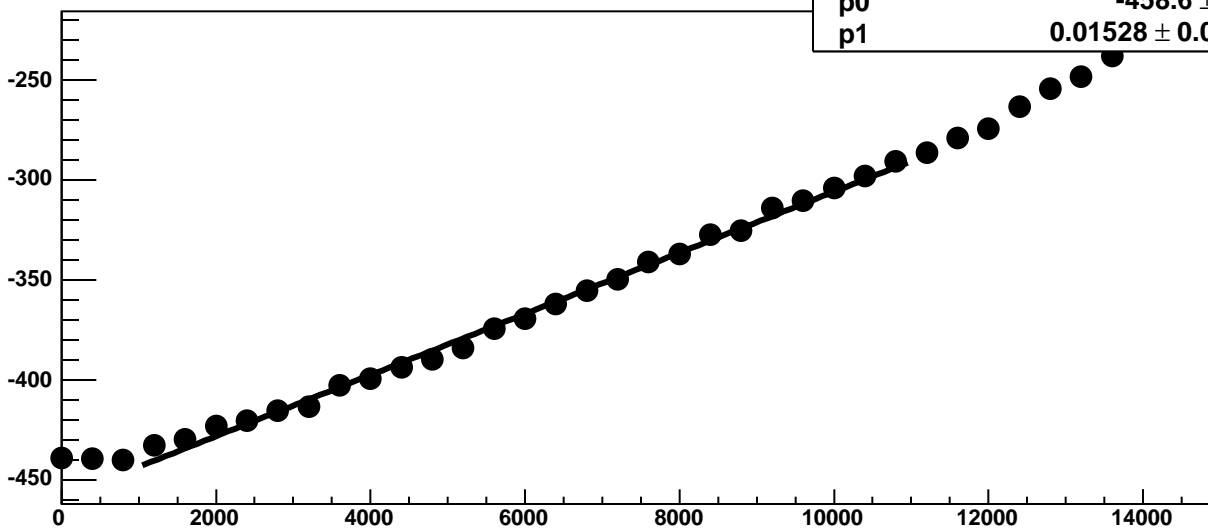


Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



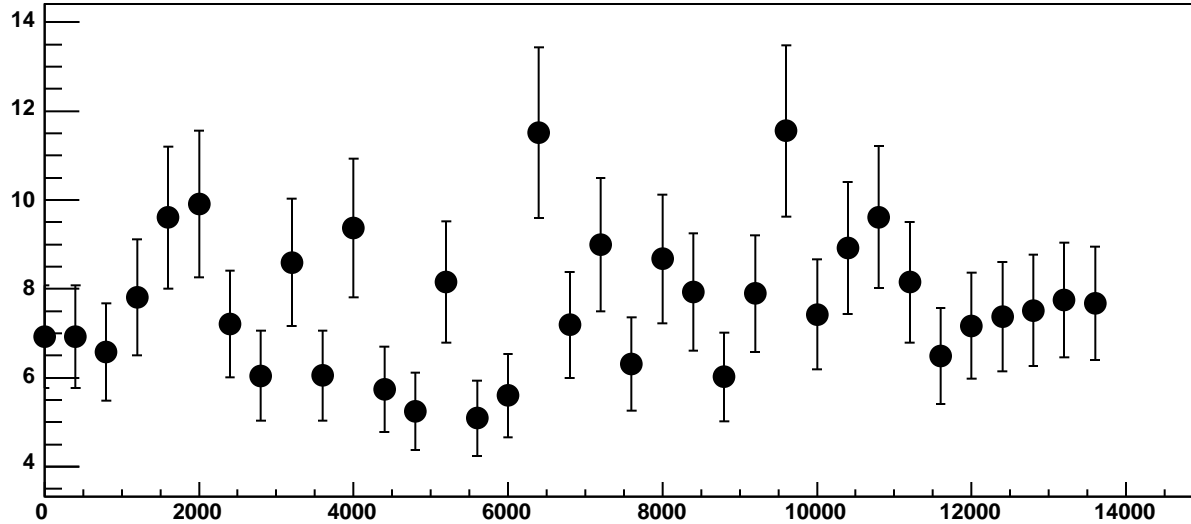


Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

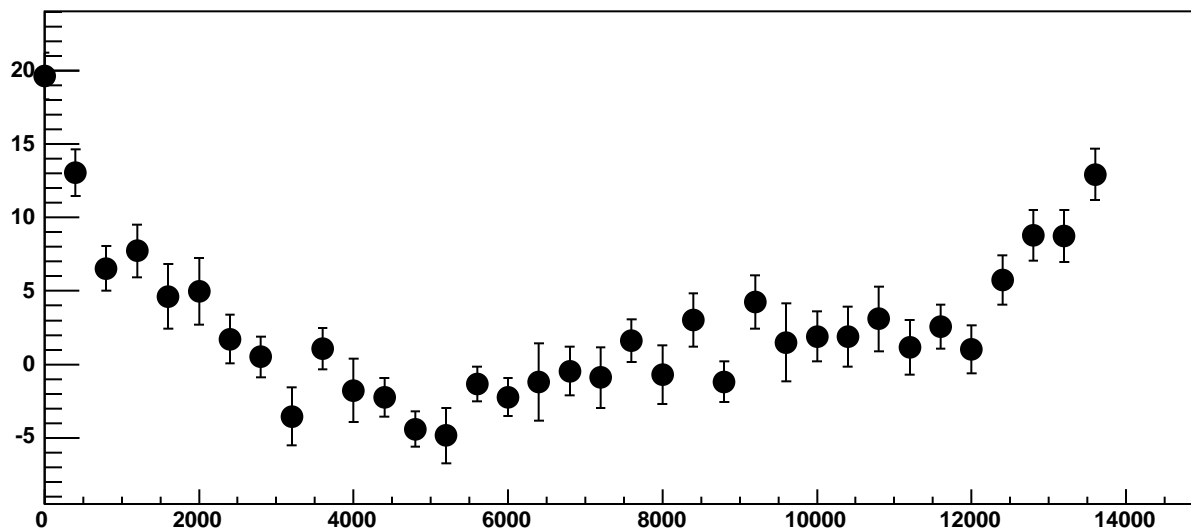


$\chi^2 / \text{ndf}$  76.45 / 23  
p0  $-458.6 \pm 0.8128$   
p1  $0.01528 \pm 0.0001279$

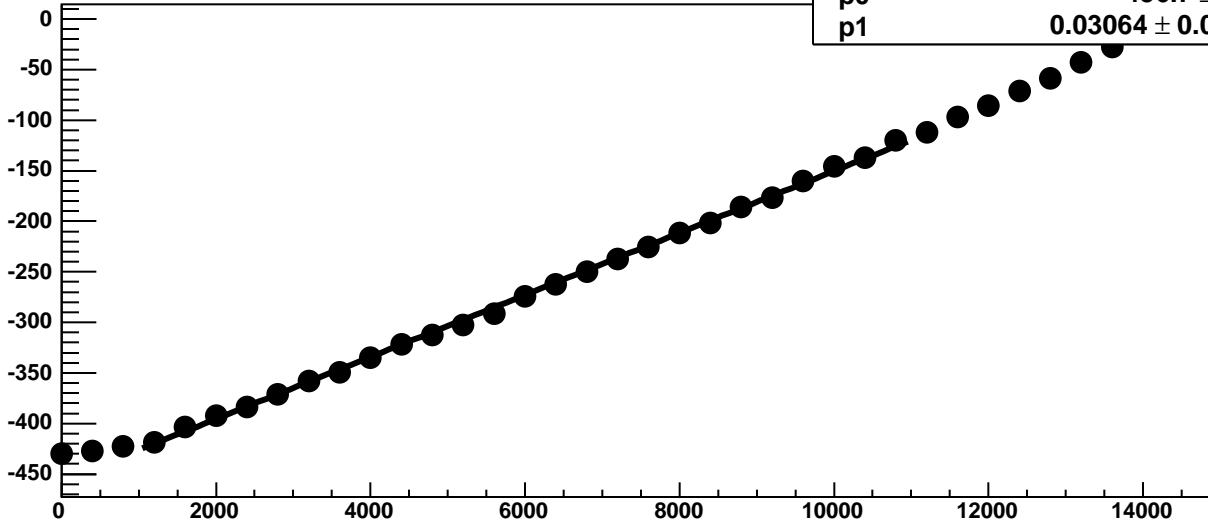
Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

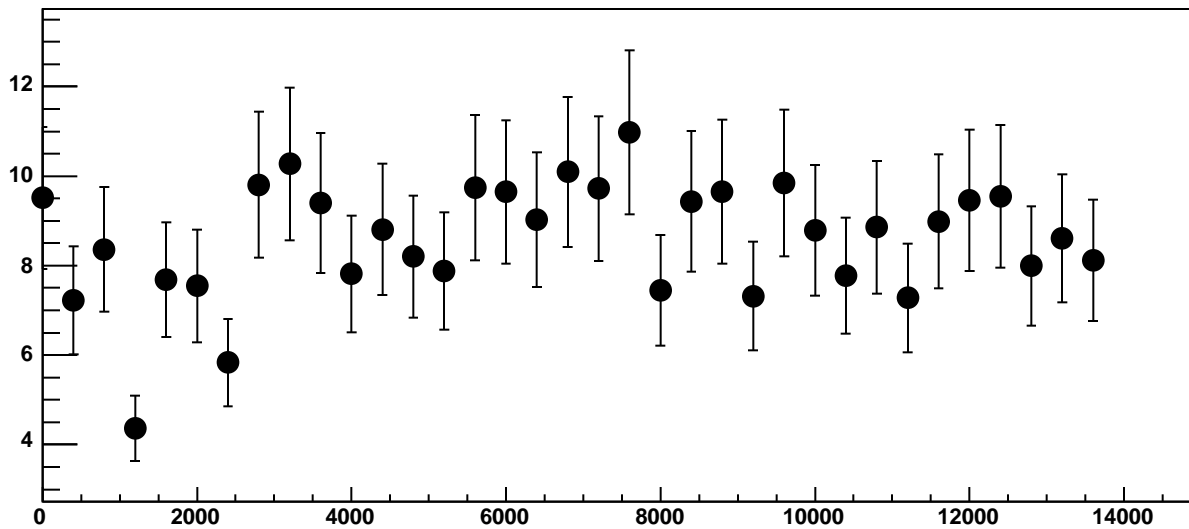


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

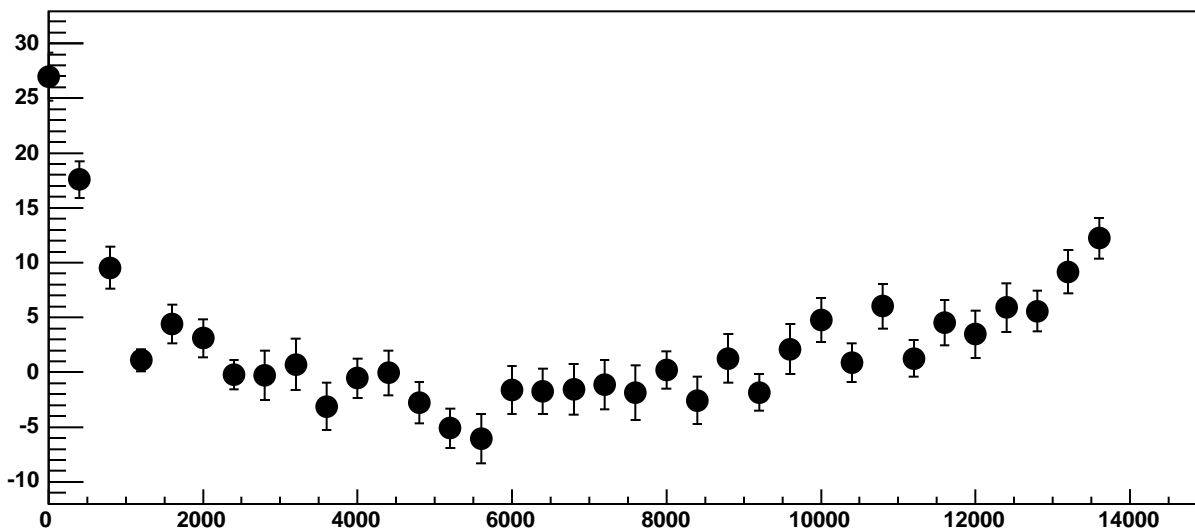


$\chi^2 / \text{ndf}$  51.34 / 23  
p0  $-456.7 \pm 0.7263$   
p1  $0.03064 \pm 0.0001176$

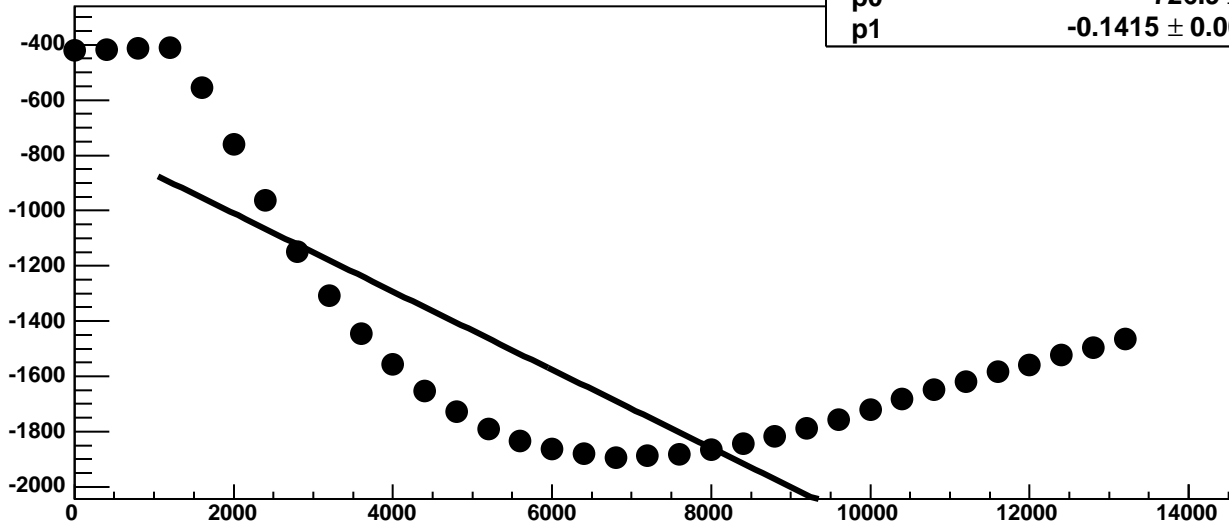
Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

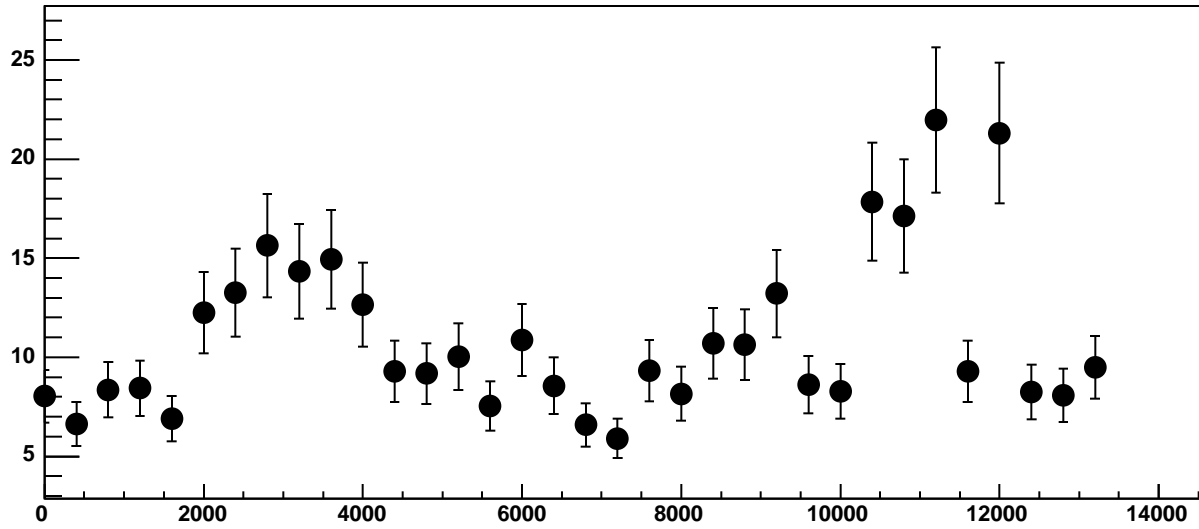


Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

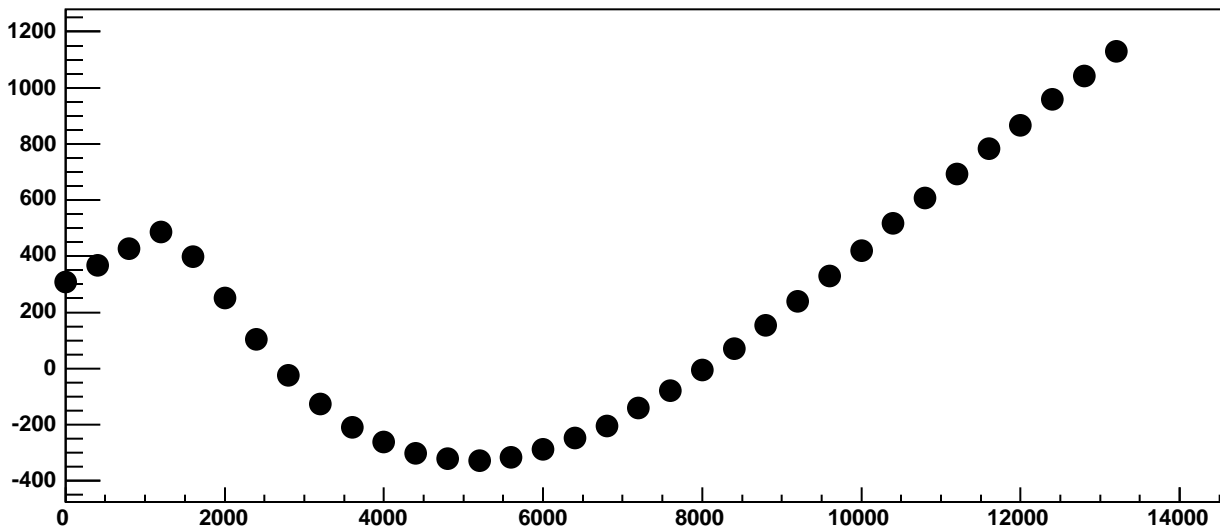


$\chi^2 / \text{ndf}$  4.33e+05 / 23  
p0 -726.9 ± 1.076  
p1 -0.1415 ± 0.0001635

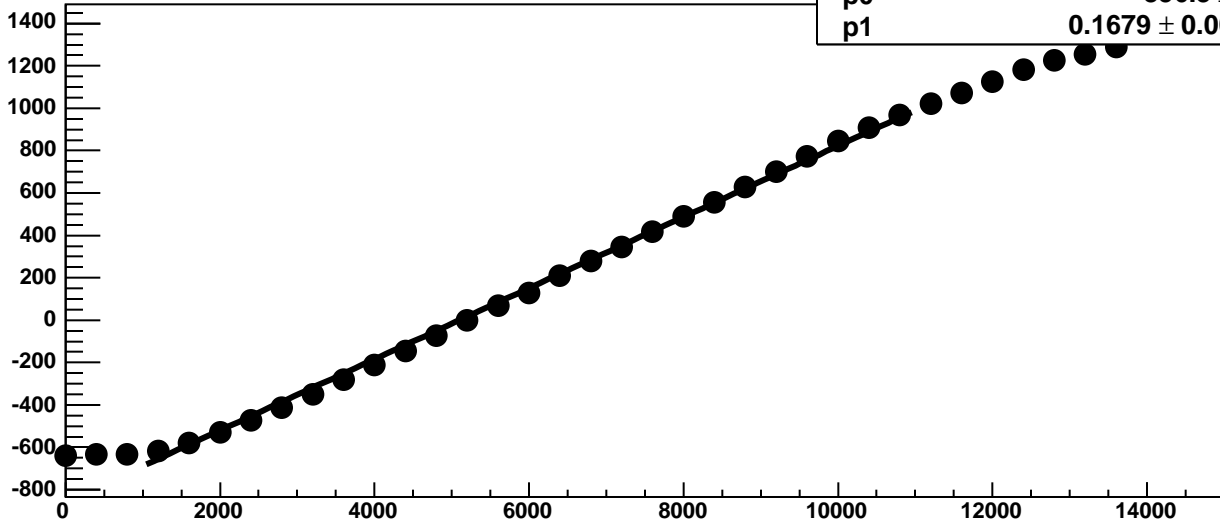
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

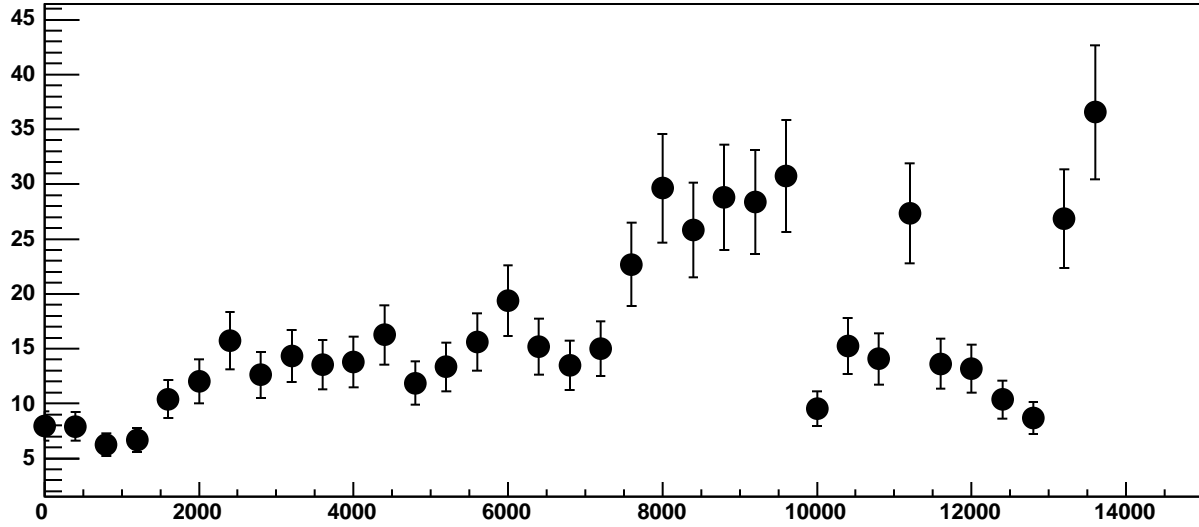


Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

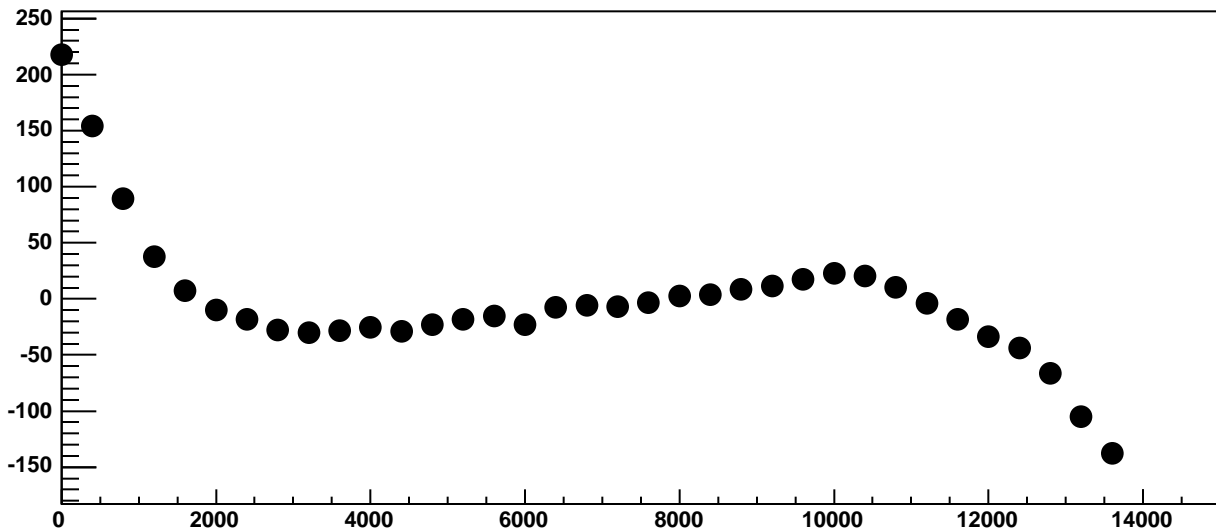


$\chi^2 / \text{ndf}$  1357 / 23  
p0  $-856.3 \pm 1.163$   
p1  $0.1679 \pm 0.0002008$

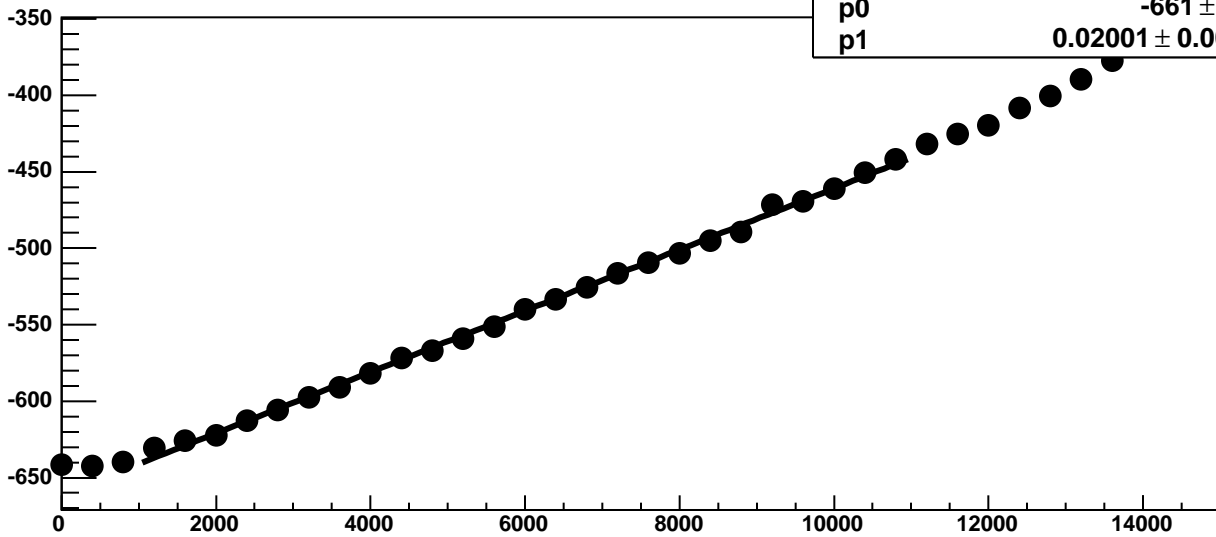
Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

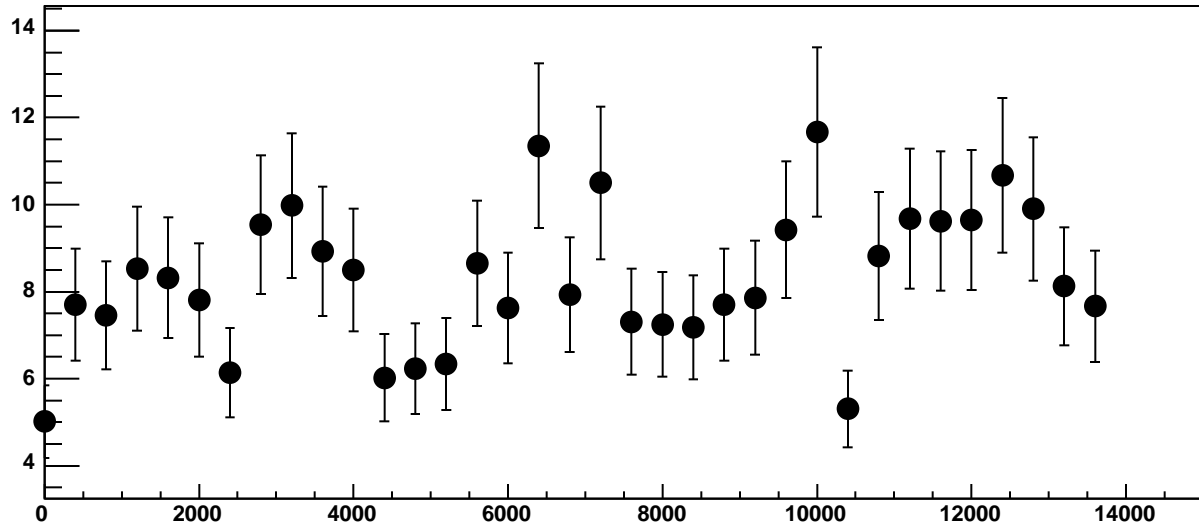


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

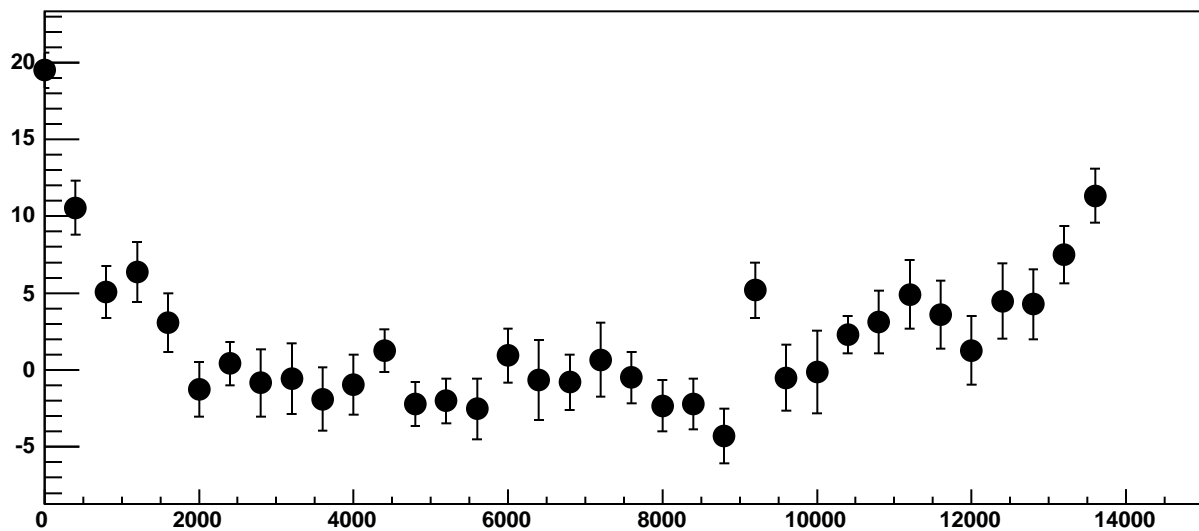


$\chi^2 / \text{ndf}$  46.5 / 23  
p0  $-661 \pm 0.8258$   
p1  $0.02001 \pm 0.0001235$

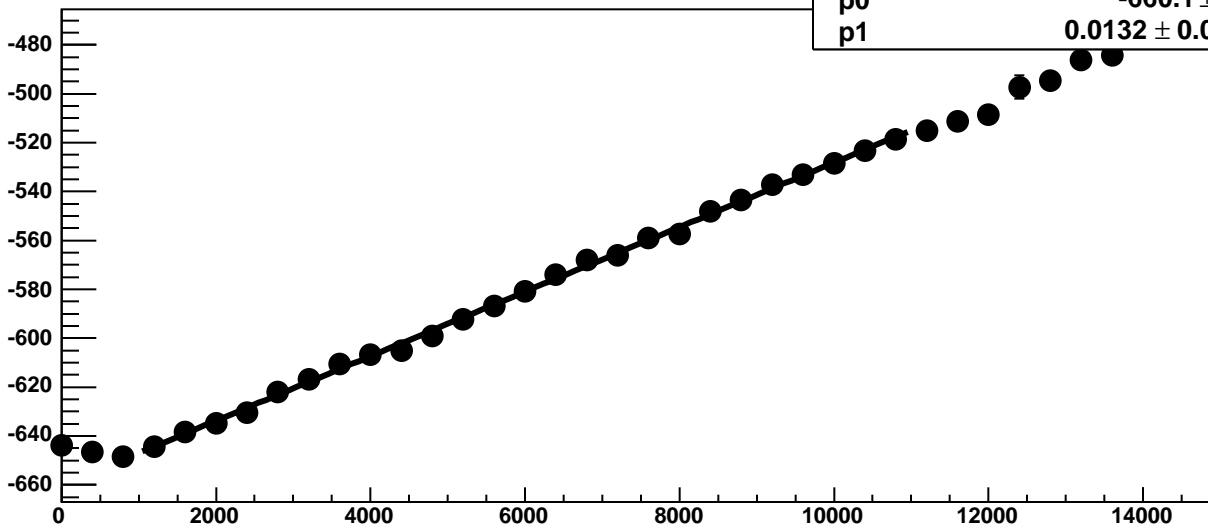
Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



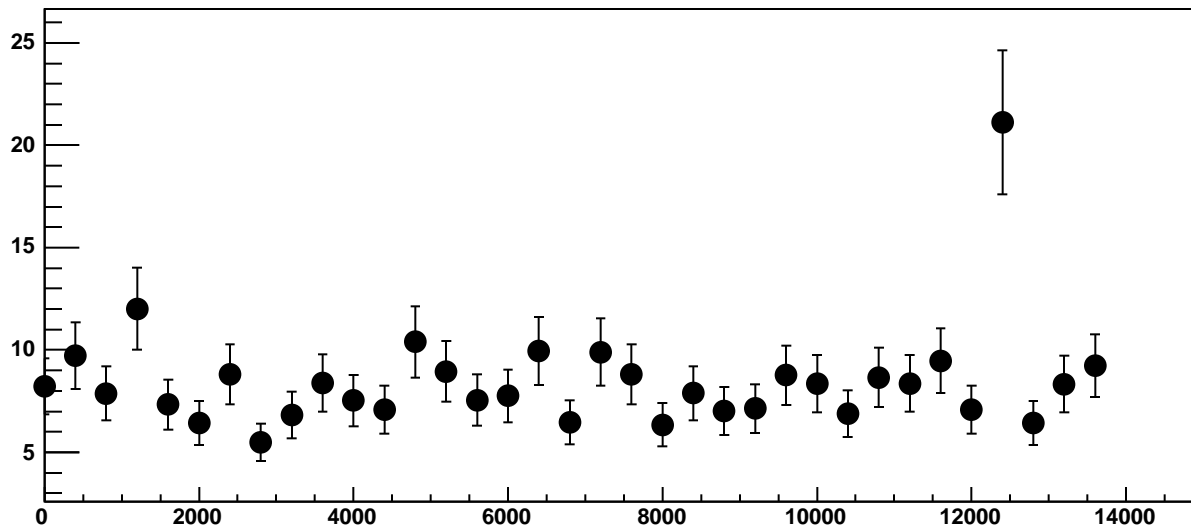
Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



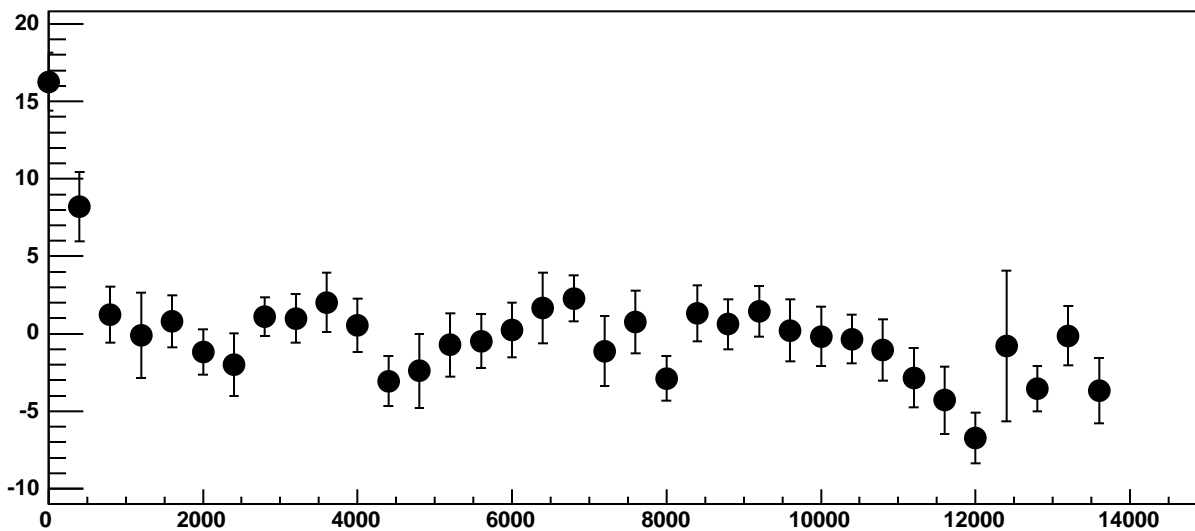
Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



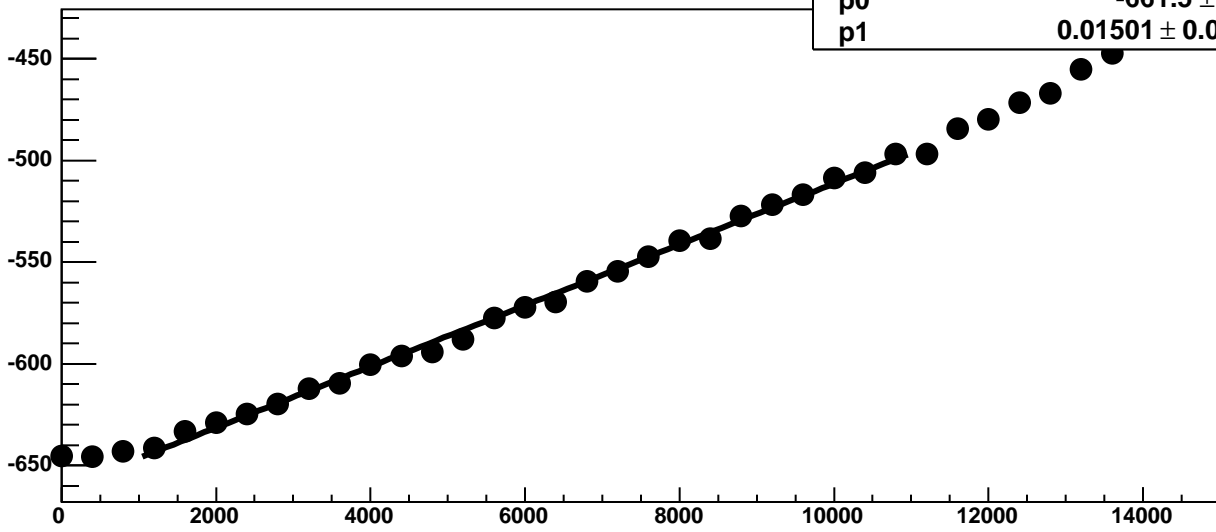
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



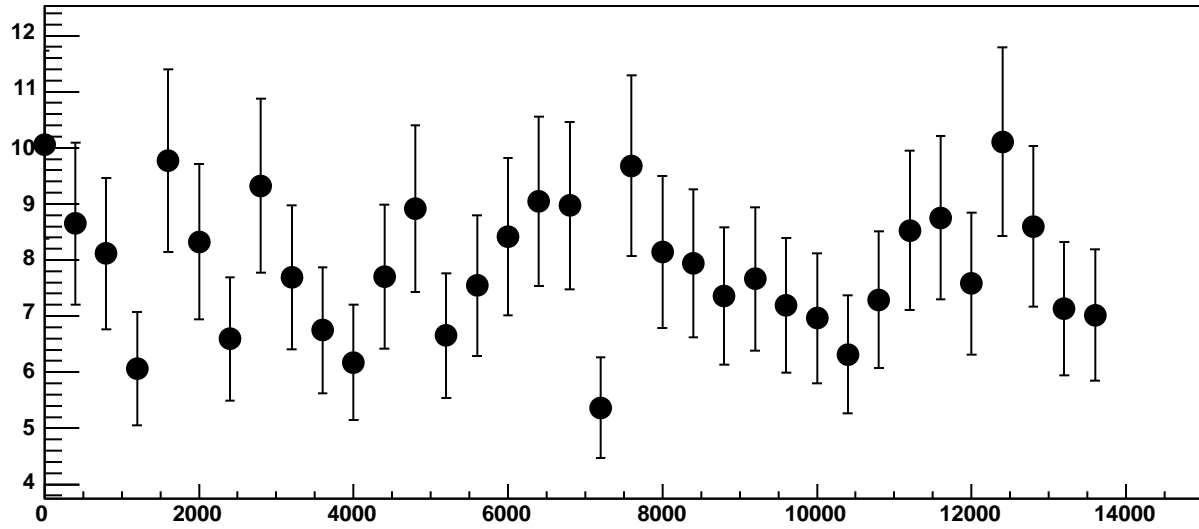
Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



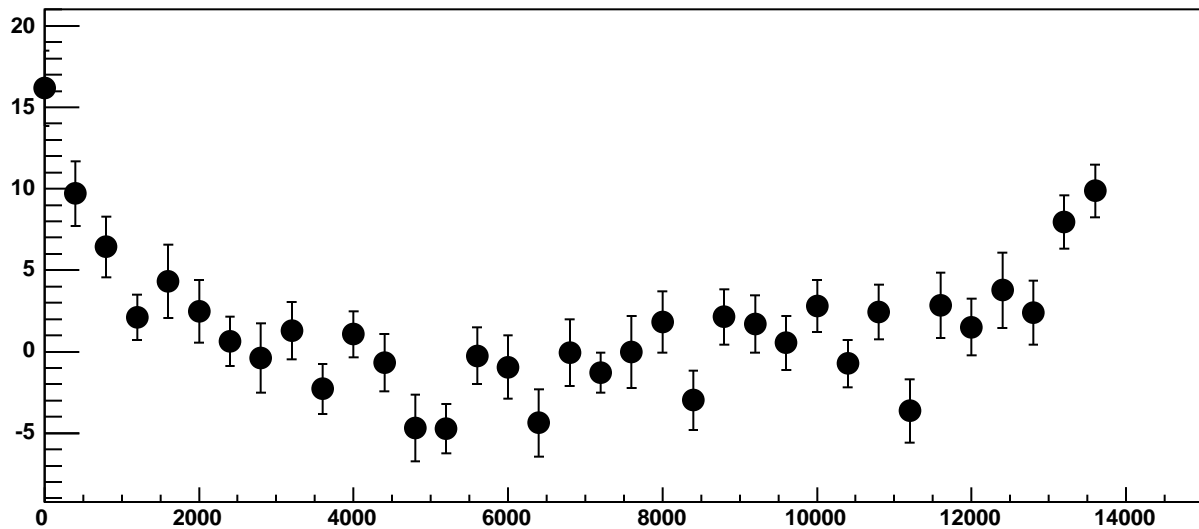
Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



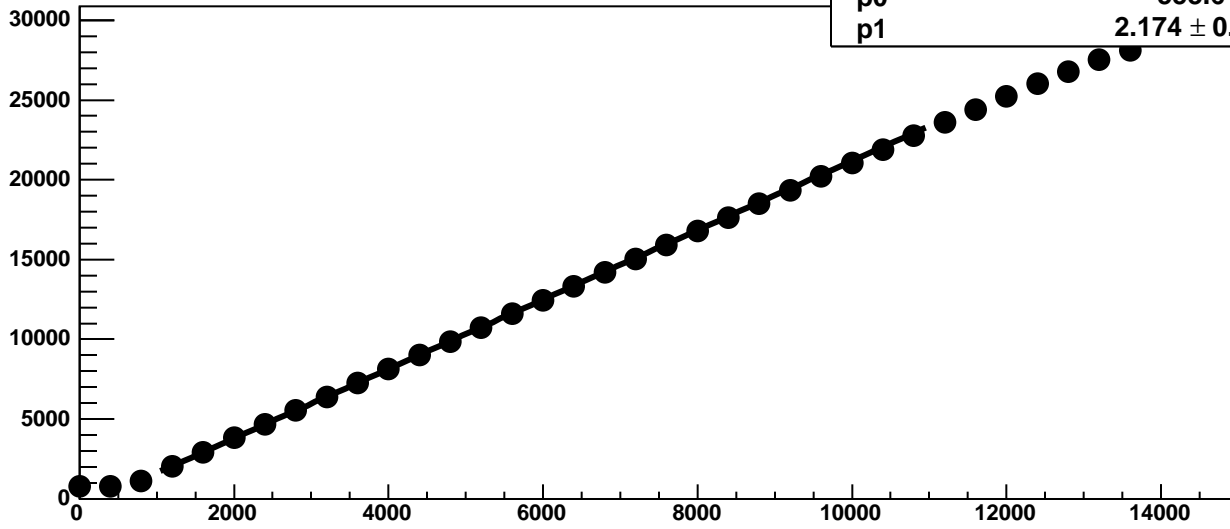
Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

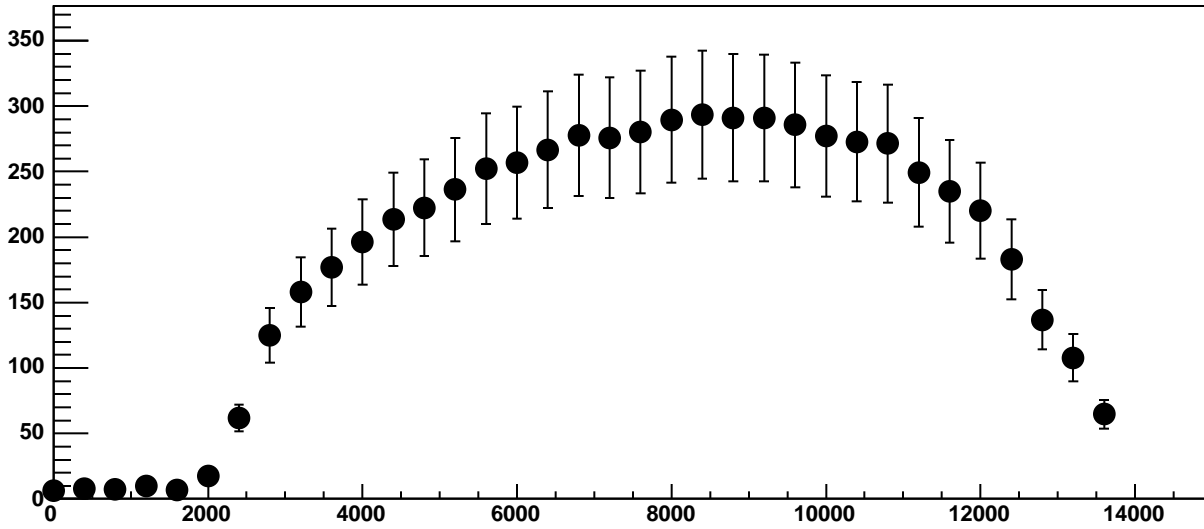


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

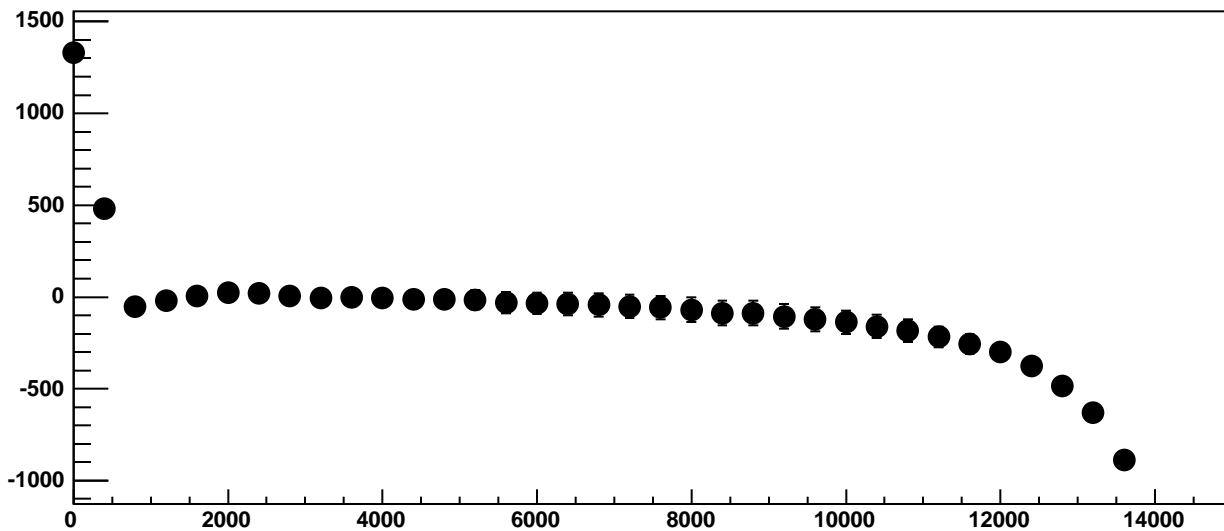


$\chi^2 / \text{ndf}$  165.7 / 23  
p0  $-553.6 \pm 3.535$   
p1  $2.174 \pm 0.002118$

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



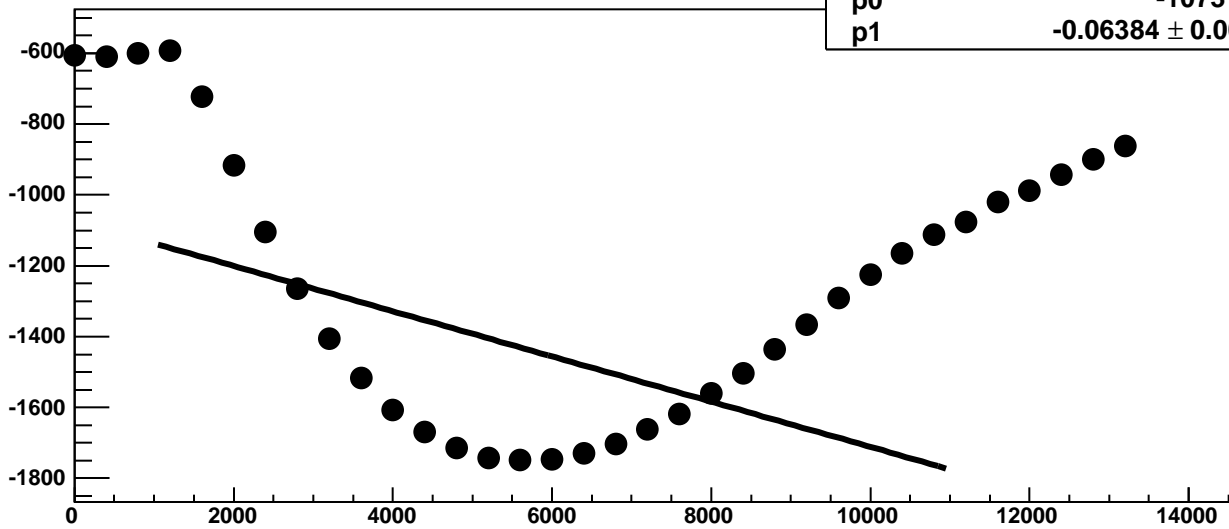
Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



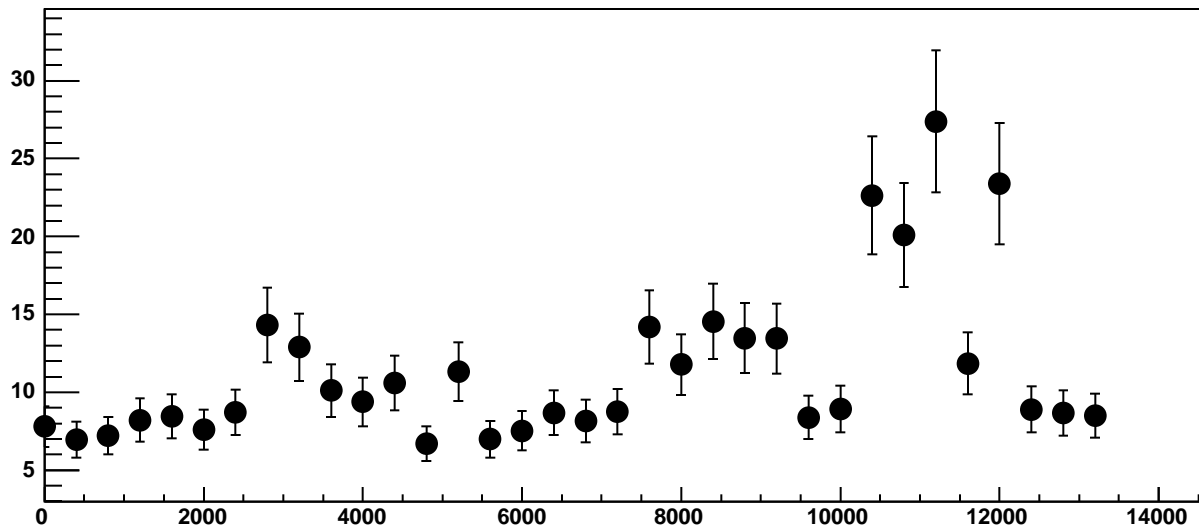


Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

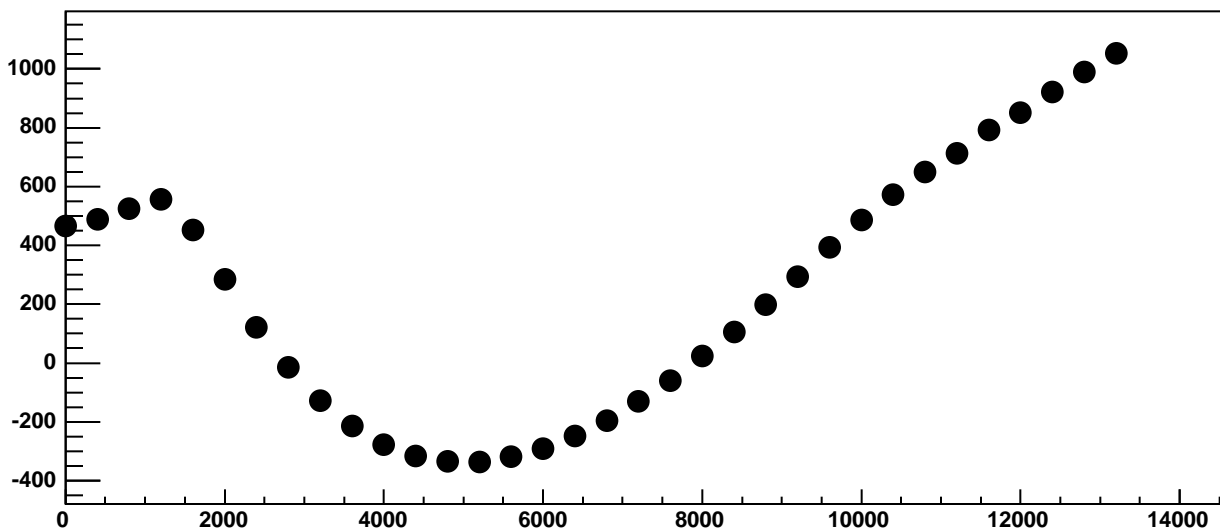
$\chi^2 / \text{ndf}$  5.223e+05 / 23  
p0 -1073 ± 1.001  
p1 -0.06384 ± 0.0001664



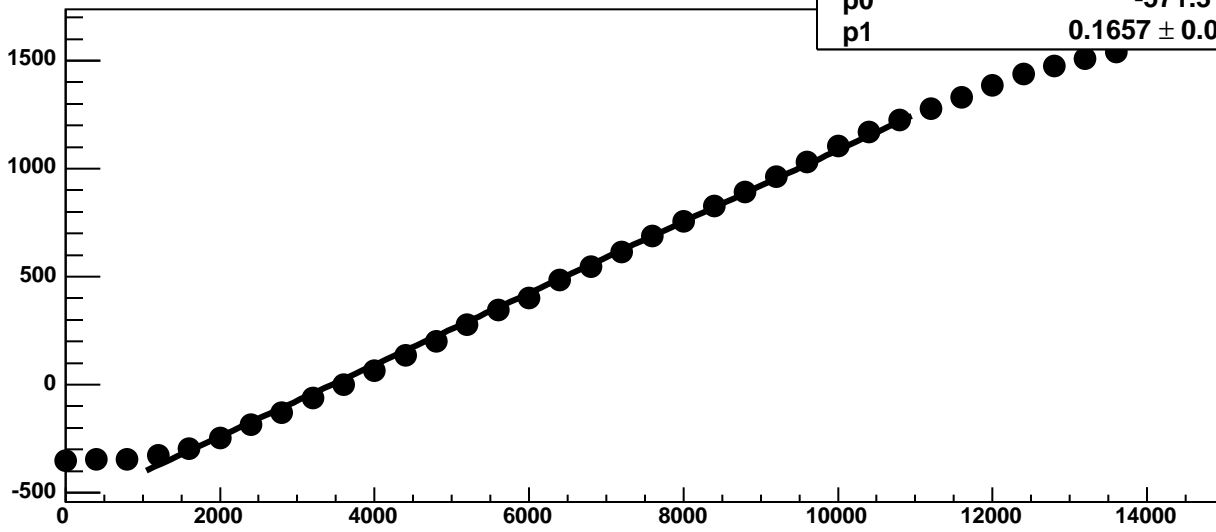
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



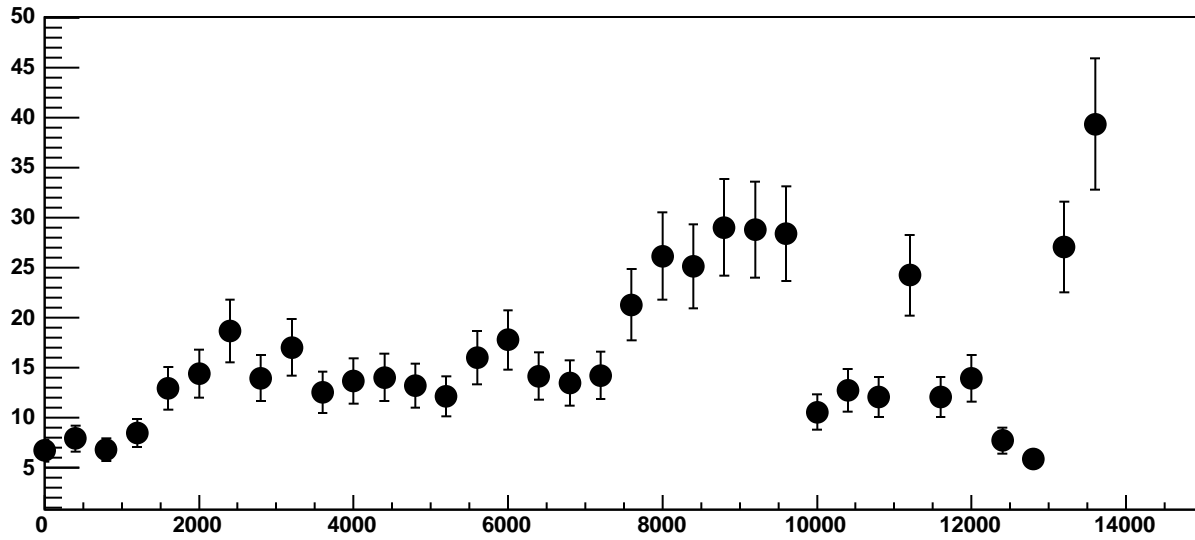
Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



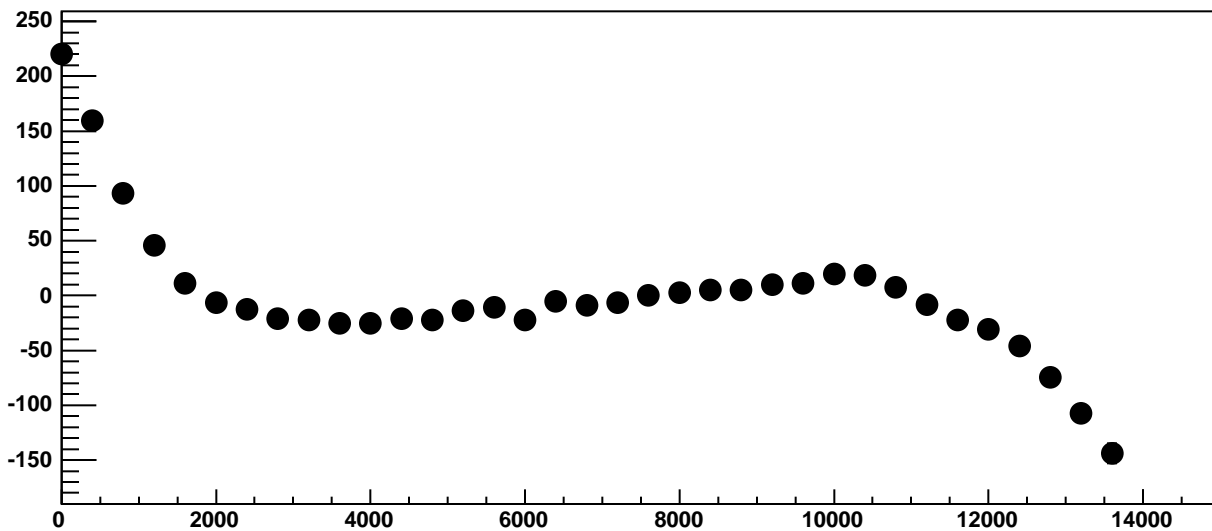
Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



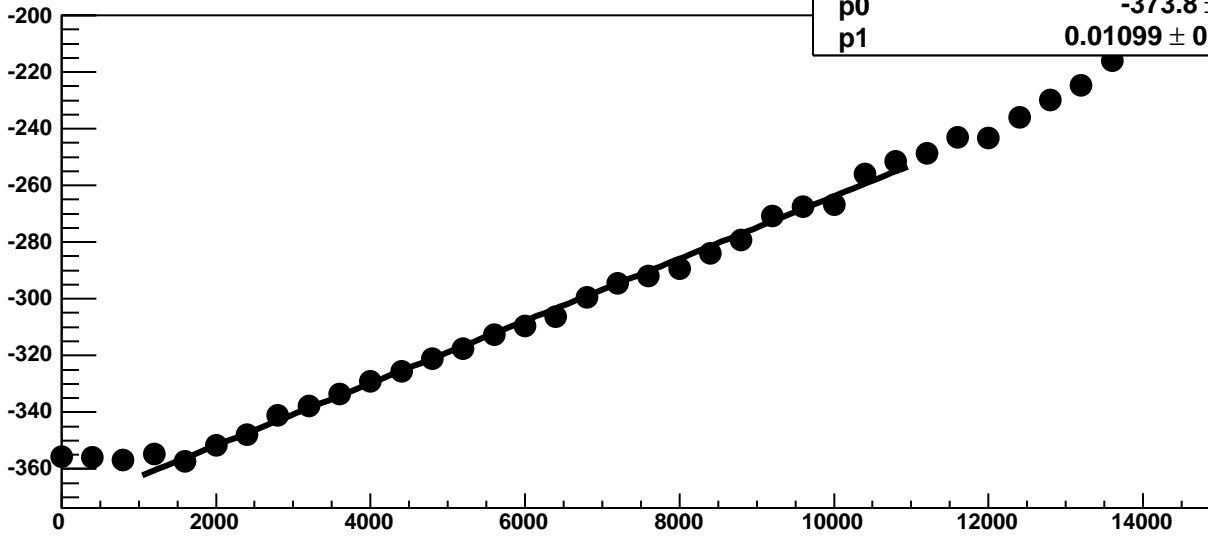
Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

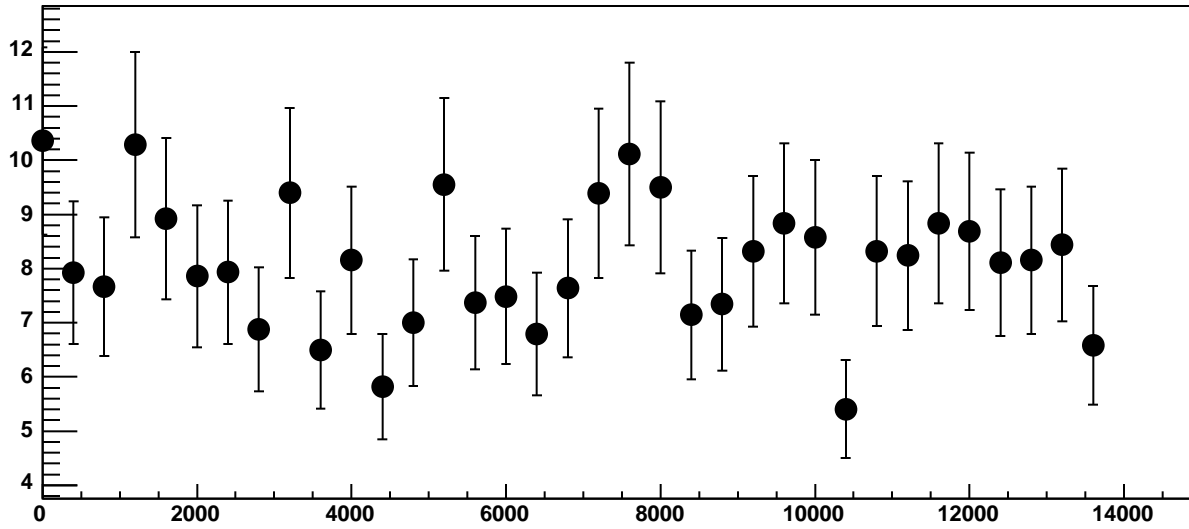


Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

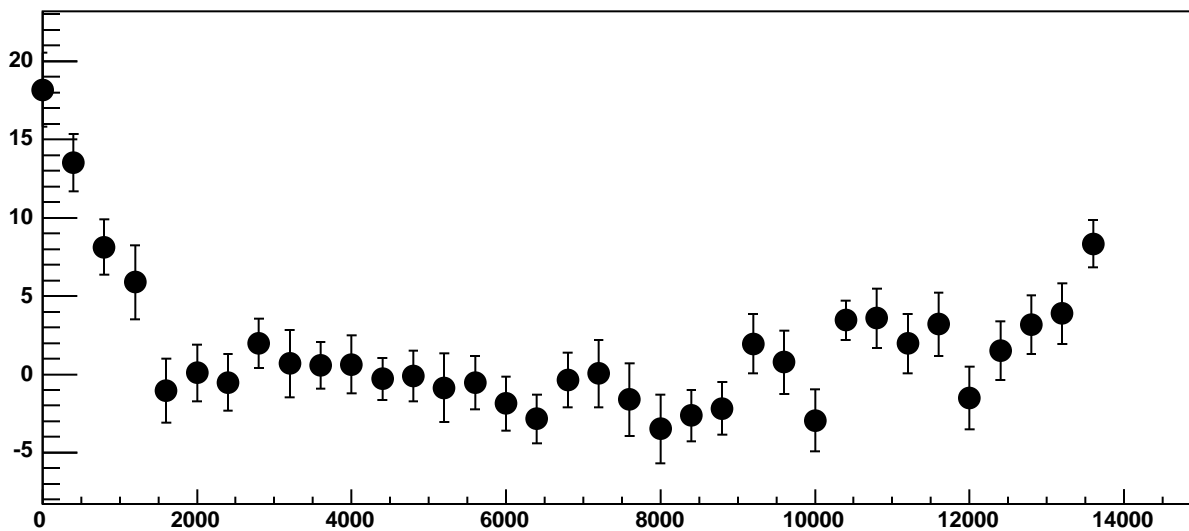


$\chi^2 / \text{ndf}$  35.32 / 23  
p0  $-373.8 \pm 0.8351$   
p1  $0.01099 \pm 0.000124$

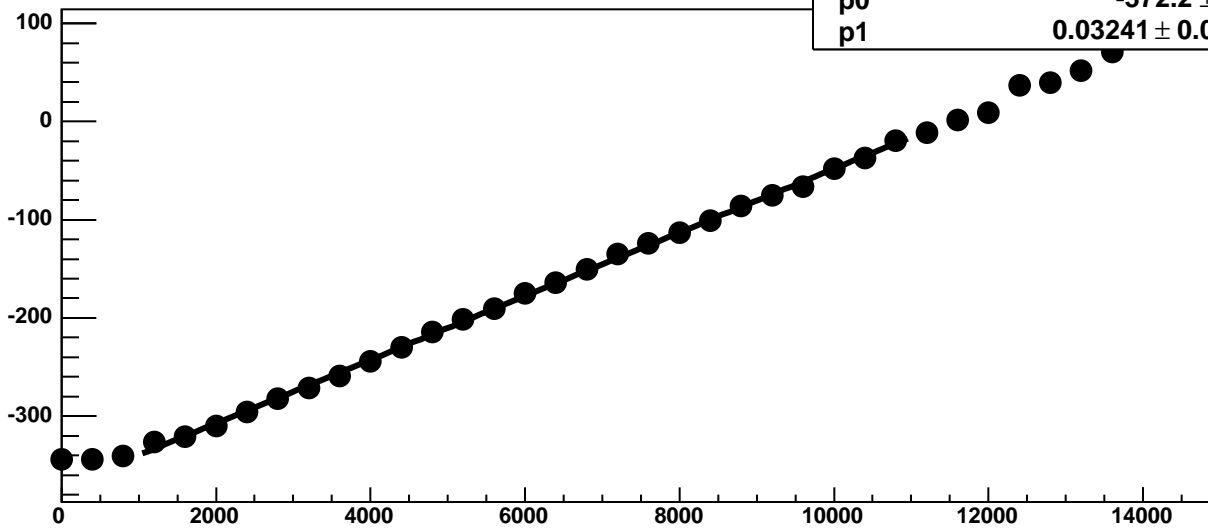
Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



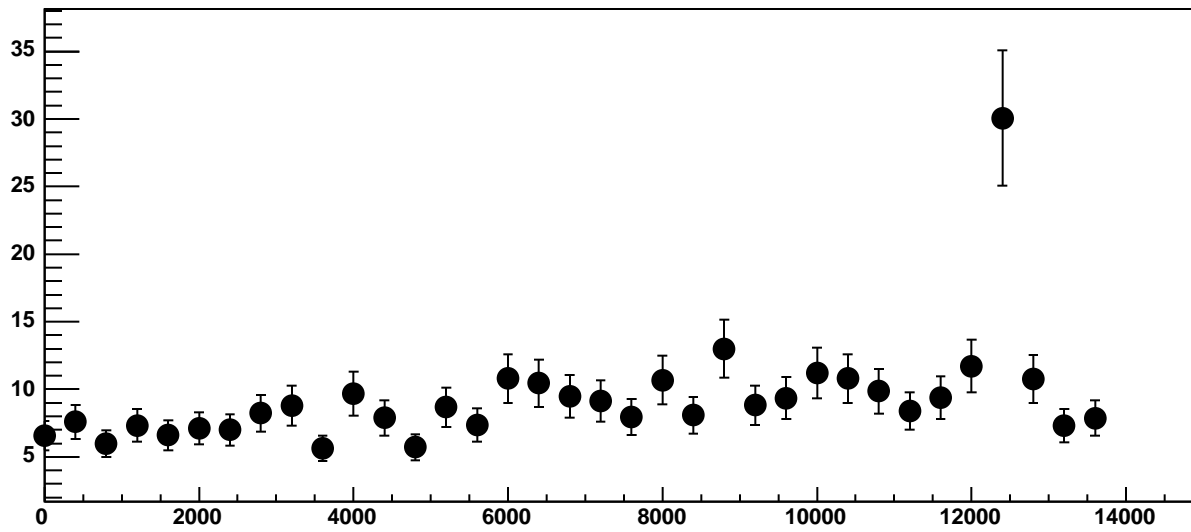
Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



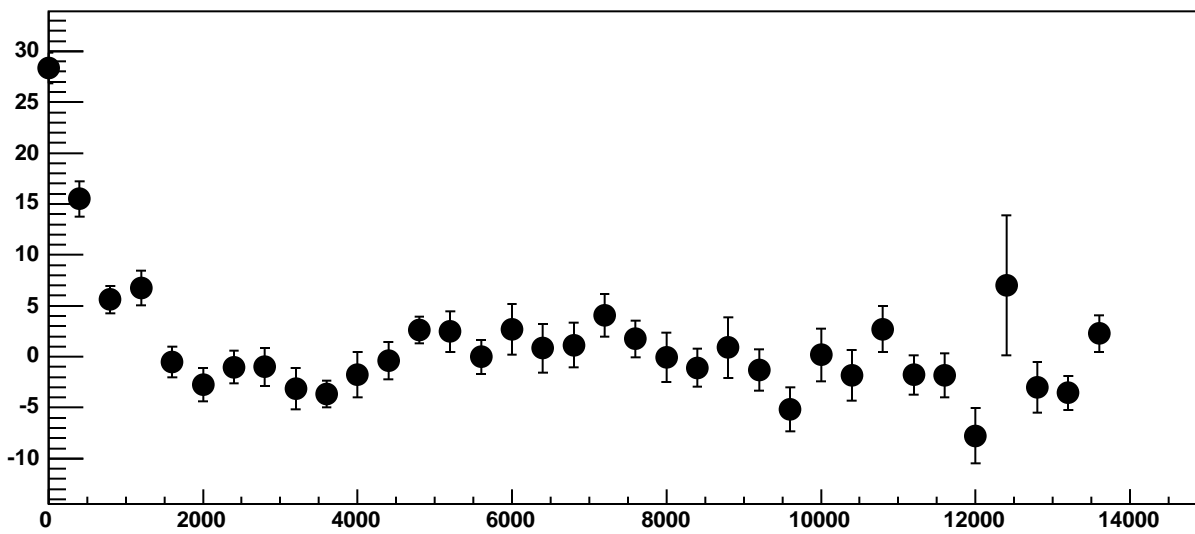
Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



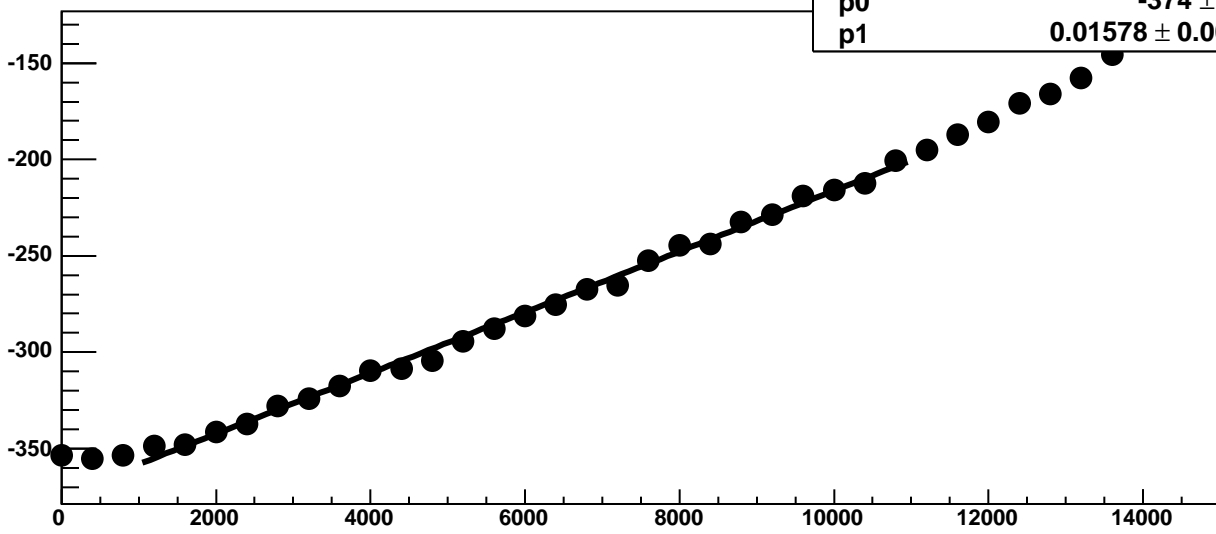
Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

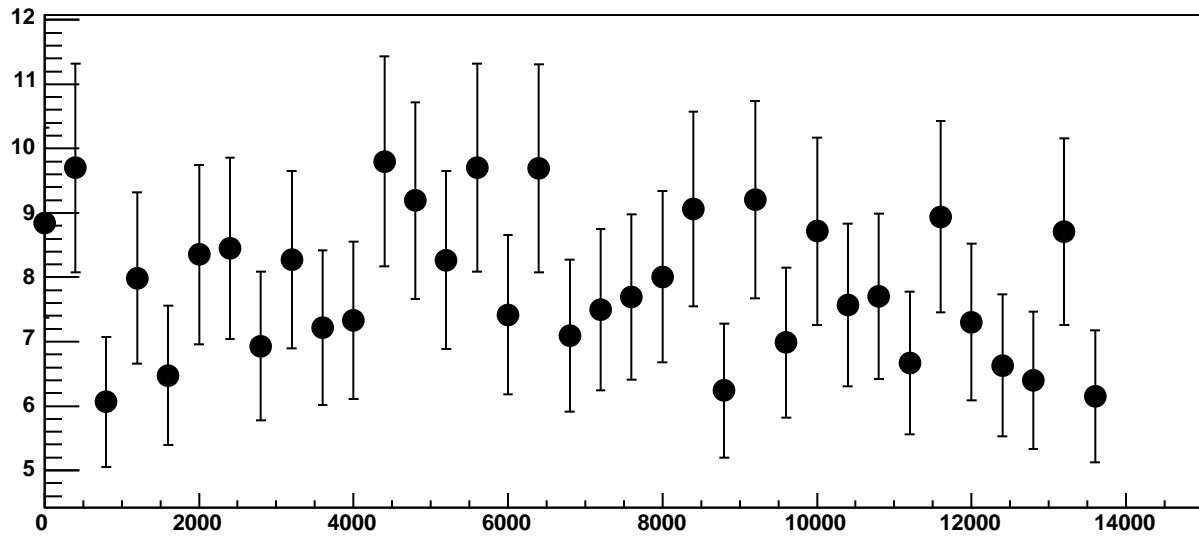


Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

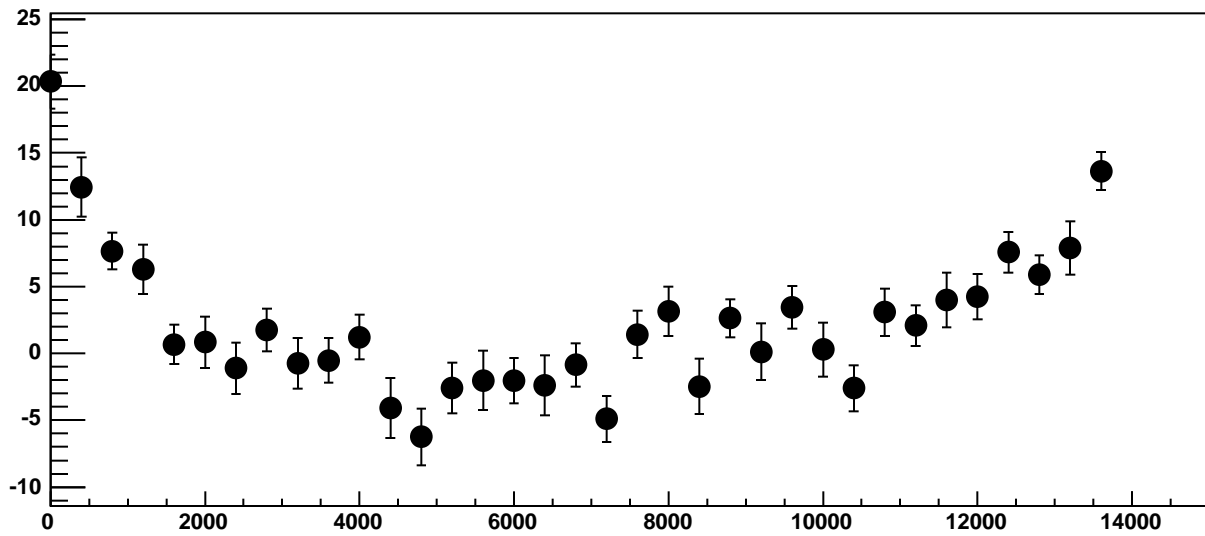


$\chi^2 / \text{ndf}$  58.59 / 23  
p0  $-374 \pm 0.8147$   
p1  $0.01578 \pm 0.0001219$

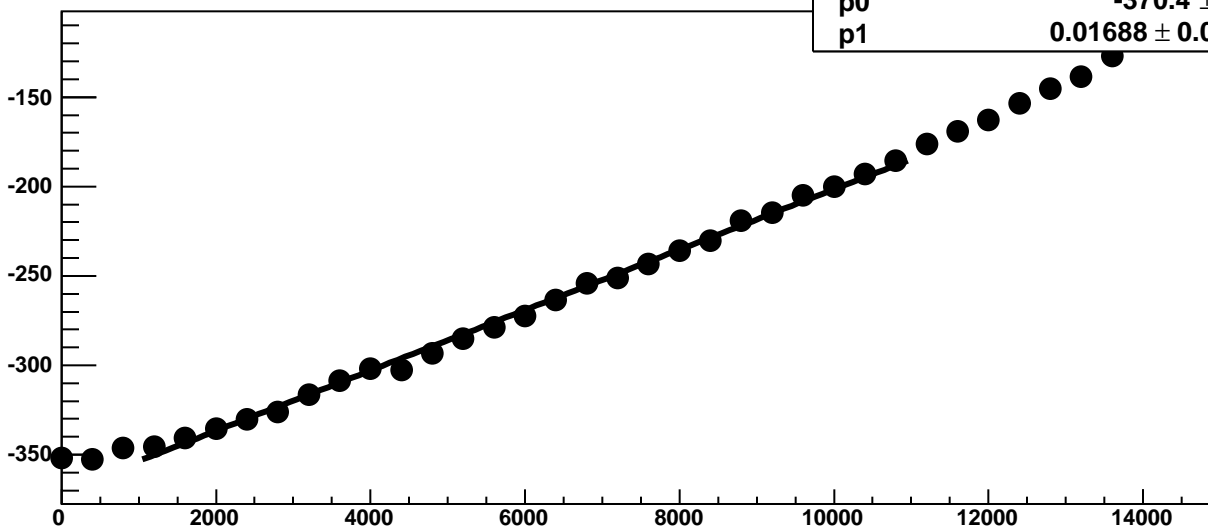
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

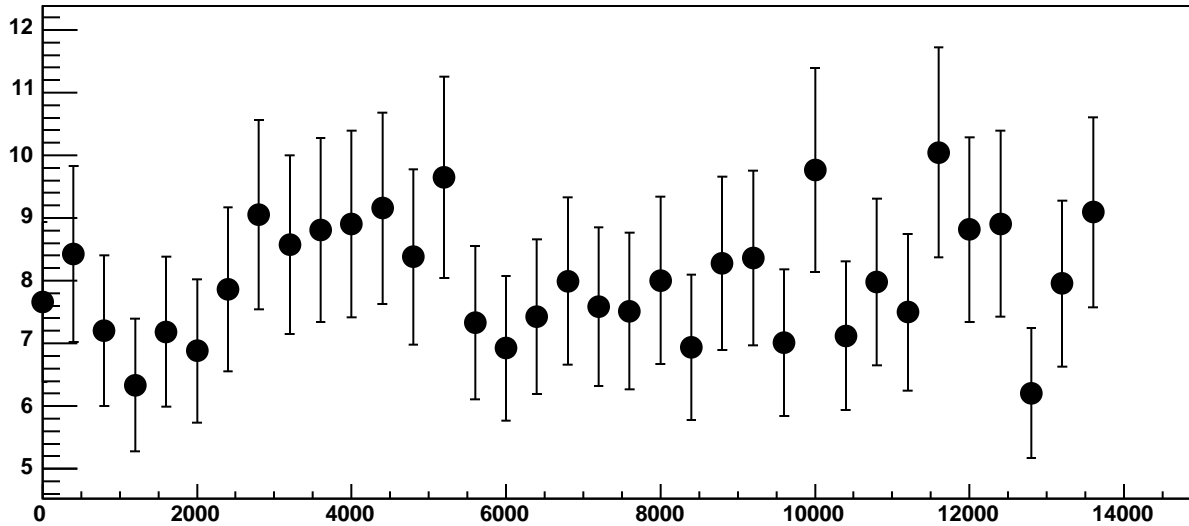


Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

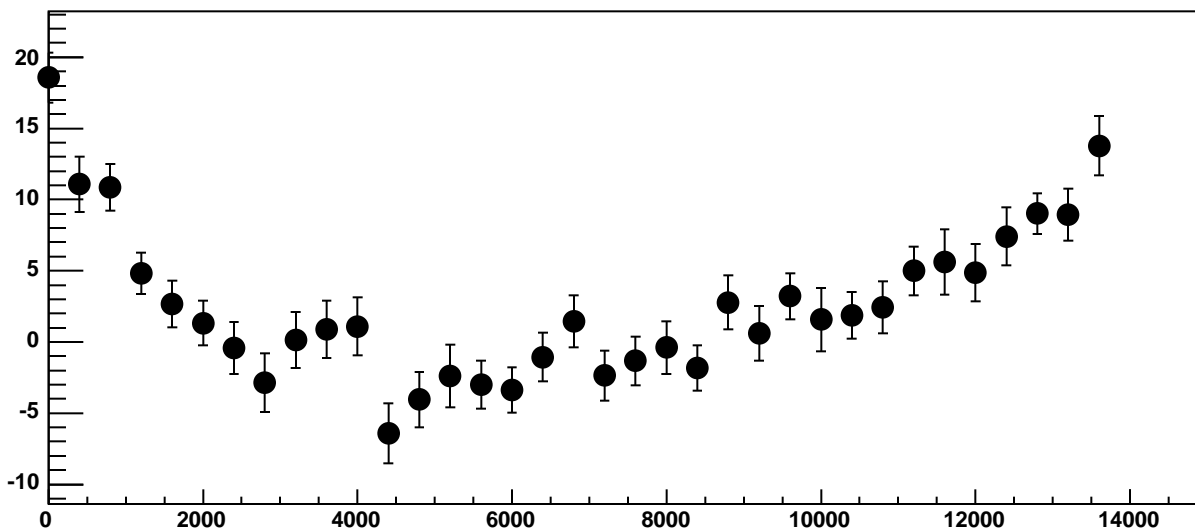


$\chi^2 / \text{ndf}$  53.99 / 23  
p0  $-370.4 \pm 0.8029$   
p1  $0.01688 \pm 0.0001207$

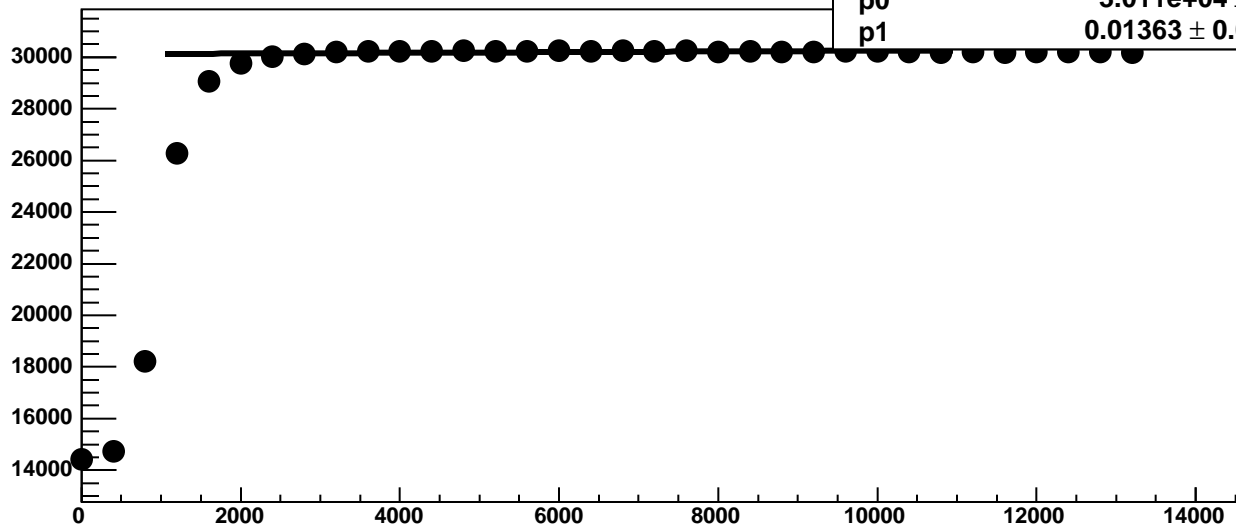
Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



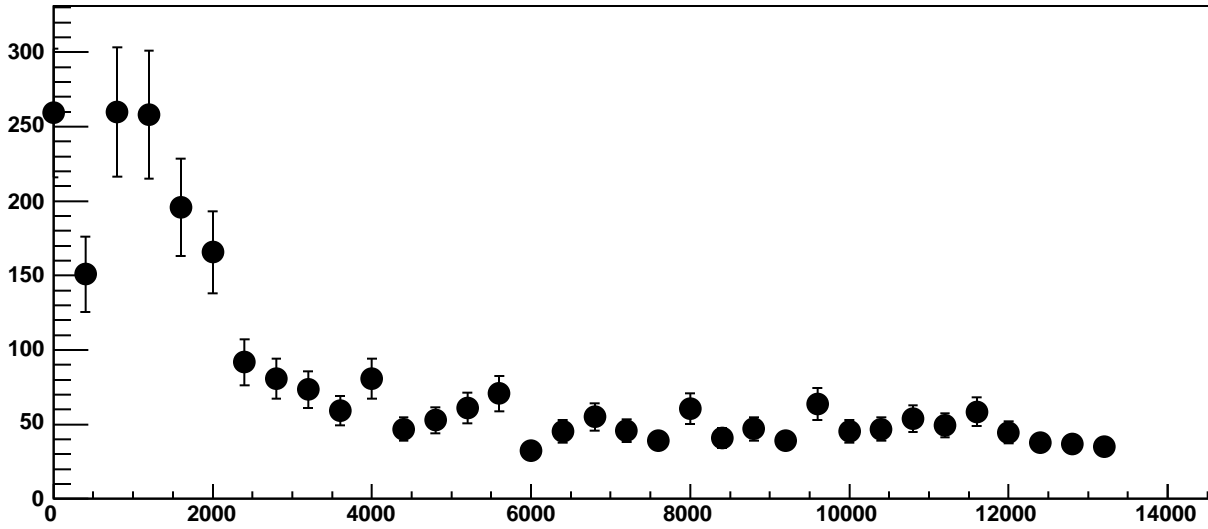
Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



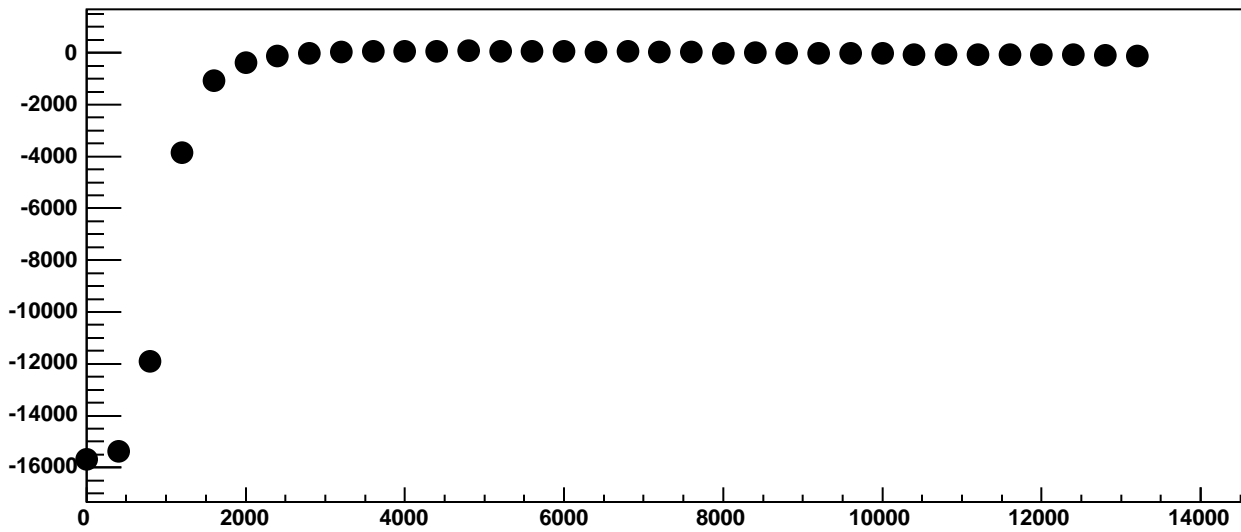
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



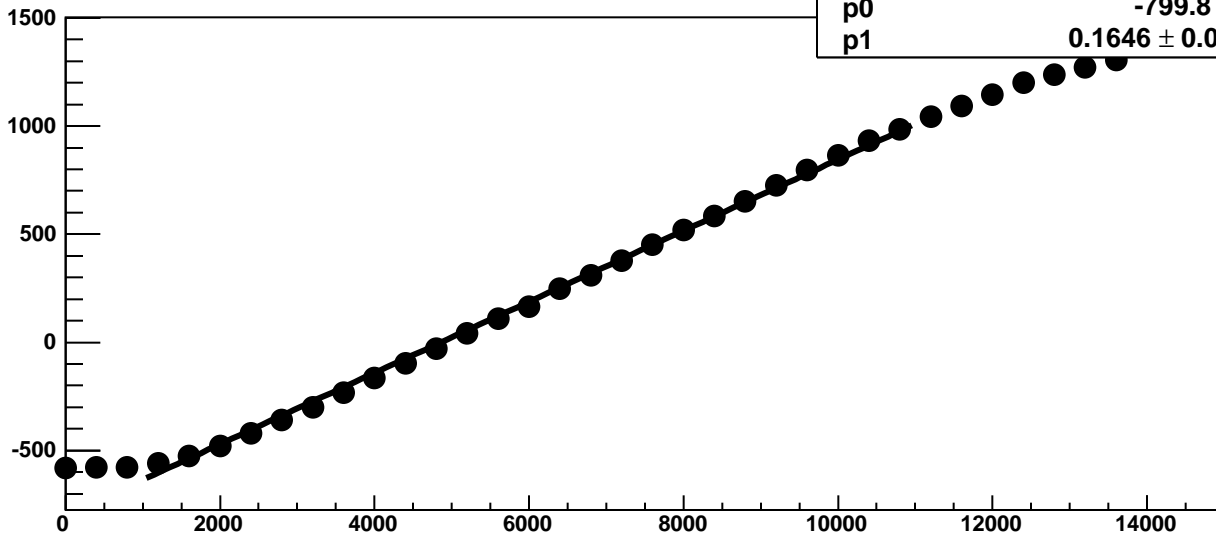
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



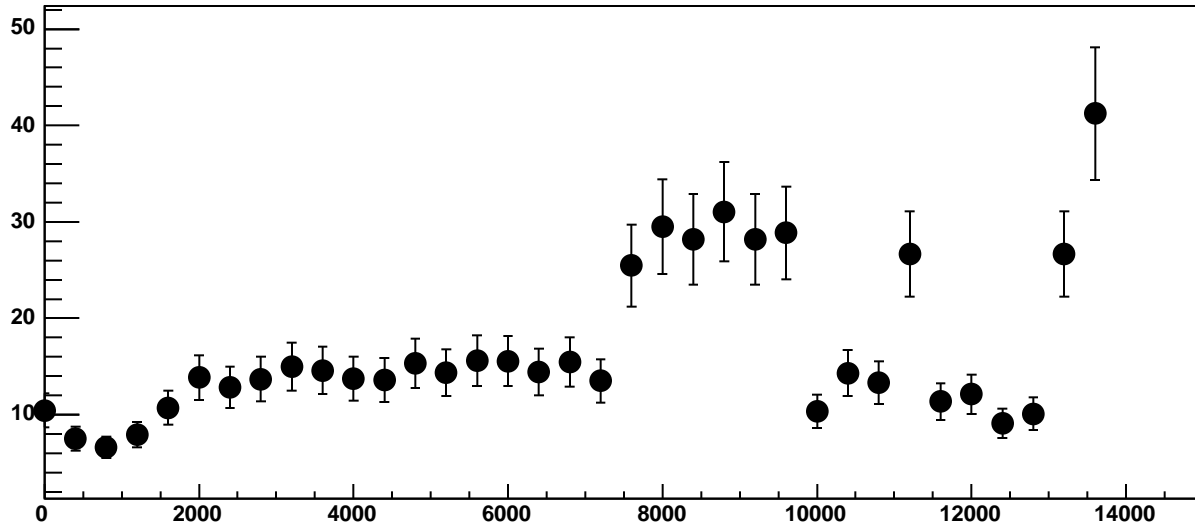
Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



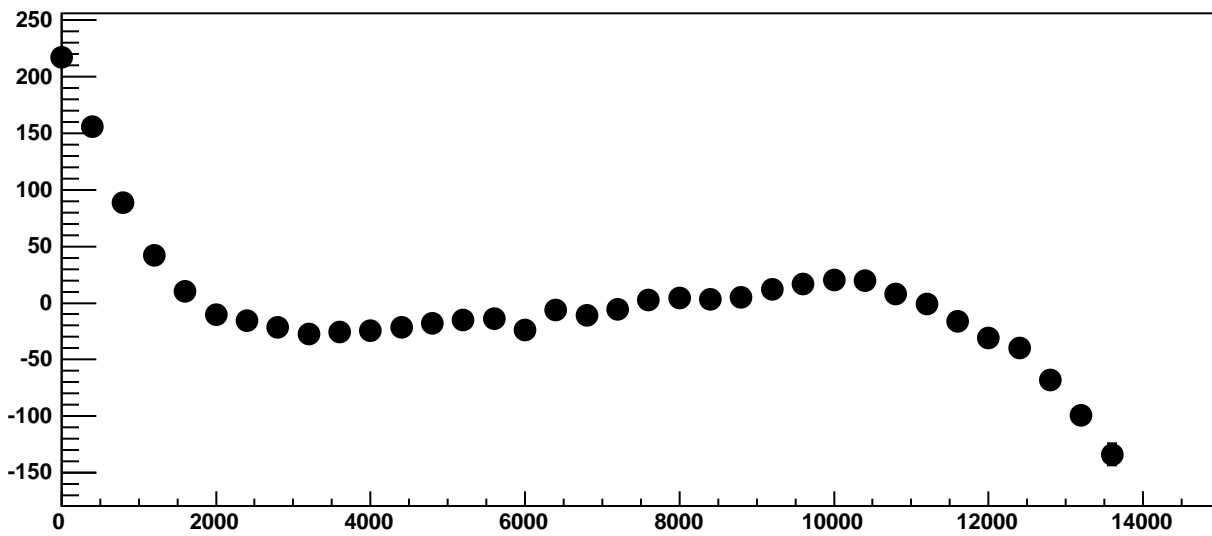
Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

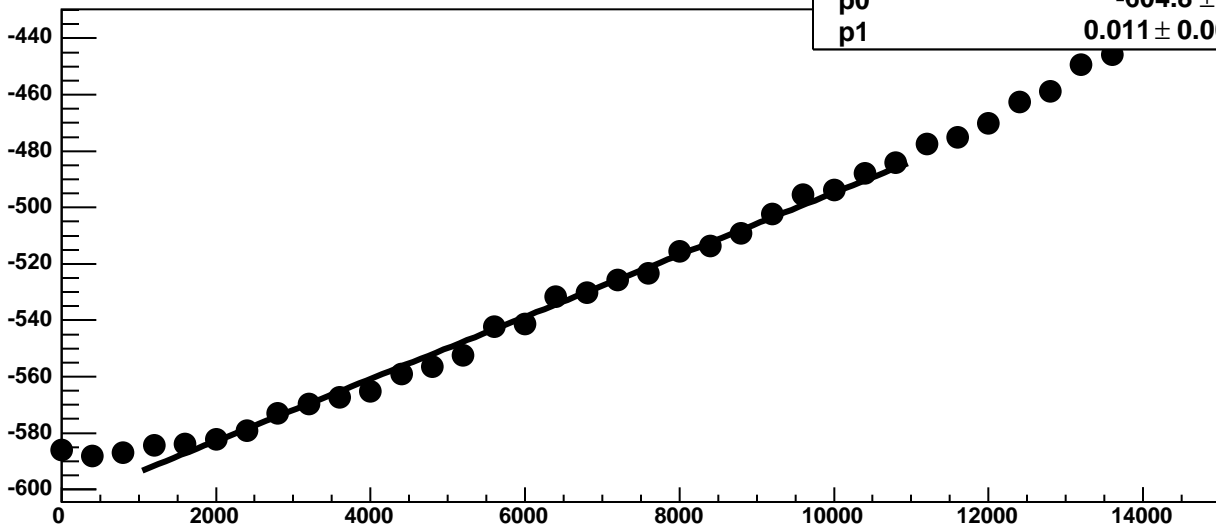


Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



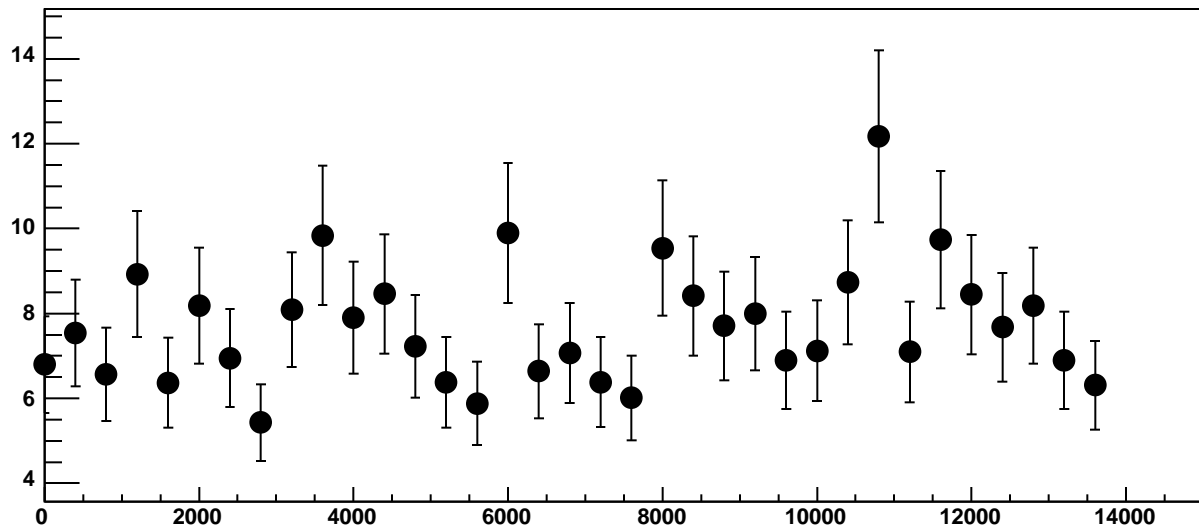


Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

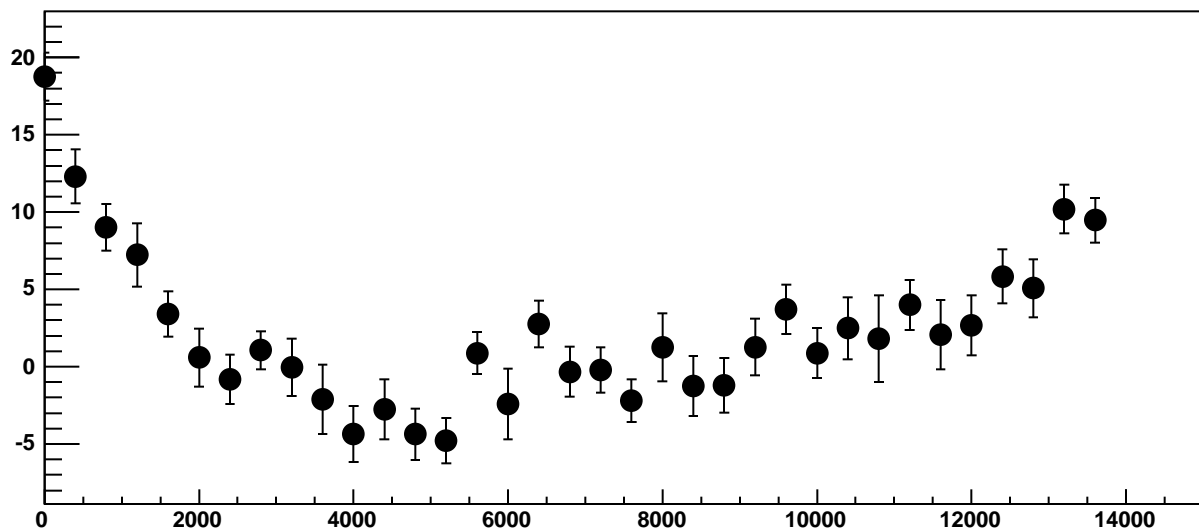


$\chi^2 / \text{ndf}$  62.17 / 23  
p0  $-604.8 \pm 0.7909$   
p1  $0.011 \pm 0.0001232$

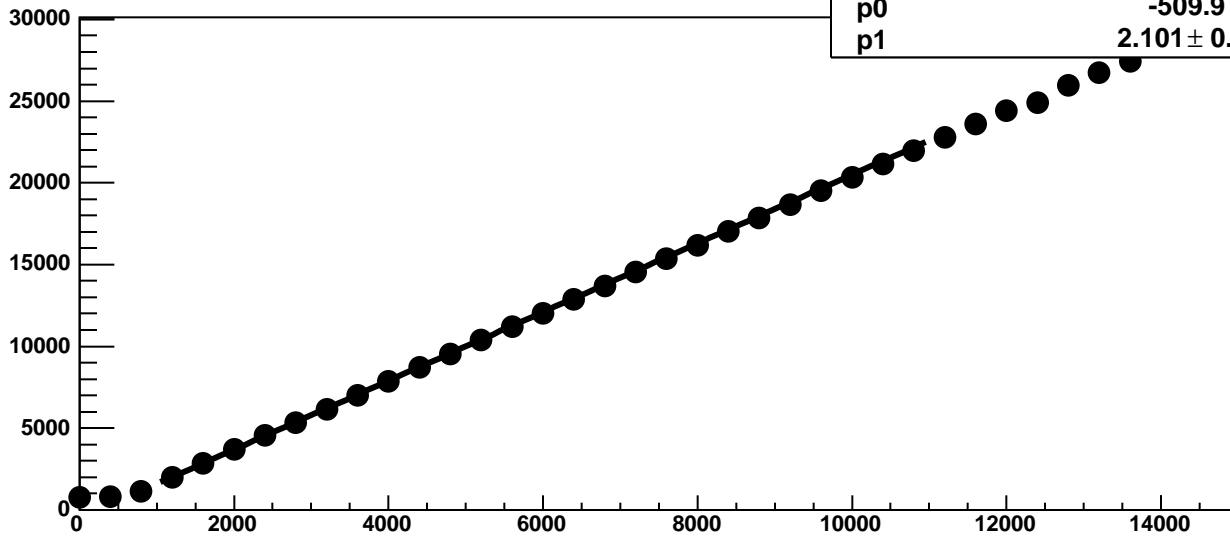
Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

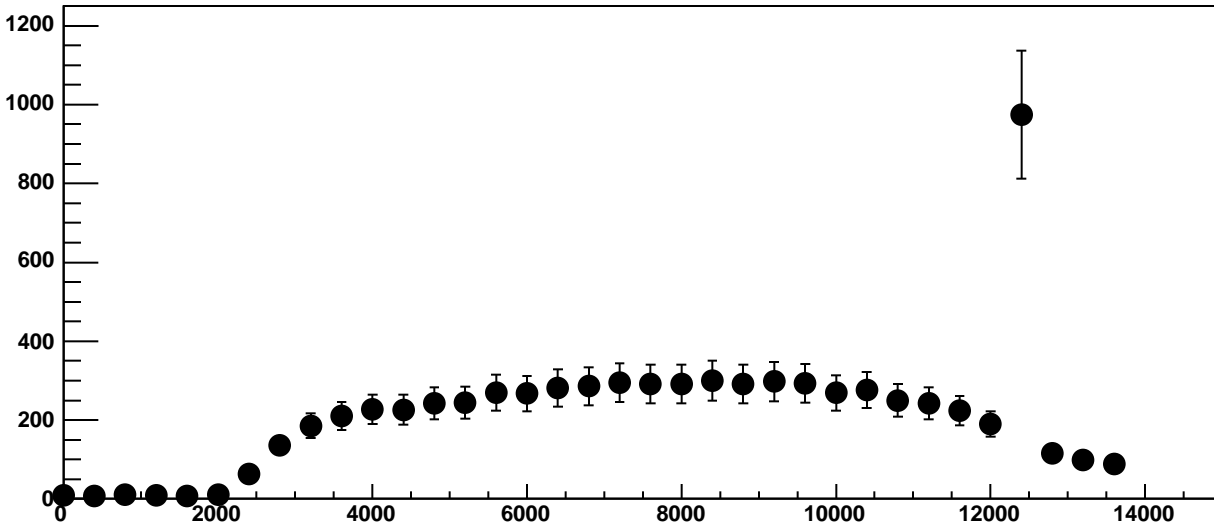


Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

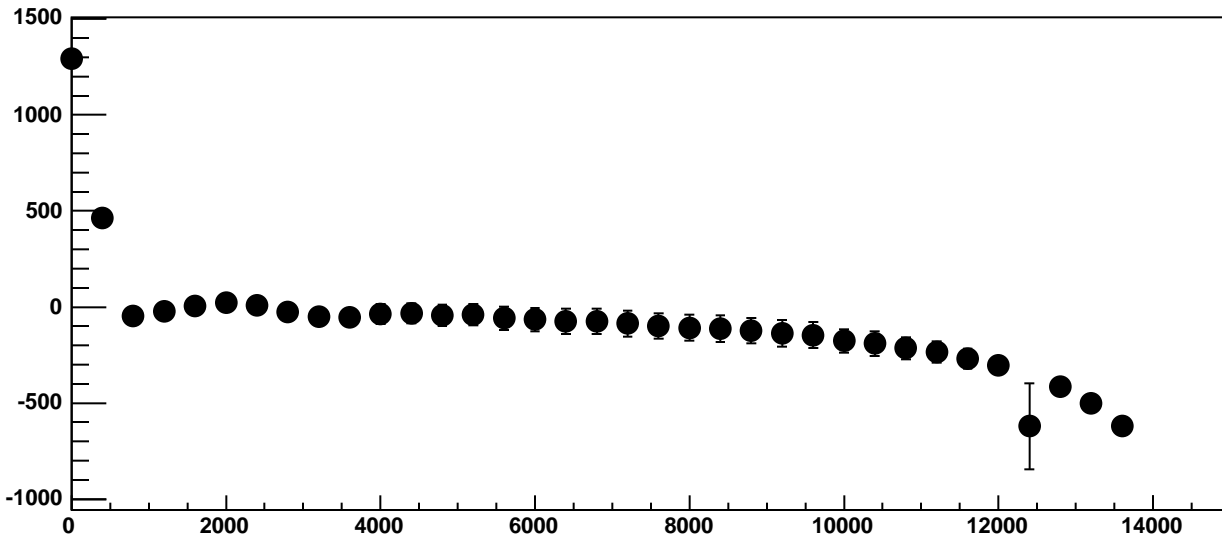


$\chi^2 / \text{ndf}$  248.9 / 23  
p0  $-509.9 \pm 3.573$   
p1  $2.101 \pm 0.002067$

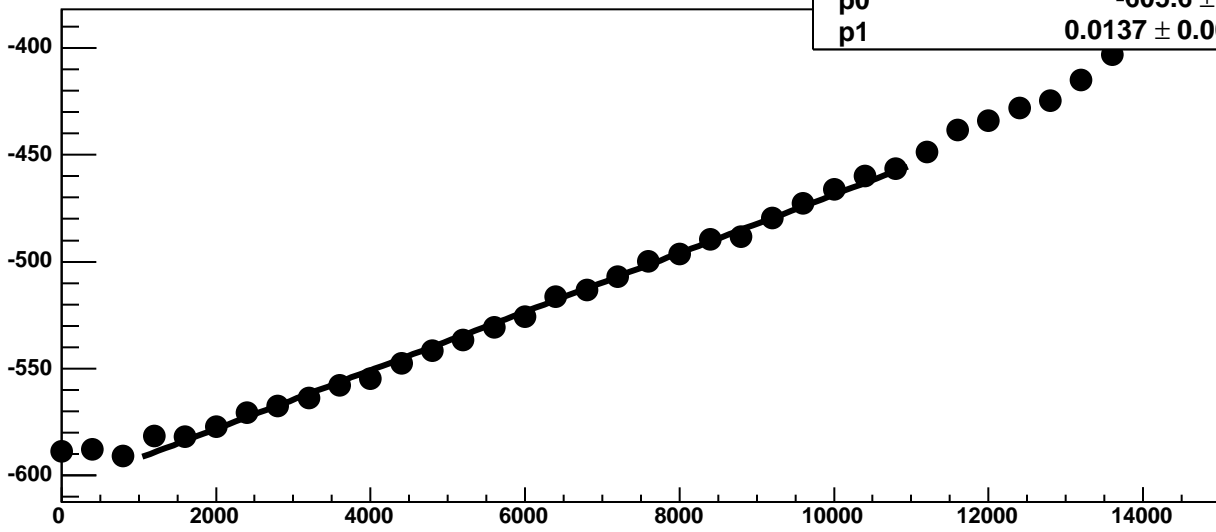
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



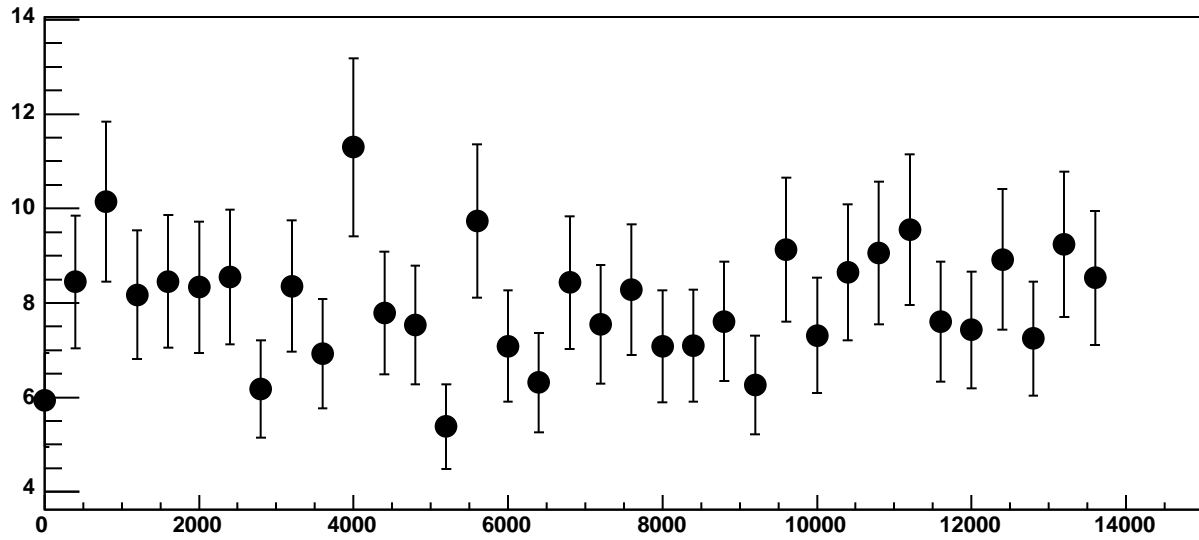
Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



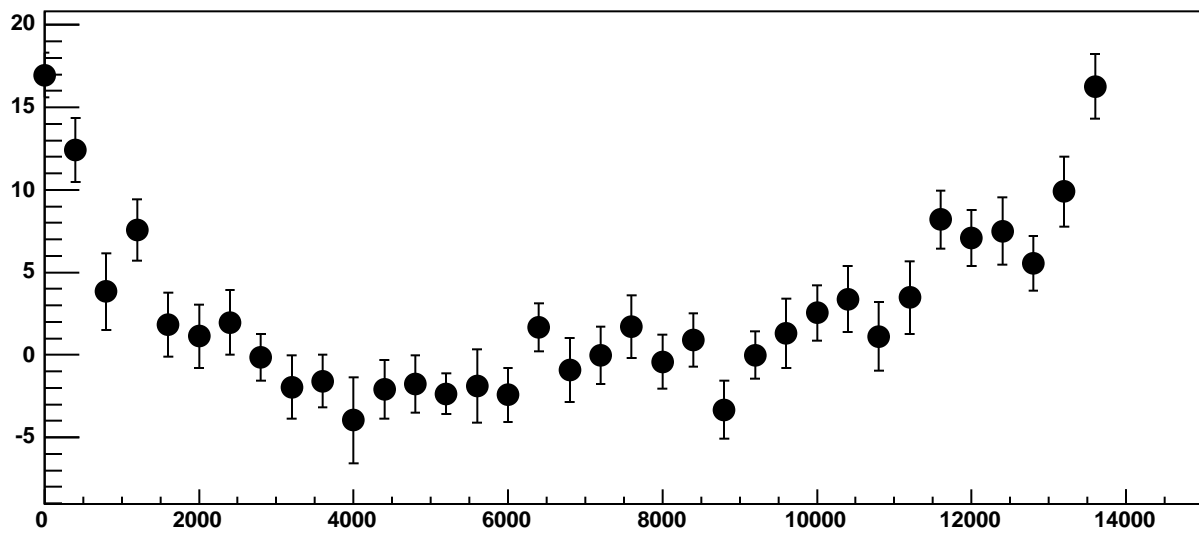
Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



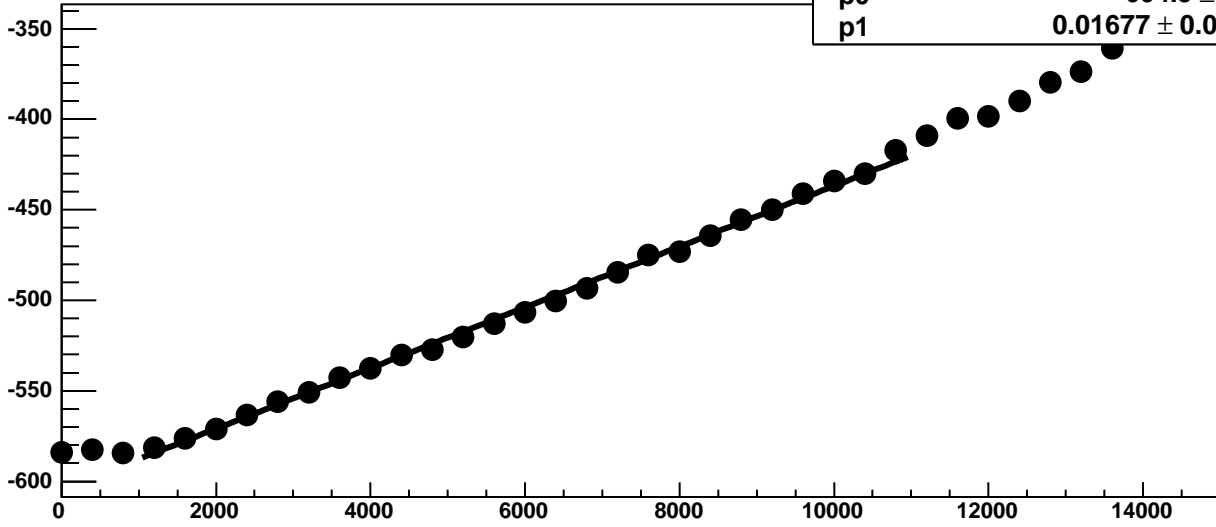
Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

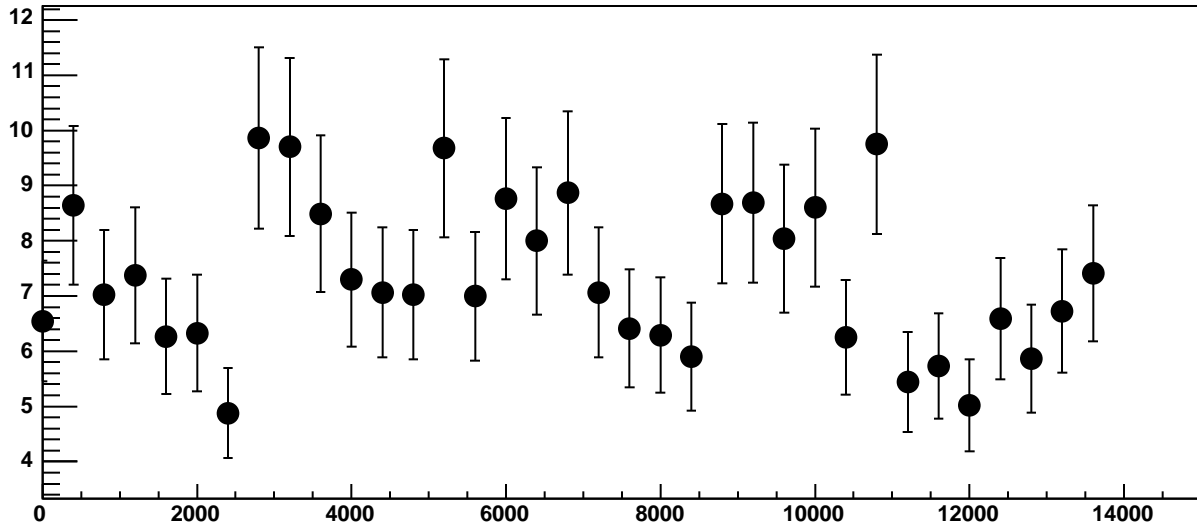


Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

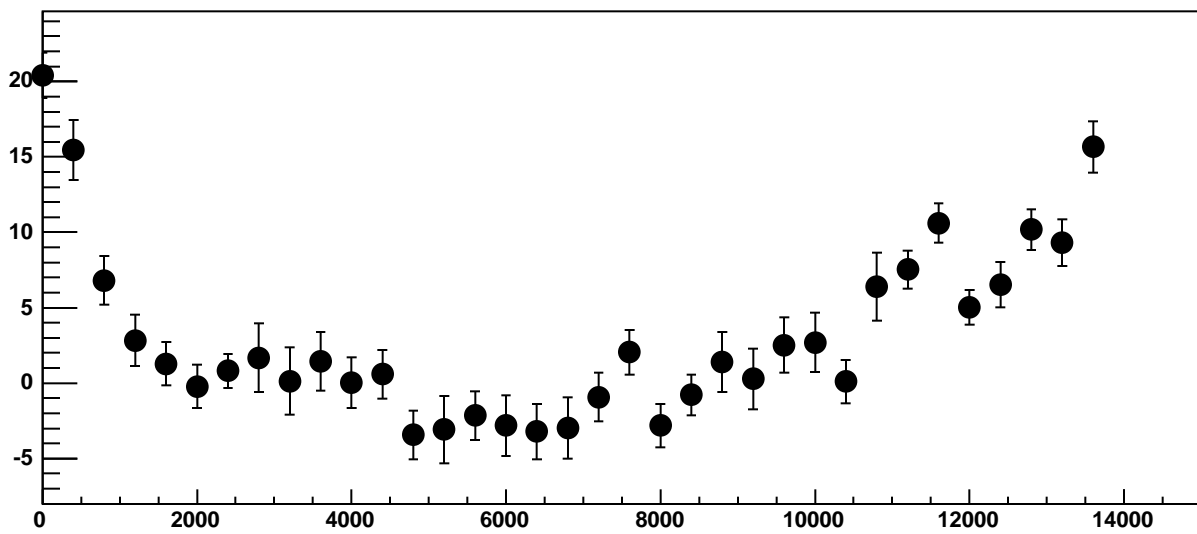


$\chi^2 / \text{ndf}$  39.56 / 23  
p0  $-604.5 \pm 0.7398$   
p1  $0.01677 \pm 0.0001142$

Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

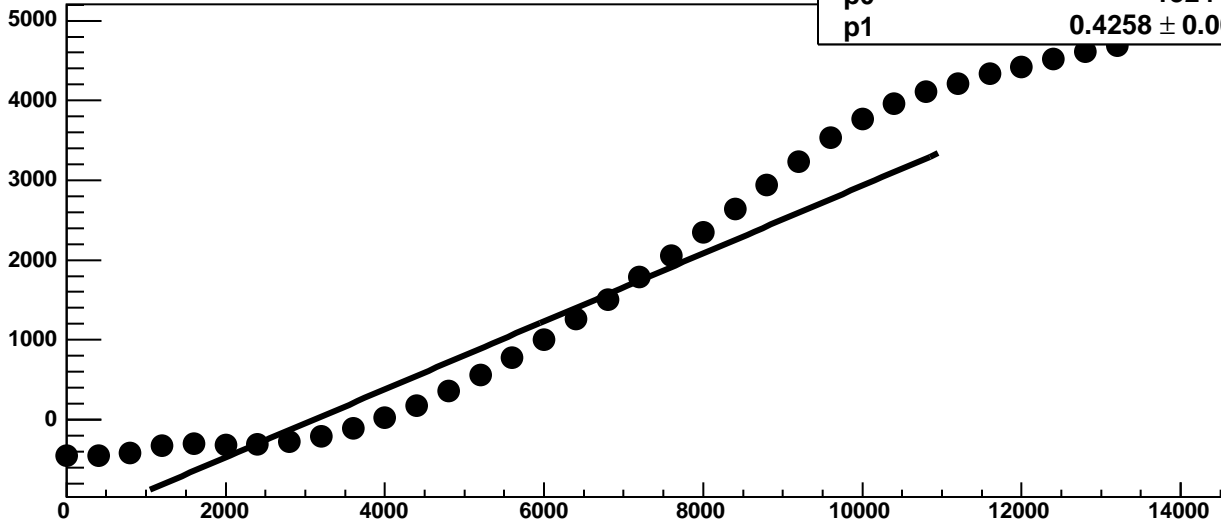


Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

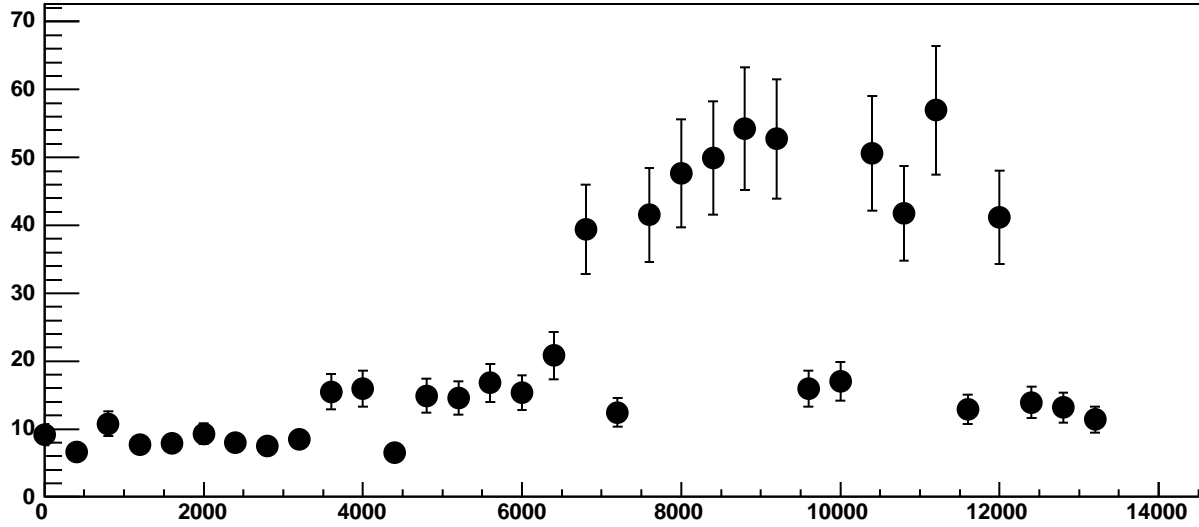


Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

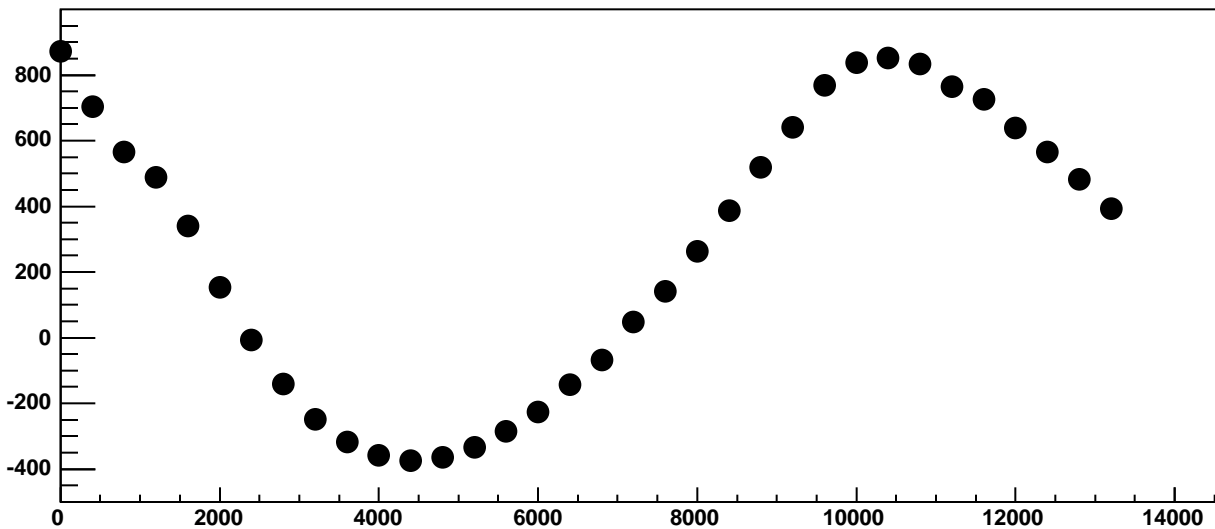
$\chi^2 / \text{ndf}$  3.605e+05 / 23  
p0 -1324 ± 1.118  
p1 0.4258 ± 0.0002594



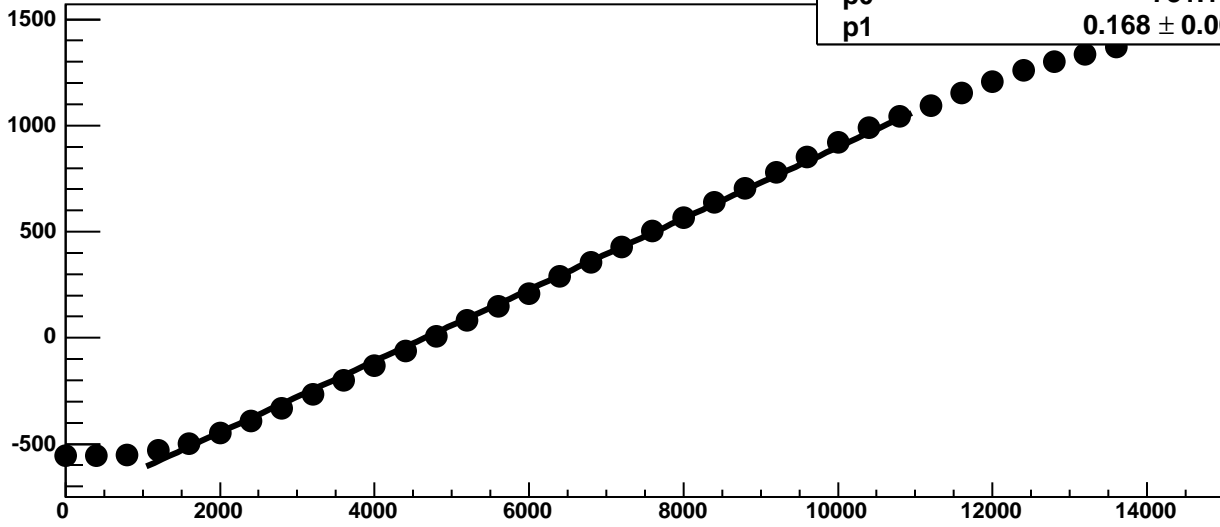
Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

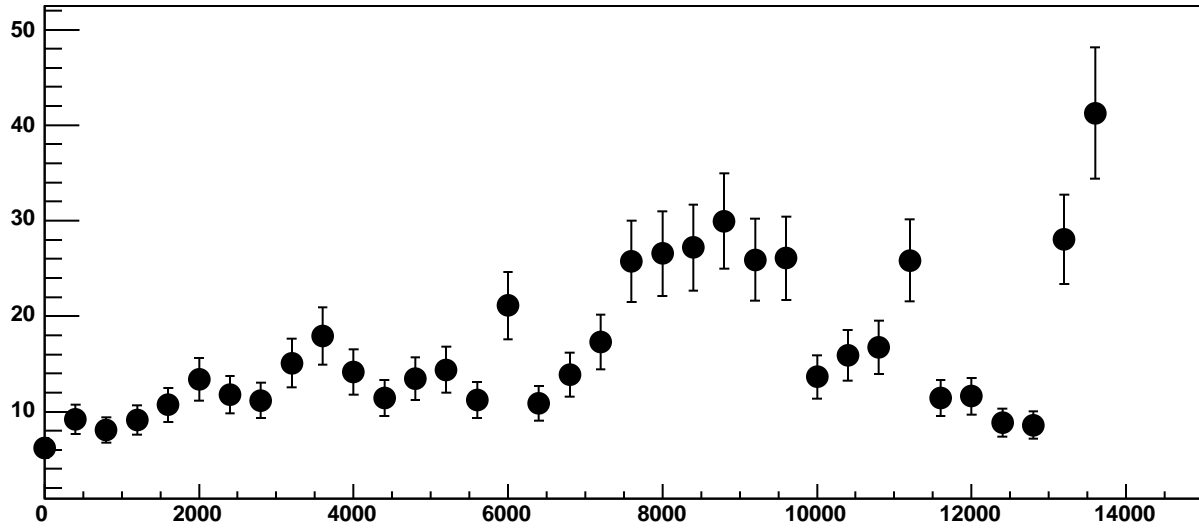


Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

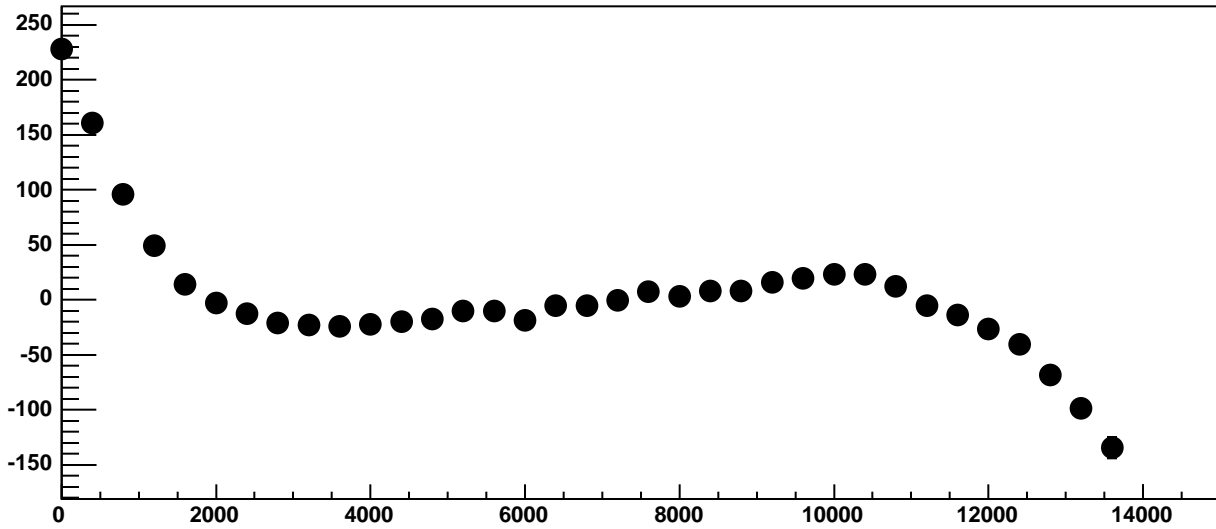


$\chi^2 / \text{ndf}$  1065 / 23  
p0 -781.1 ± 1.311  
p1 0.168 ± 0.0002325

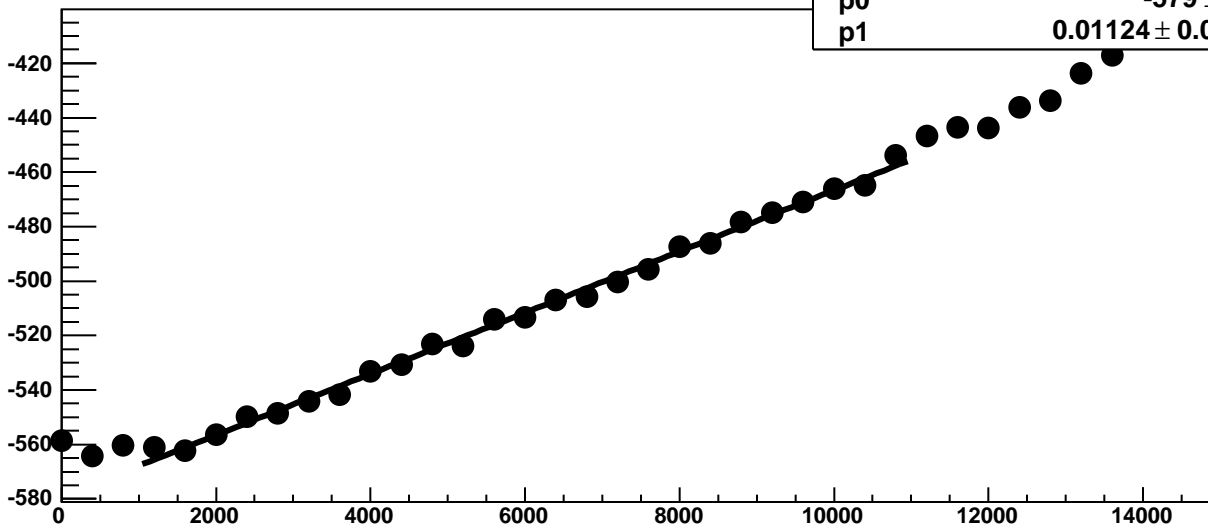
Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

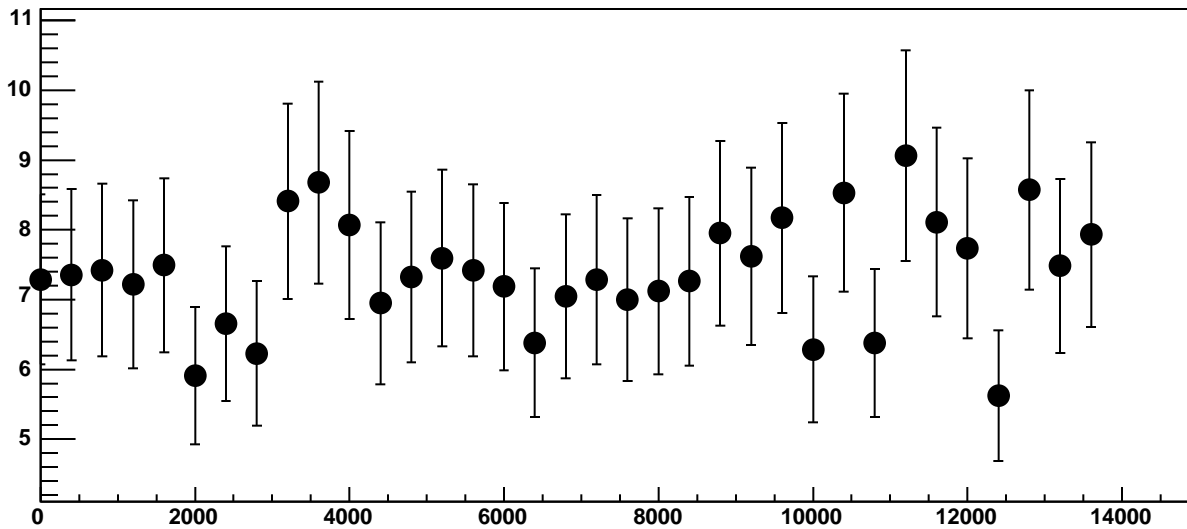


Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

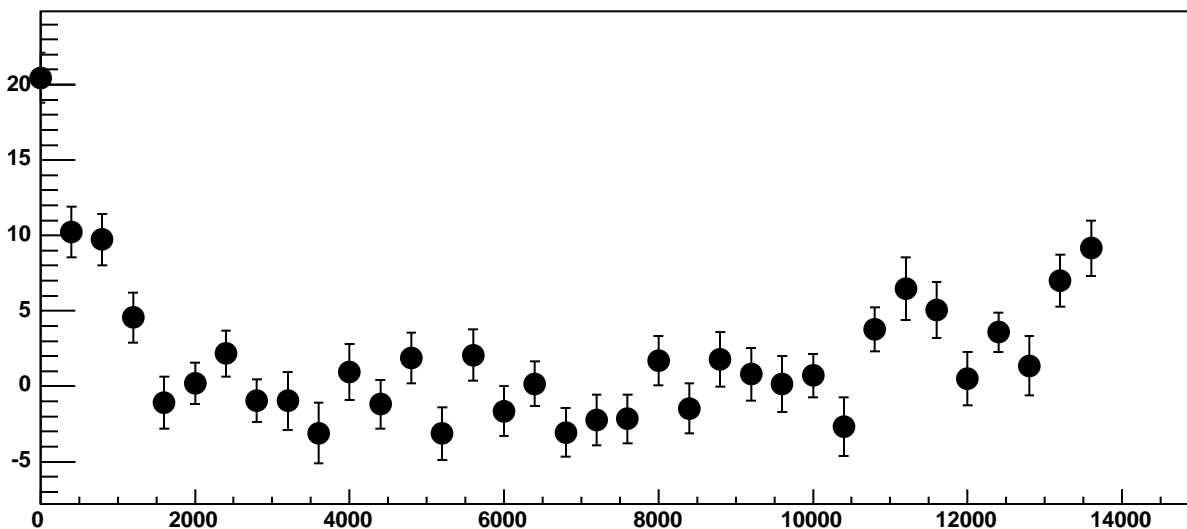


$\chi^2 / \text{ndf}$  39.94 / 23  
p0  $-579 \pm 0.7451$   
p1  $0.01124 \pm 0.0001125$

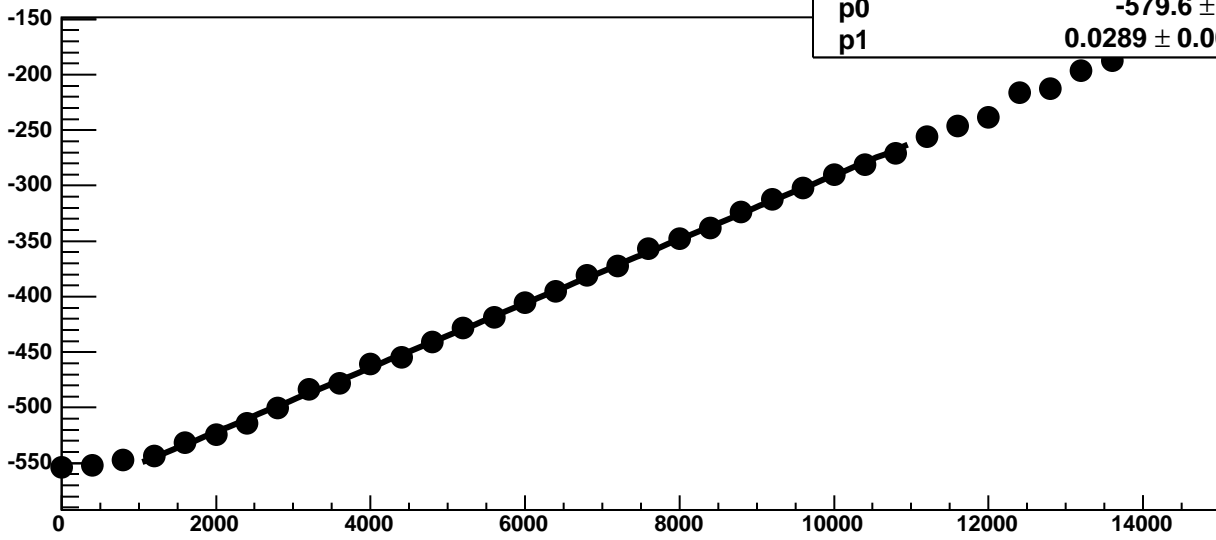
Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



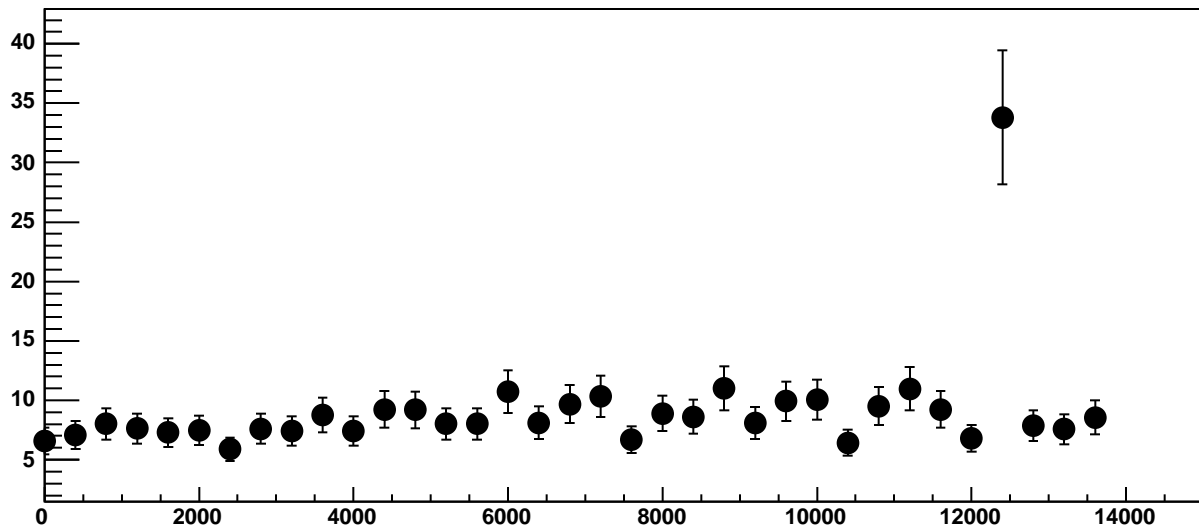
Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



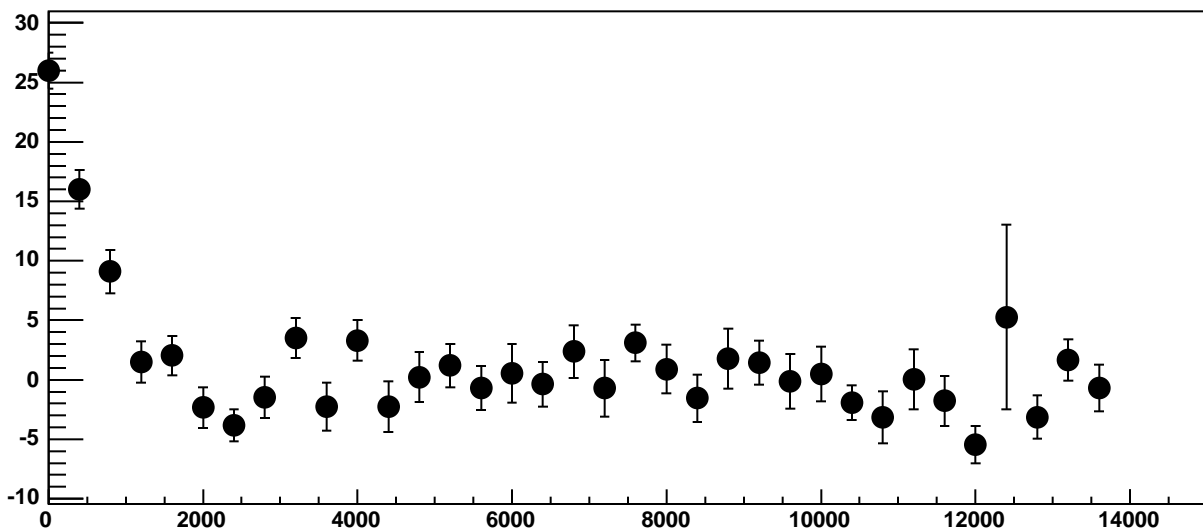
Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

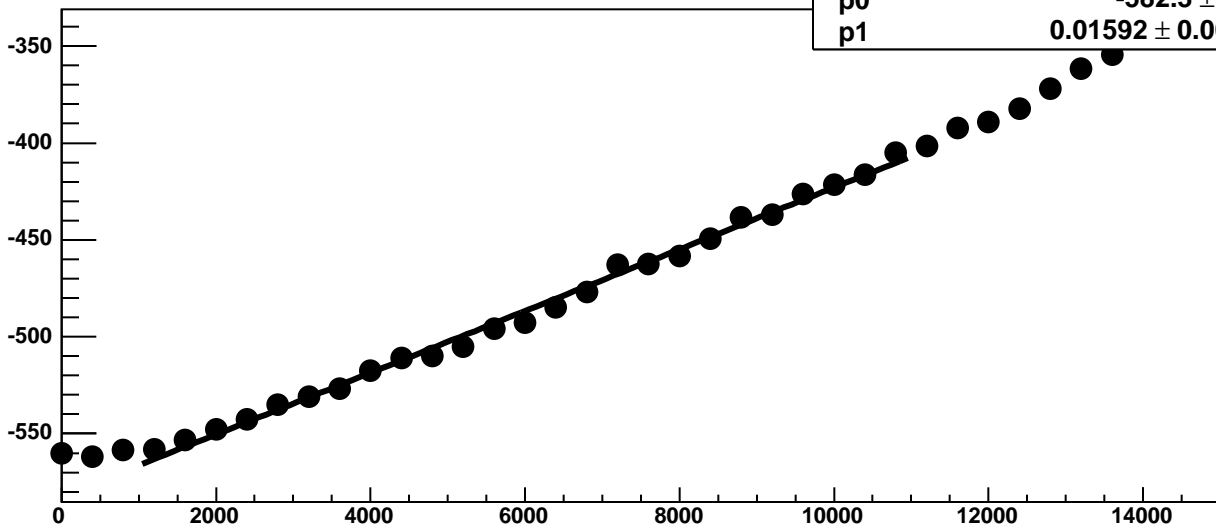


Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



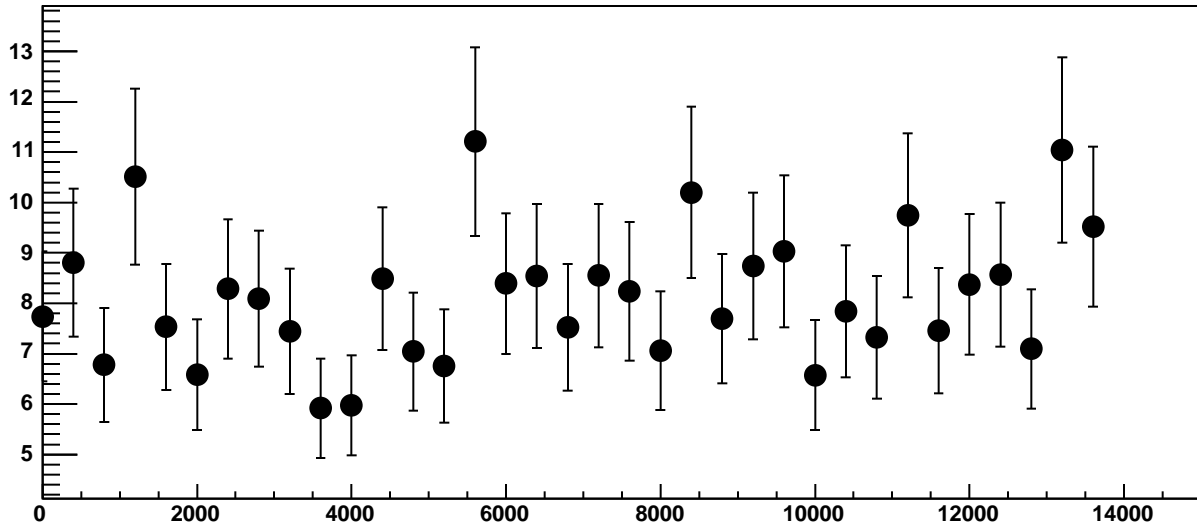


Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

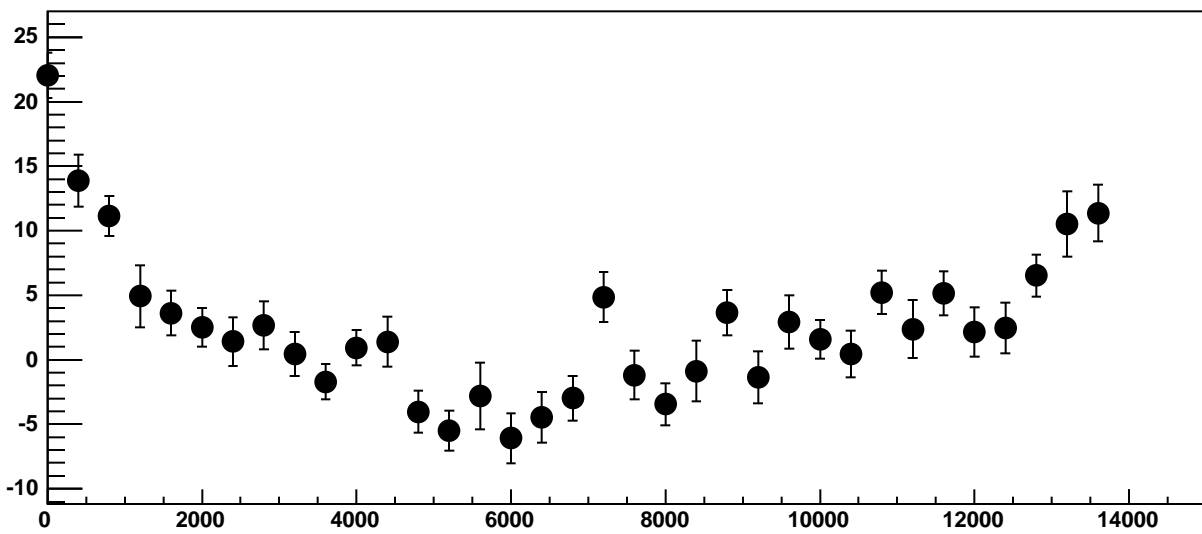


$\chi^2 / \text{ndf}$  83.44 / 23  
p0  $-582.3 \pm 0.8034$   
p1  $0.01592 \pm 0.0001226$

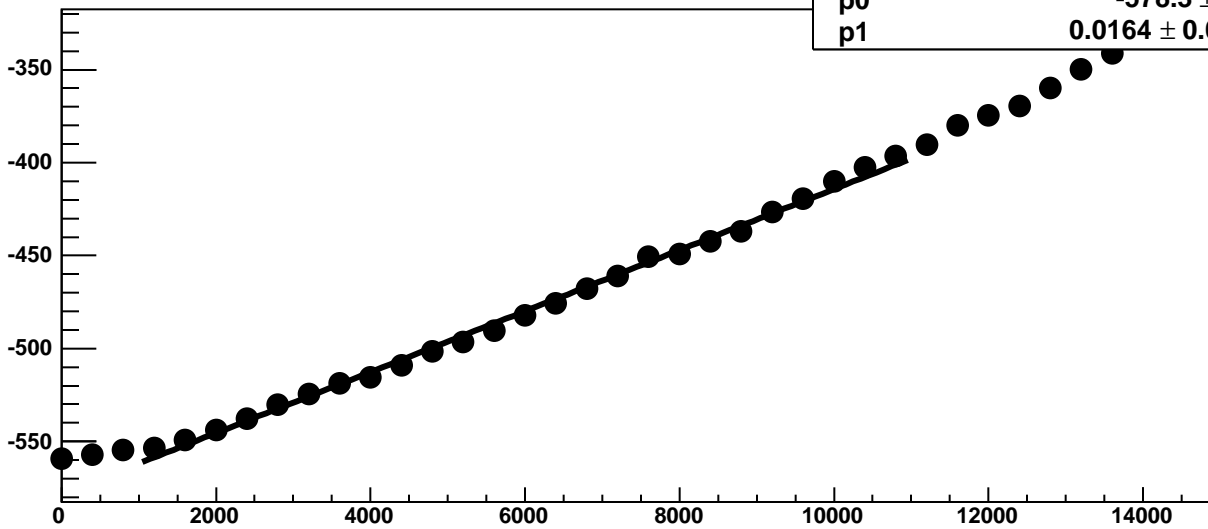
Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

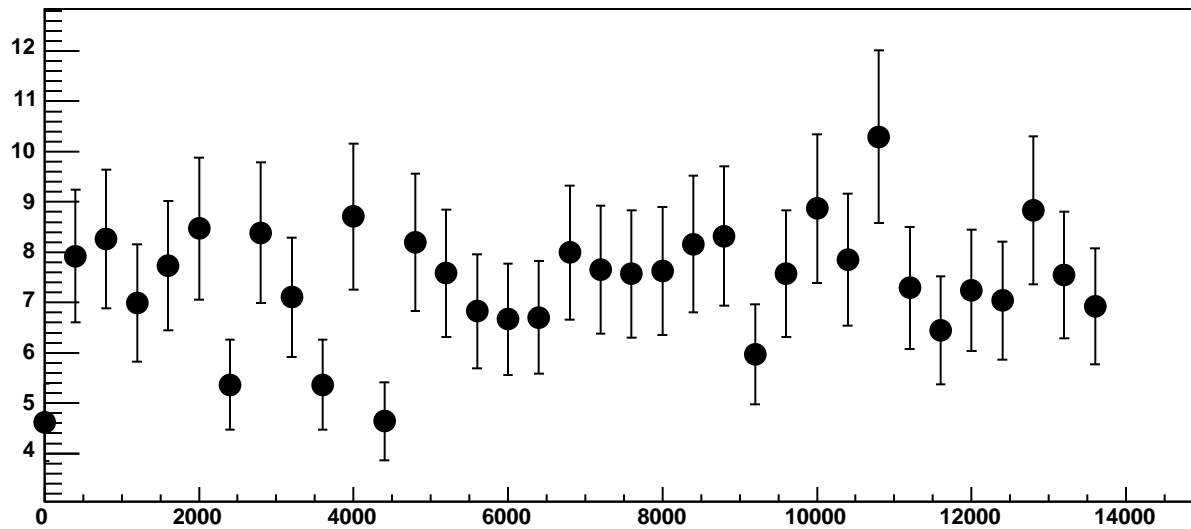


Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

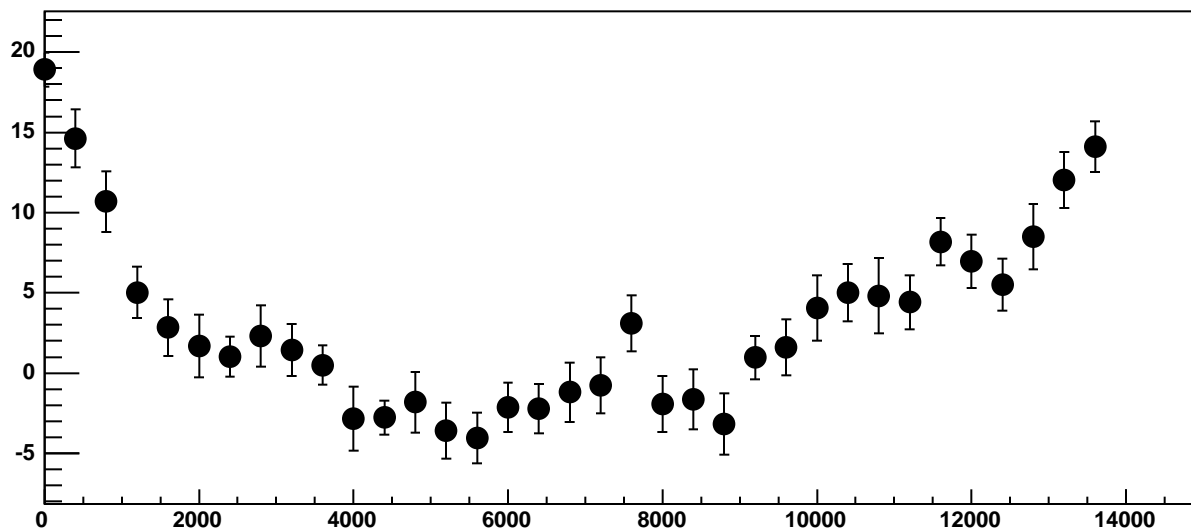


$\chi^2 / \text{ndf}$  66.67 / 23  
p0  $-578.3 \pm 0.7467$   
p1  $0.0164 \pm 0.0001191$

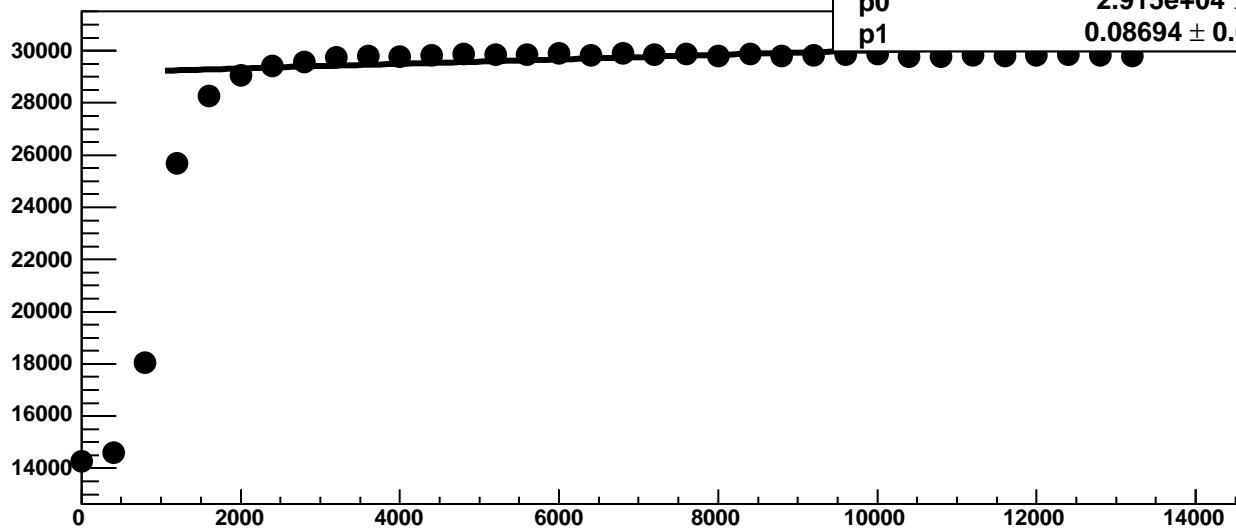
Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



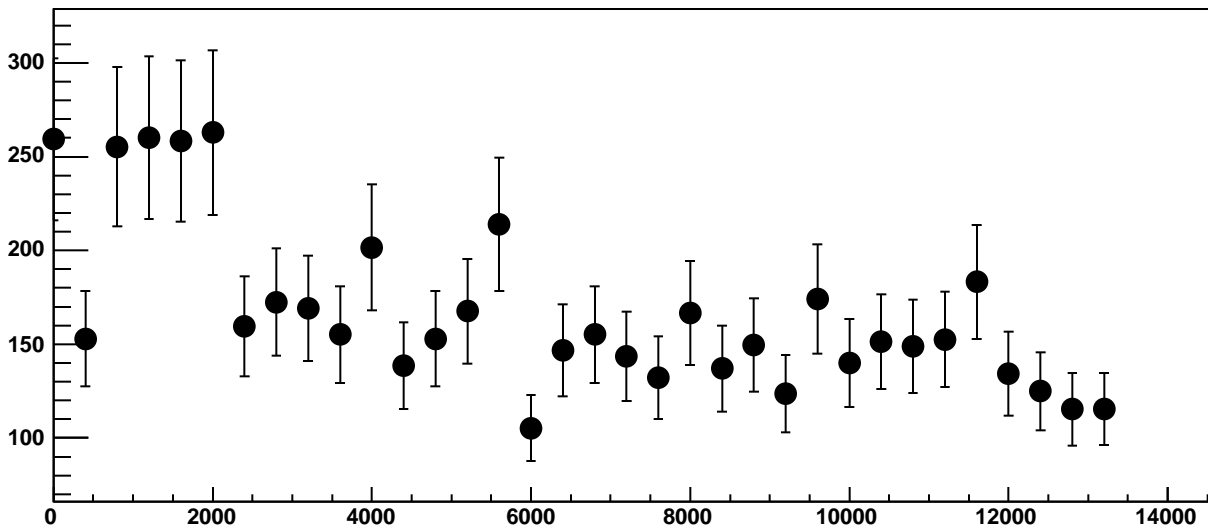
Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



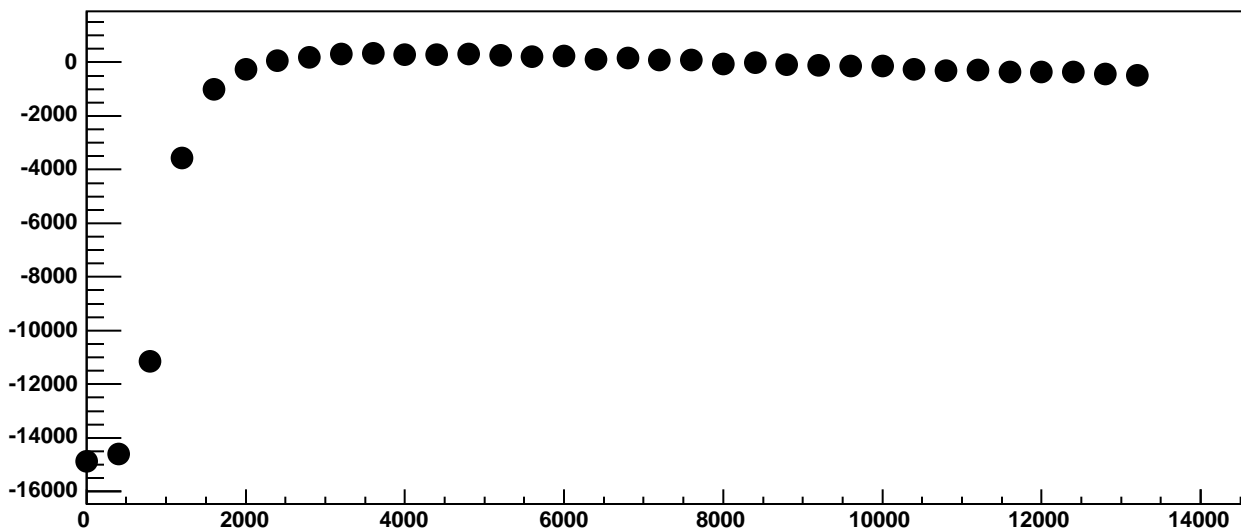
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



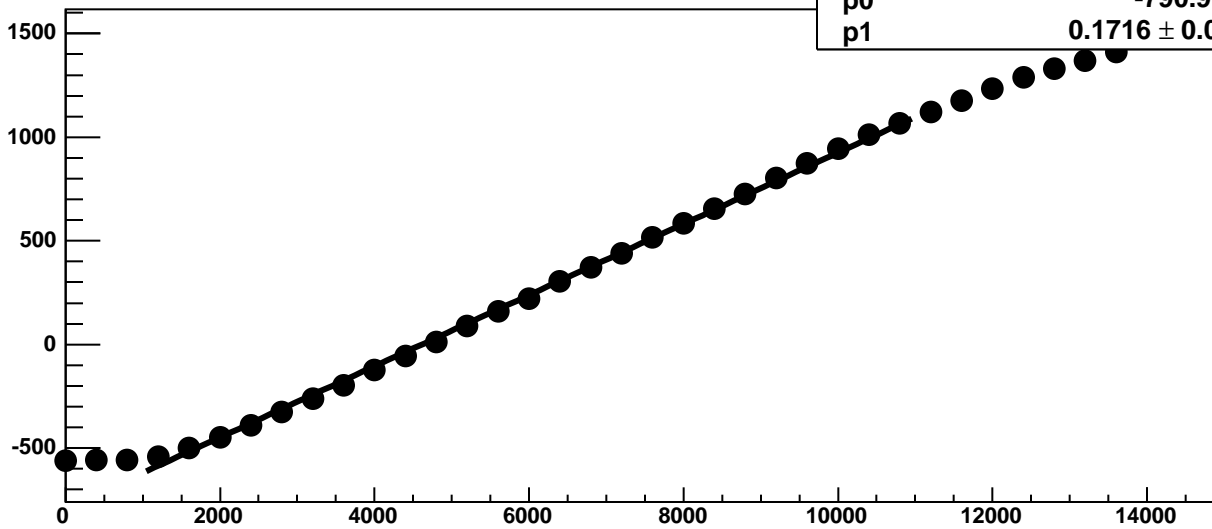
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



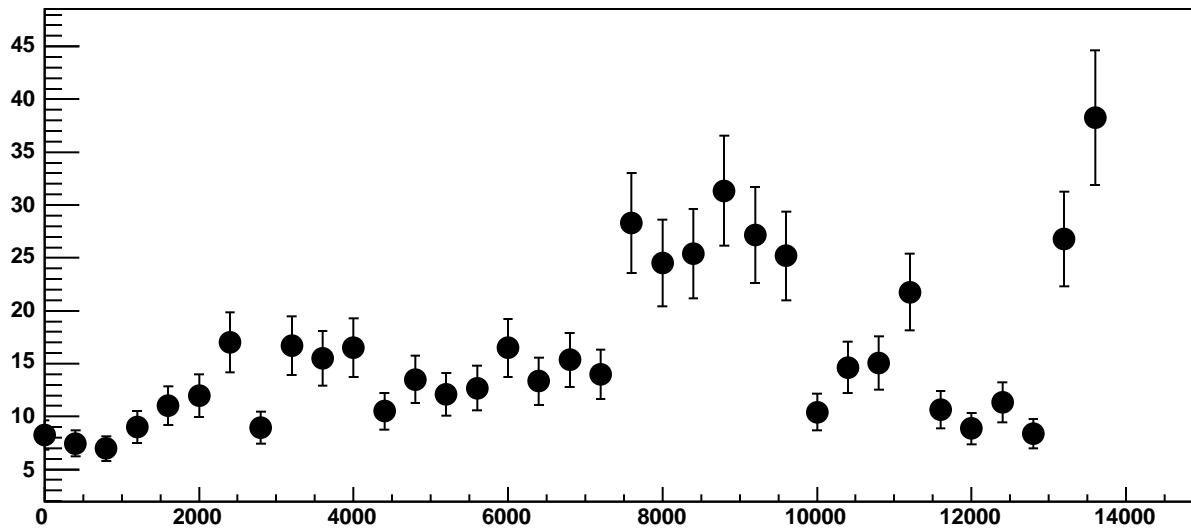
Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



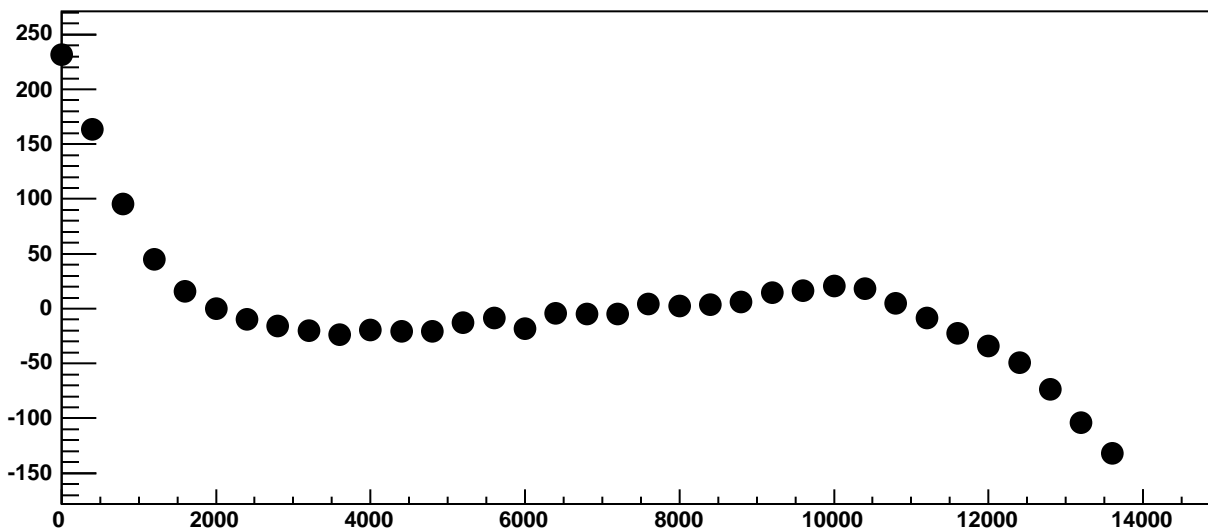
Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



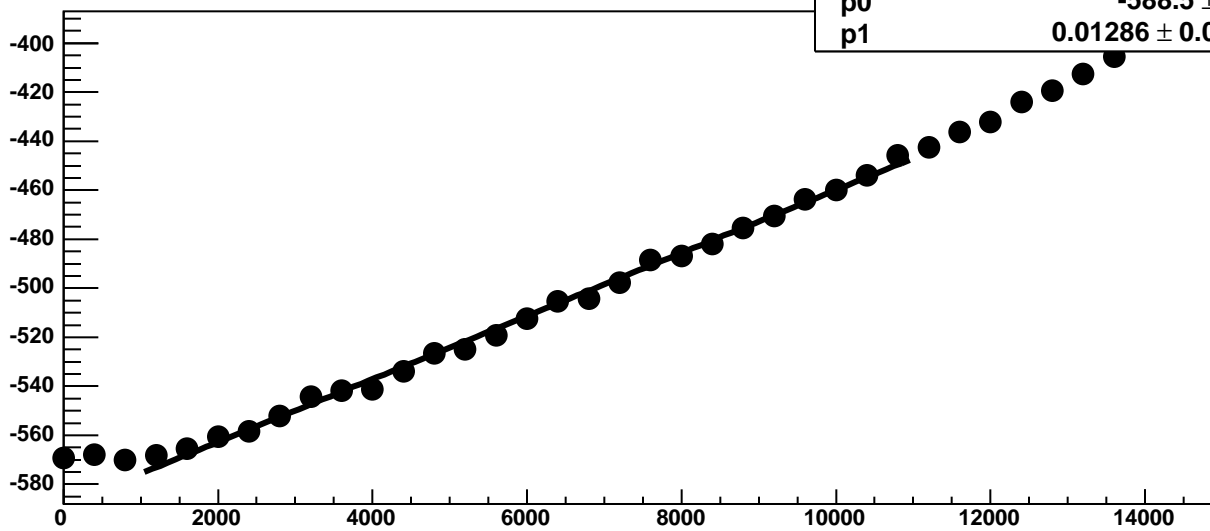
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

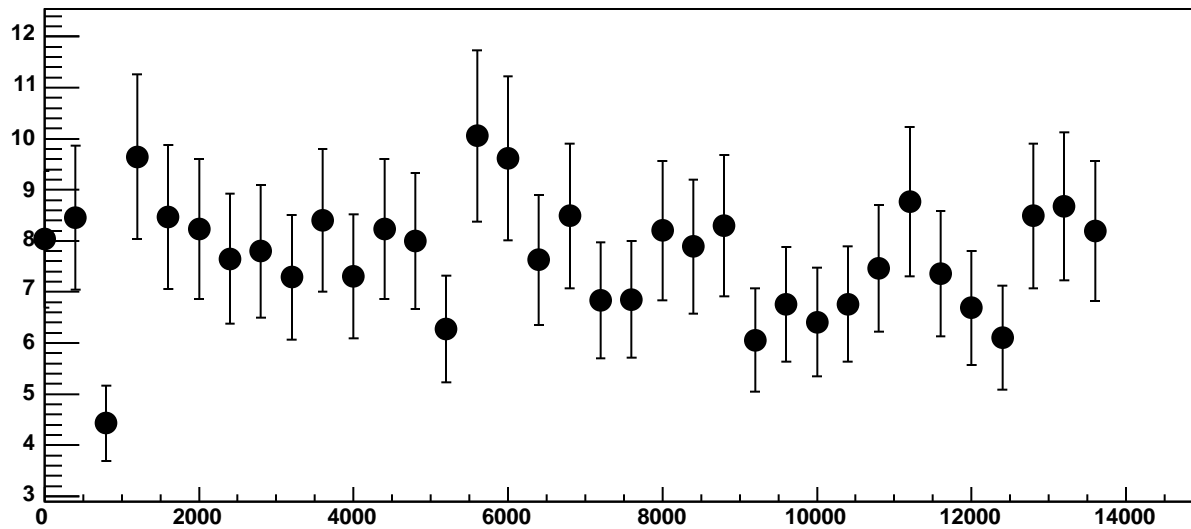


Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

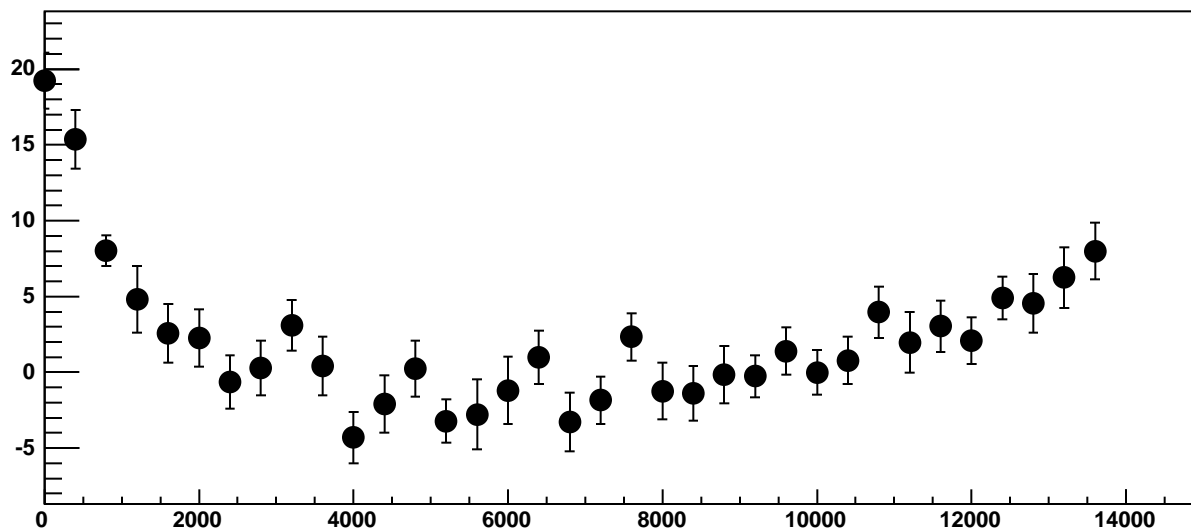


$\chi^2 / \text{ndf}$  40.28 / 23  
p0  $-588.5 \pm 0.8419$   
p1  $0.01286 \pm 0.0001205$

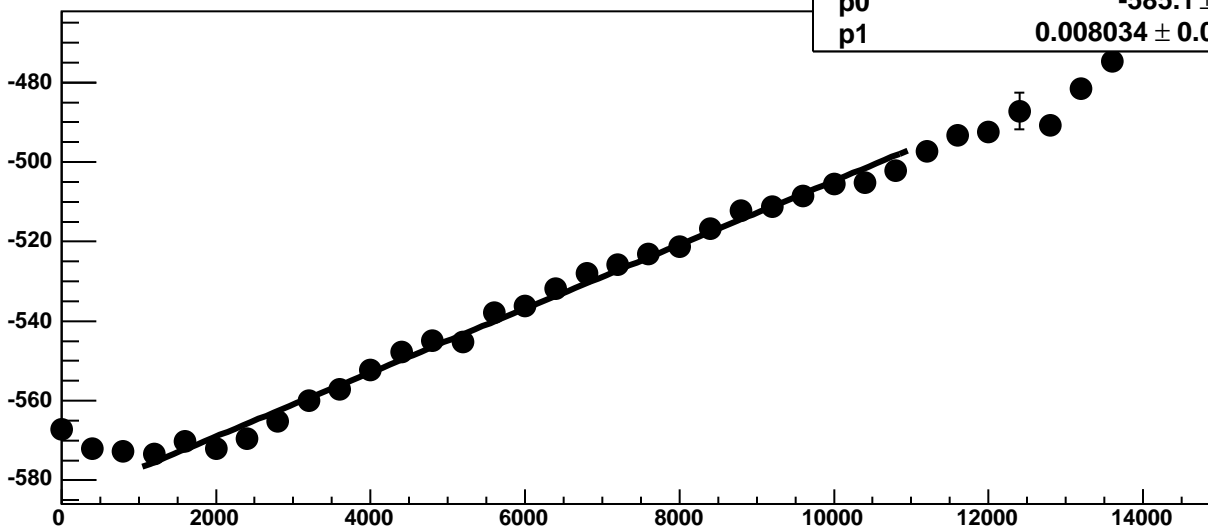
Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



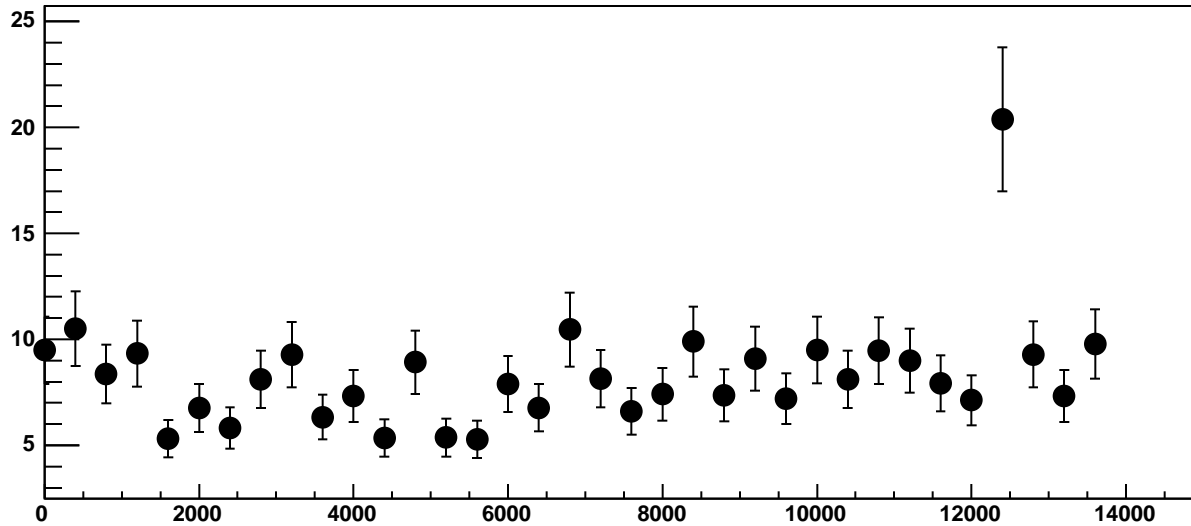
Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



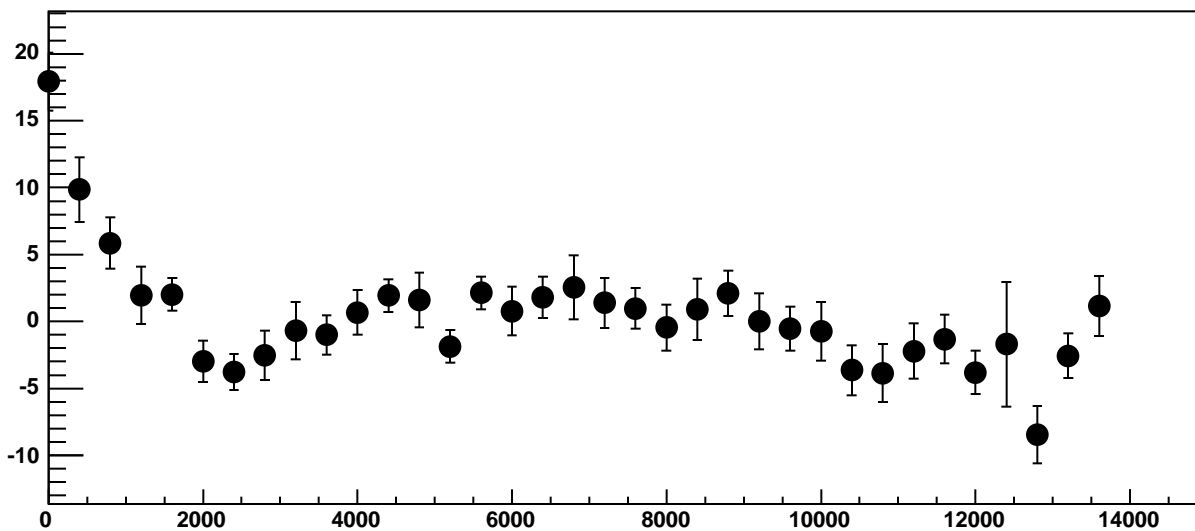
Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



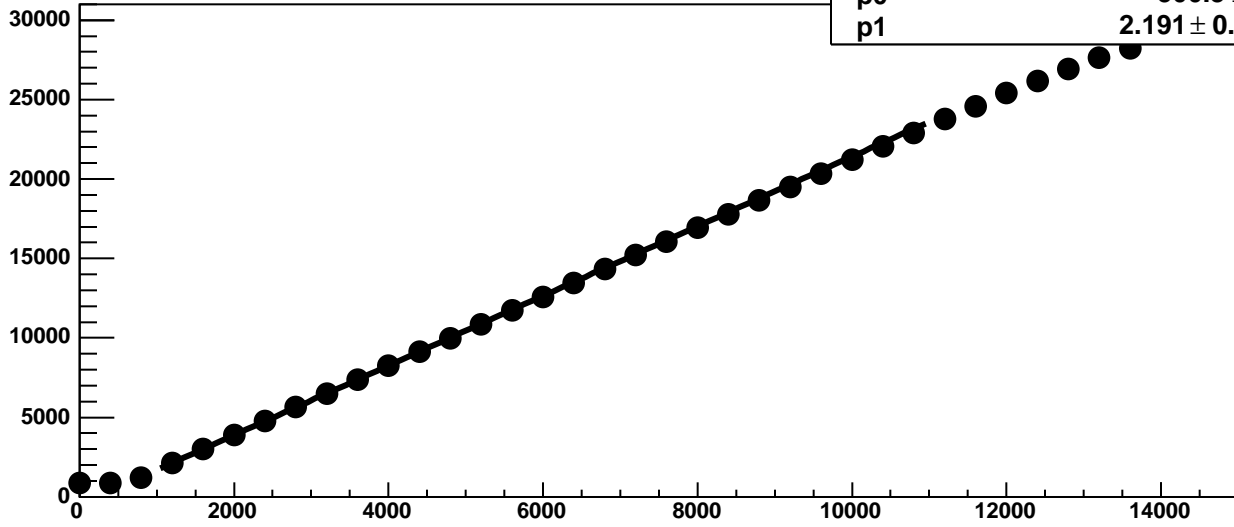
Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



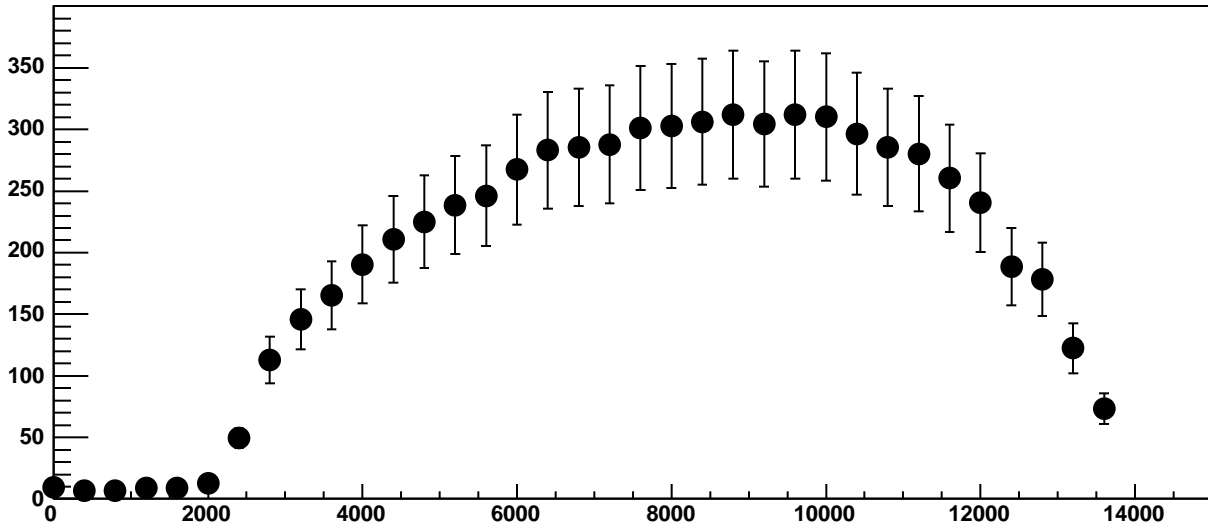
Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



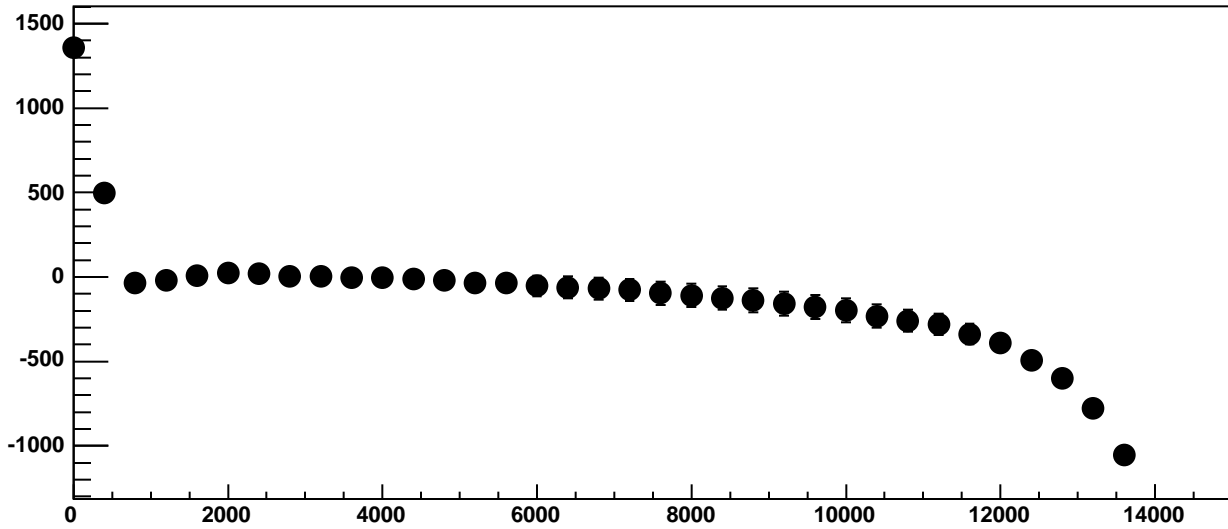
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



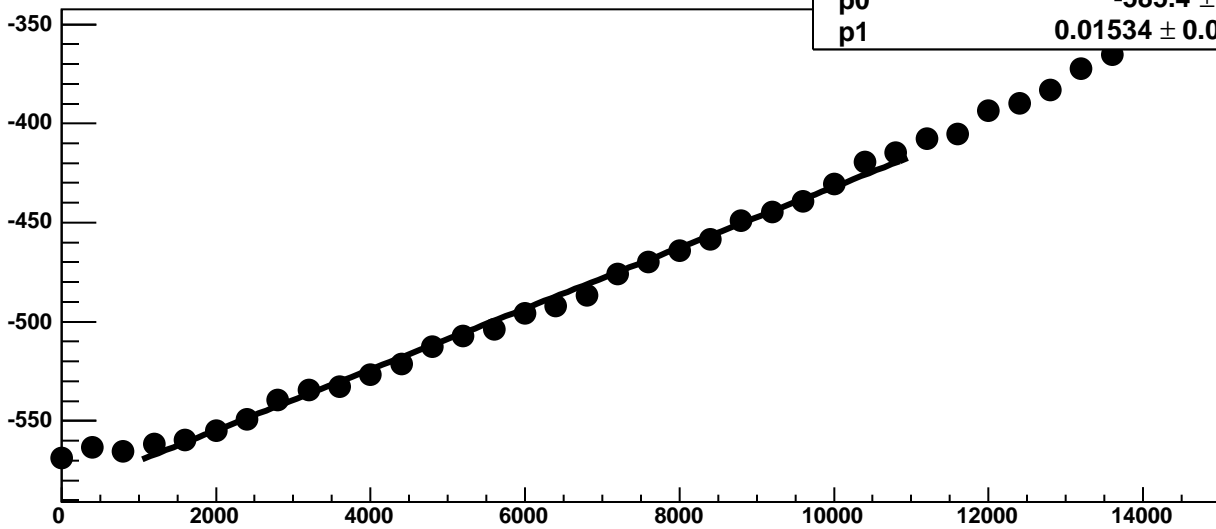
Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

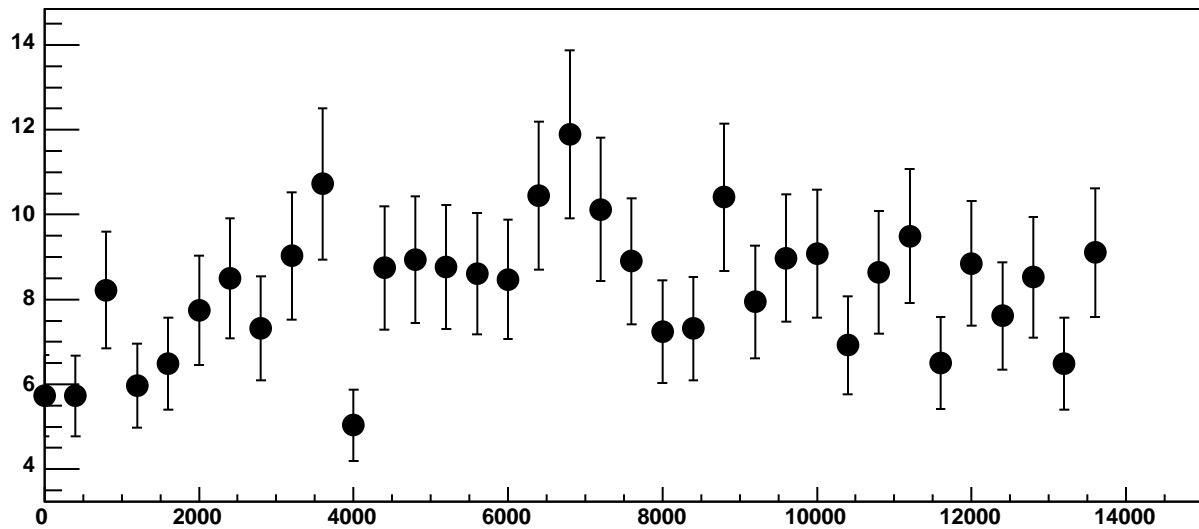


Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

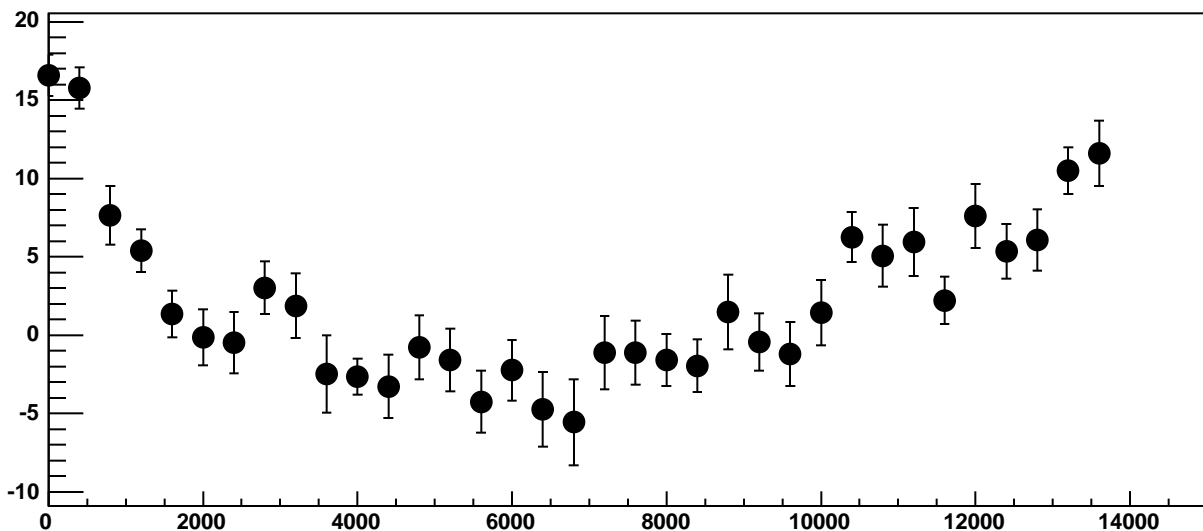


$\chi^2 / \text{ndf}$  70.49 / 23  
p0  $-585.4 \pm 0.7727$   
p1  $0.01534 \pm 0.0001211$

Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



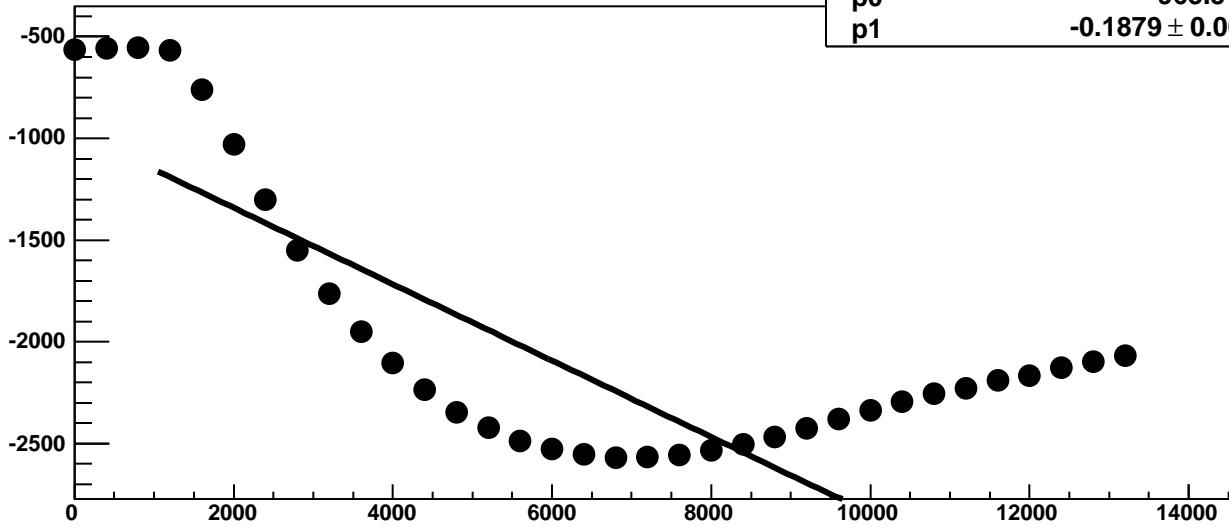
Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



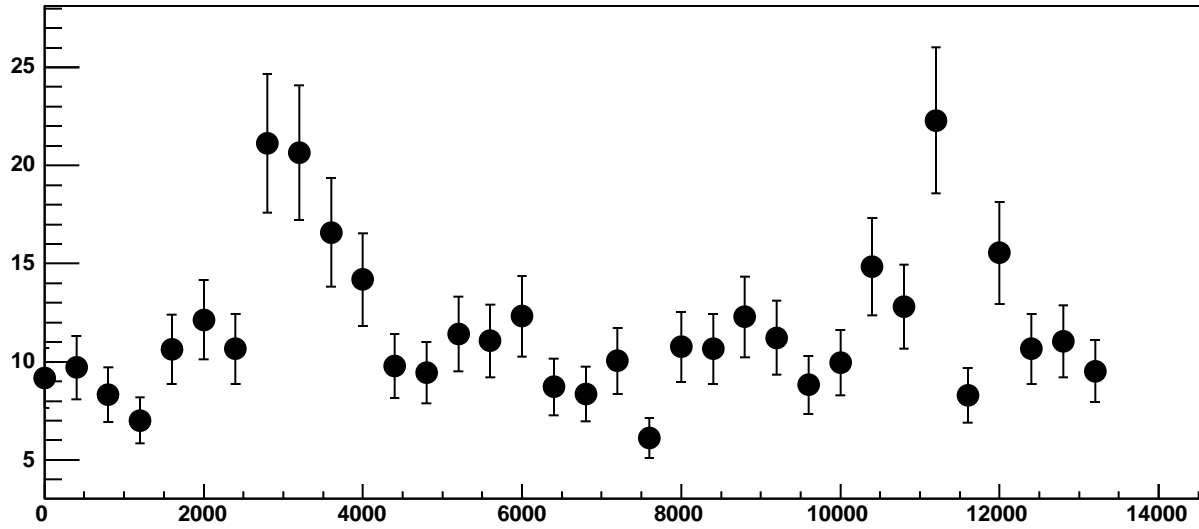


Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

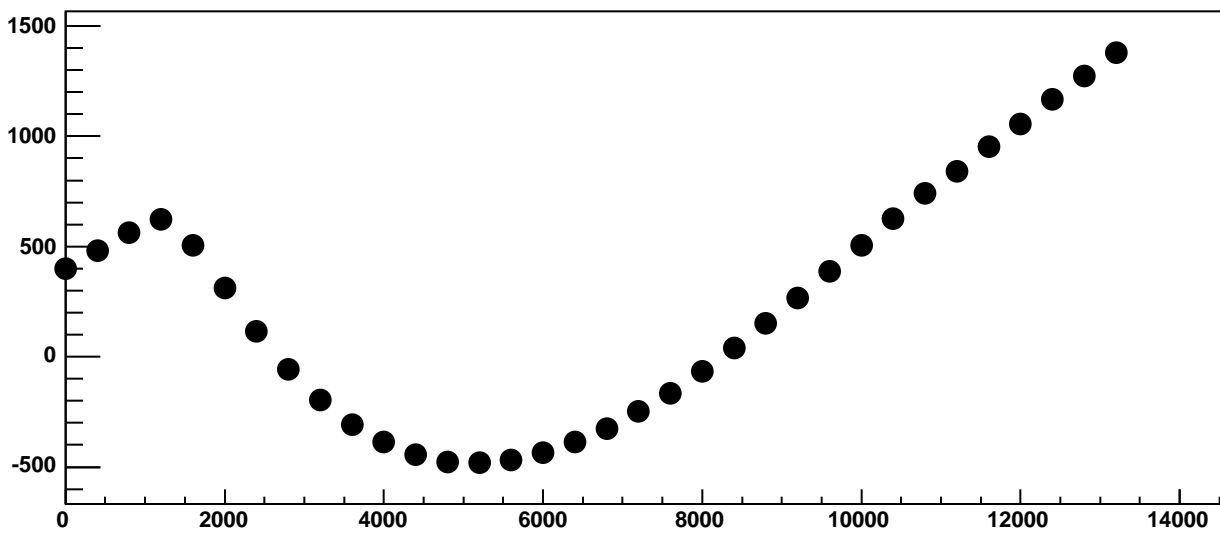
$\chi^2 / \text{ndf}$  6.984e+05 / 23  
p0 -965.5 ± 1.131  
p1 -0.1879 ± 0.0001683



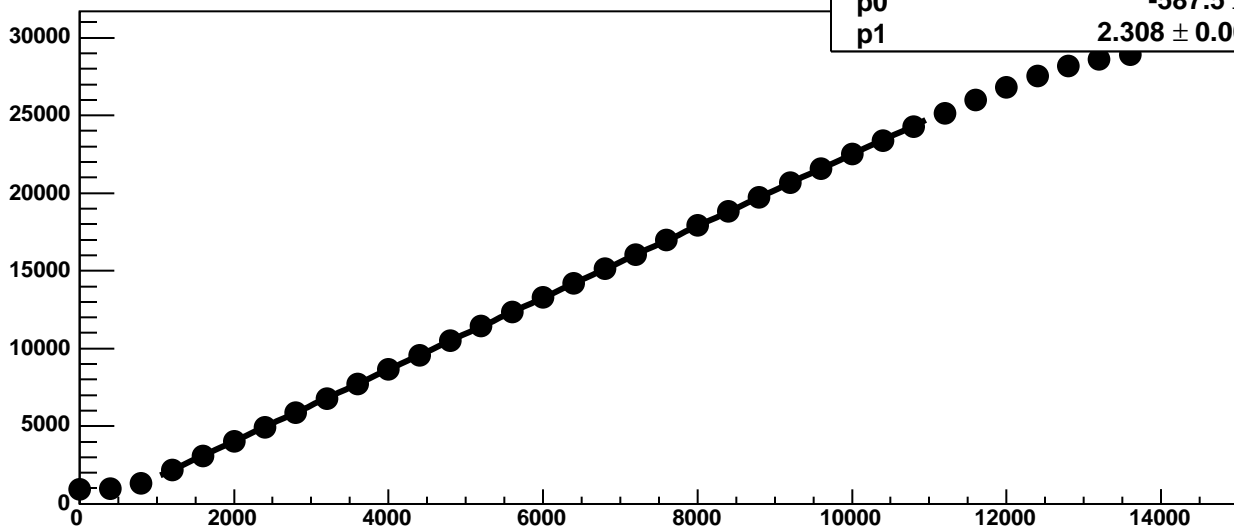
Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



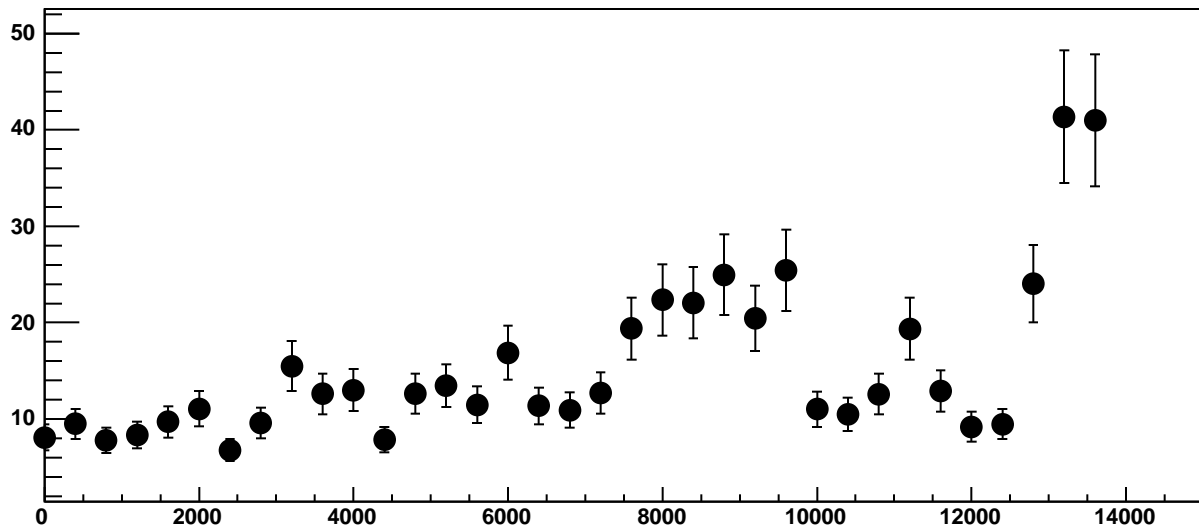
Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



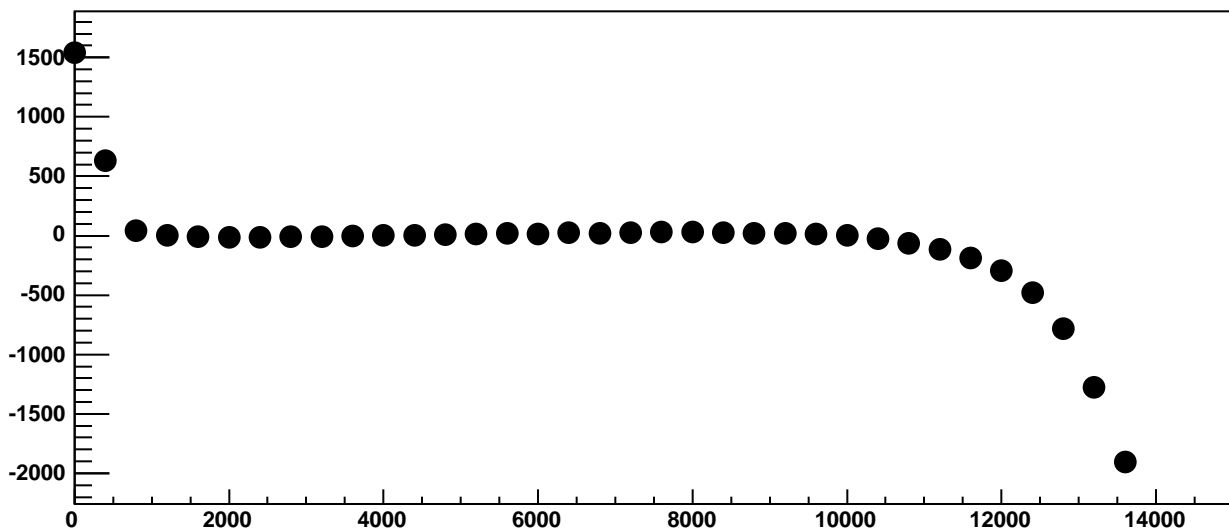
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



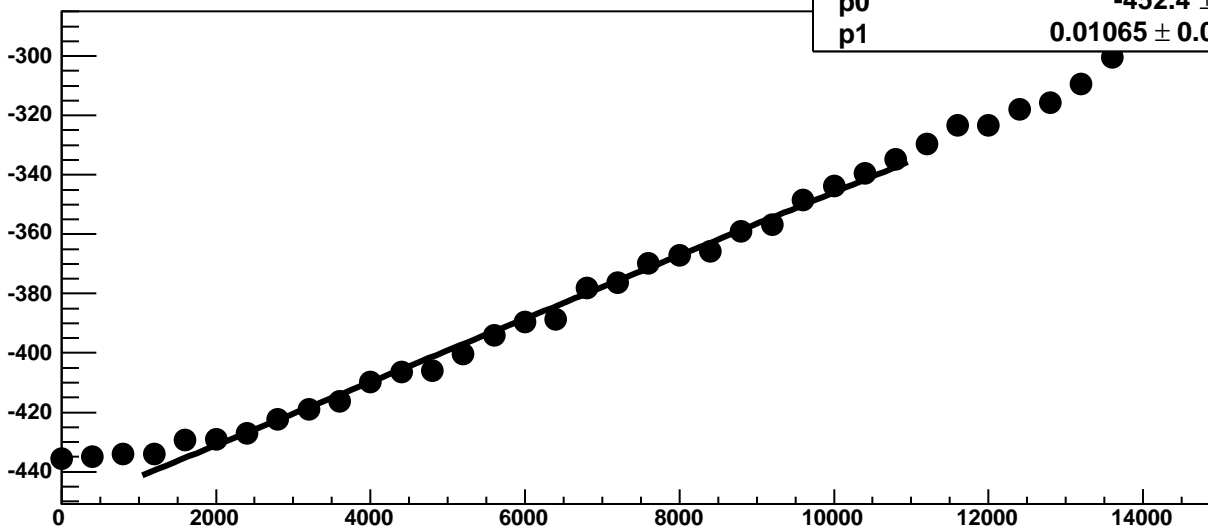
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



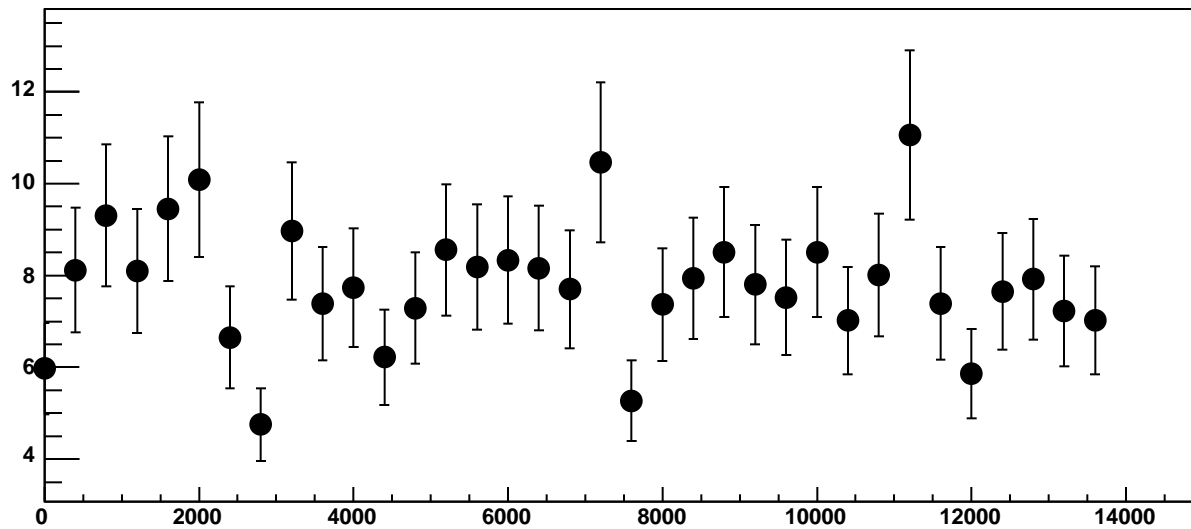
Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



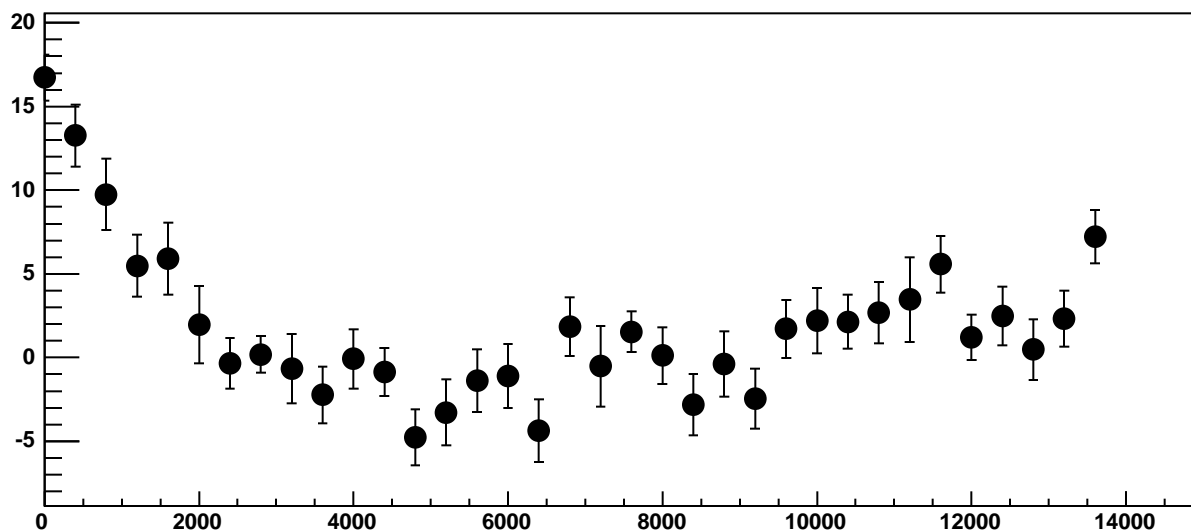
Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



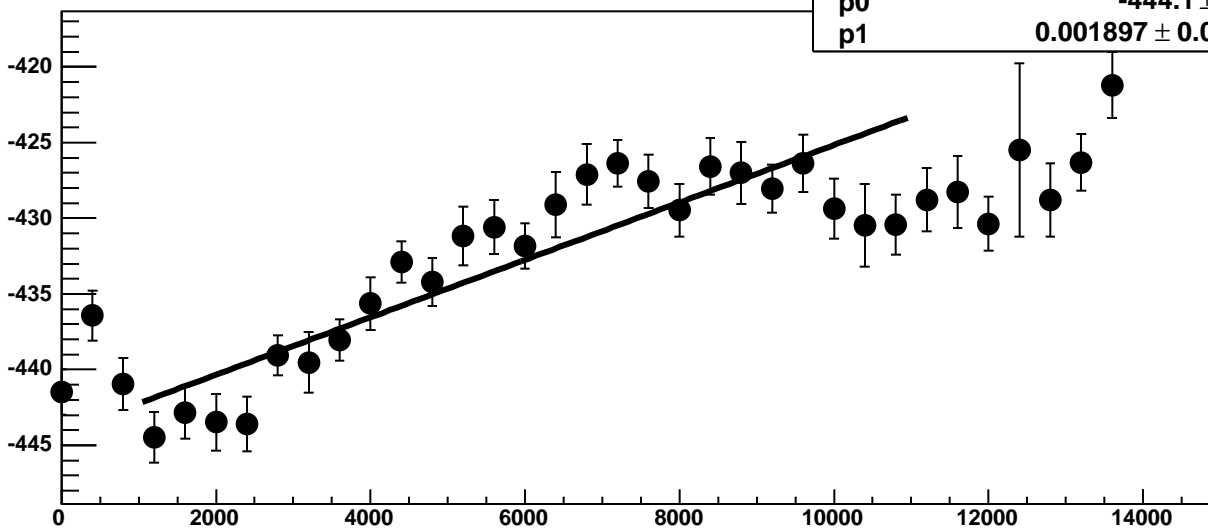
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



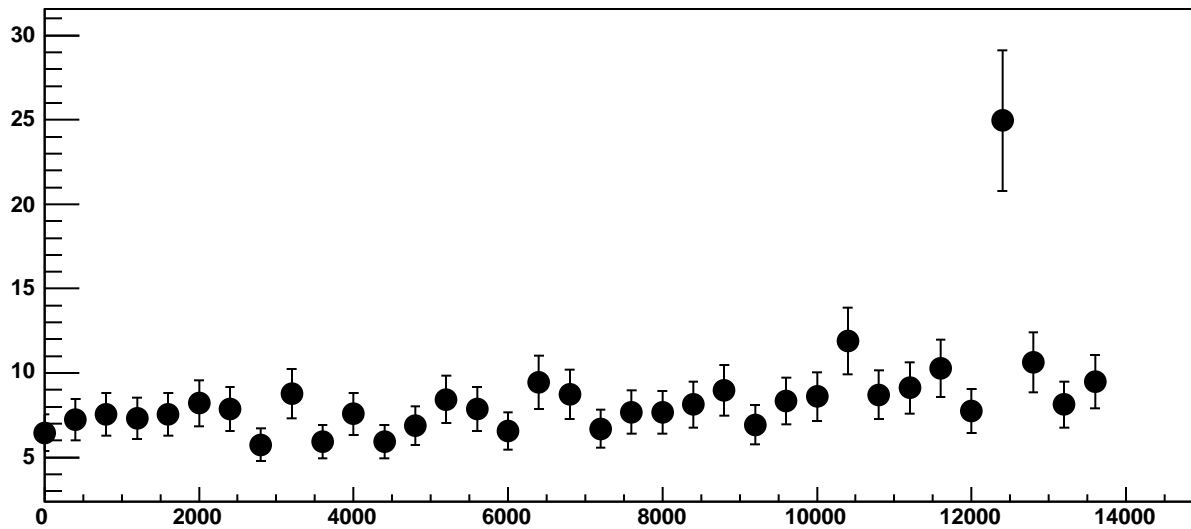
Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



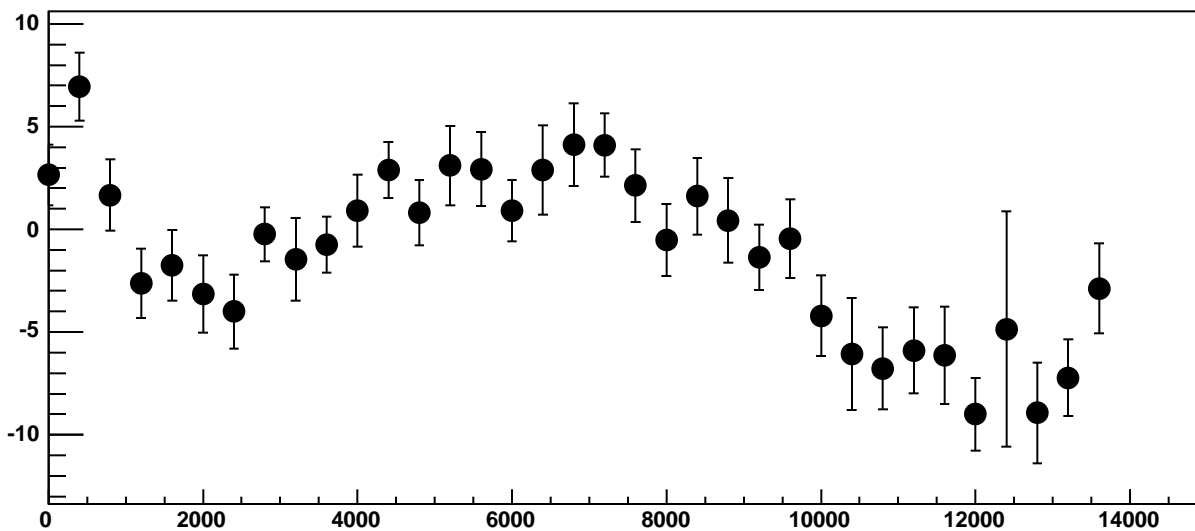
Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



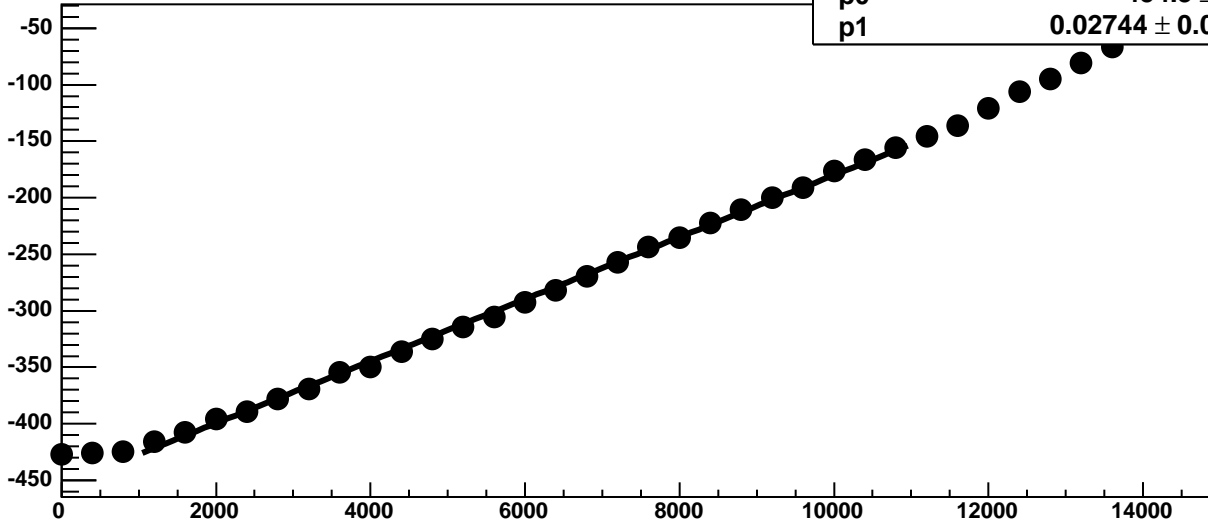
Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

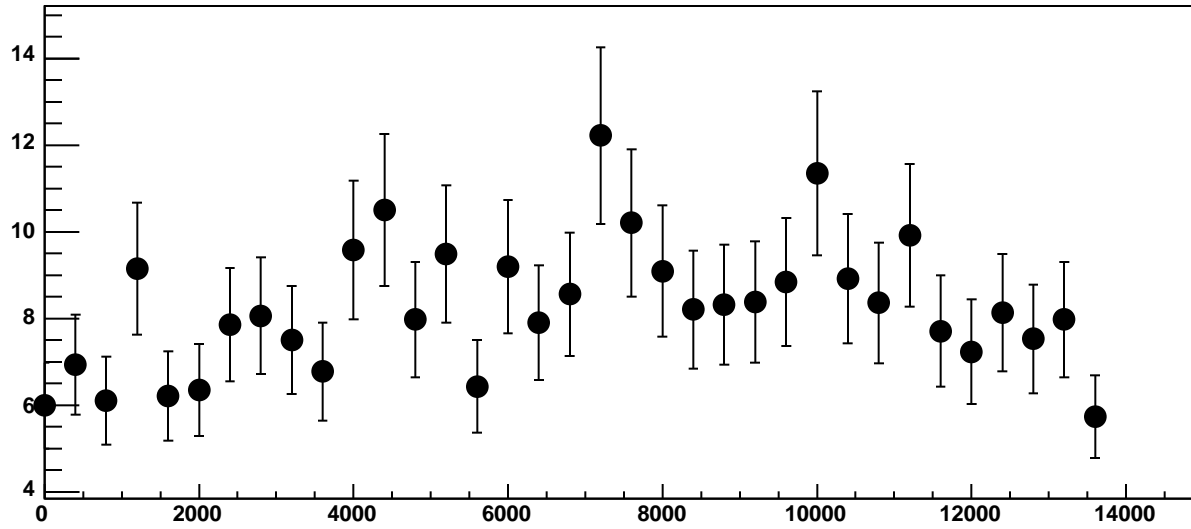


Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

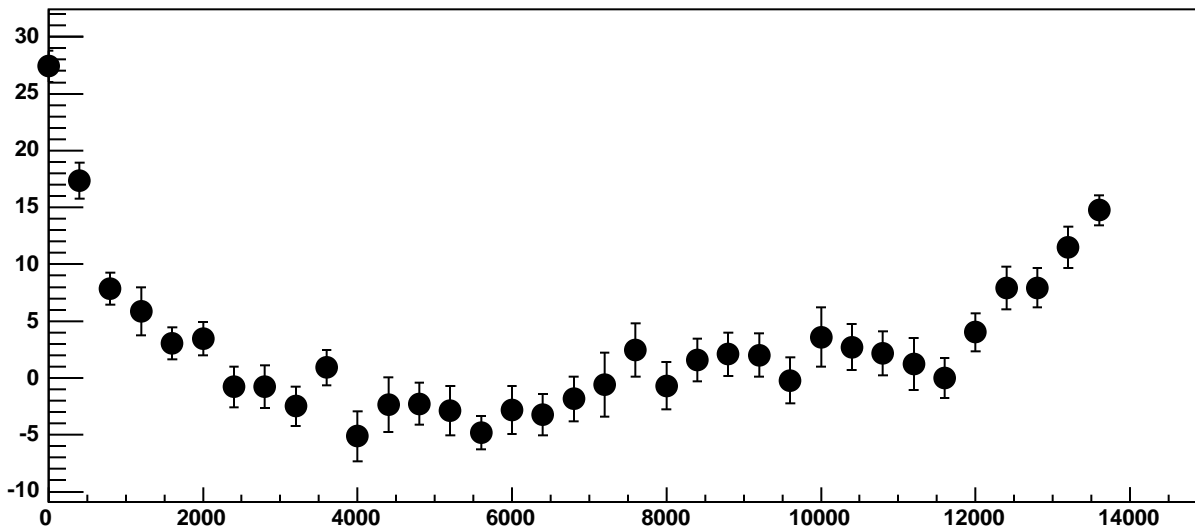


$\chi^2 / \text{ndf}$  55.99 / 23  
p0  $-454.3 \pm 0.8129$   
p1  $0.02744 \pm 0.0001296$

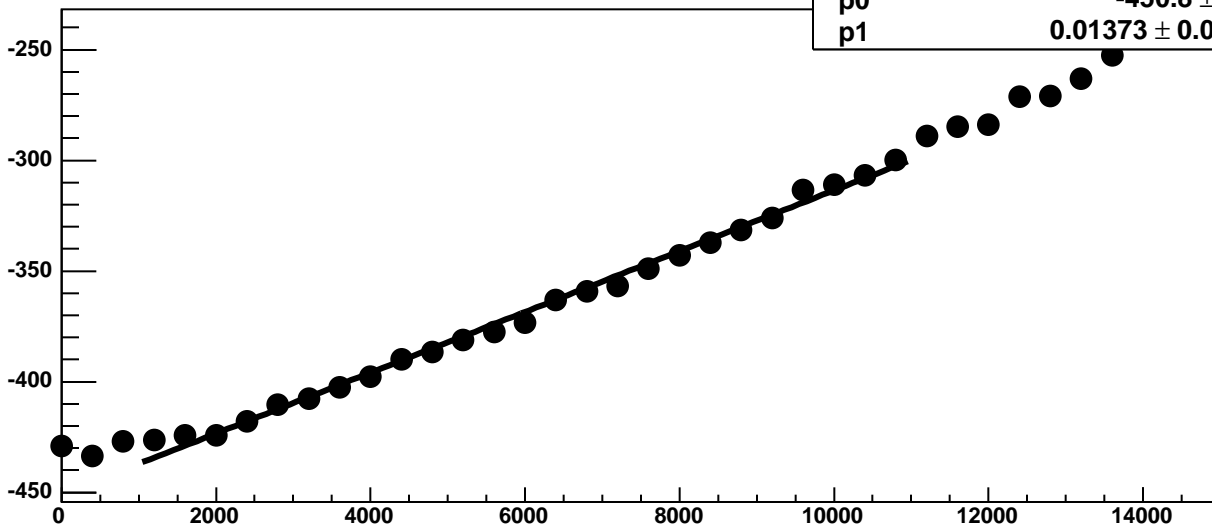
Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

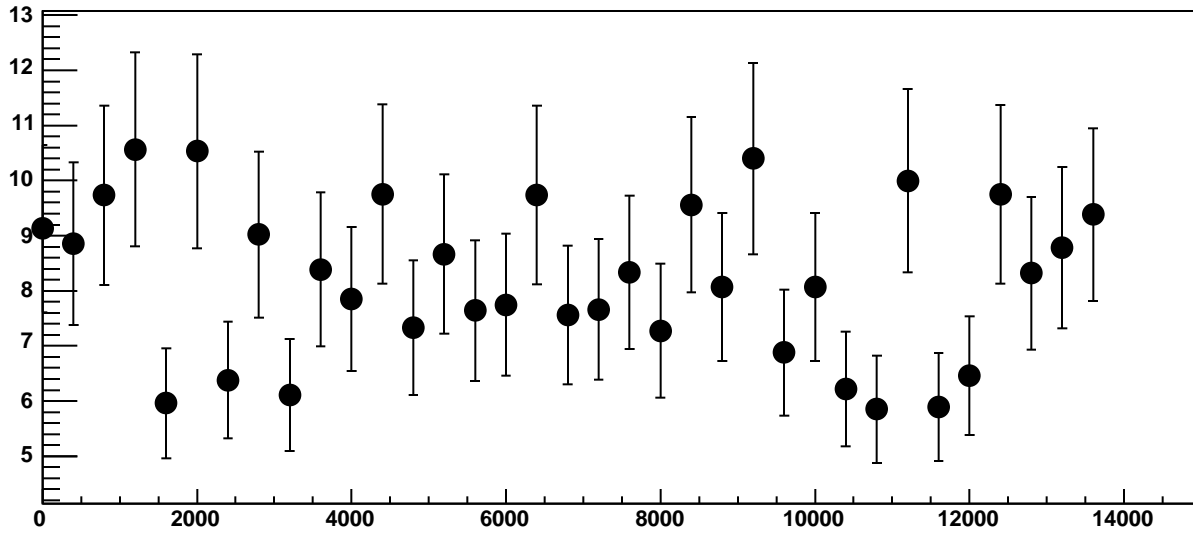


Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

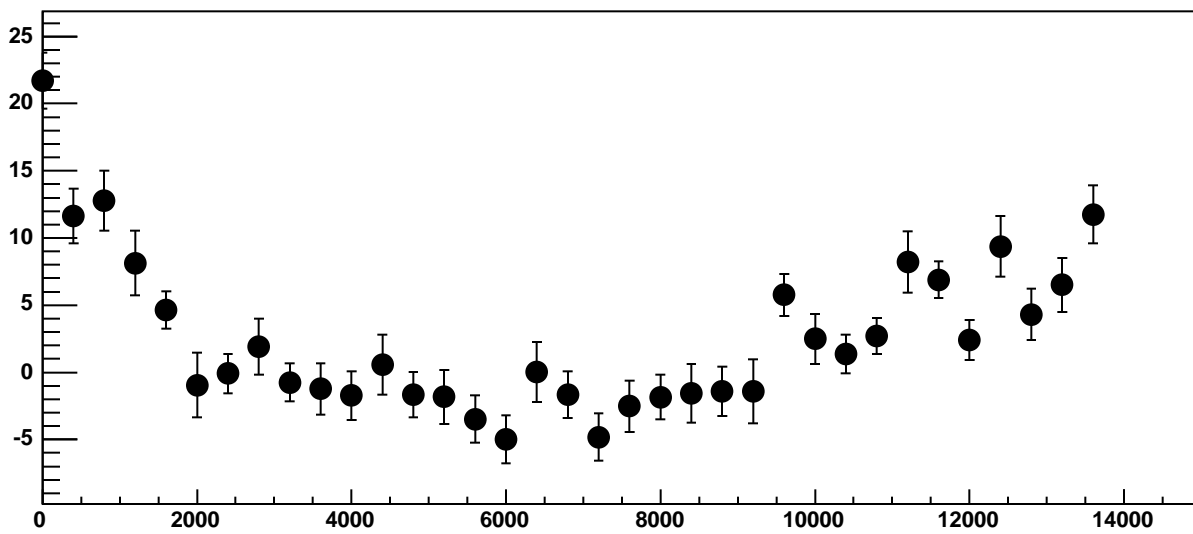


$\chi^2 / \text{ndf}$  71.98 / 23  
p0  $-450.8 \pm 0.7986$   
p1  $0.01373 \pm 0.0001166$

Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

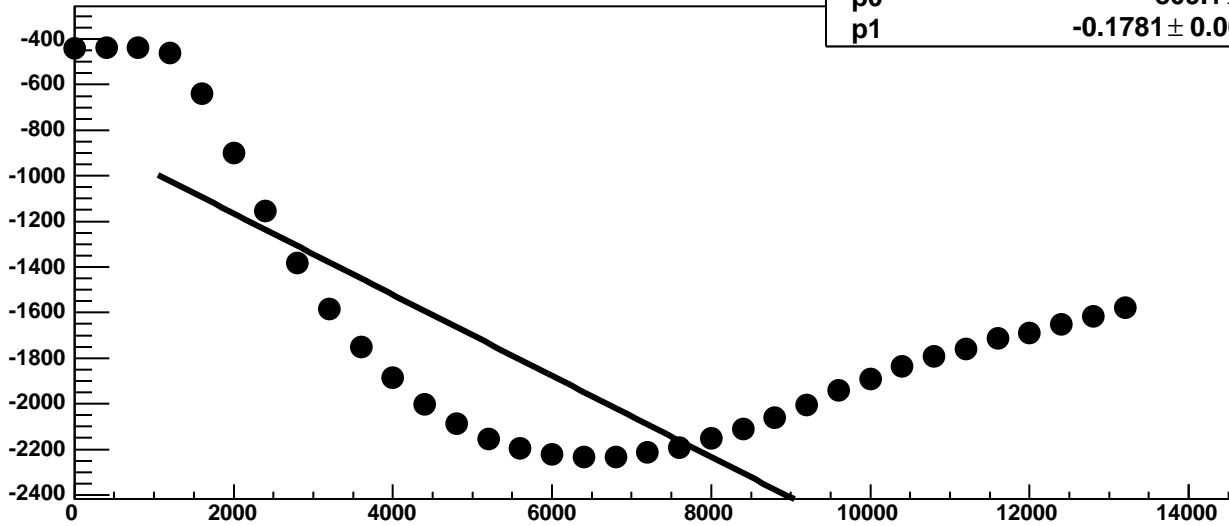


Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

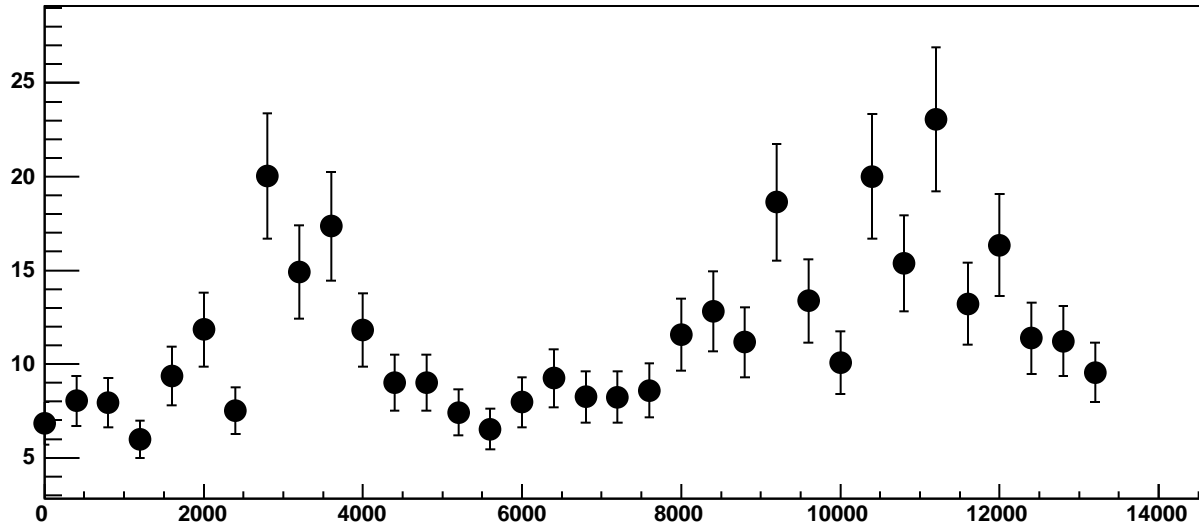


Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

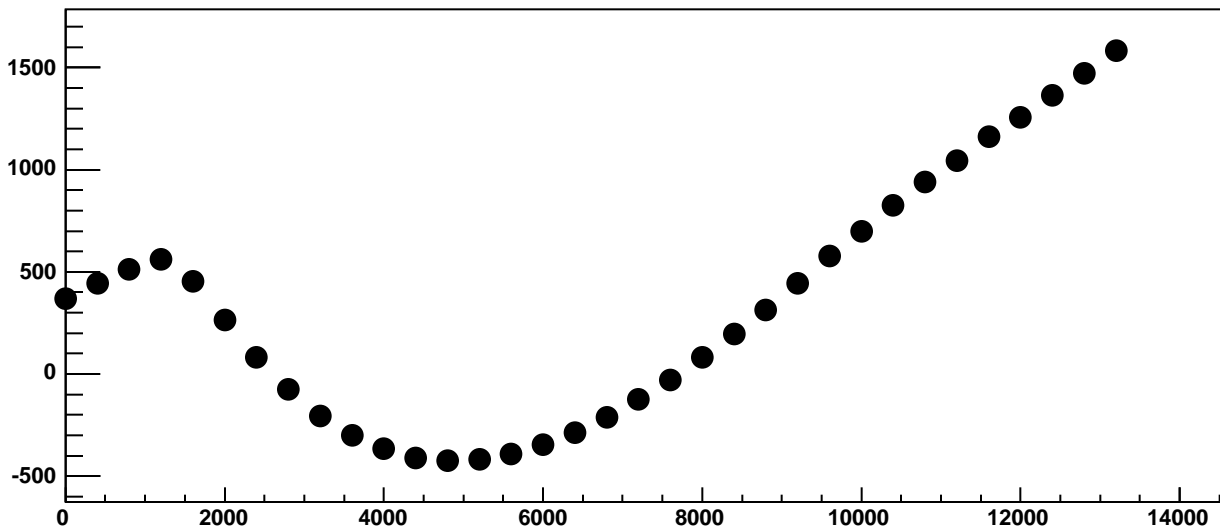
$\chi^2 / \text{ndf}$  7.929e+05 / 23  
p0 -809.1 ± 1.003  
p1 -0.1781 ± 0.0001677



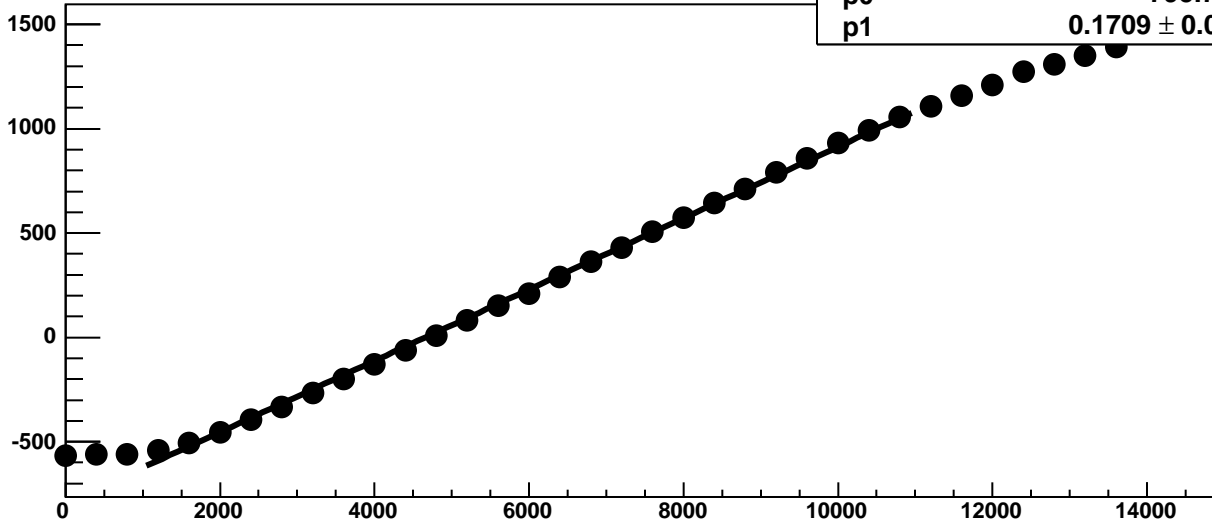
Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



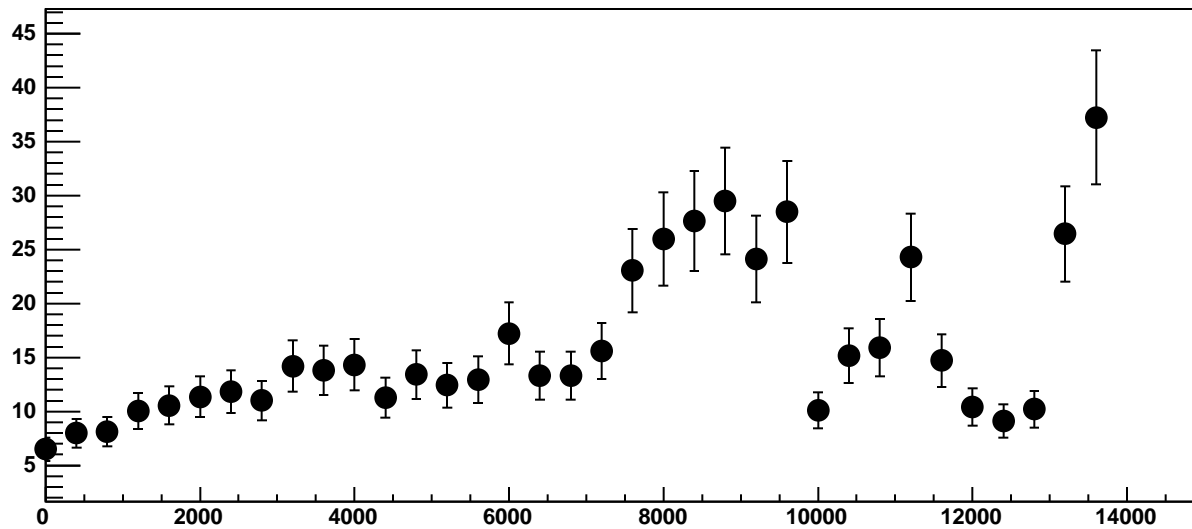
Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



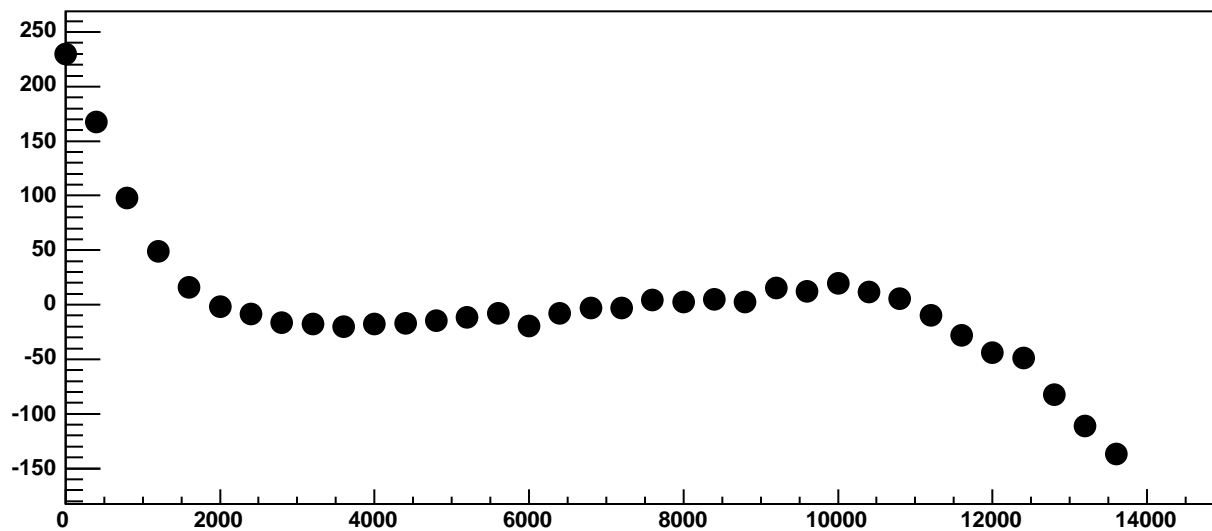
Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

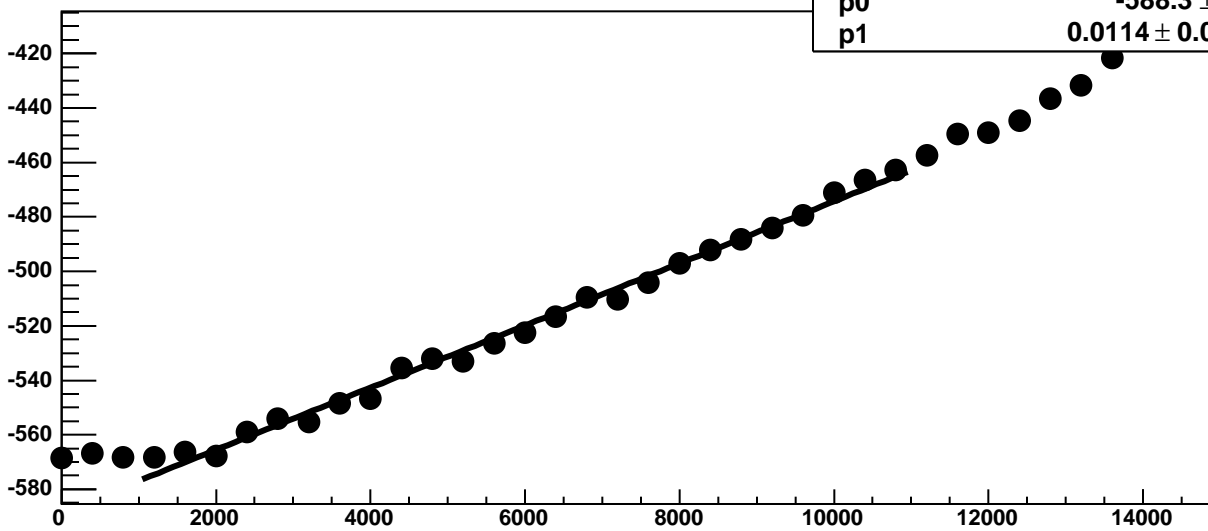


Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



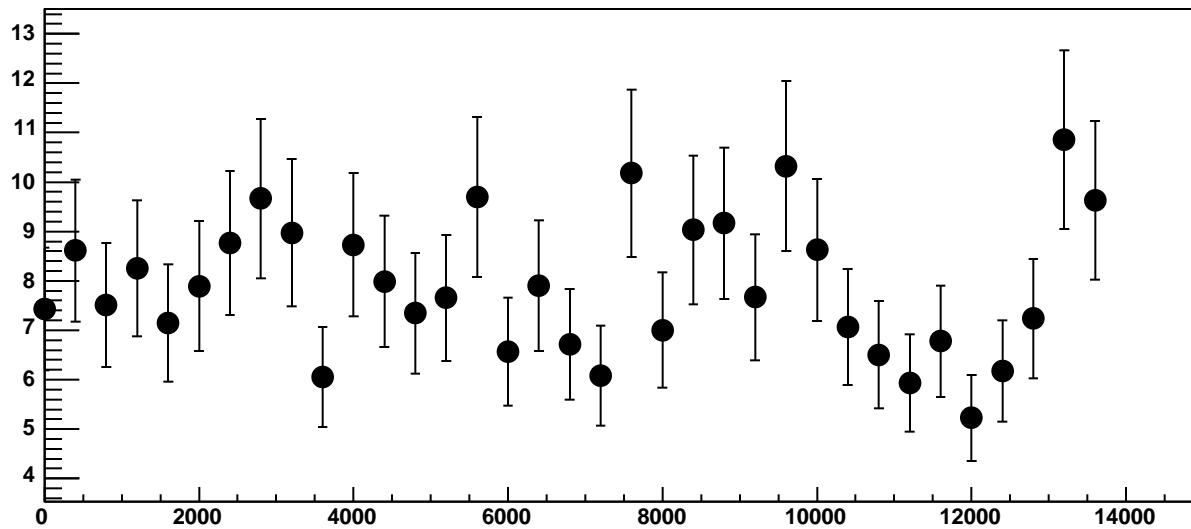


Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

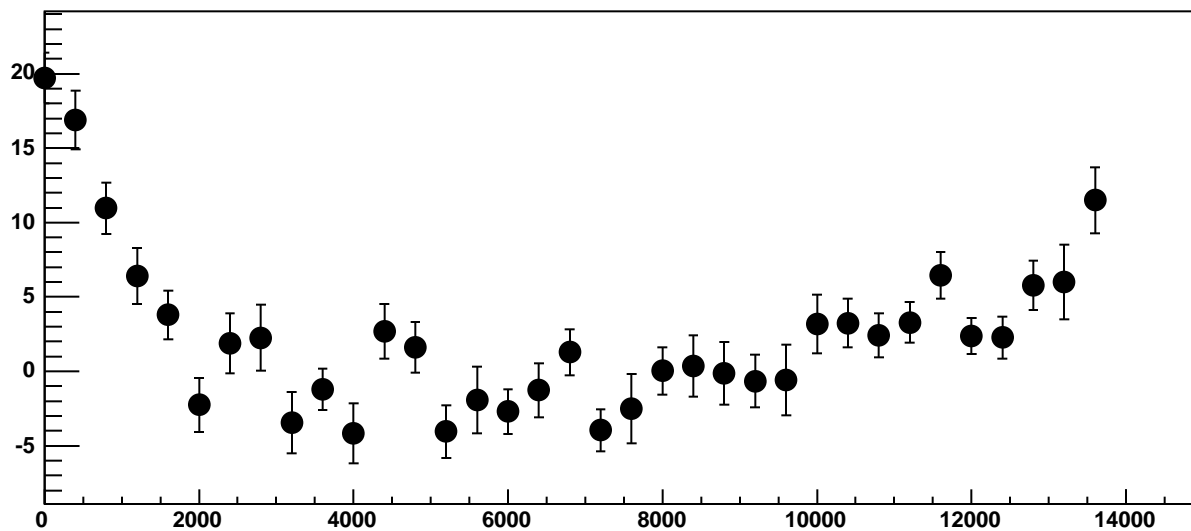


$\chi^2 / \text{ndf}$  60.27 / 23  
p0  $-588.3 \pm 0.8336$   
p1  $0.0114 \pm 0.0001243$

Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

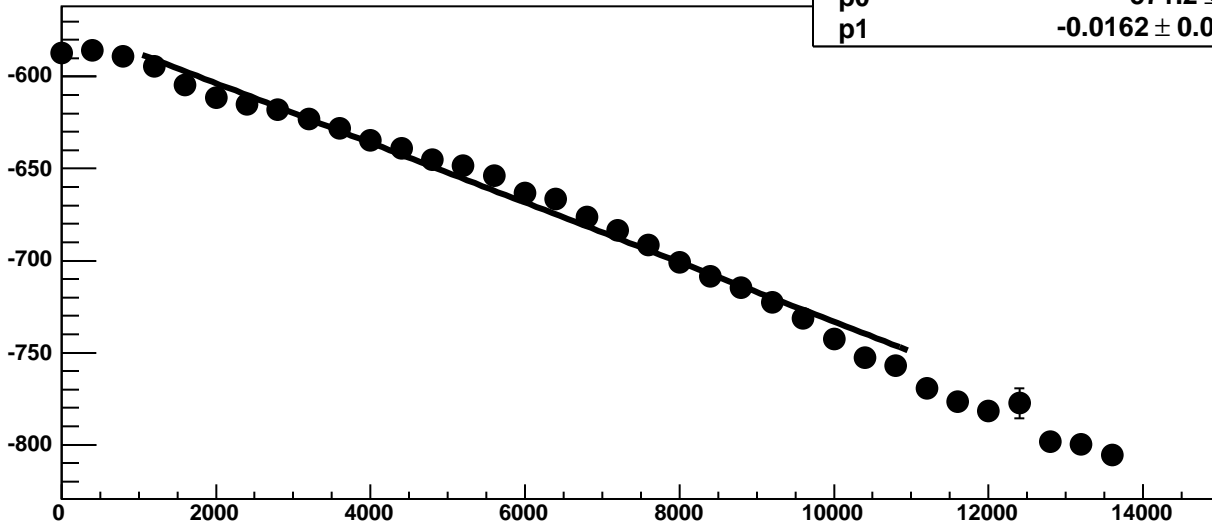


Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

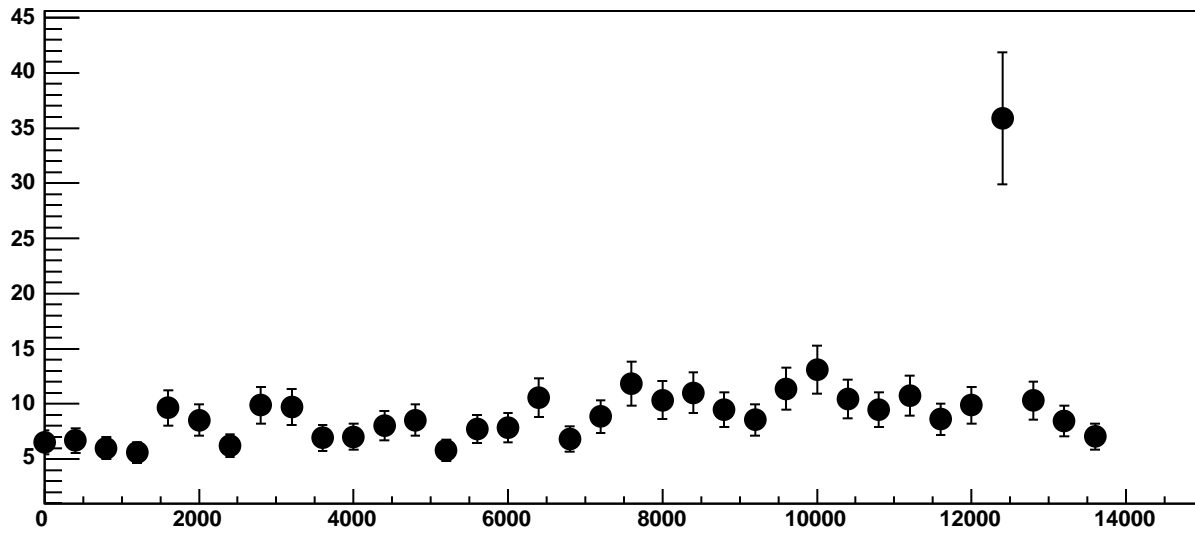


Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

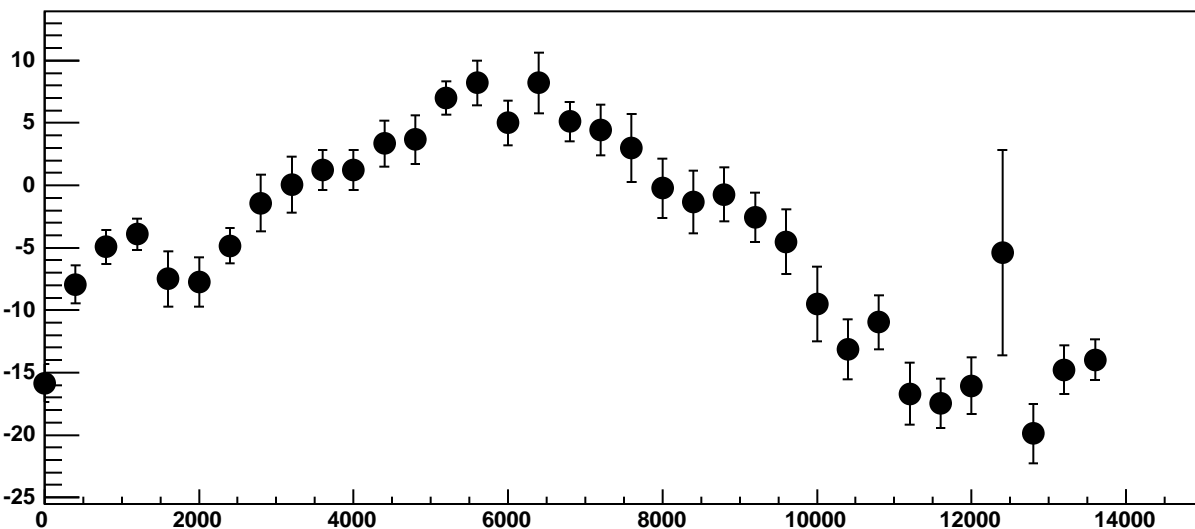
$\chi^2 / \text{ndf}$  211.9 / 23  
p0  $-571.2 \pm 0.8191$   
p1  $-0.0162 \pm 0.0001379$



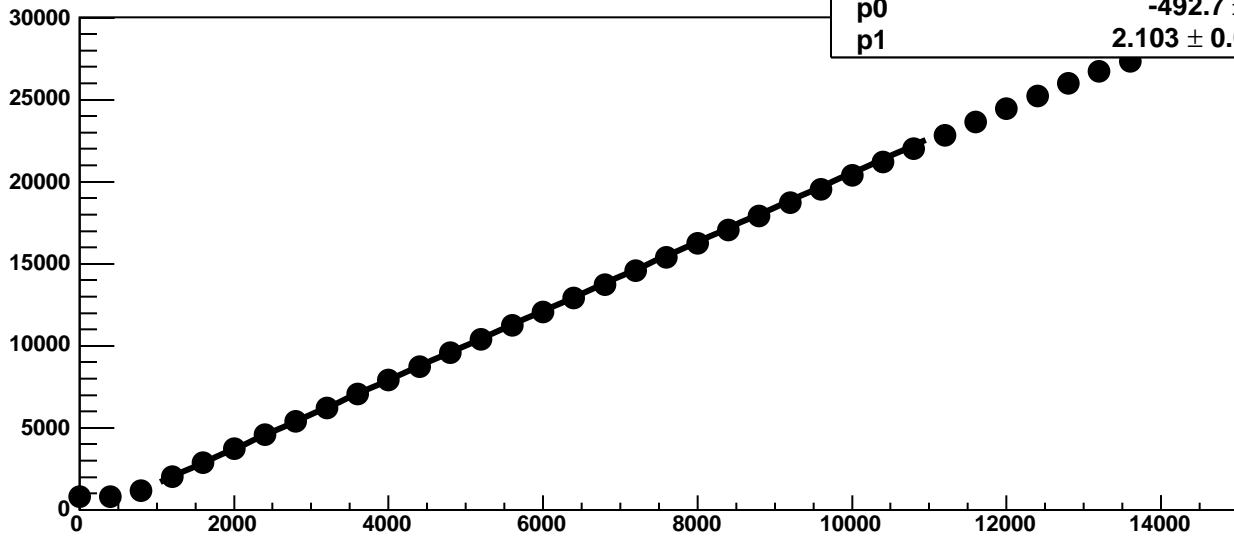
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



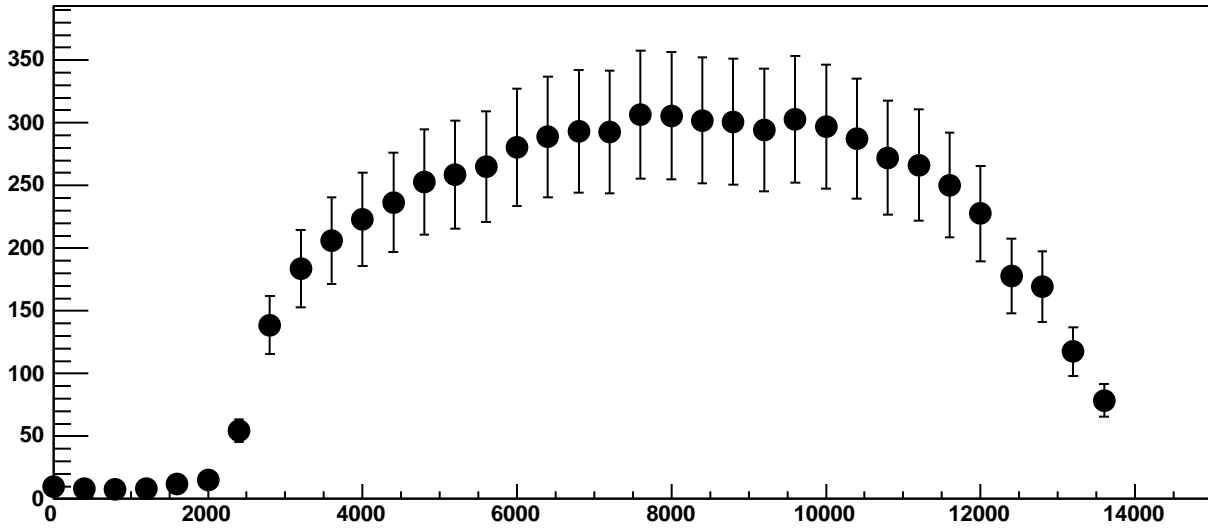
Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



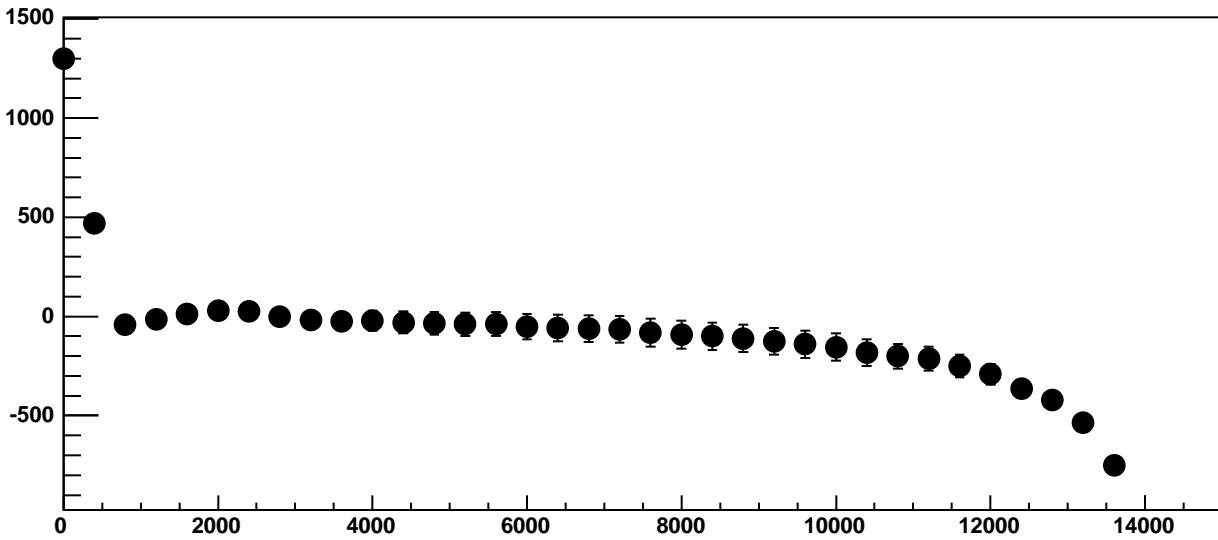
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



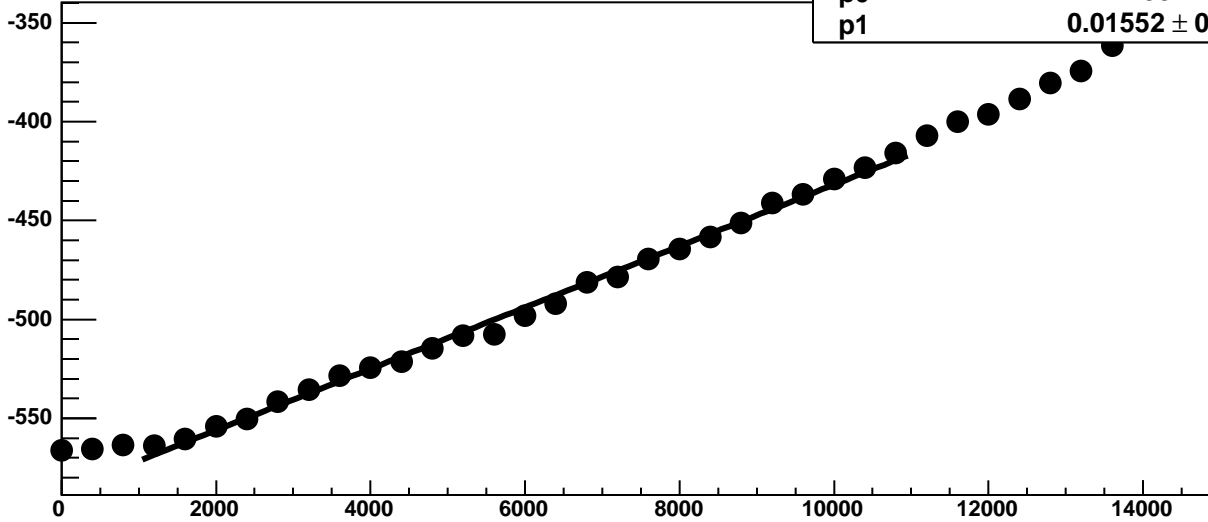
Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

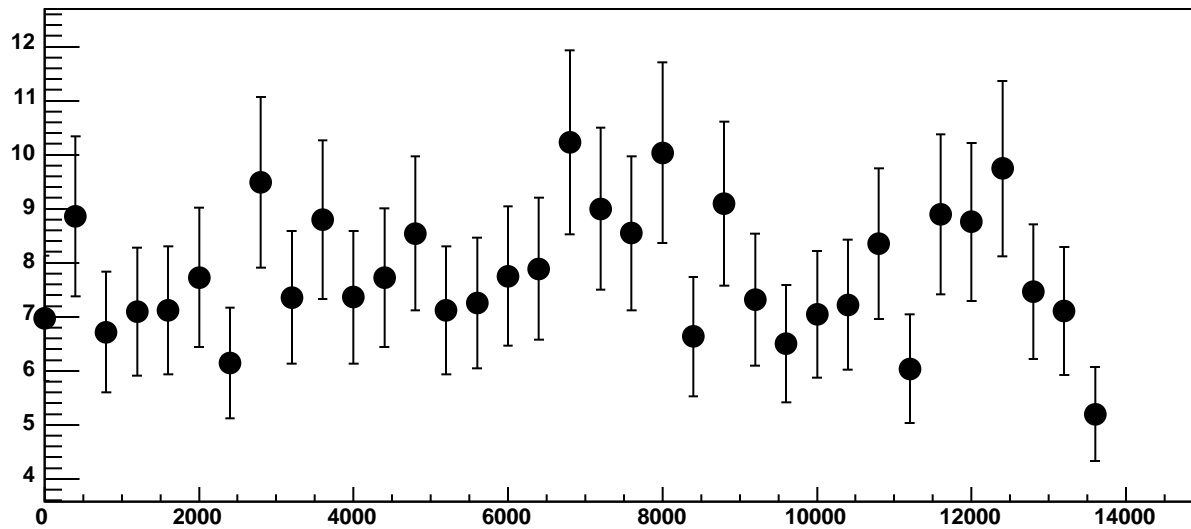


Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

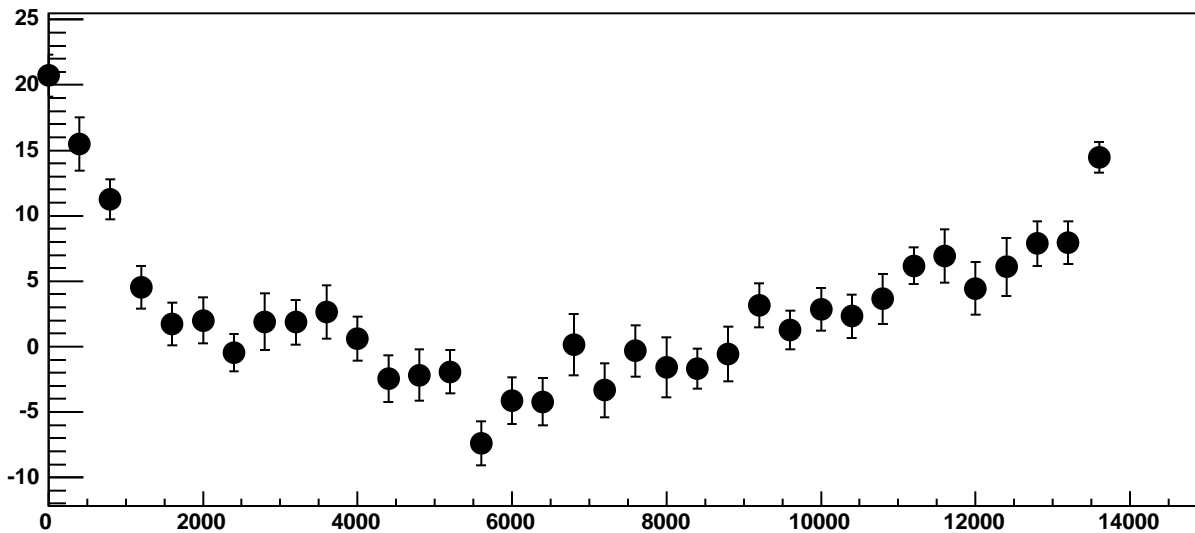


$\chi^2 / \text{ndf}$  66.55 / 23  
p0  $-587.1 \pm 0.784$   
p1  $0.01552 \pm 0.000118$

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

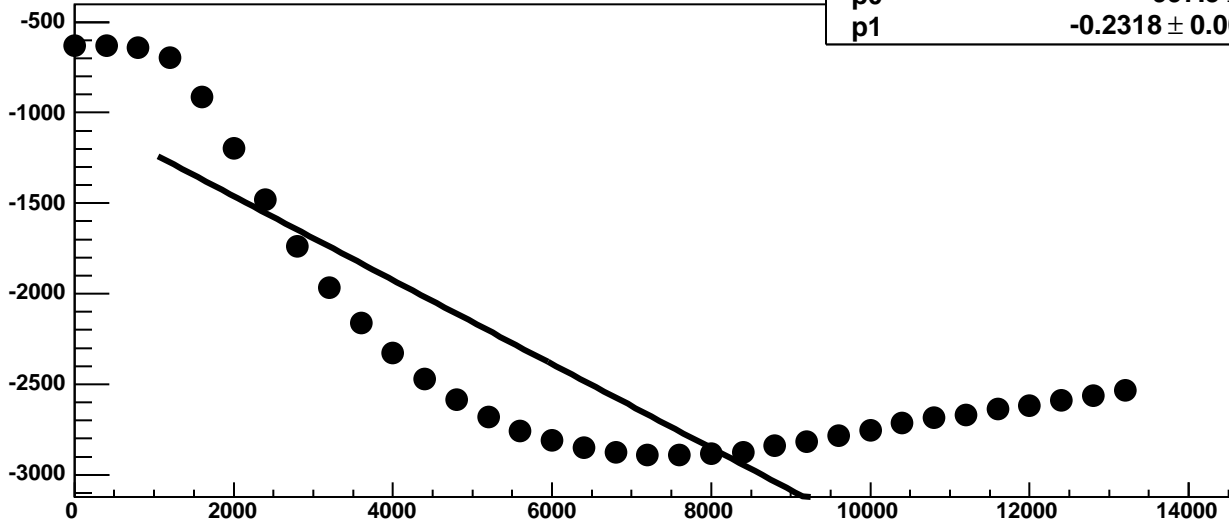


Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

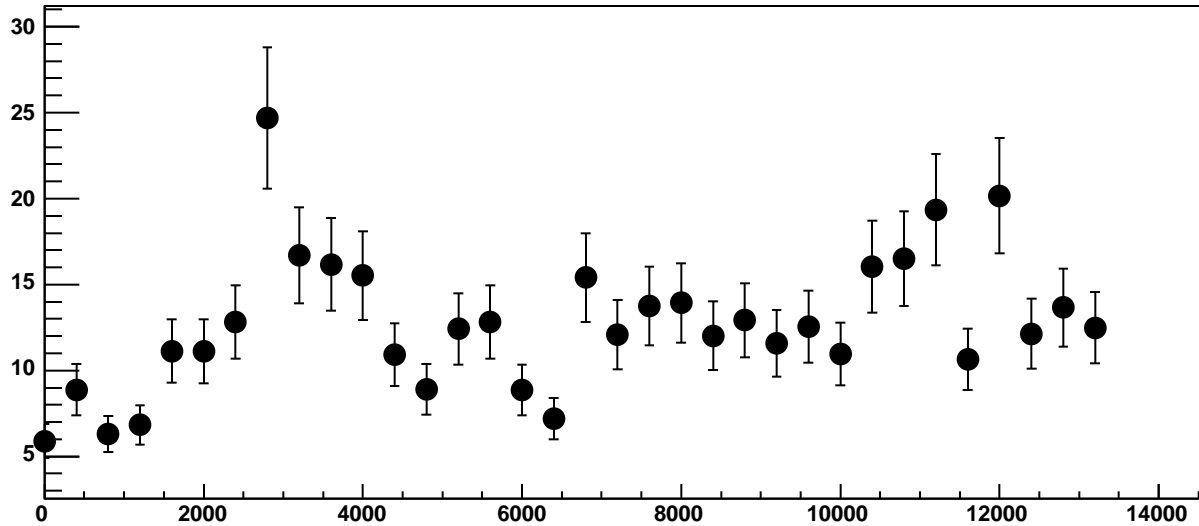


Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

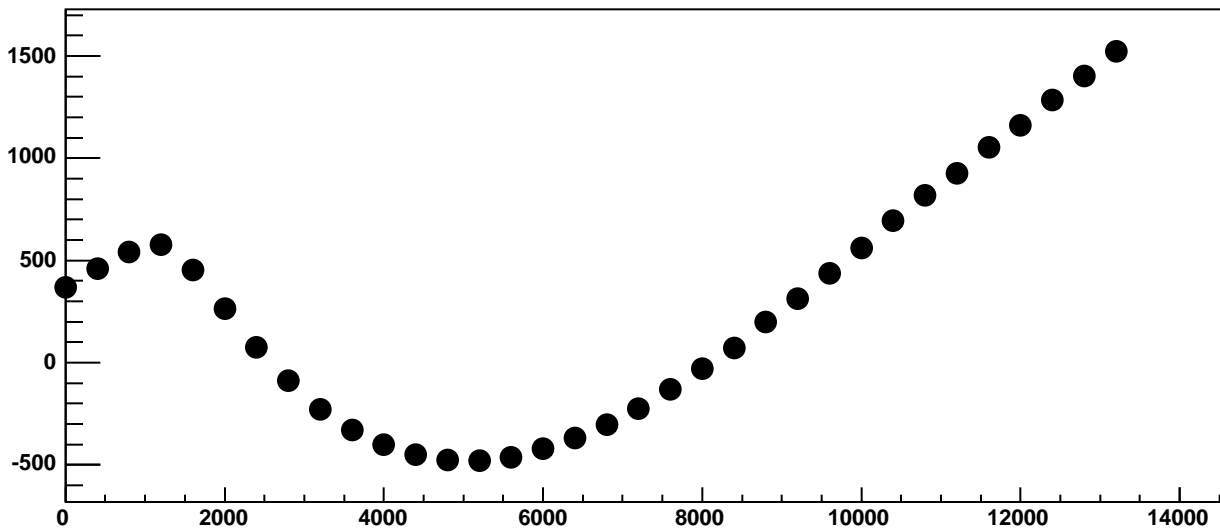
$\chi^2 / \text{ndf}$  6.254e+05 / 23  
p0 -997.5 ± 1.162  
p1 -0.2318 ± 0.0001846



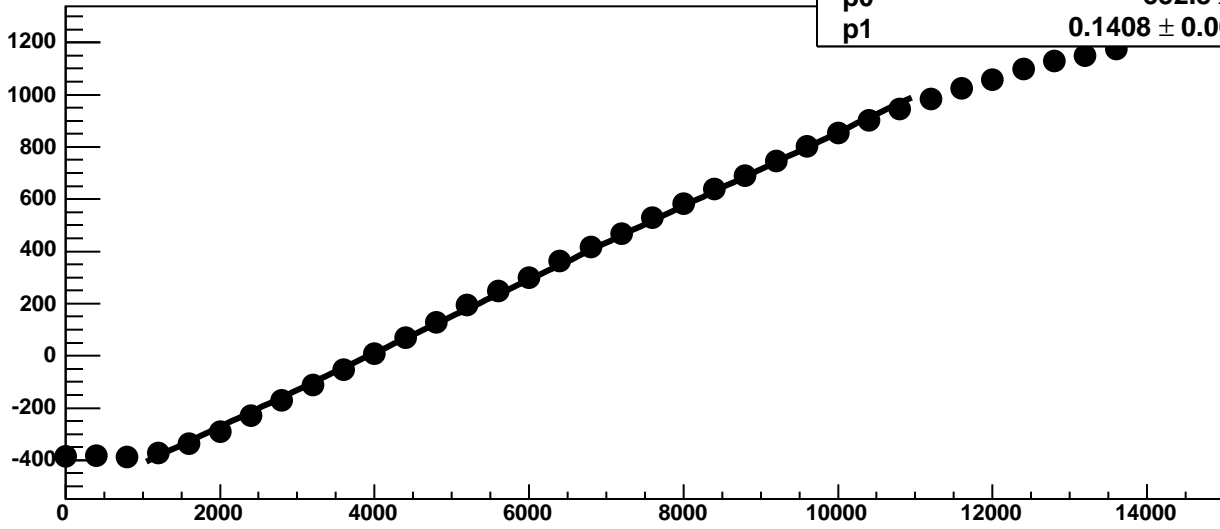
Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



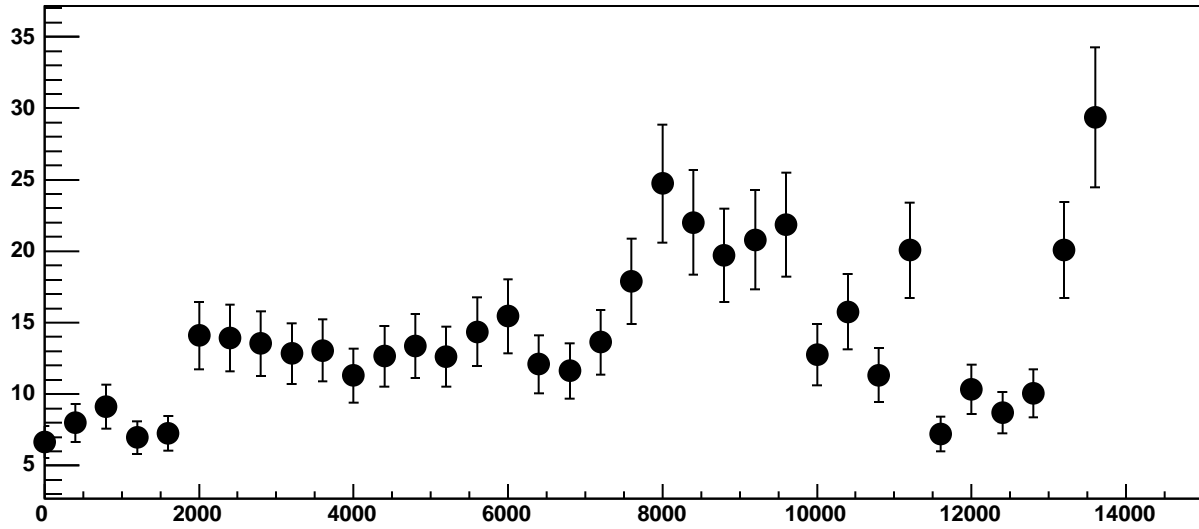
Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



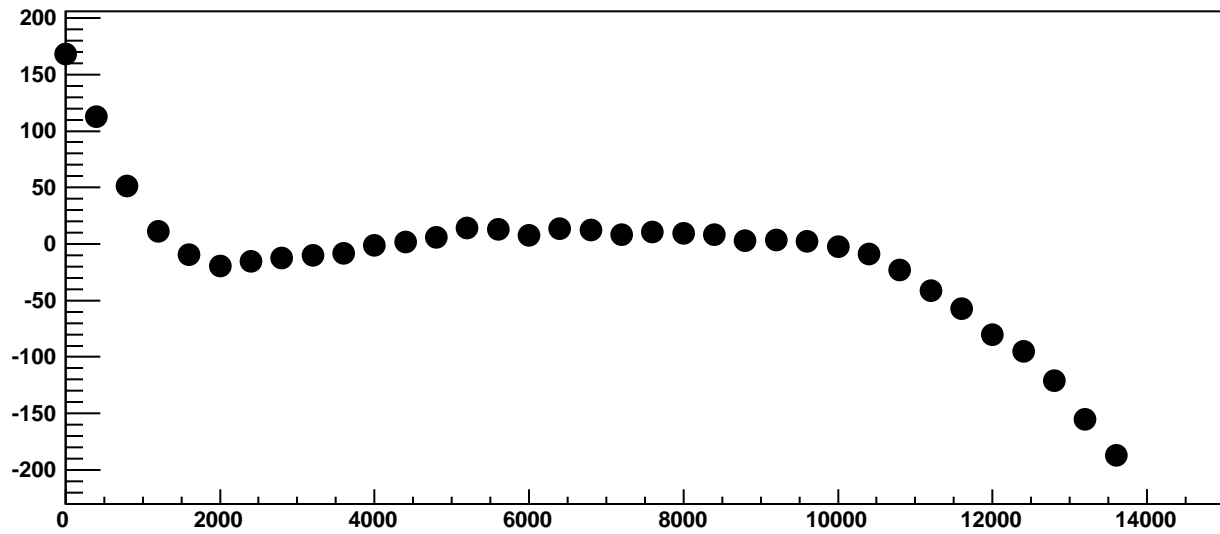
Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



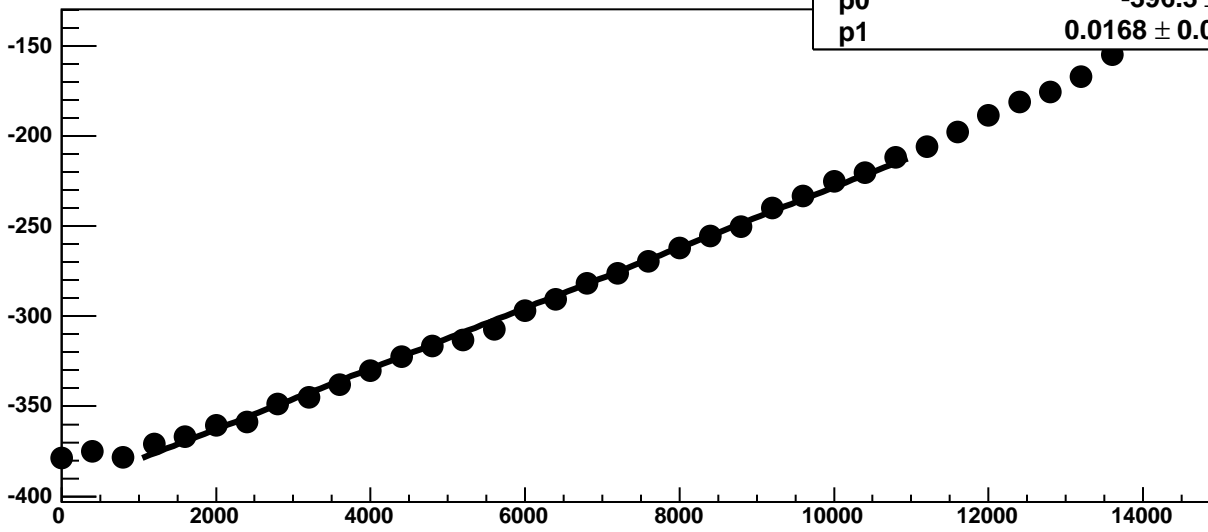
Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

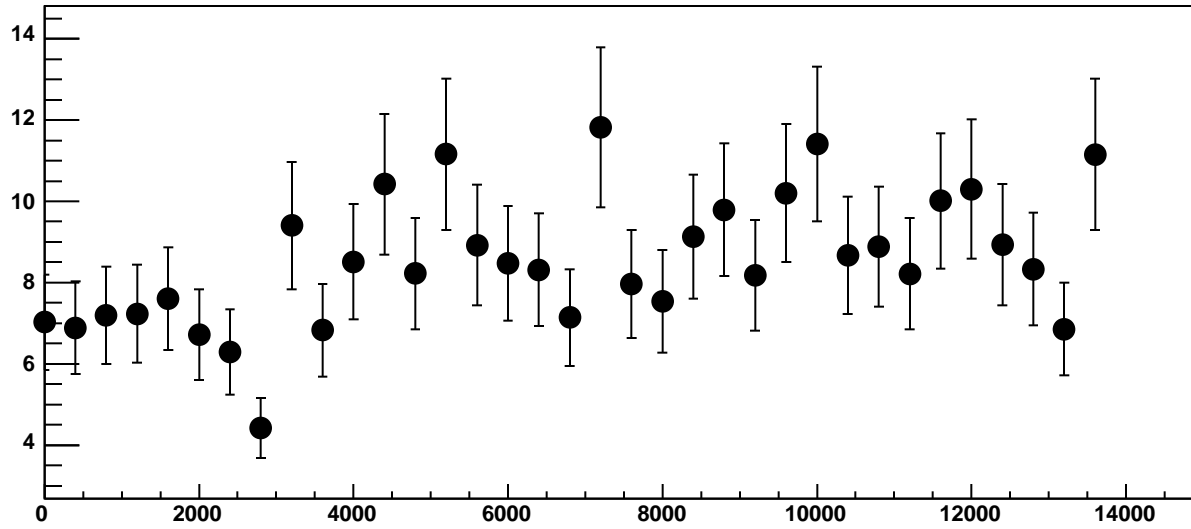


Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

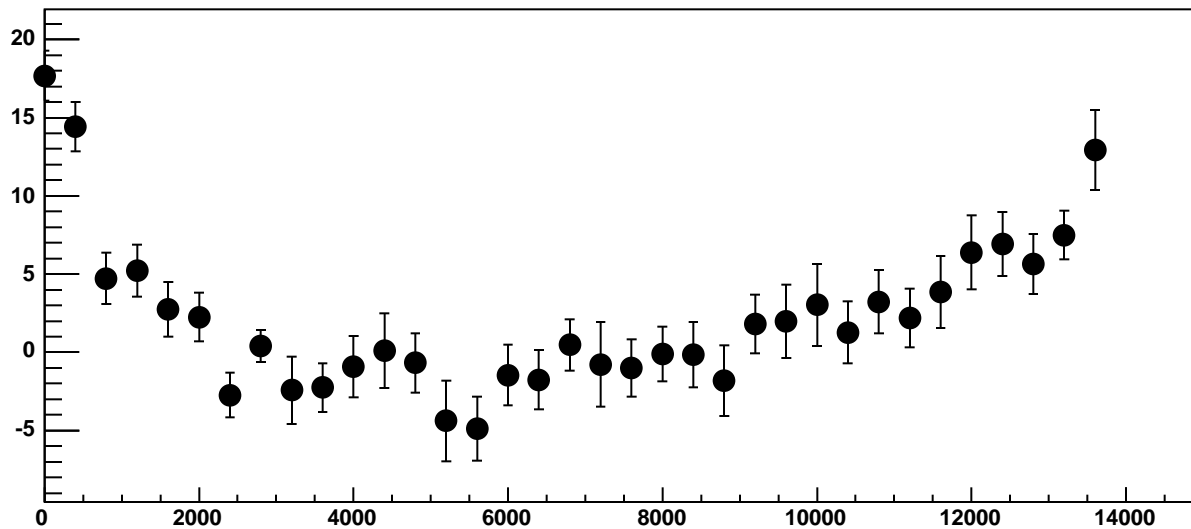


$\chi^2 / \text{ndf}$  39.05 / 23  
p0  $-396.3 \pm 0.7571$   
p1  $0.0168 \pm 0.0001257$

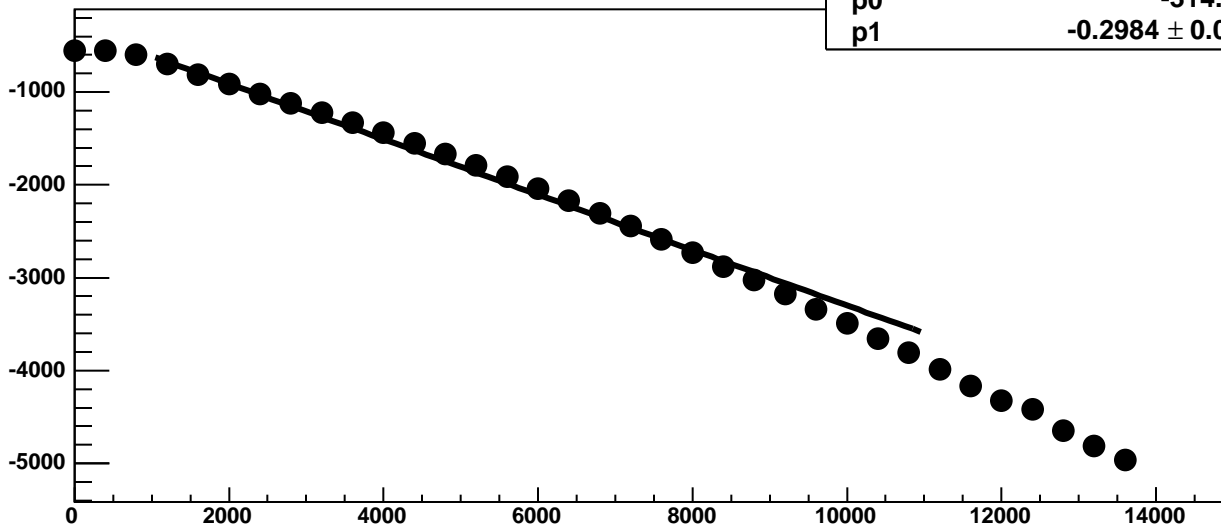
Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

3208 / 23

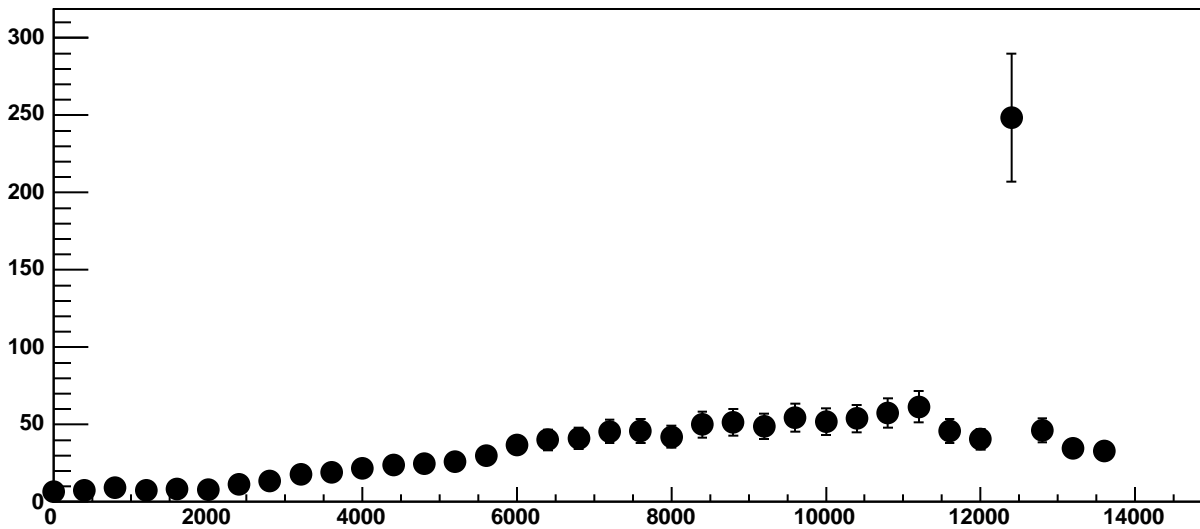
p0

$-314.1 \pm 1.4$

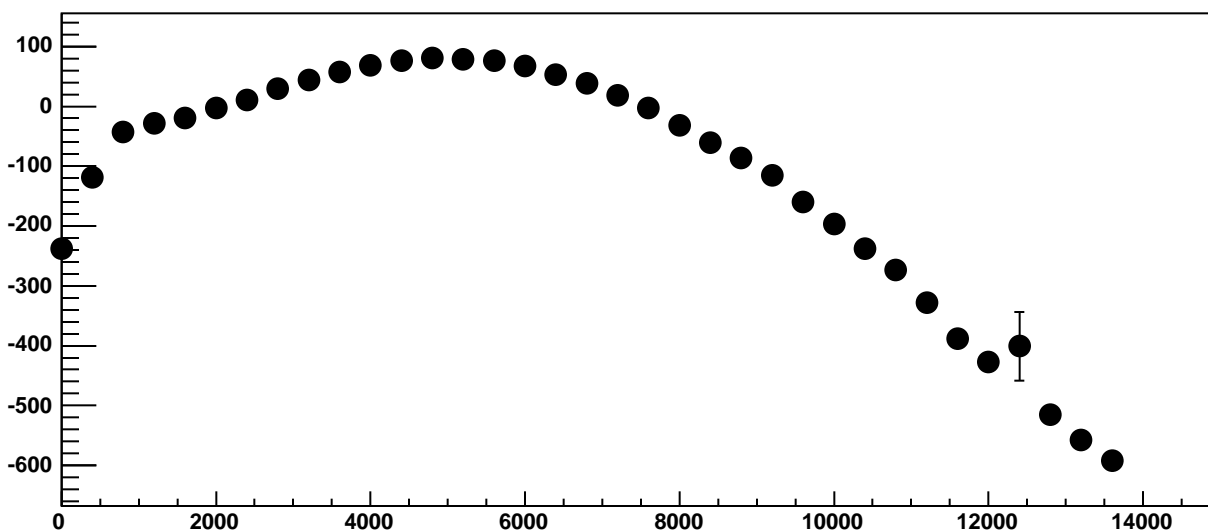
p1

$-0.2984 \pm 0.0004283$

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

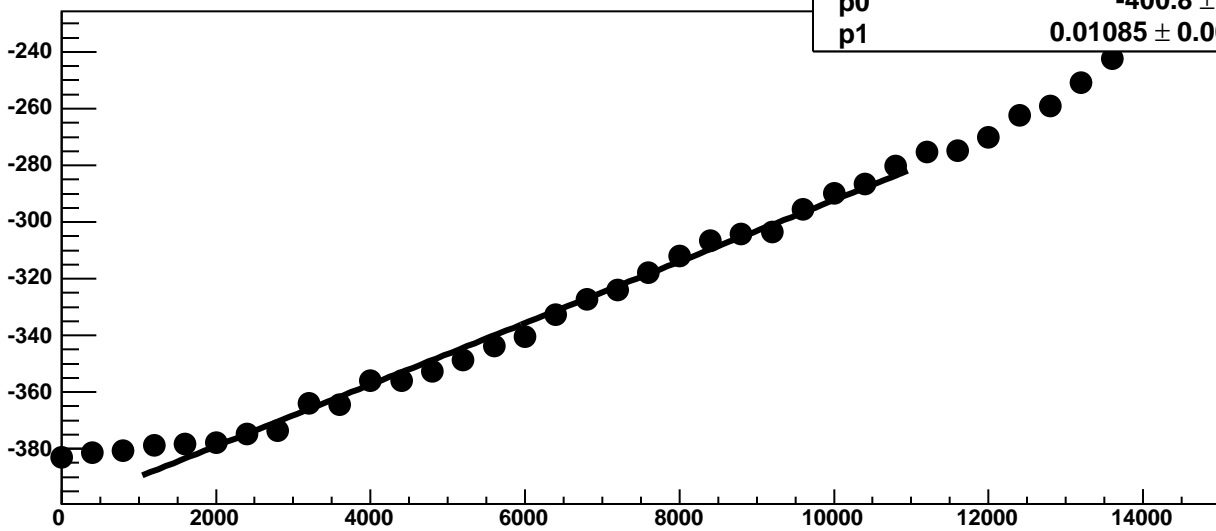


Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



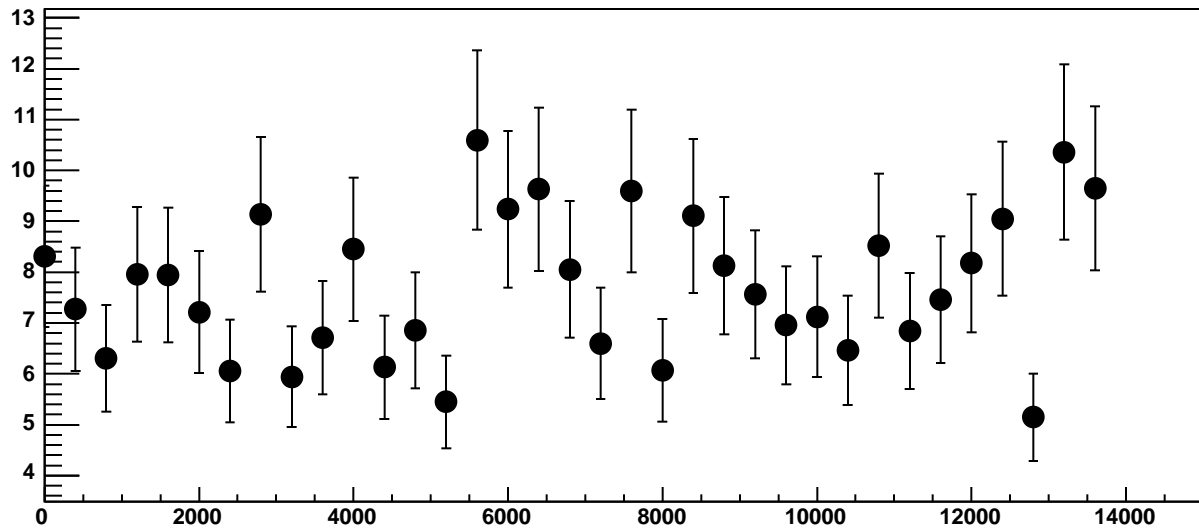


Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

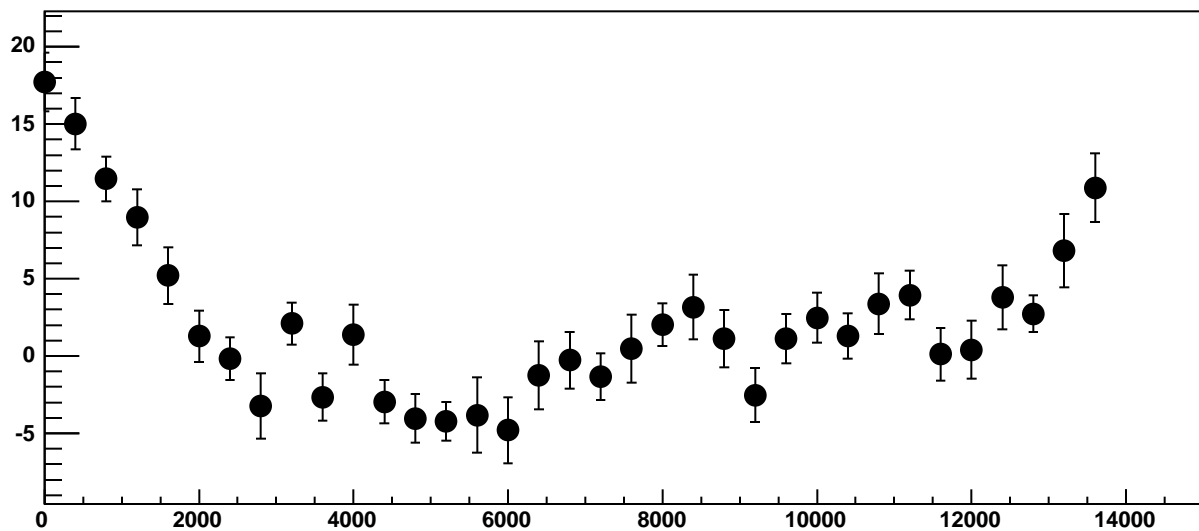


$\chi^2 / \text{ndf}$  85.88 / 23  
p0  $-400.8 \pm 0.7633$   
p1  $0.01085 \pm 0.0001163$

Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

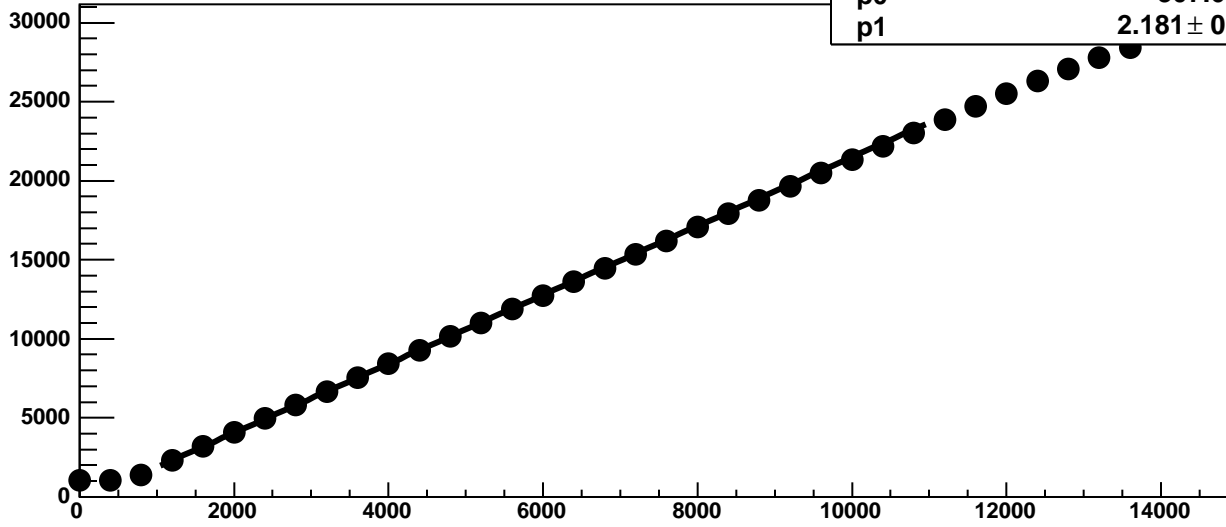


Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

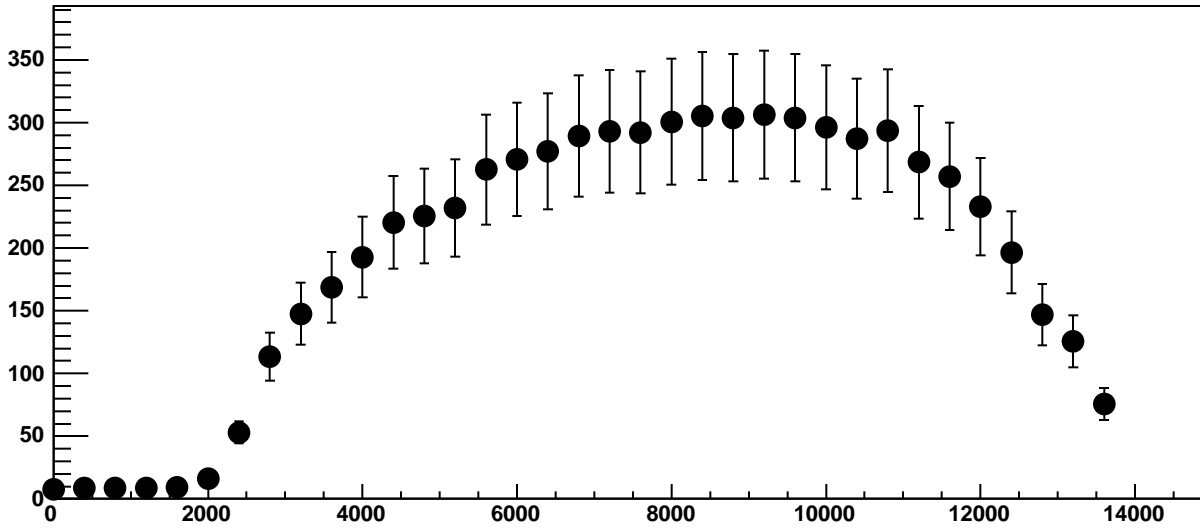


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

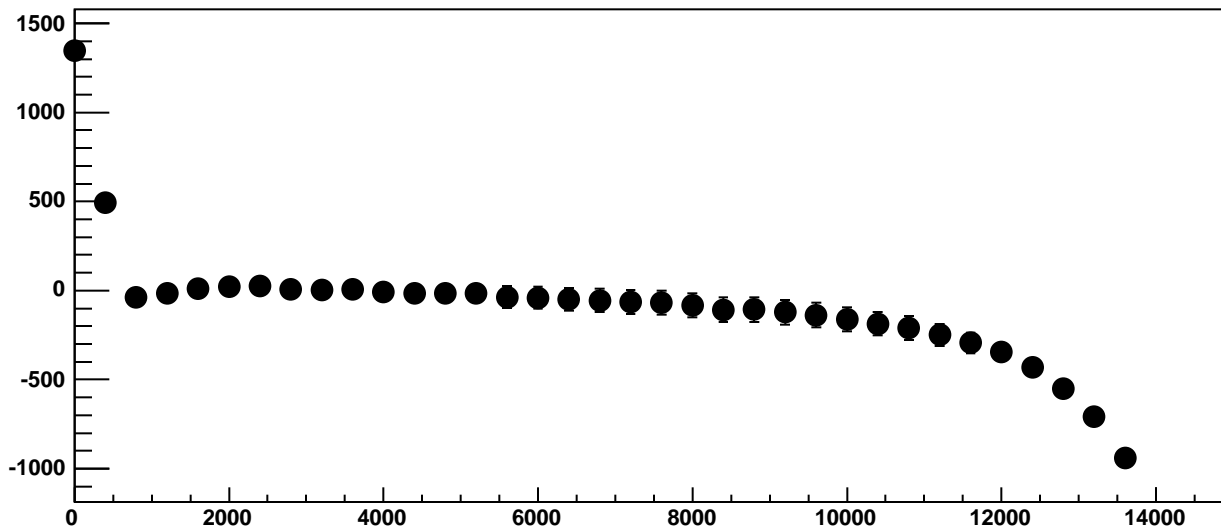
$\chi^2 / \text{ndf}$  165.2 / 23  
p0  $-307.6 \pm 3.533$   
p1  $2.181 \pm 0.002142$



Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

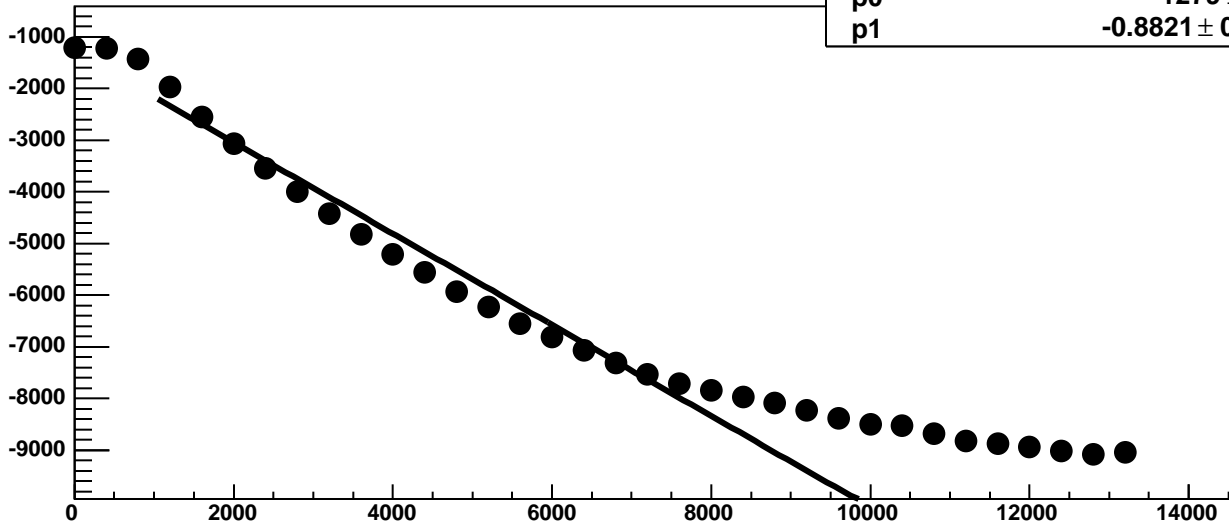


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

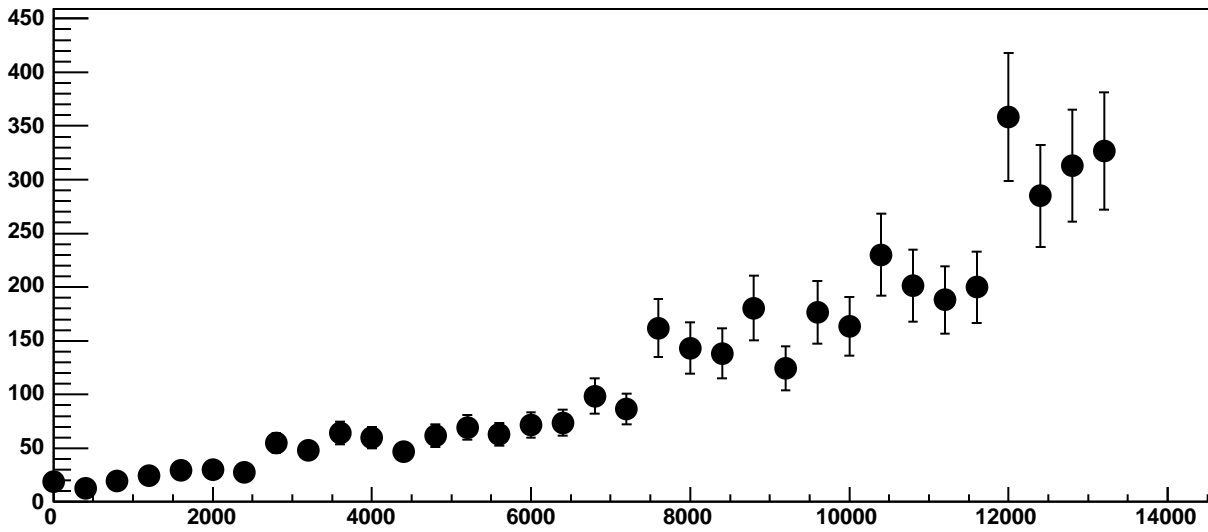


Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

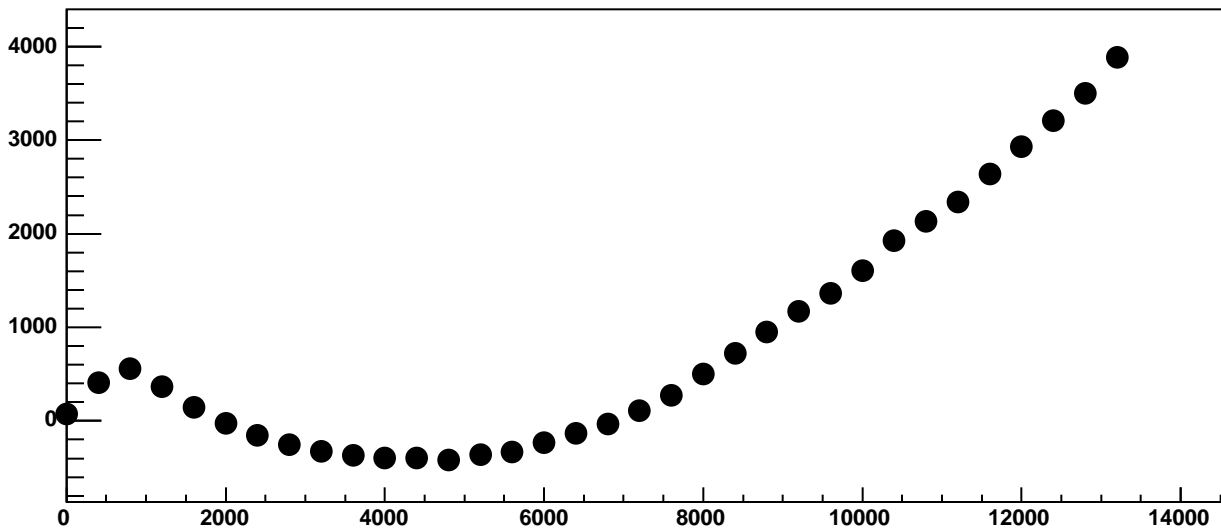
$\chi^2 / \text{ndf}$  2.115e+04 / 23  
p0 -1279 ± 4.468  
p1 -0.8821 ± 0.00123



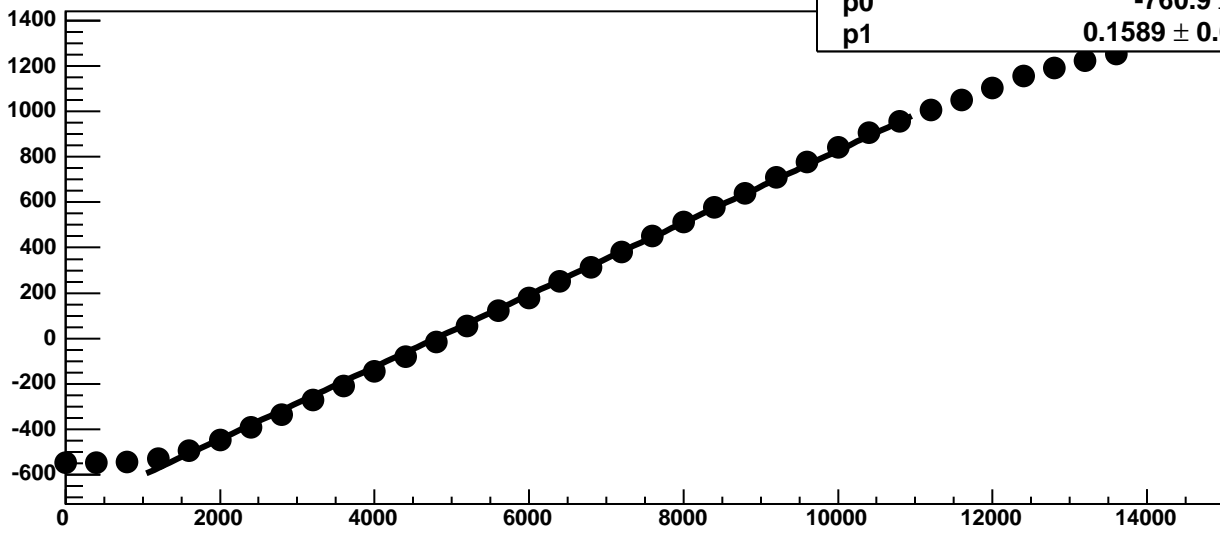
Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



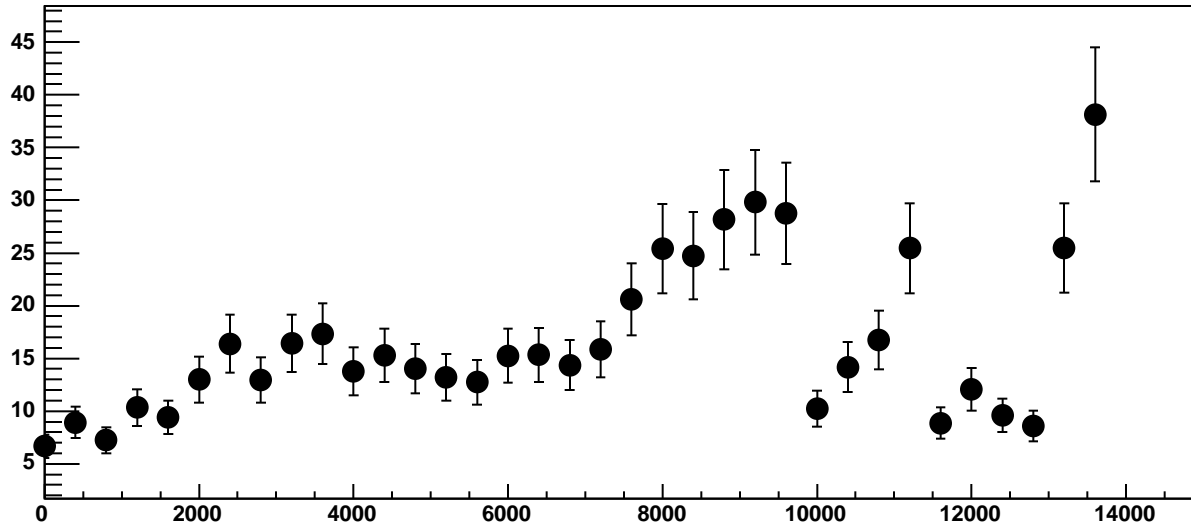
Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



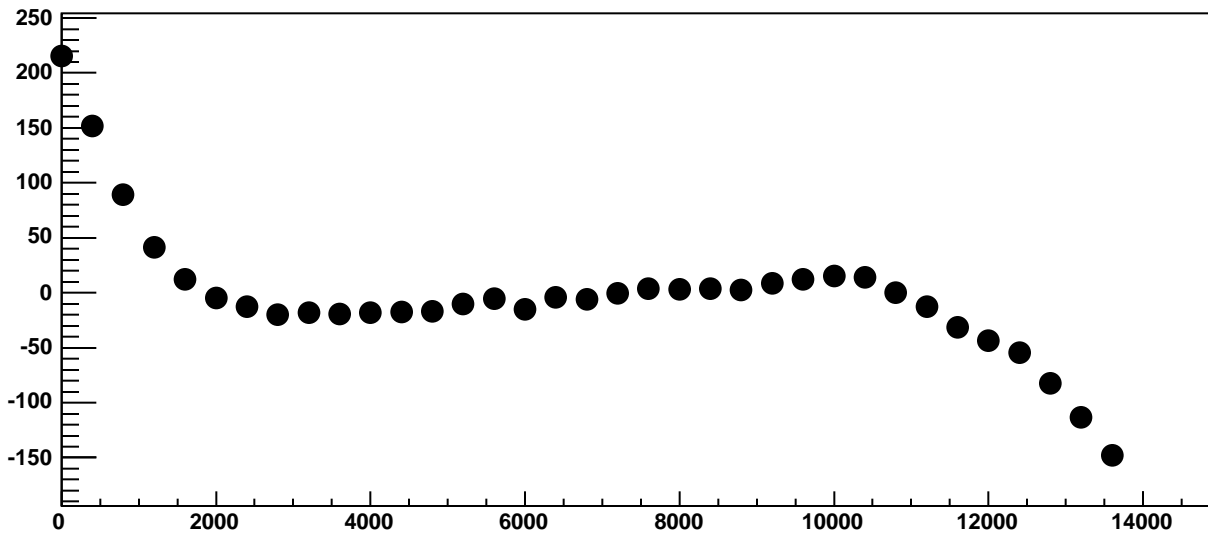
Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



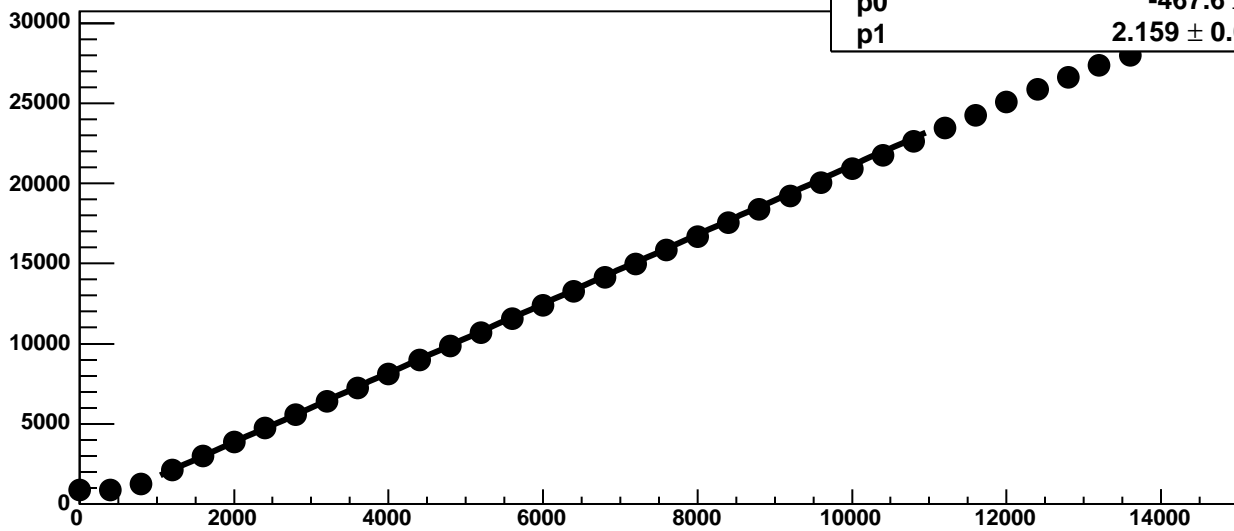
Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



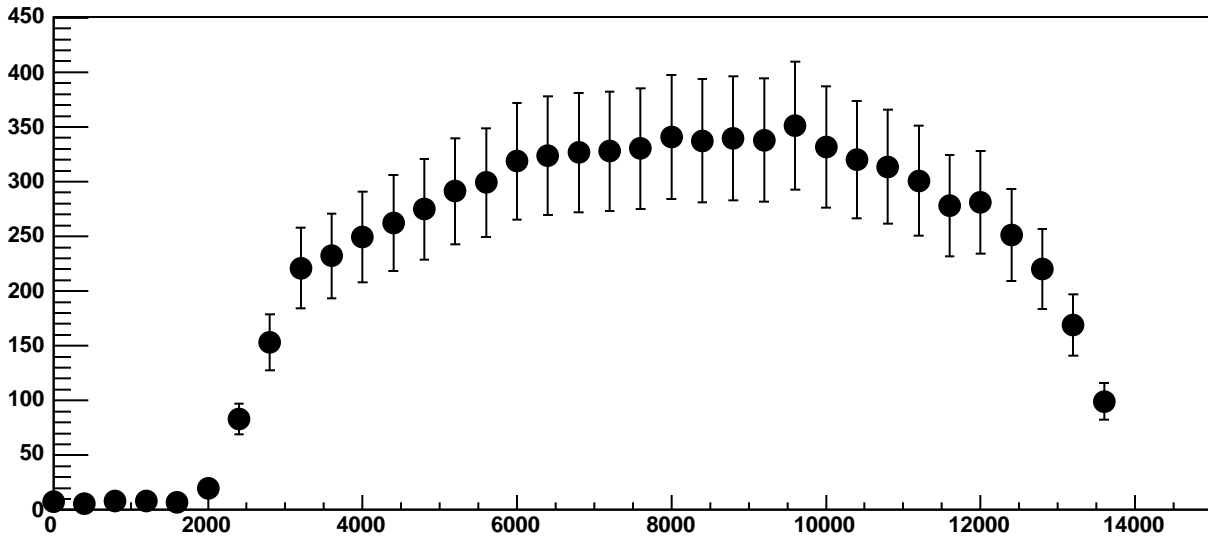
Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



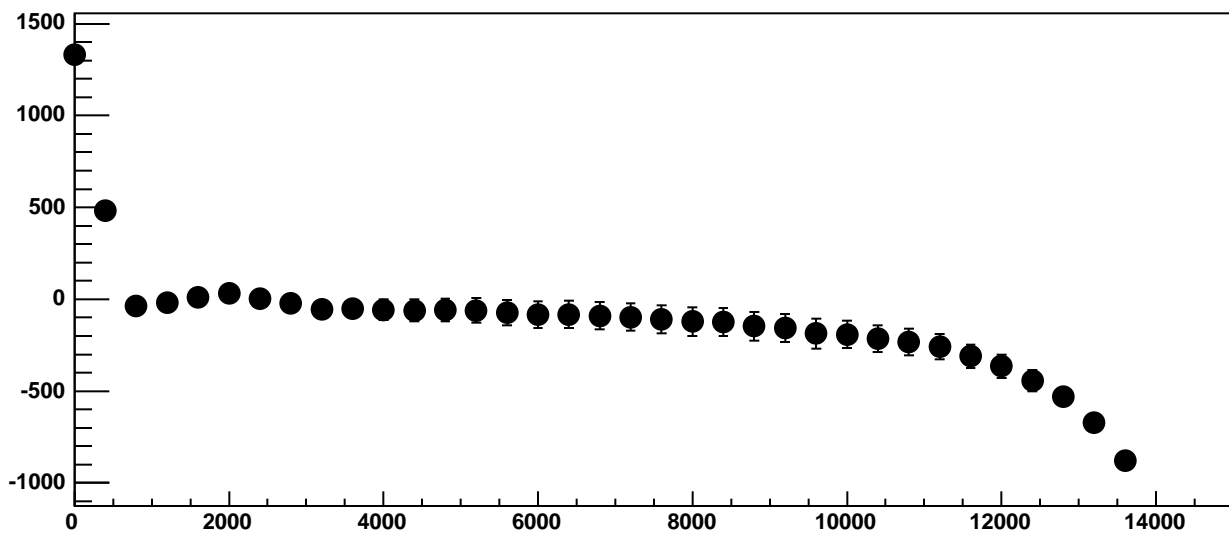
Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

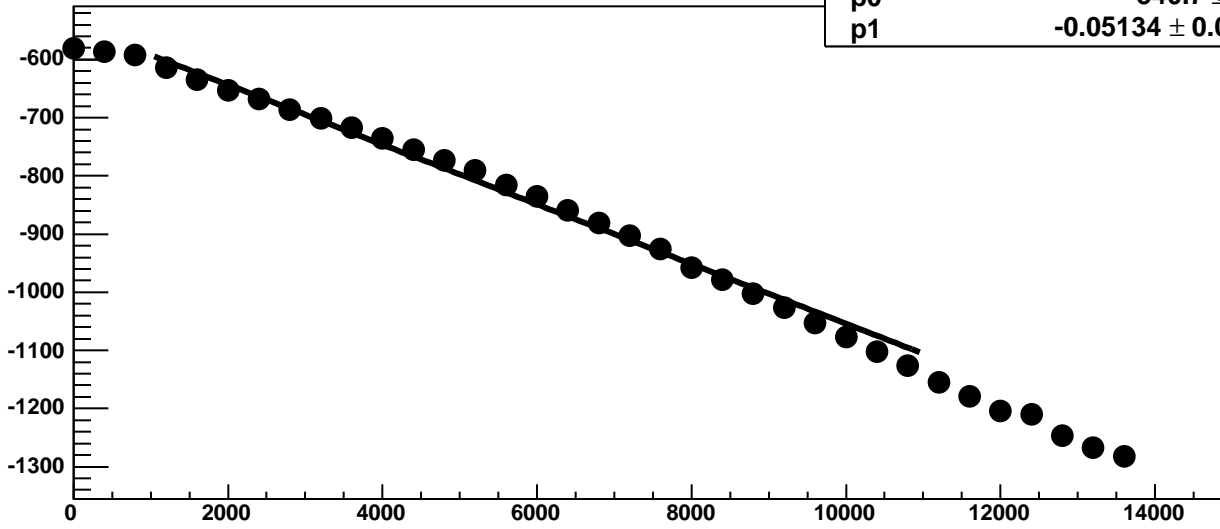


Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

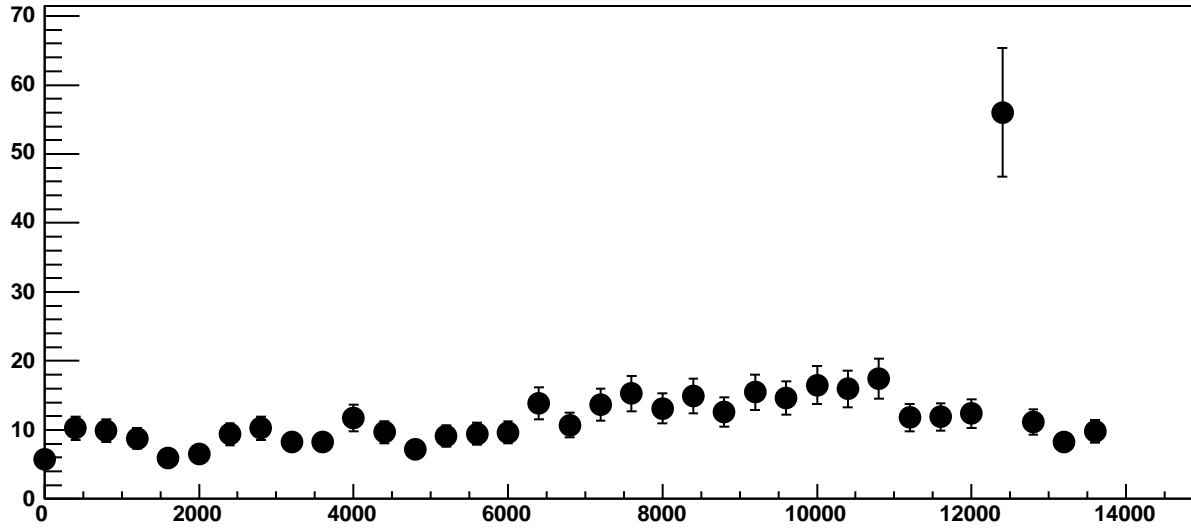


Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

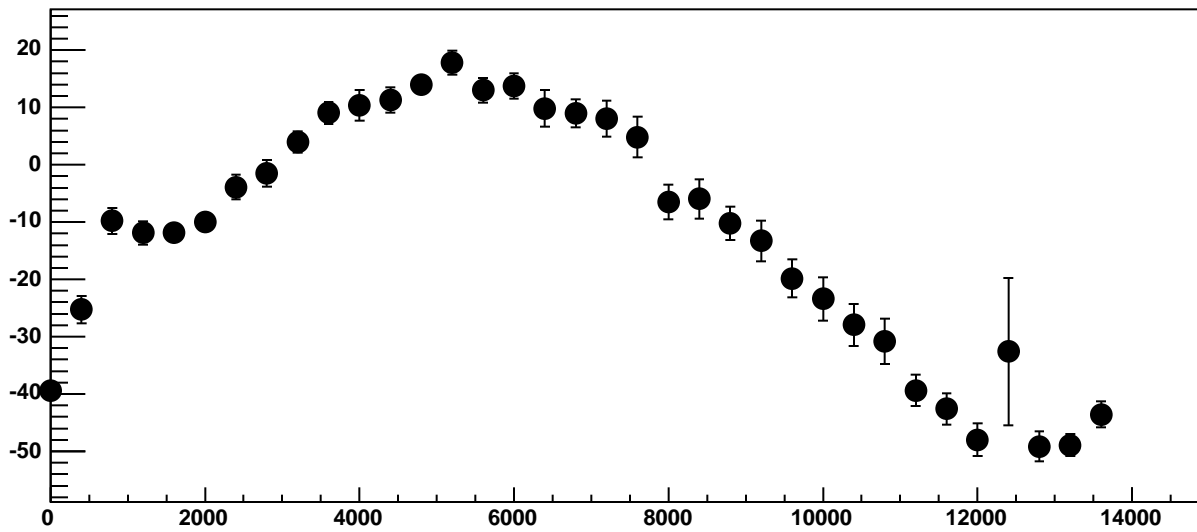
$\chi^2 / \text{ndf}$  699.7 / 23  
p0  $-540.7 \pm 0.9296$   
p1  $-0.05134 \pm 0.0001771$



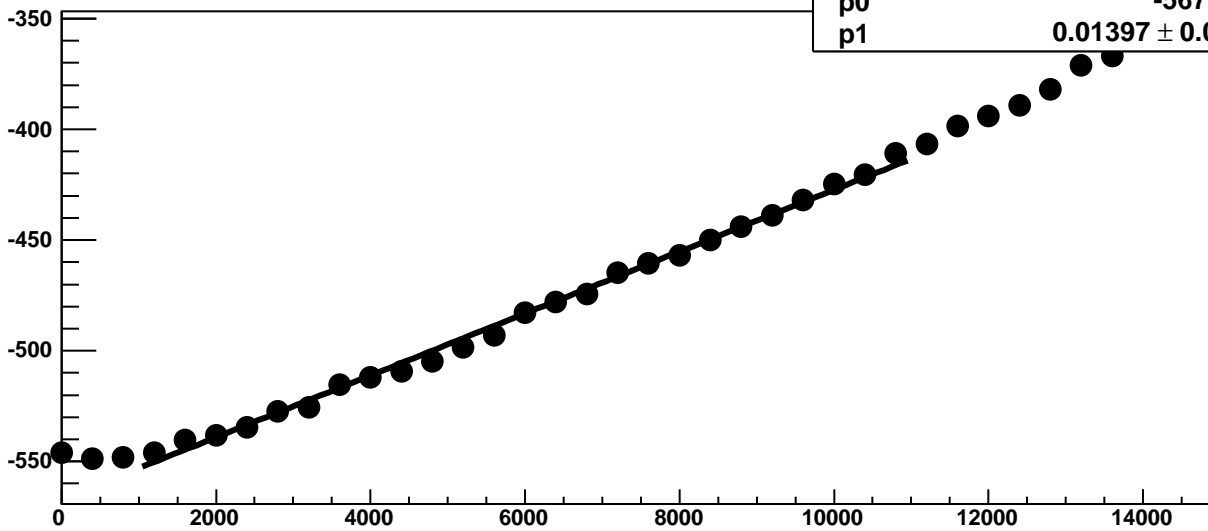
Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



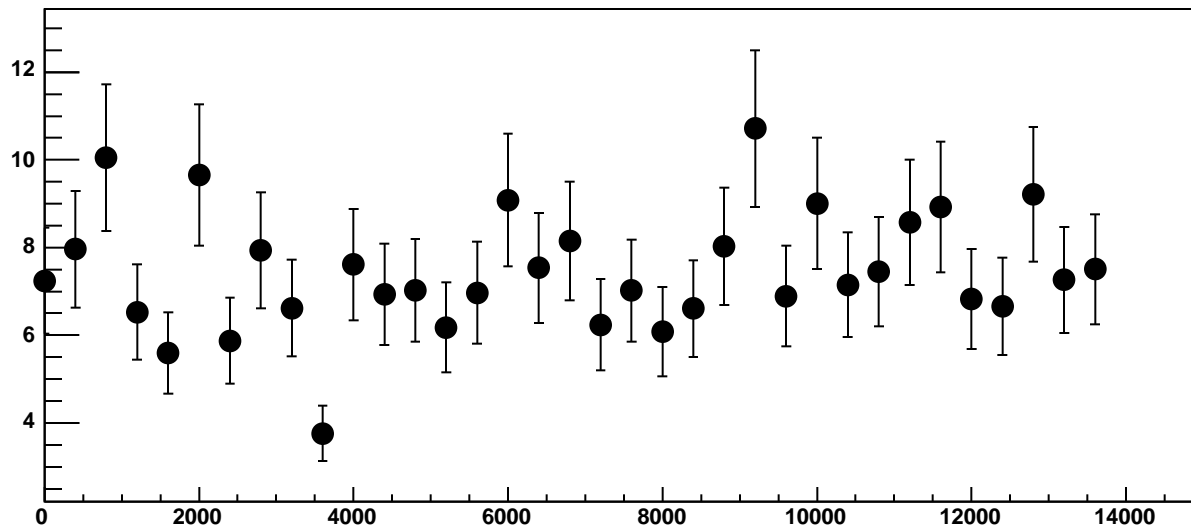
Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



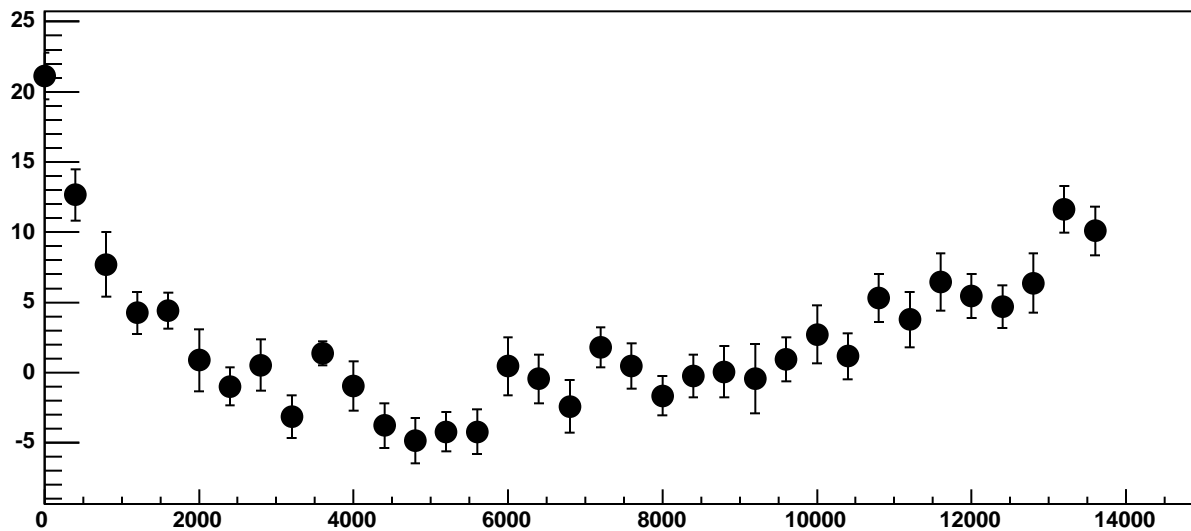
Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



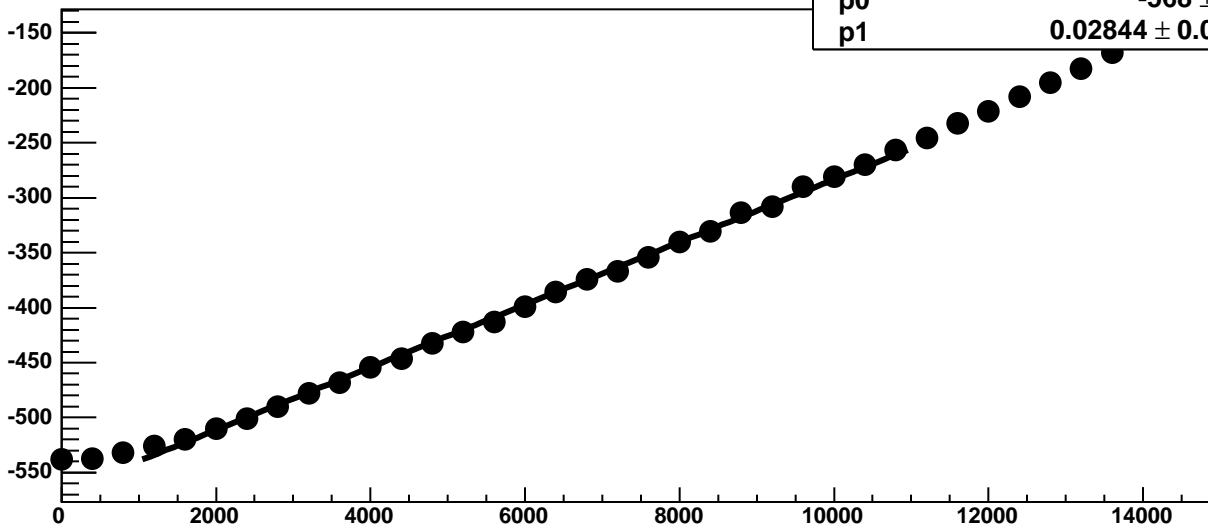
Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



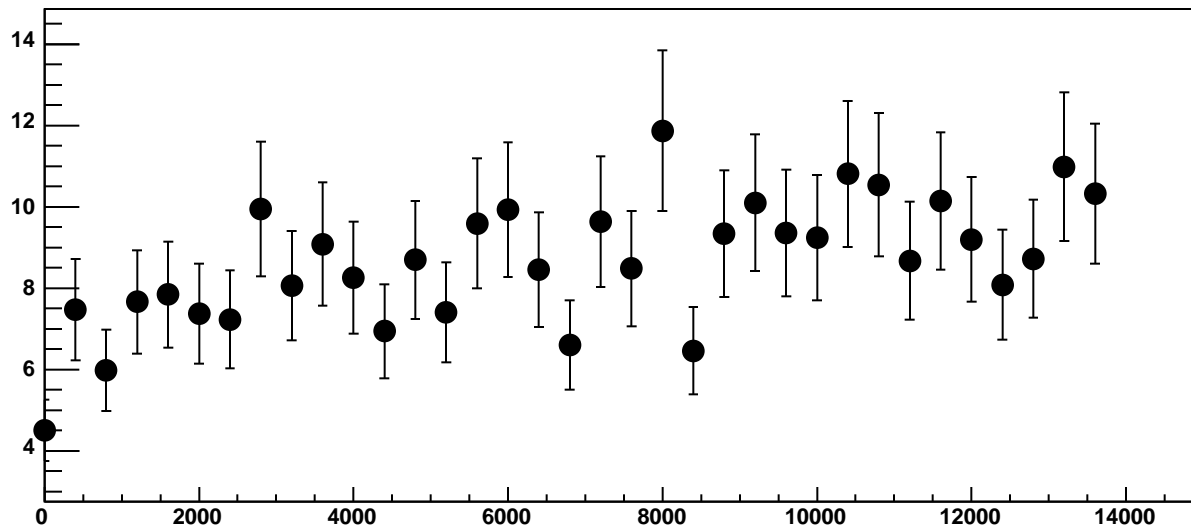
Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



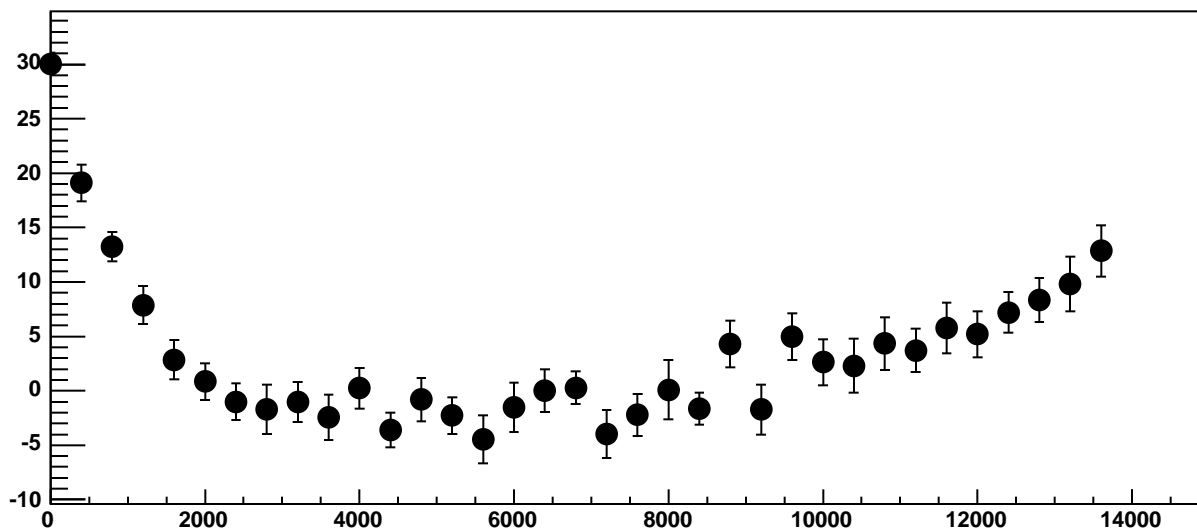
Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



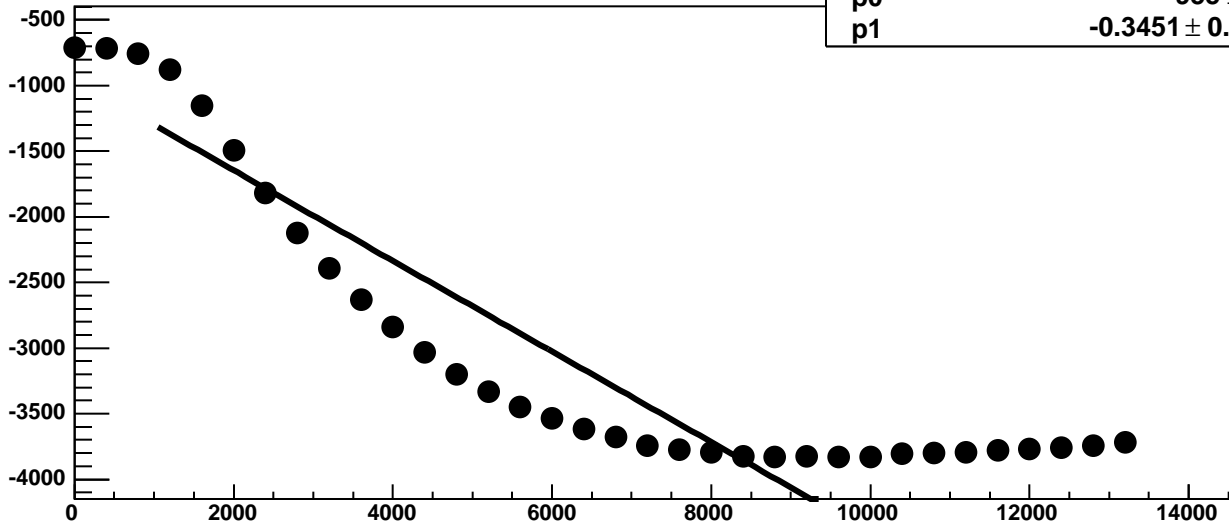
Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



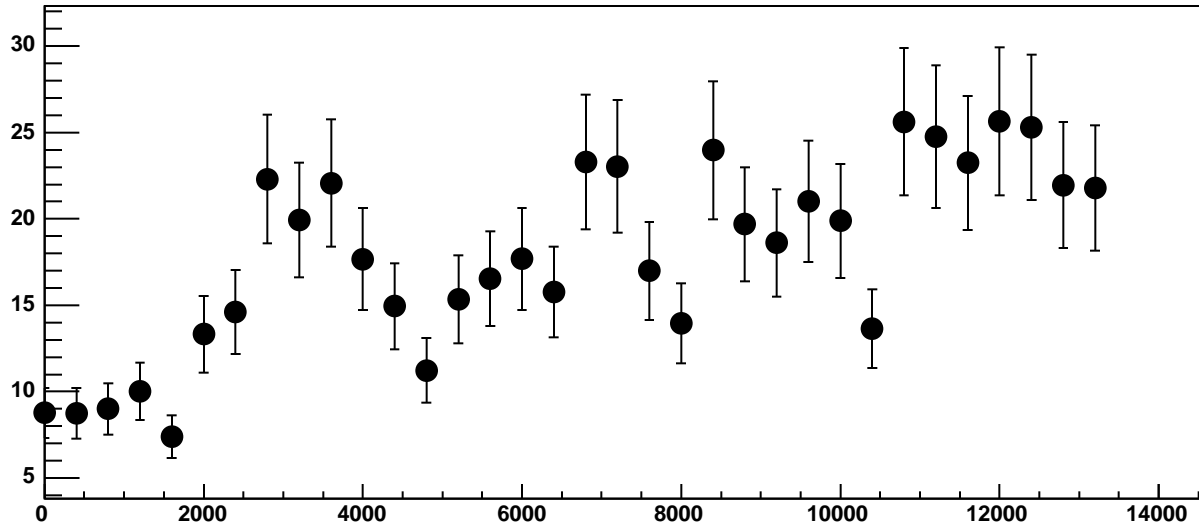


Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

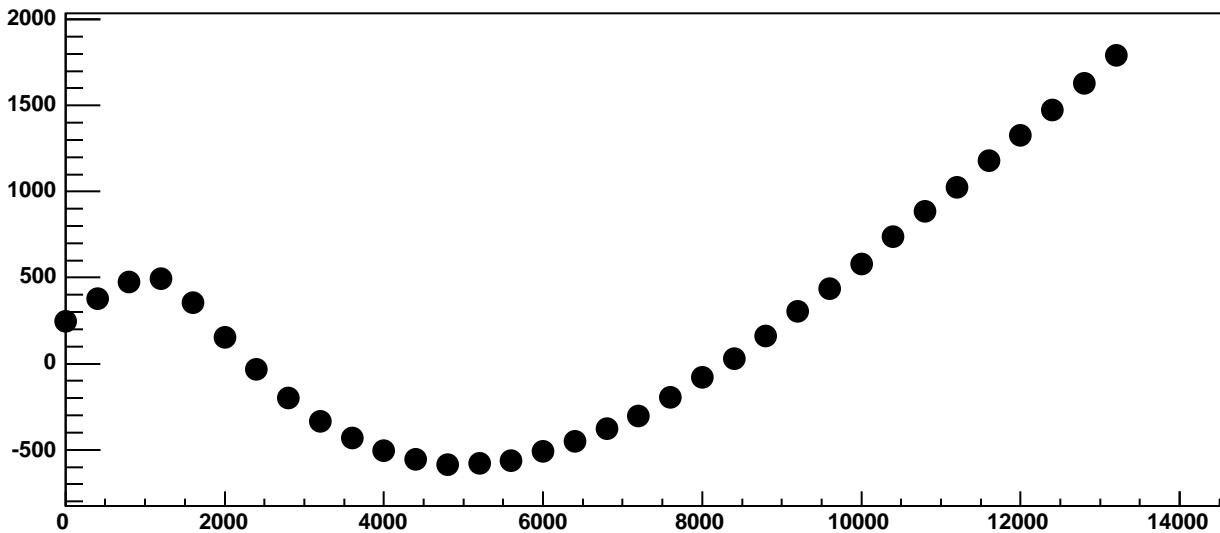
$\chi^2 / \text{ndf}$  4.003e+05 / 23  
p0 -955 ± 1.322  
p1 -0.3451 ± 0.000231



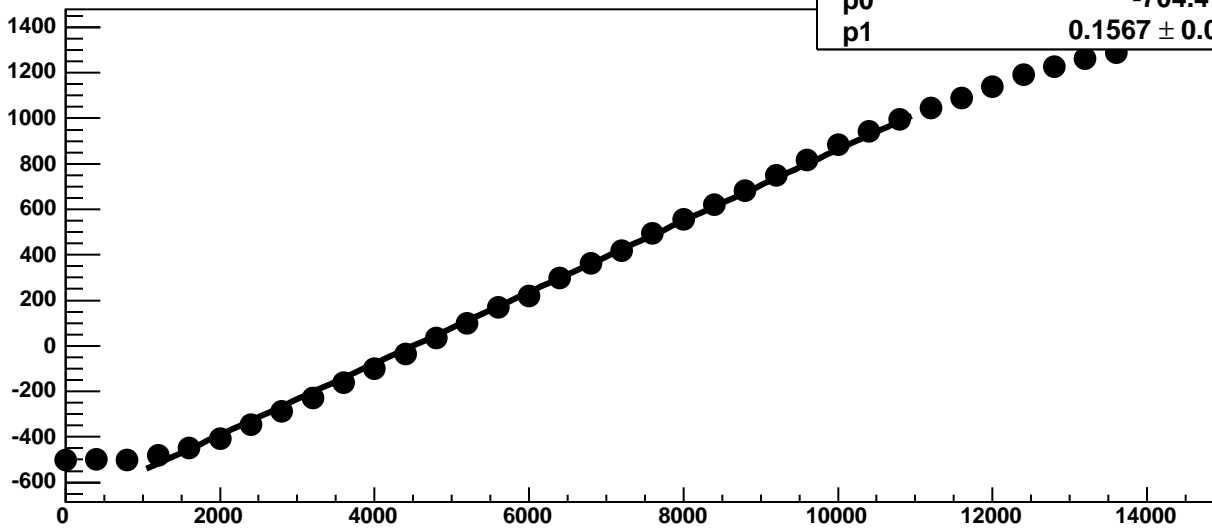
Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



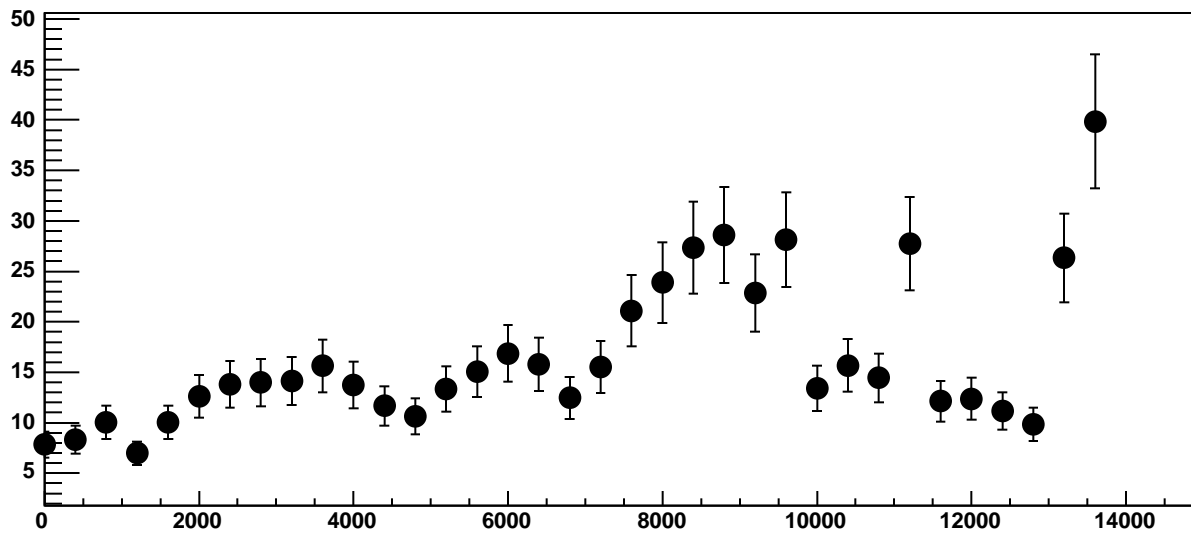
Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



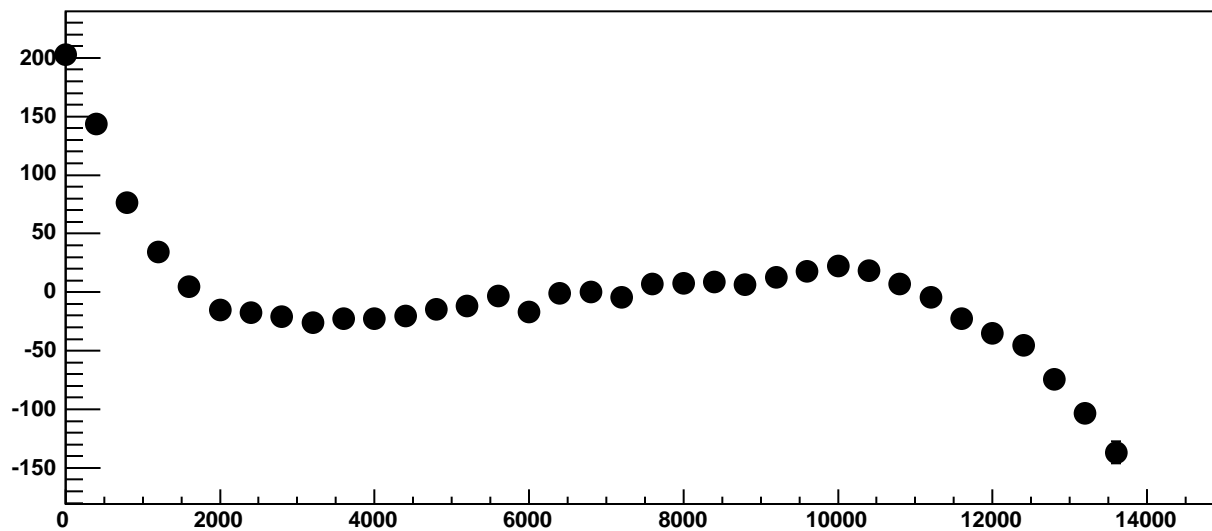
Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



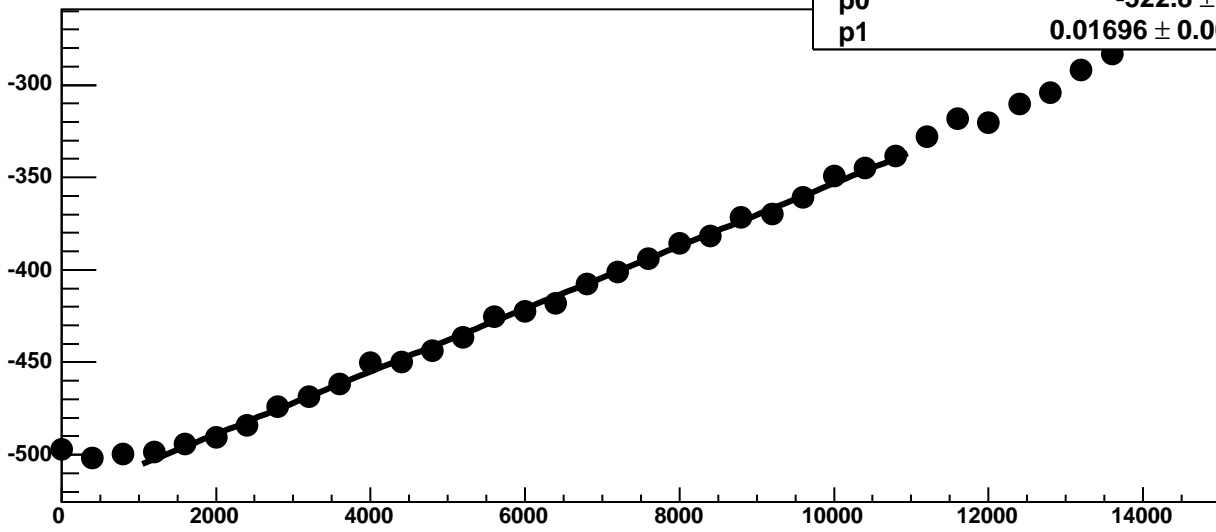
Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



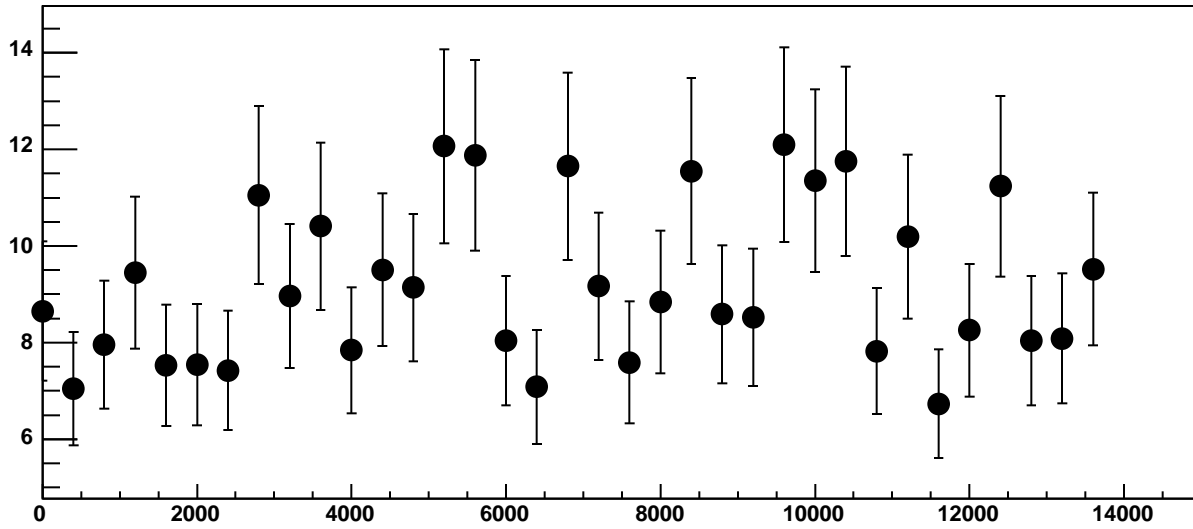
Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



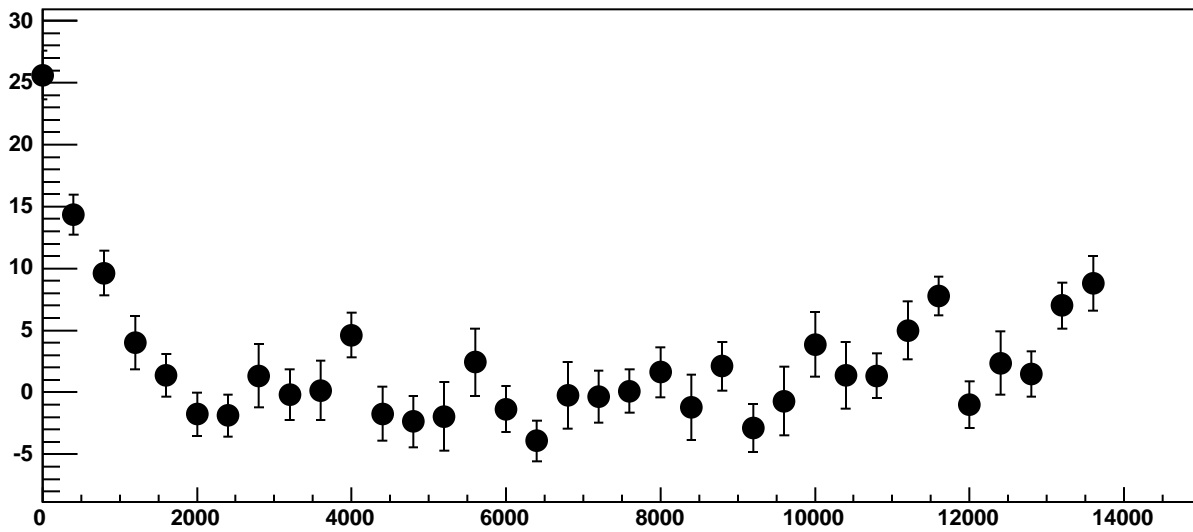
Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC



Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC

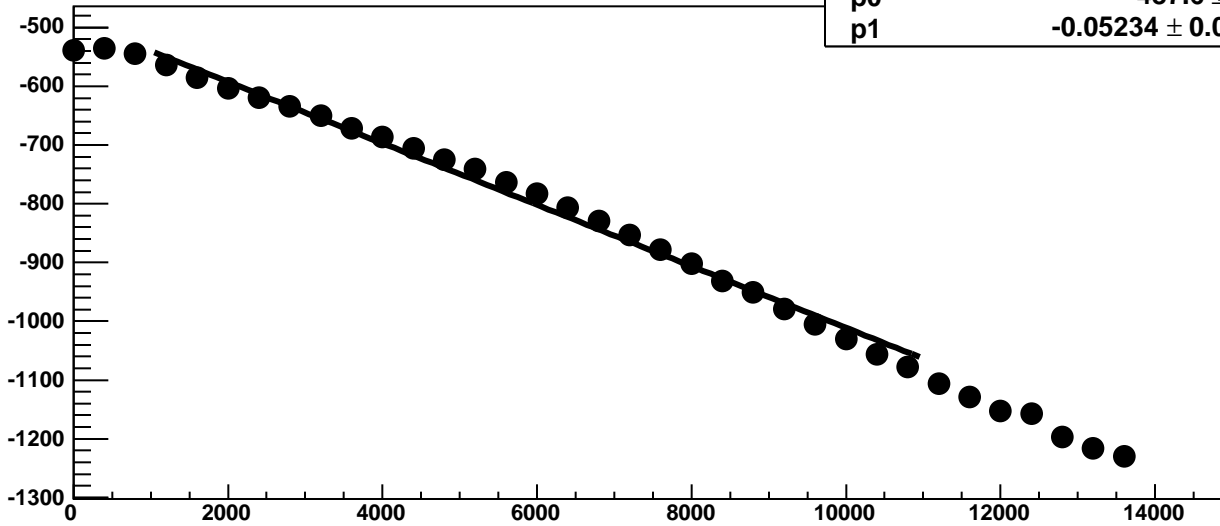


Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

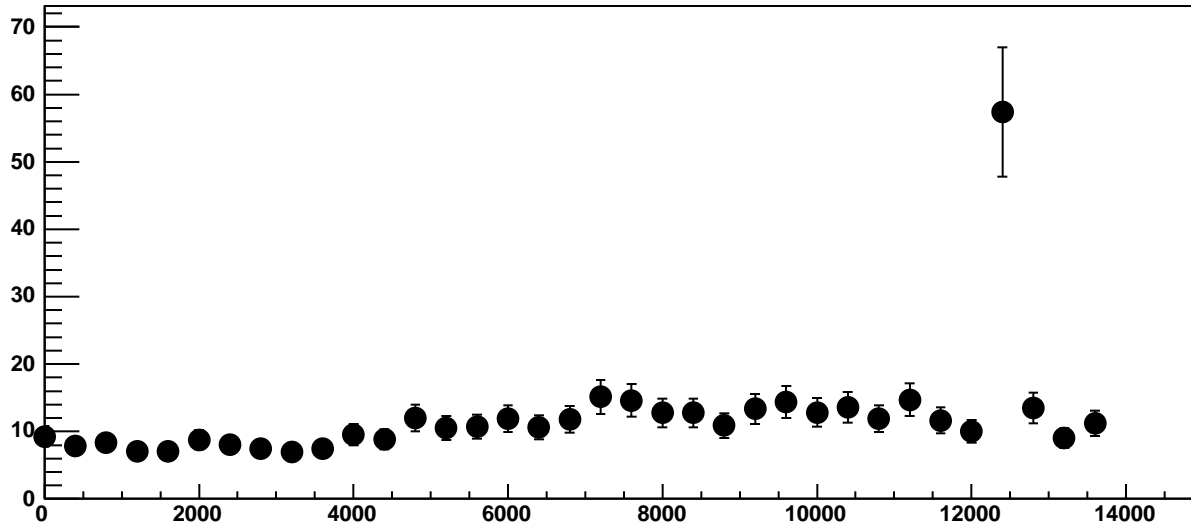


Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

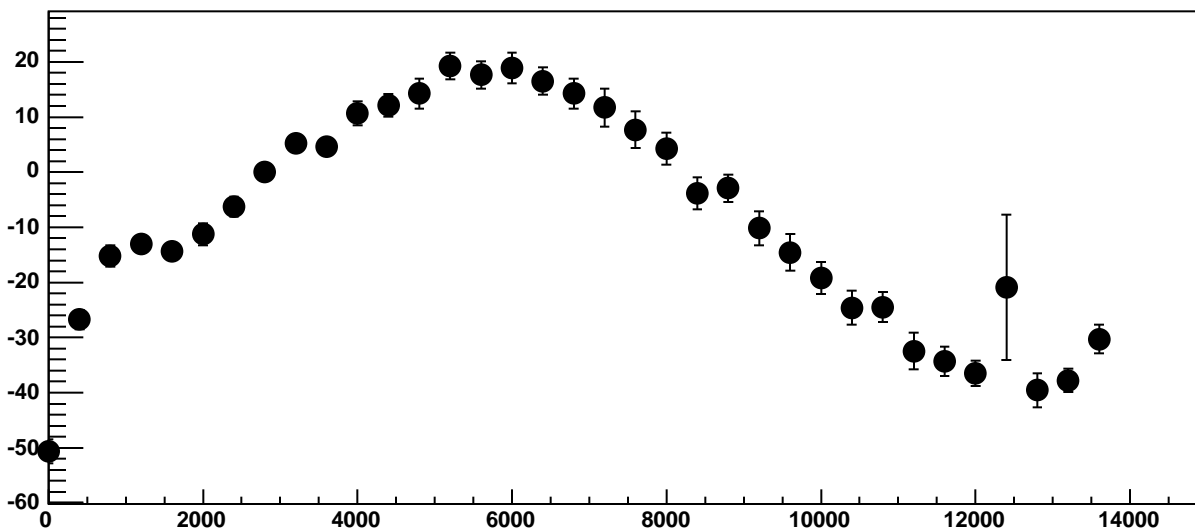
$\chi^2 / \text{ndf}$  760.6 / 23  
p0  $-487.6 \pm 0.8908$   
p1  $-0.05234 \pm 0.0001614$



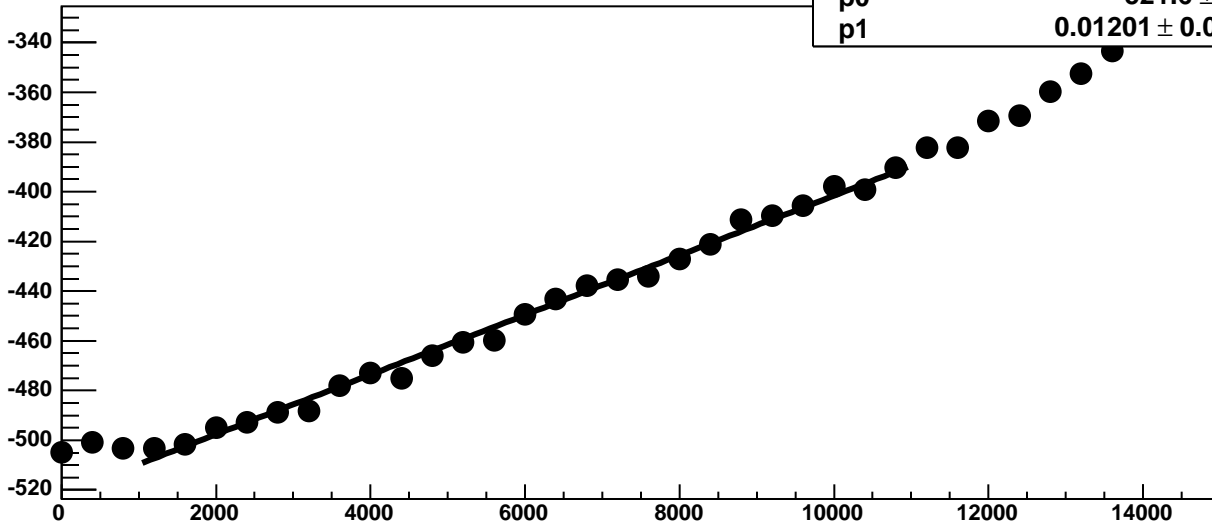
Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

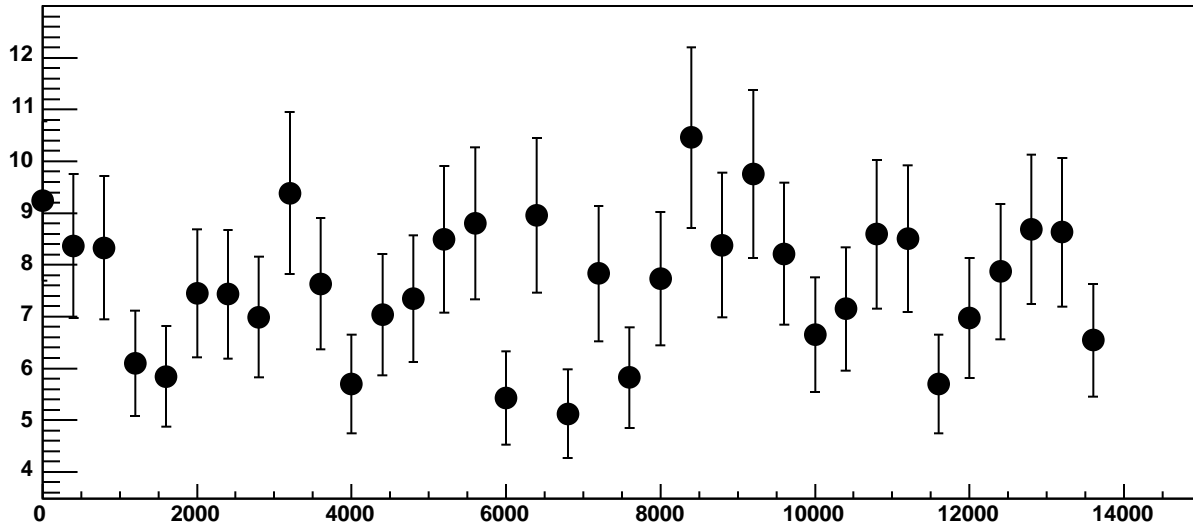


Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

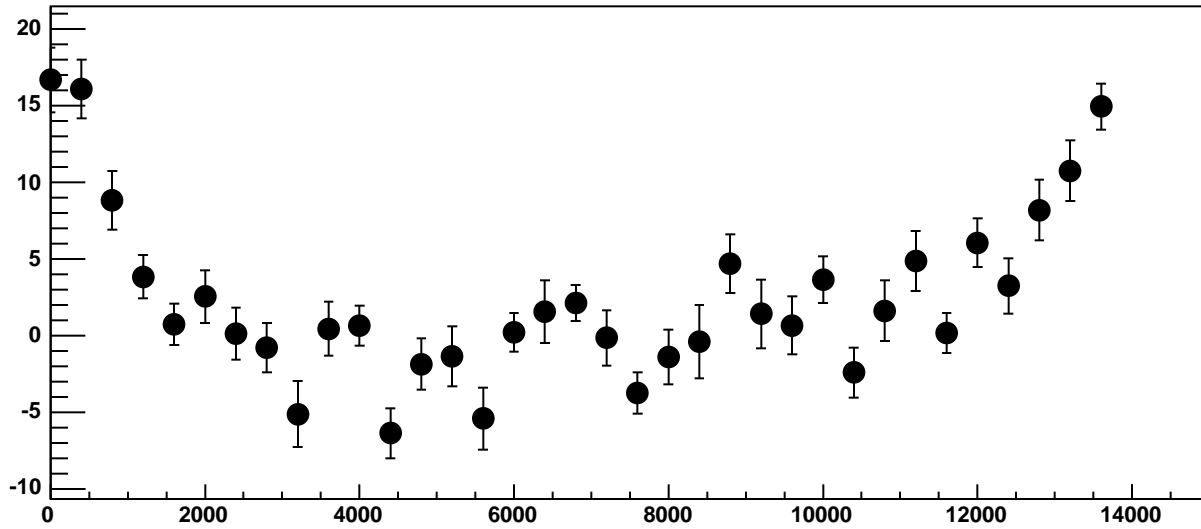


$\chi^2 / \text{ndf}$  68.12 / 23  
p0  $-521.6 \pm 0.7339$   
p1  $0.01201 \pm 0.0001149$

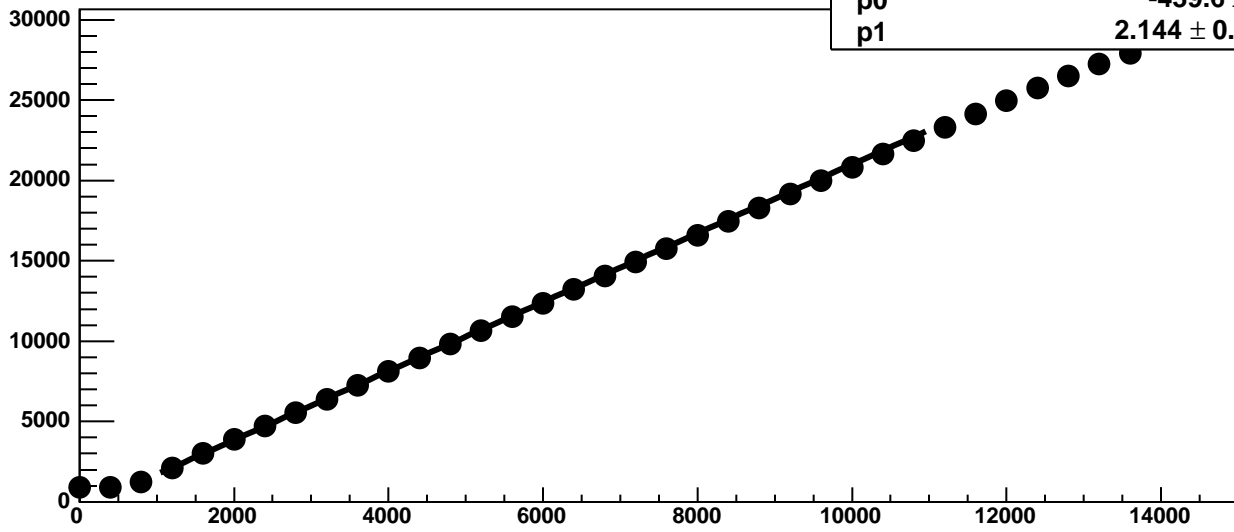
Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



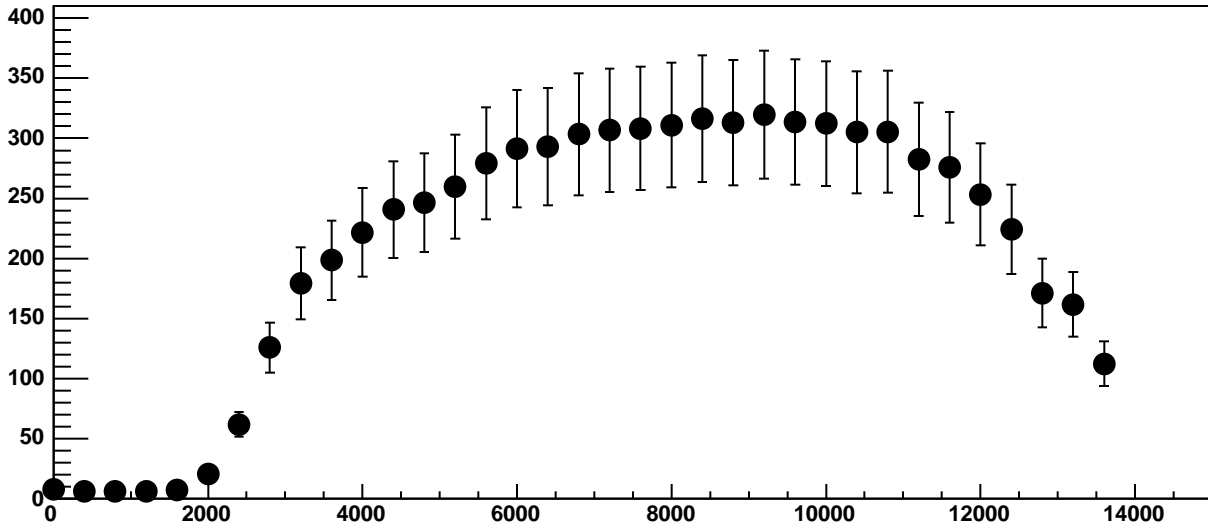
Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



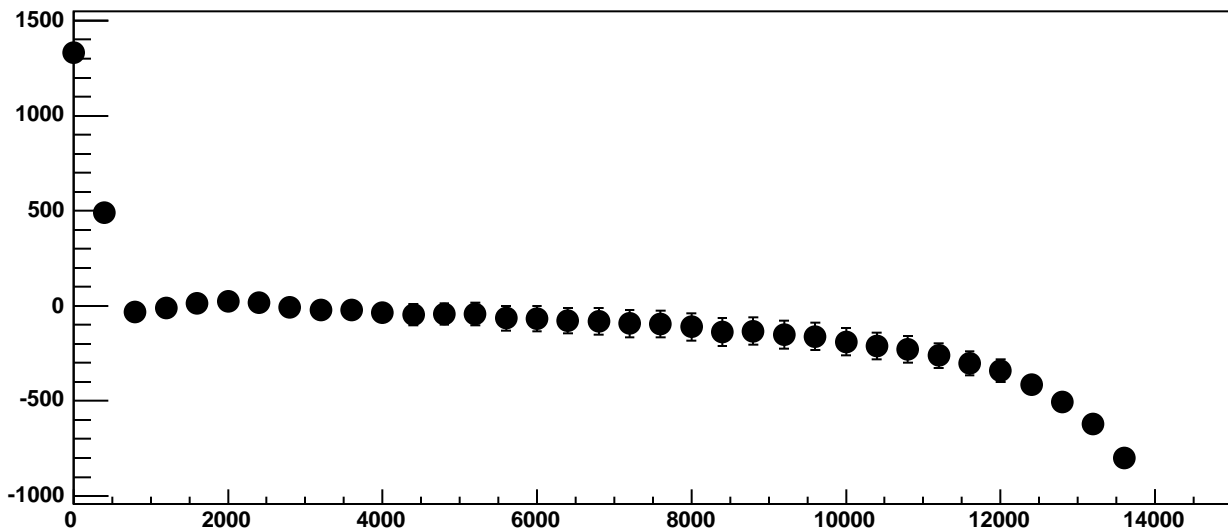
Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC

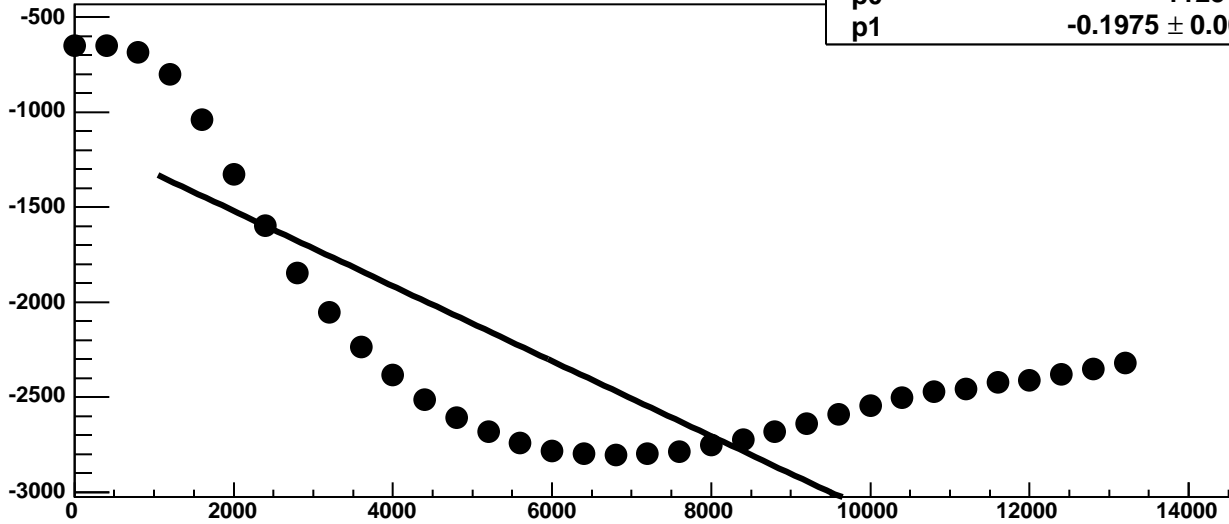


Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

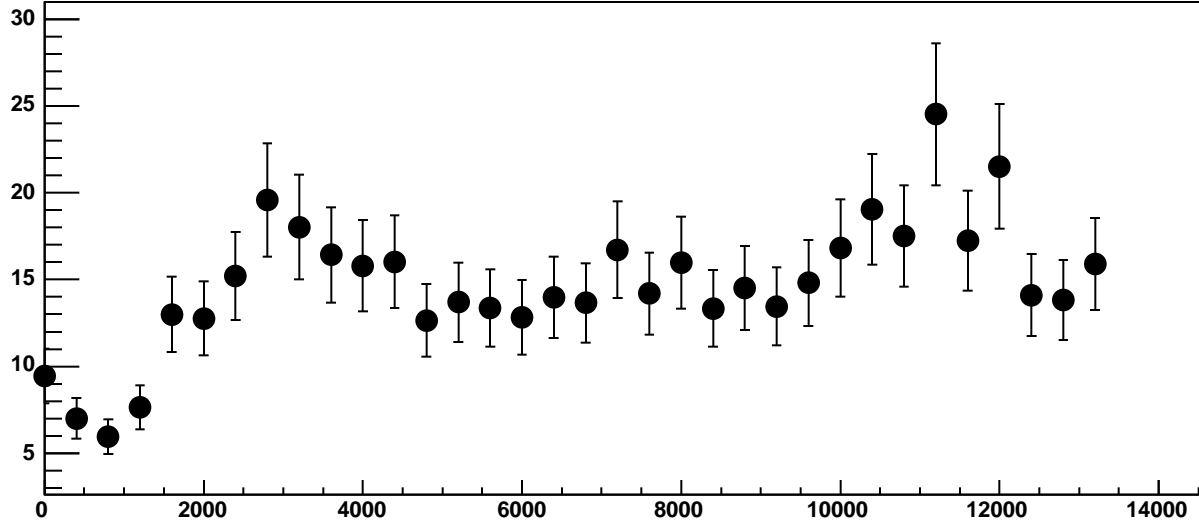


Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

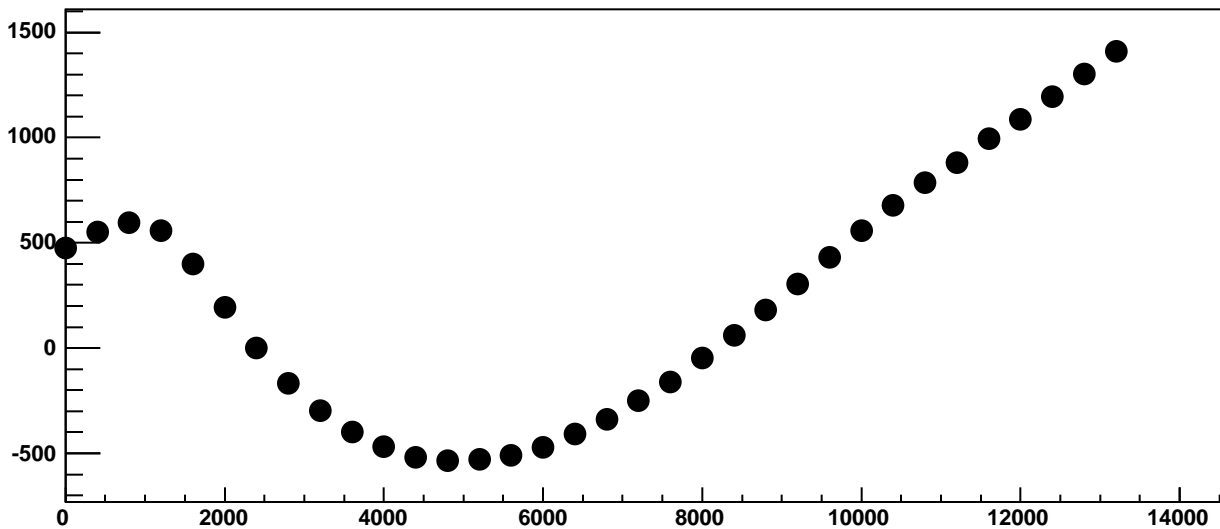
$\chi^2 / \text{ndf}$  4.409e+05 / 23  
p0 -1125 ± 1.331  
p1 -0.1975 ± 0.0002148



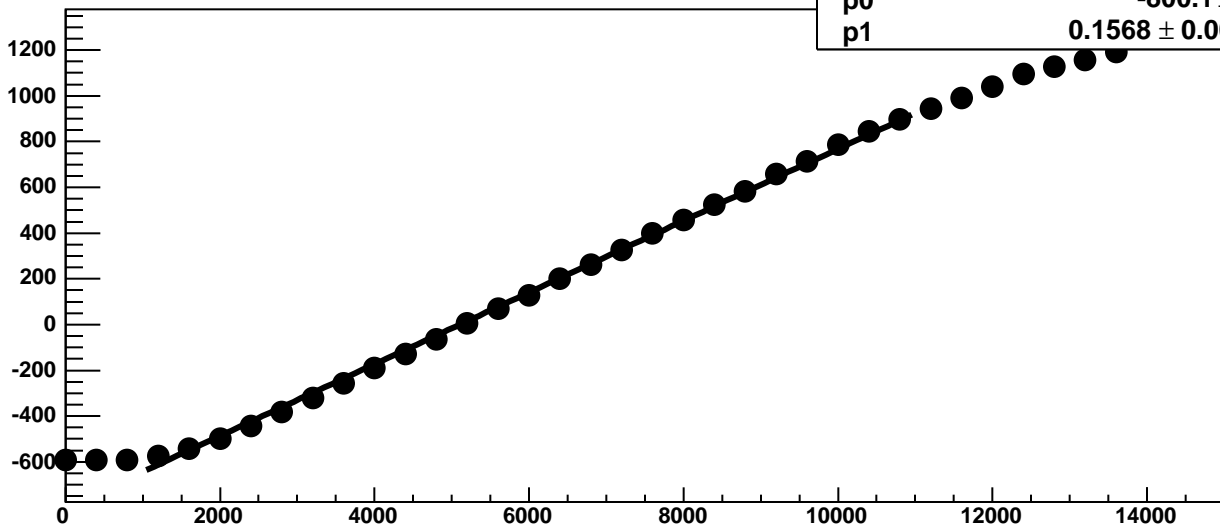
Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



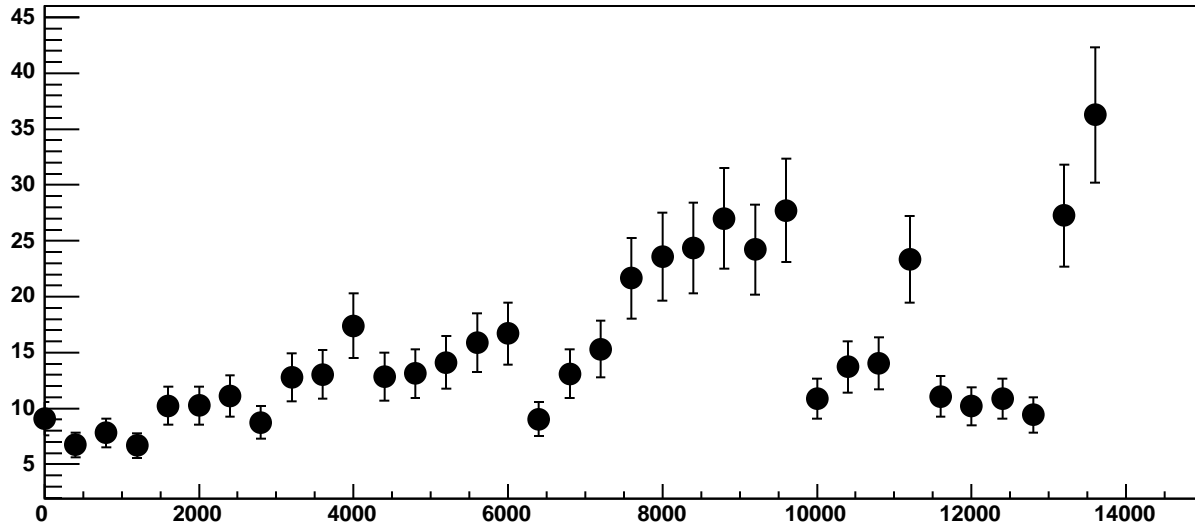
Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



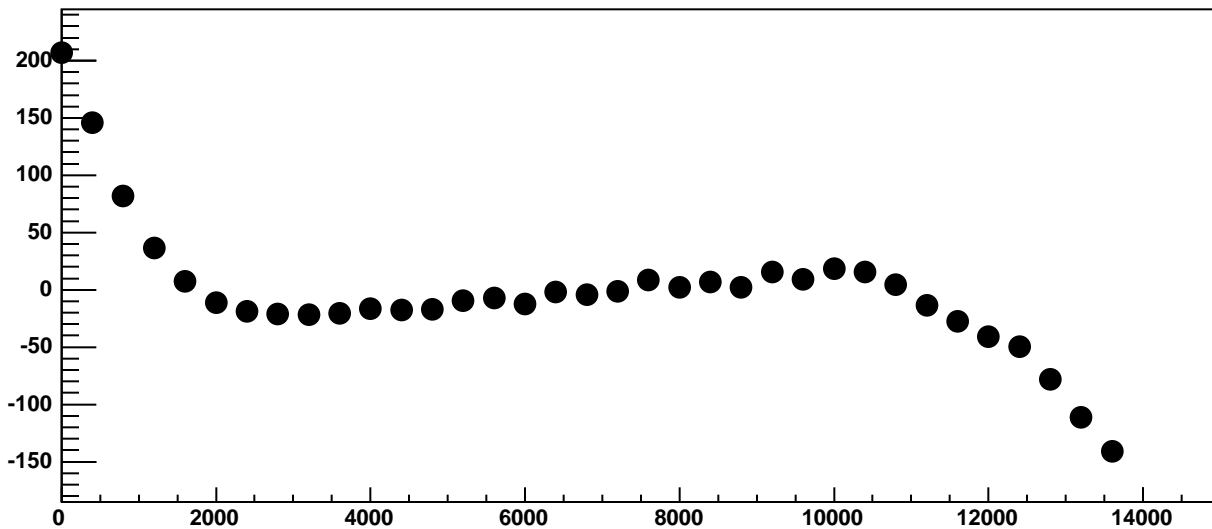
Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

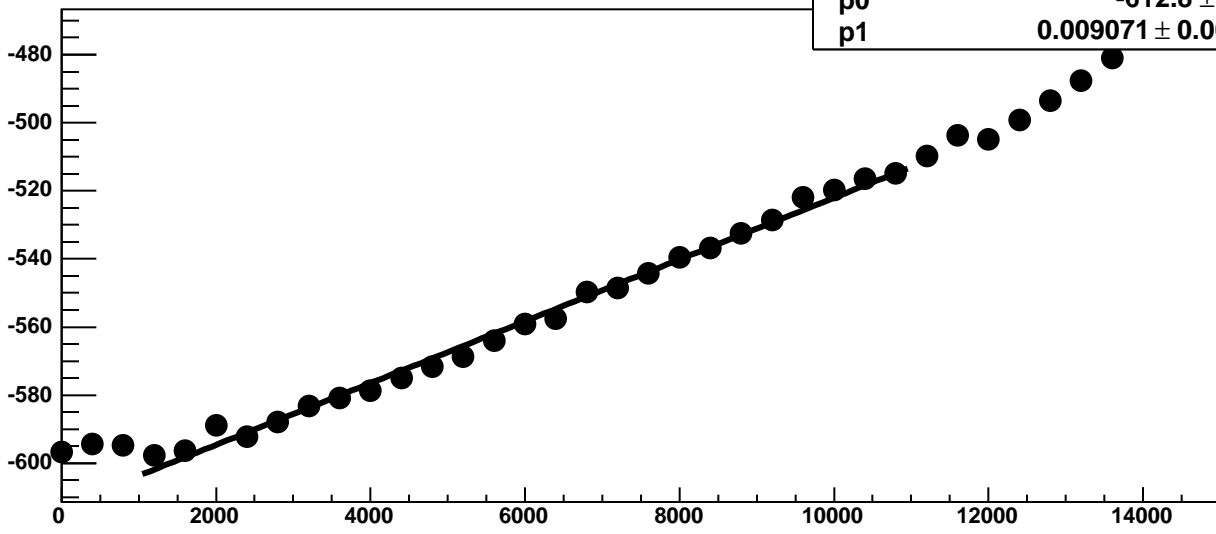


Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

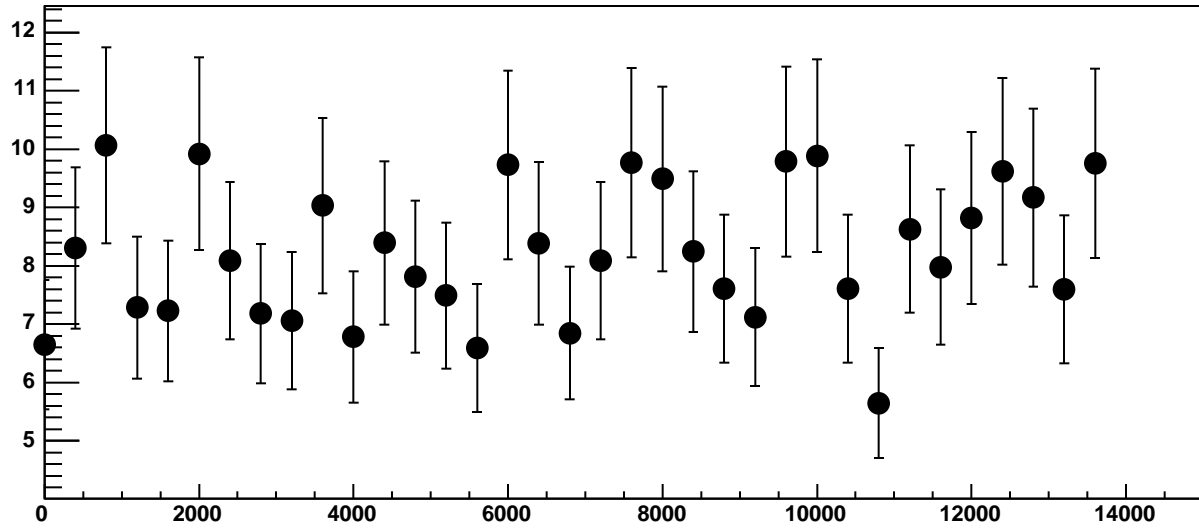




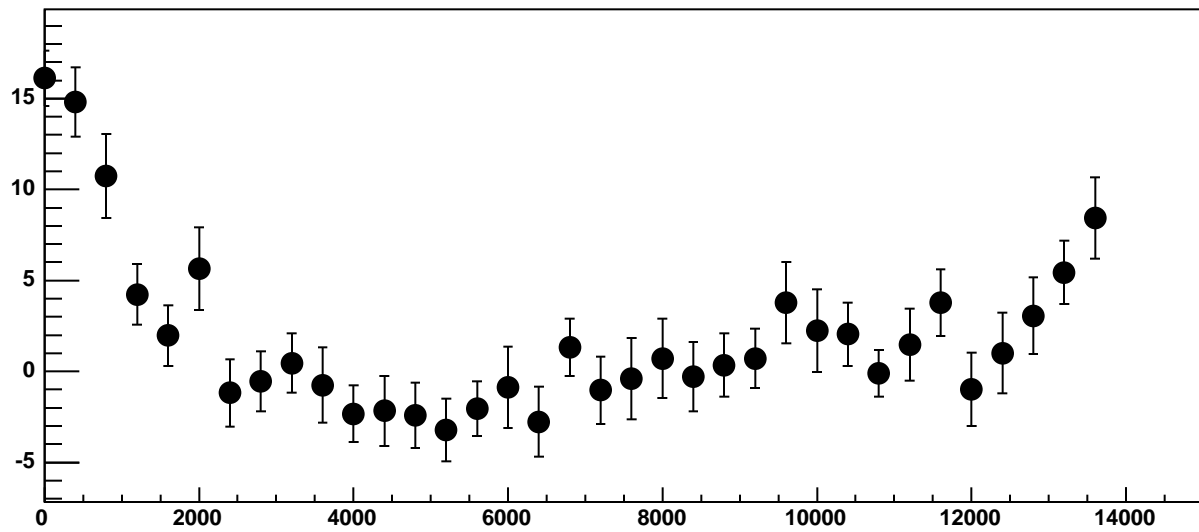
Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

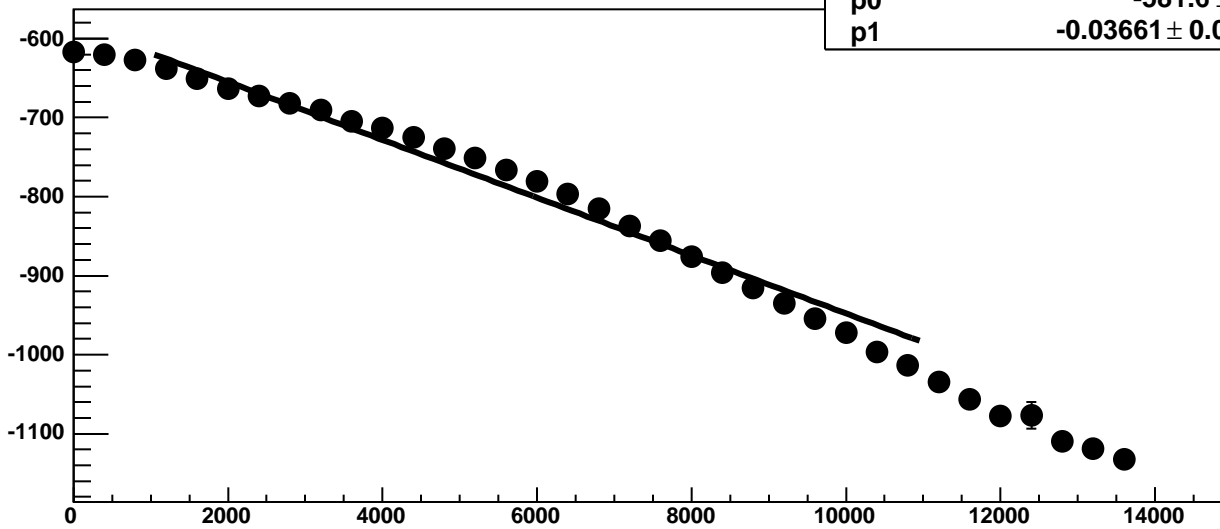


Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

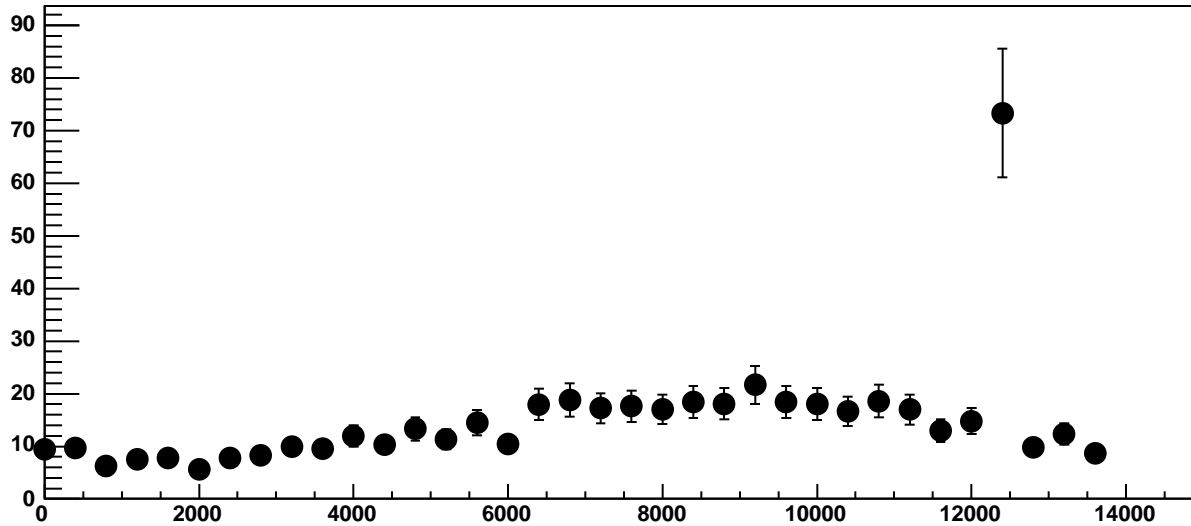


Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

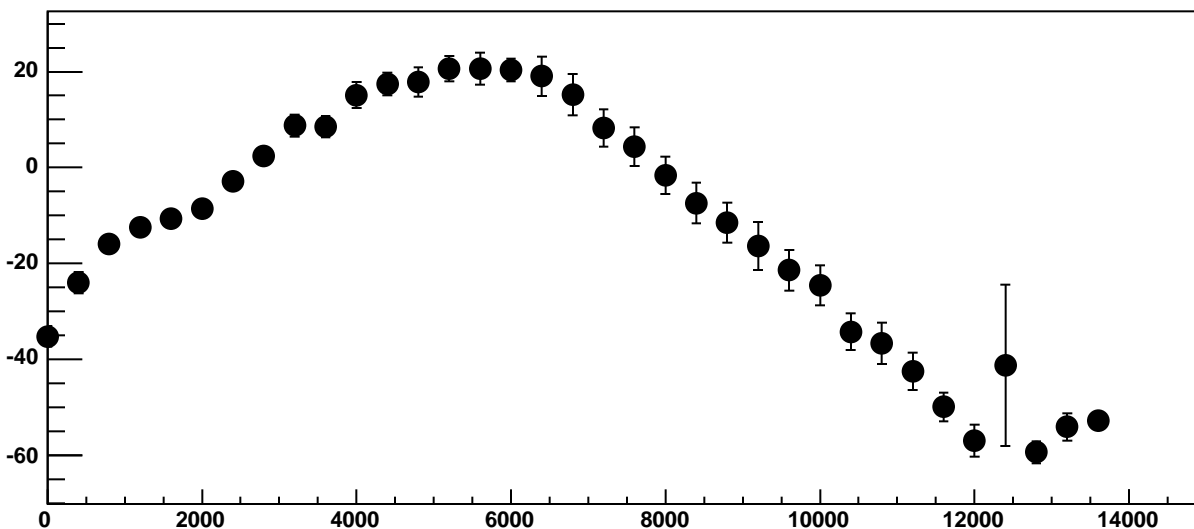
$\chi^2 / \text{ndf}$  734.2 / 23  
p0  $-581.6 \pm 0.9481$   
p1  $-0.03661 \pm 0.0001984$



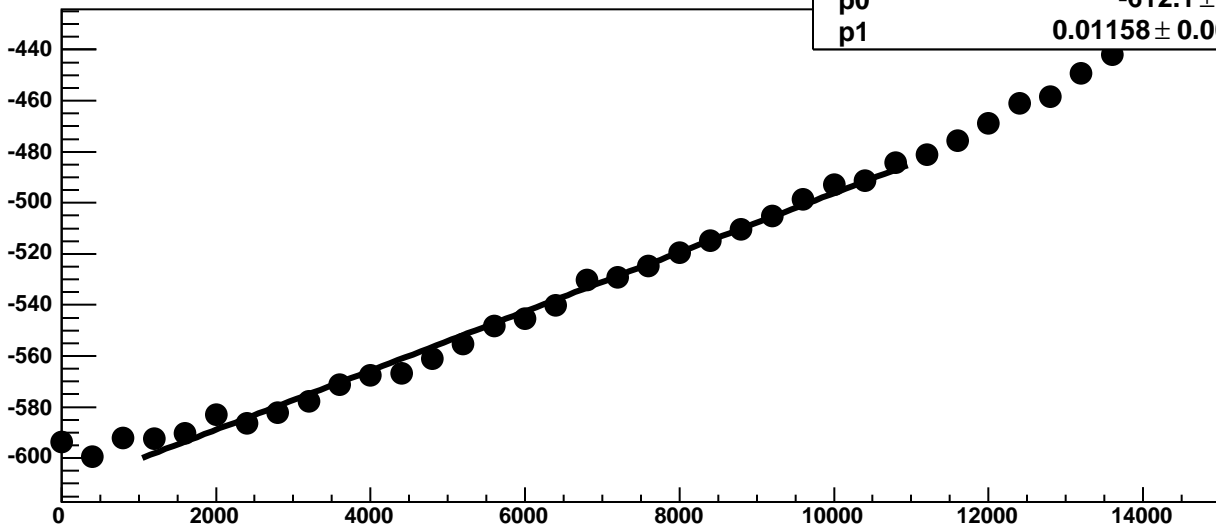
Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

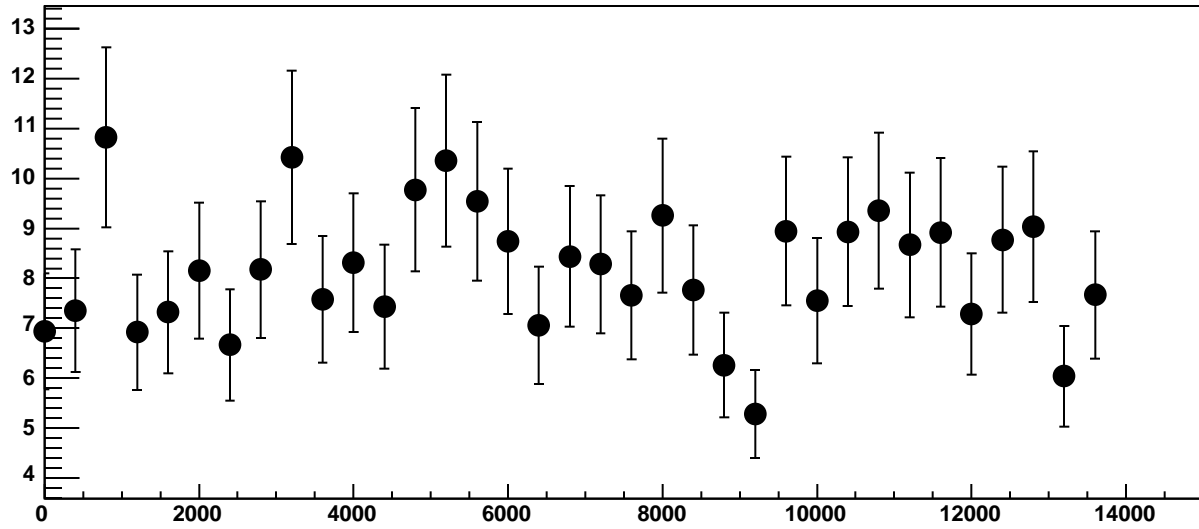


Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

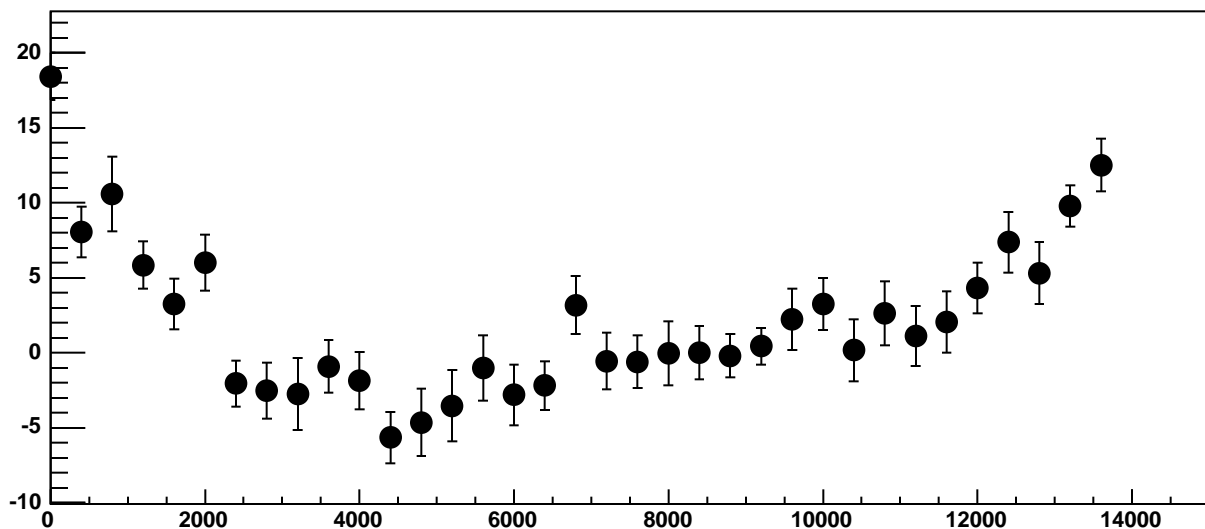


$\chi^2 / \text{ndf}$  64.58 / 23  
p0  $-612.1 \pm 0.8184$   
p1  $0.01158 \pm 0.0001216$

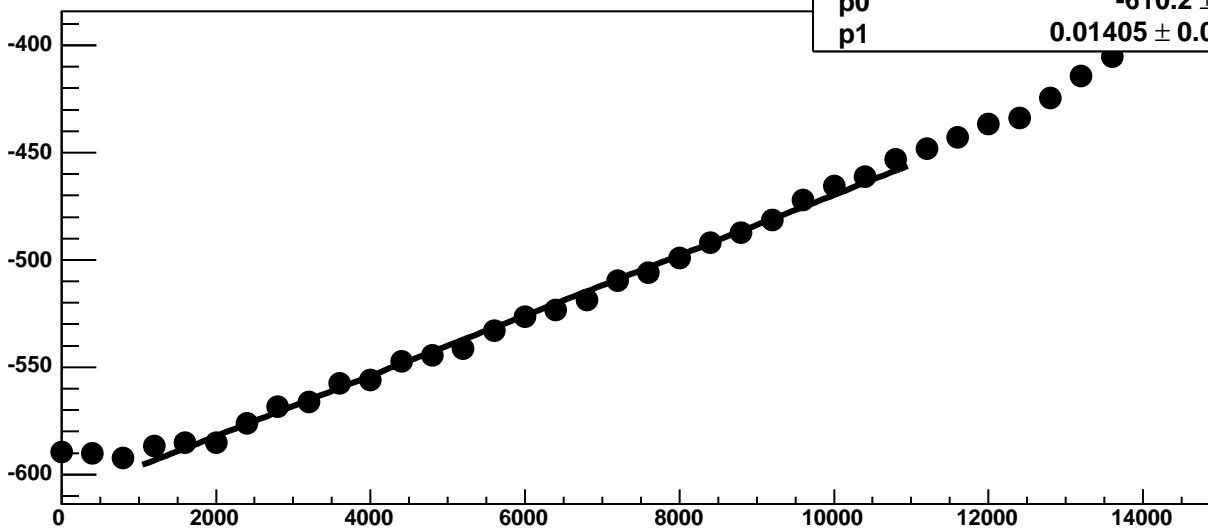
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

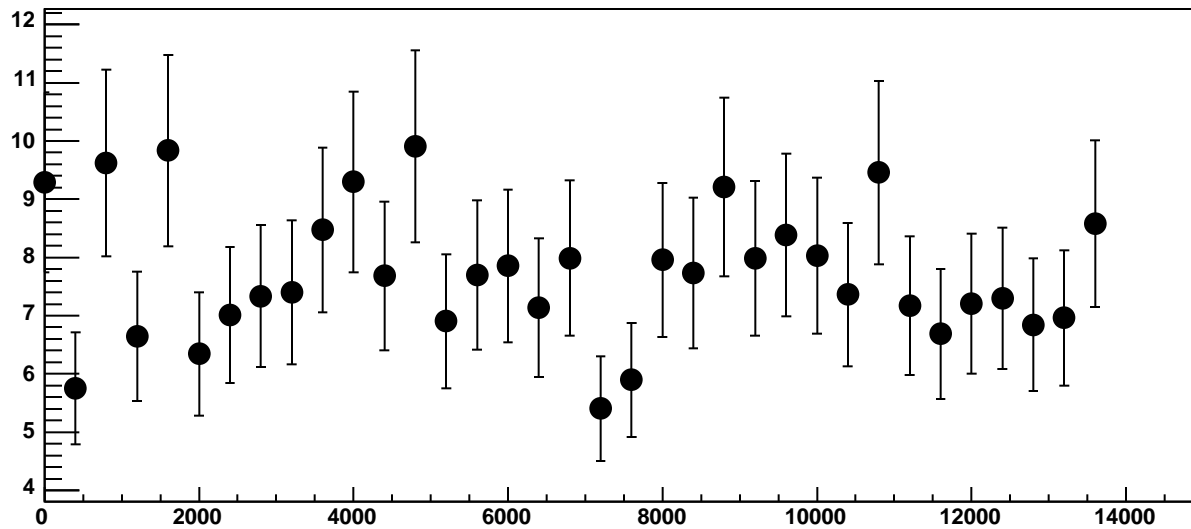


Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

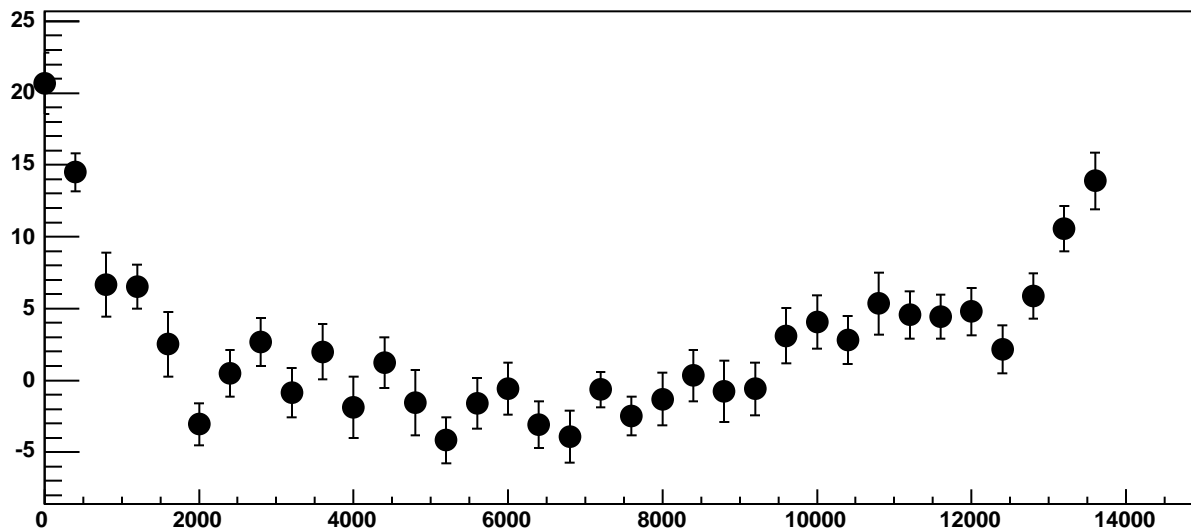


$\chi^2 / \text{ndf}$  66.18 / 23  
p0  $-610.2 \pm 0.8006$   
p1  $0.01405 \pm 0.0001225$

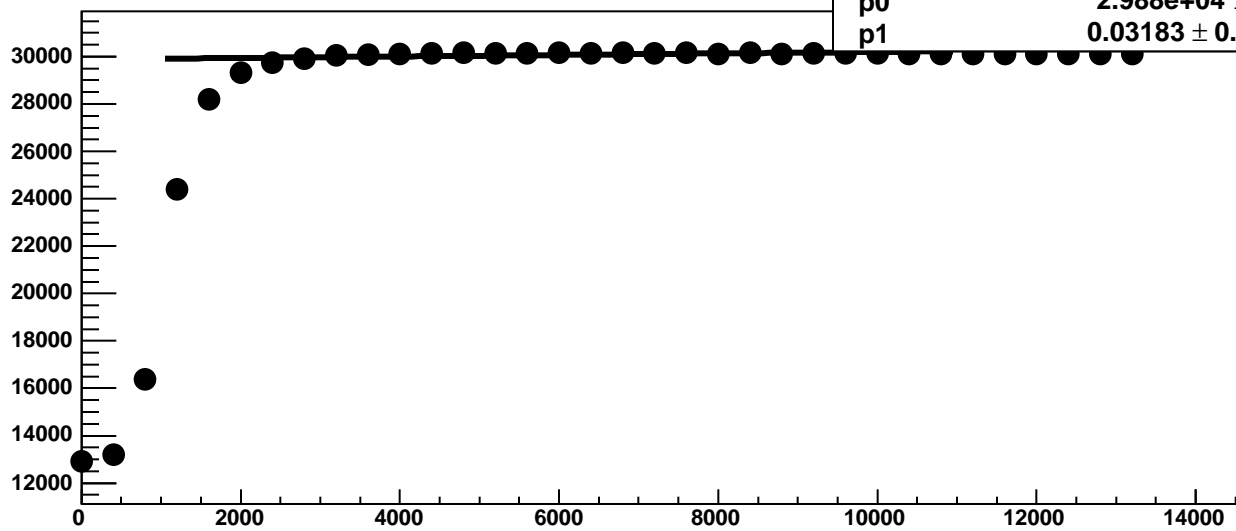
Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

8753 / 23

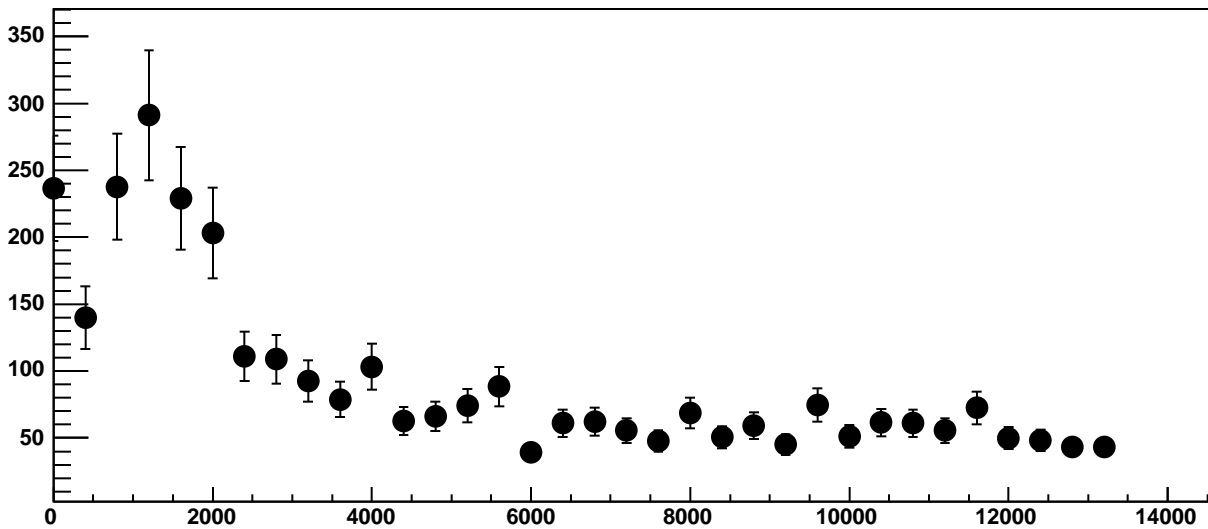
p0

$2.988e+04 \pm 10.17$

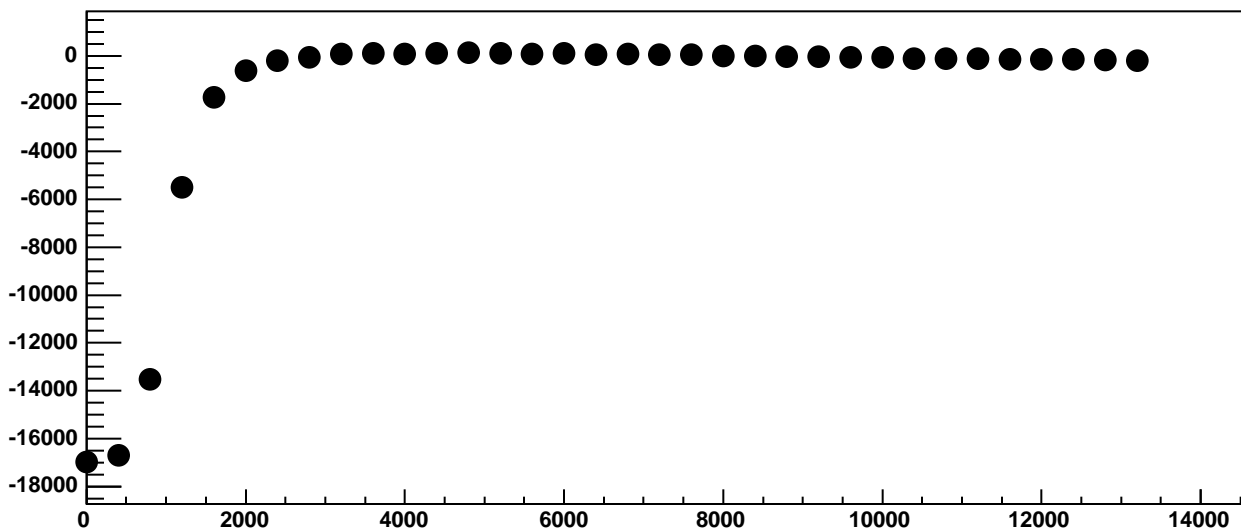
p1

$0.03183 \pm 0.001341$

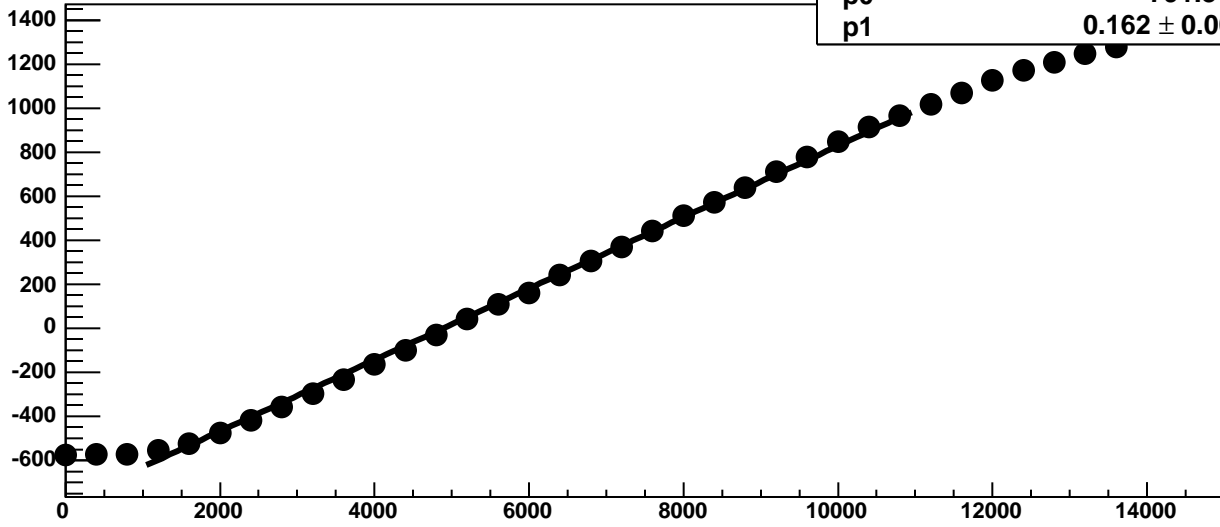
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



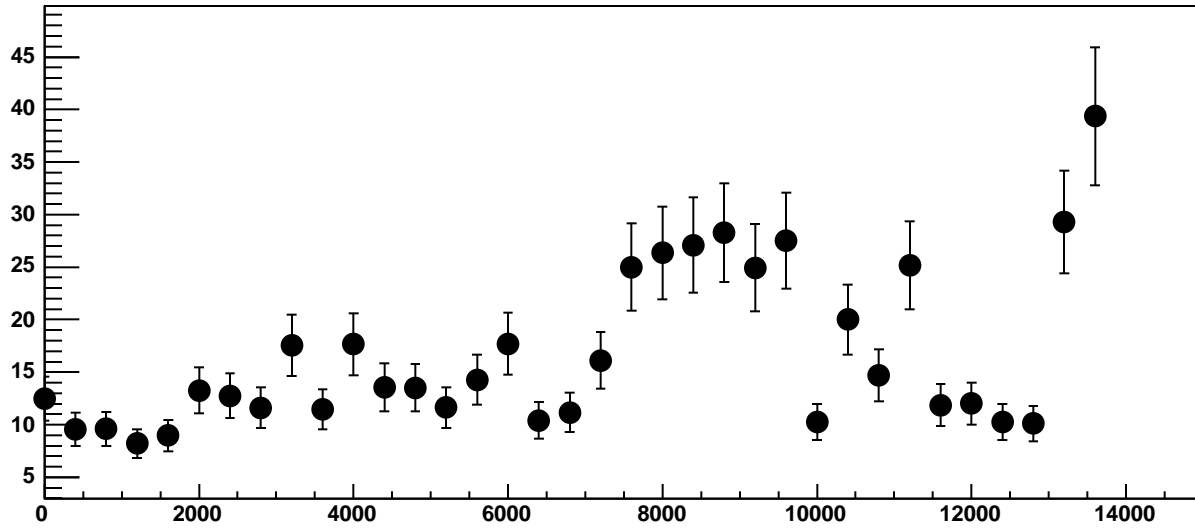
Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



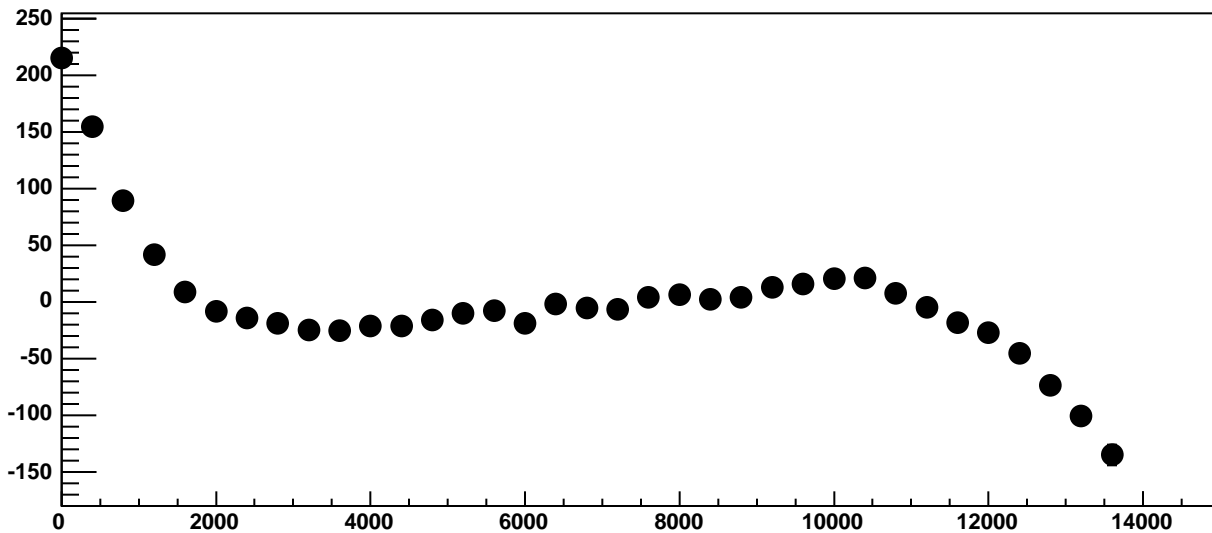
Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



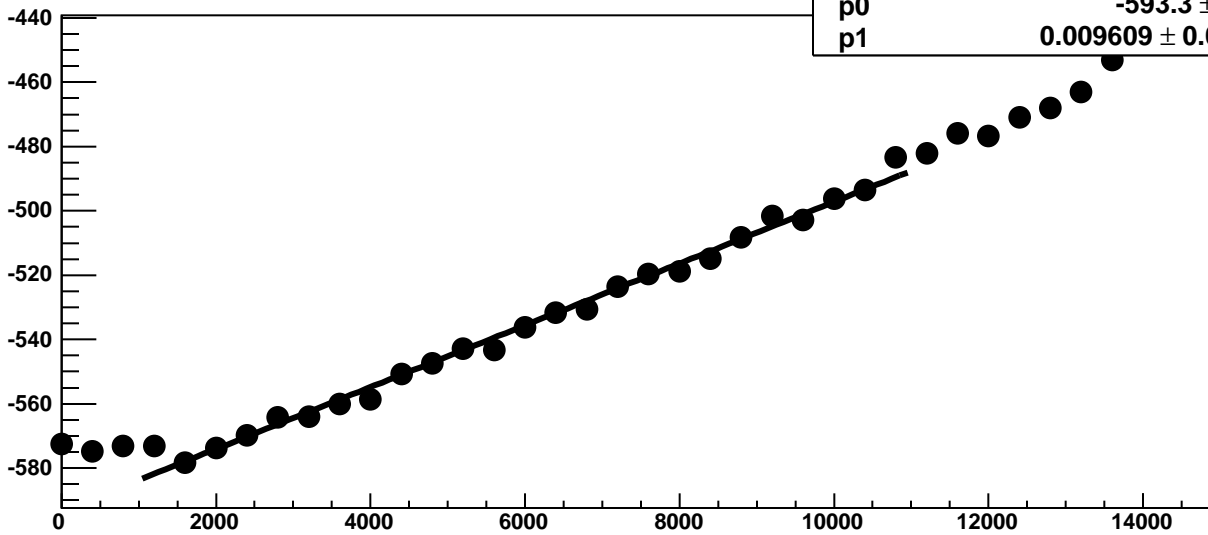
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



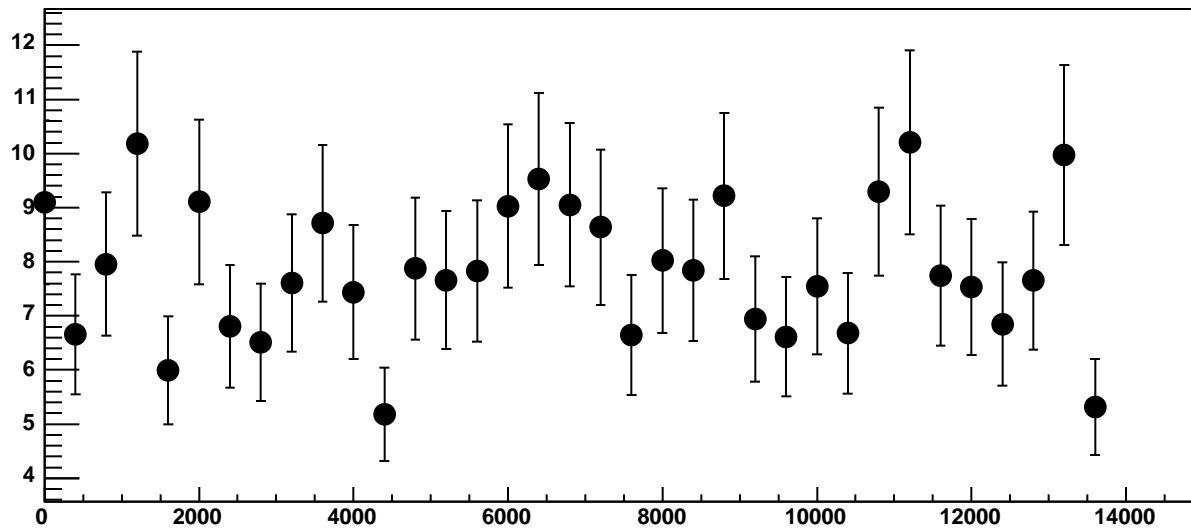
Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



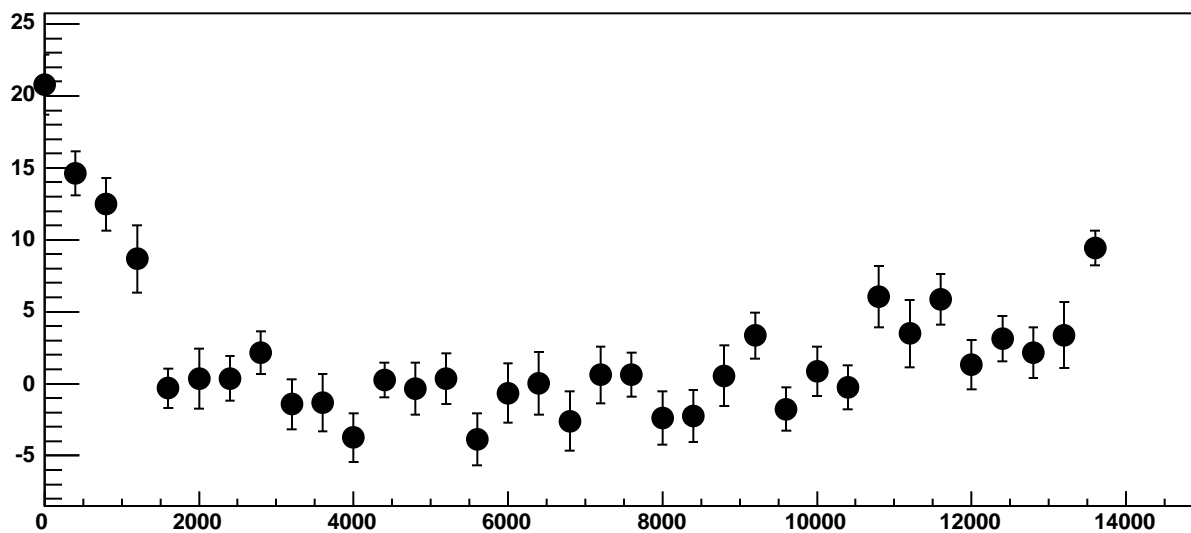
Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

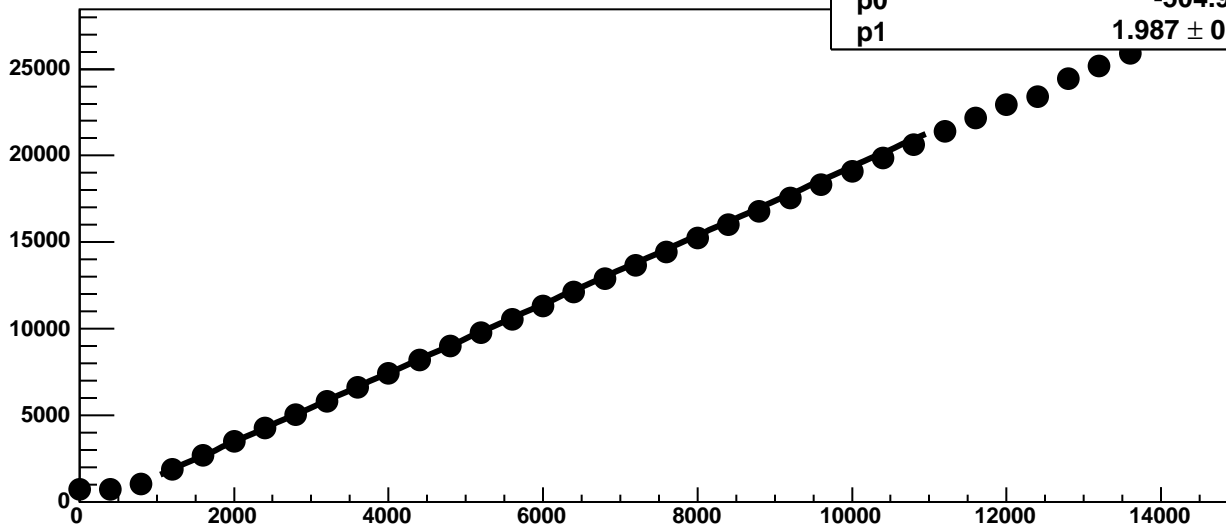


Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

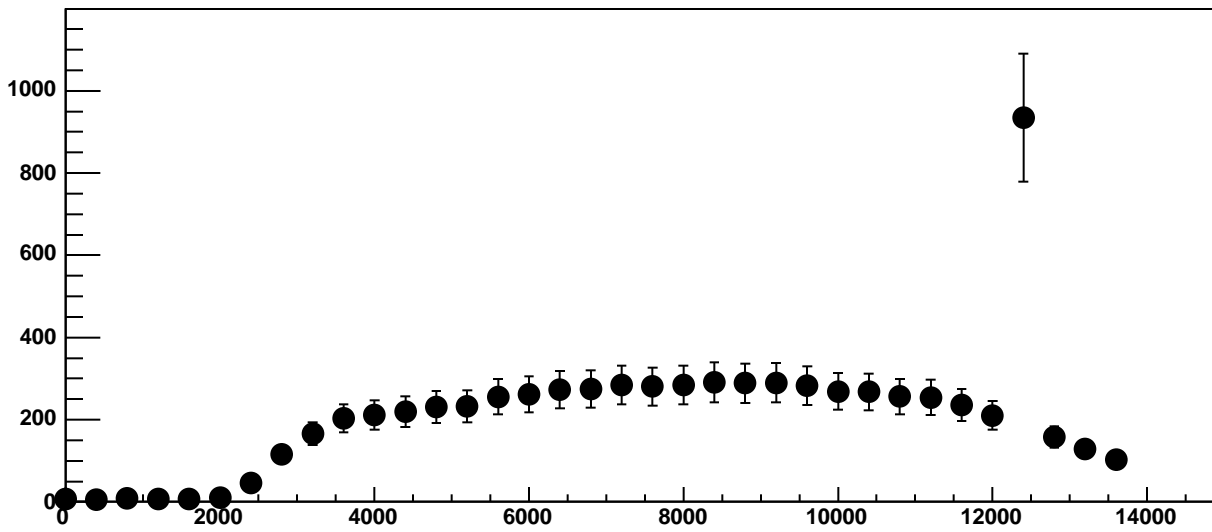


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

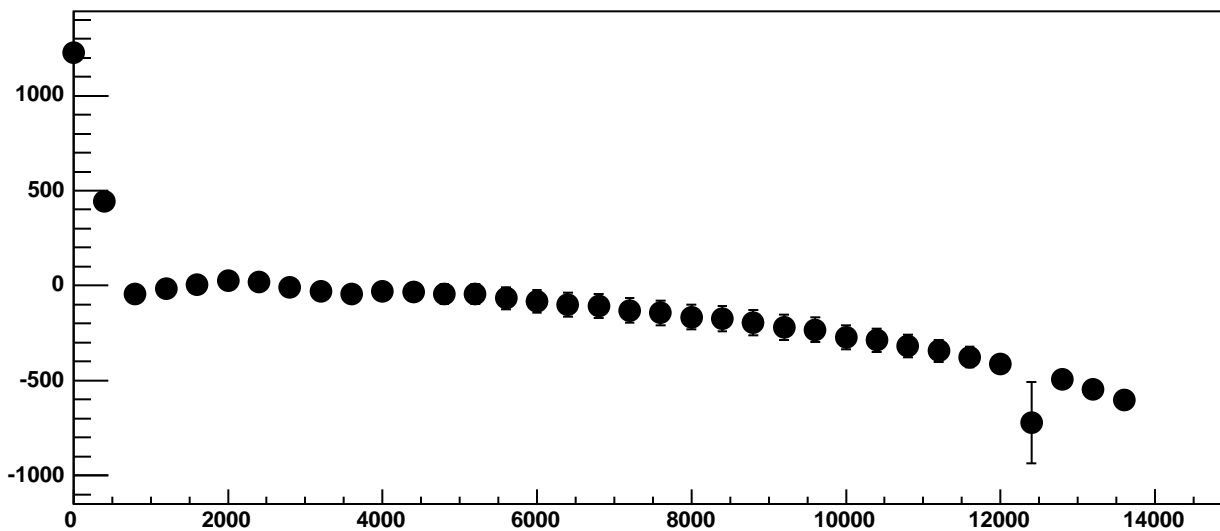
$\chi^2 / \text{ndf}$  404.2 / 23  
p0  $-504.9 \pm 3.131$   
p1  $1.987 \pm 0.001894$



Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

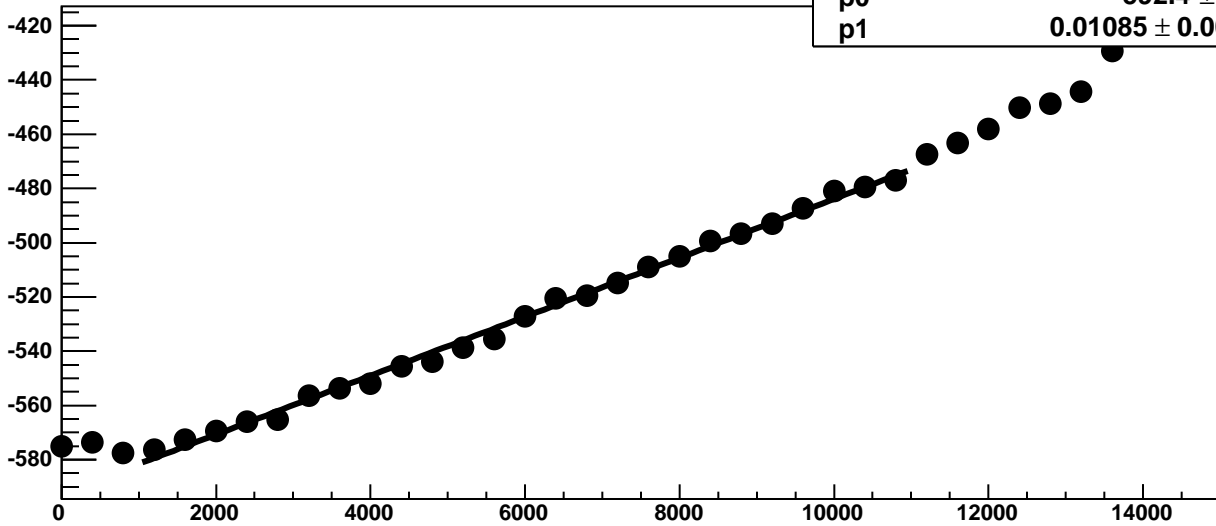


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC

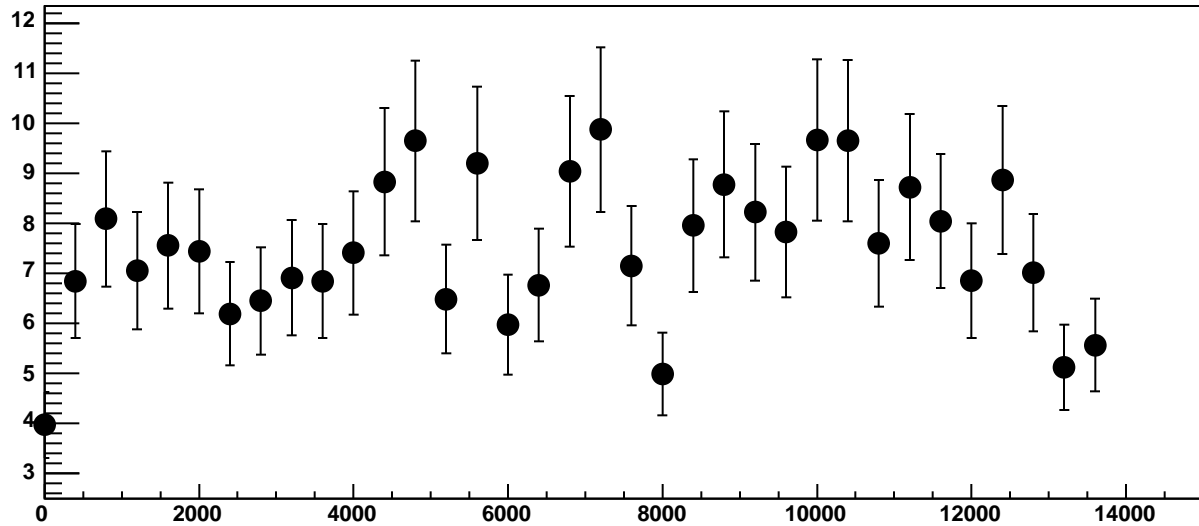




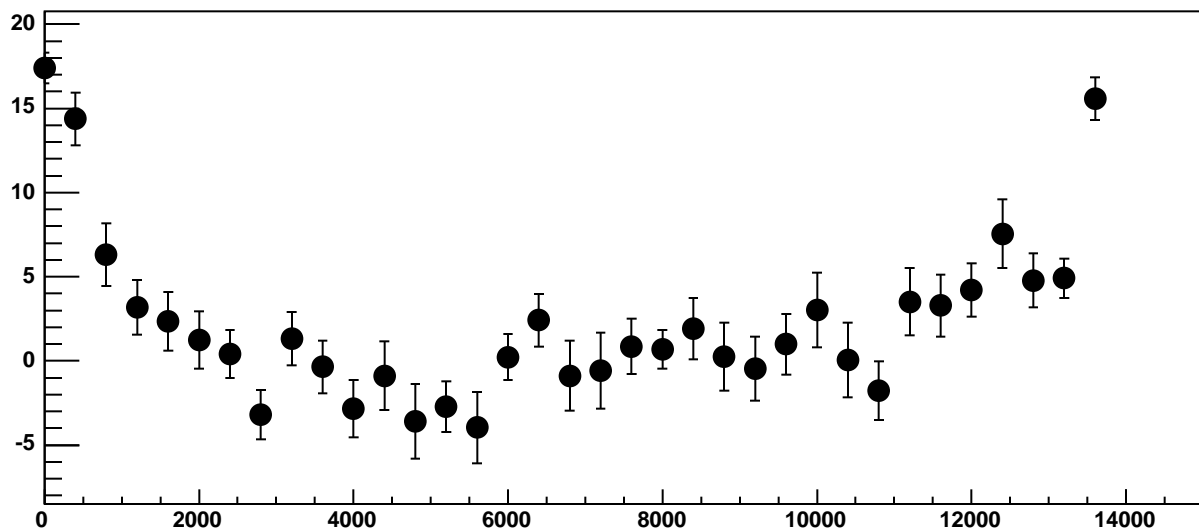
Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



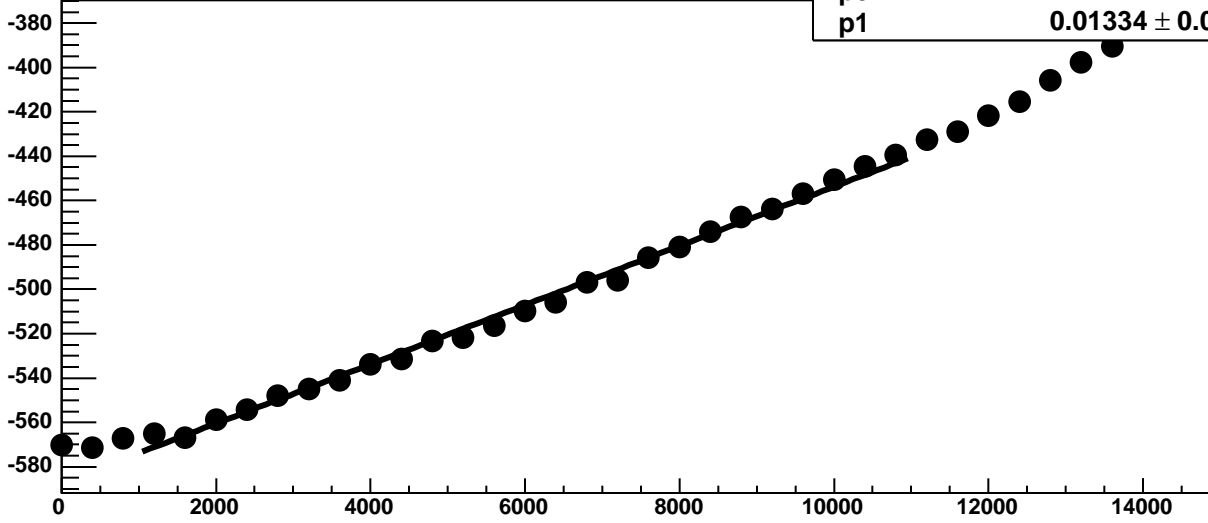
Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

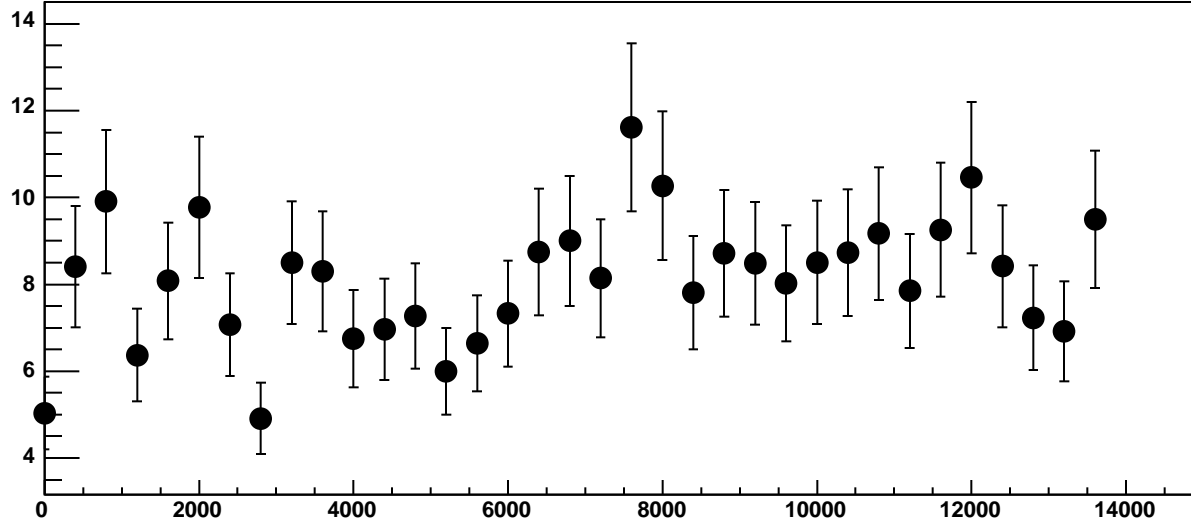


Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

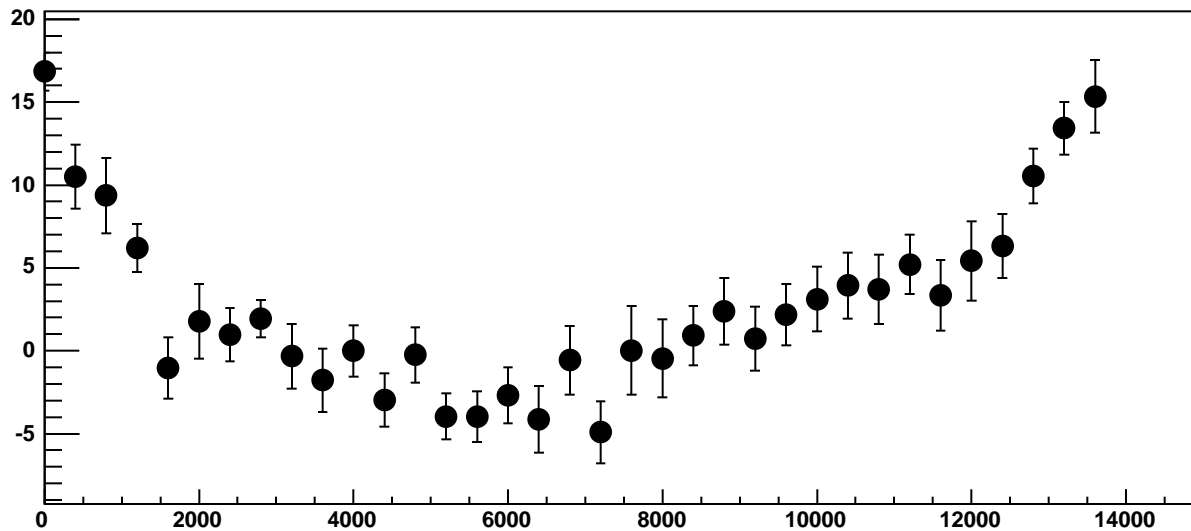


$\chi^2 / \text{ndf}$  68.26 / 23  
p0  $-587.2 \pm 0.7699$   
p1  $0.01334 \pm 0.0001252$

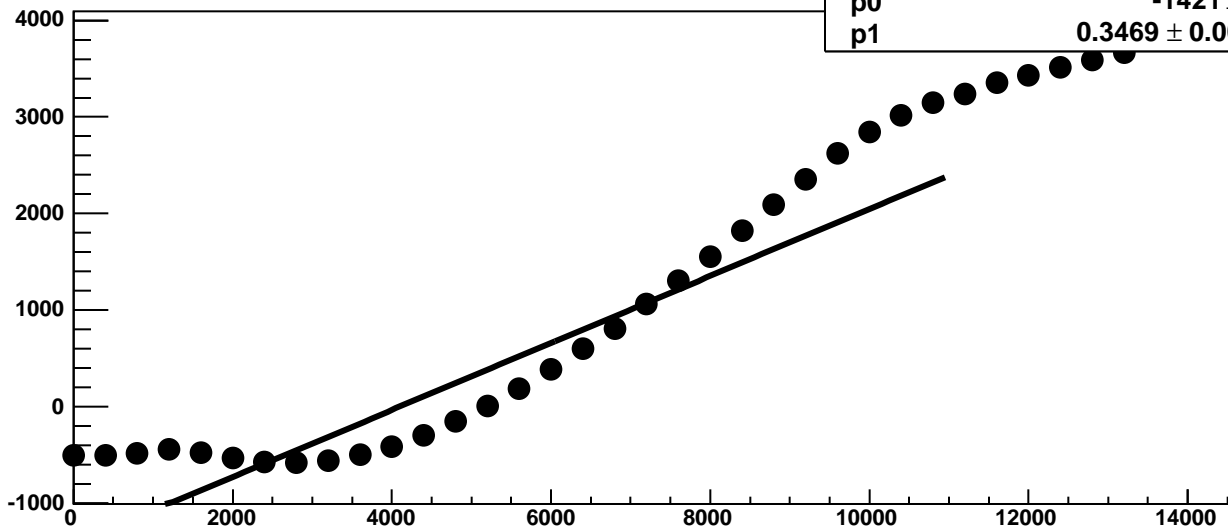
Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



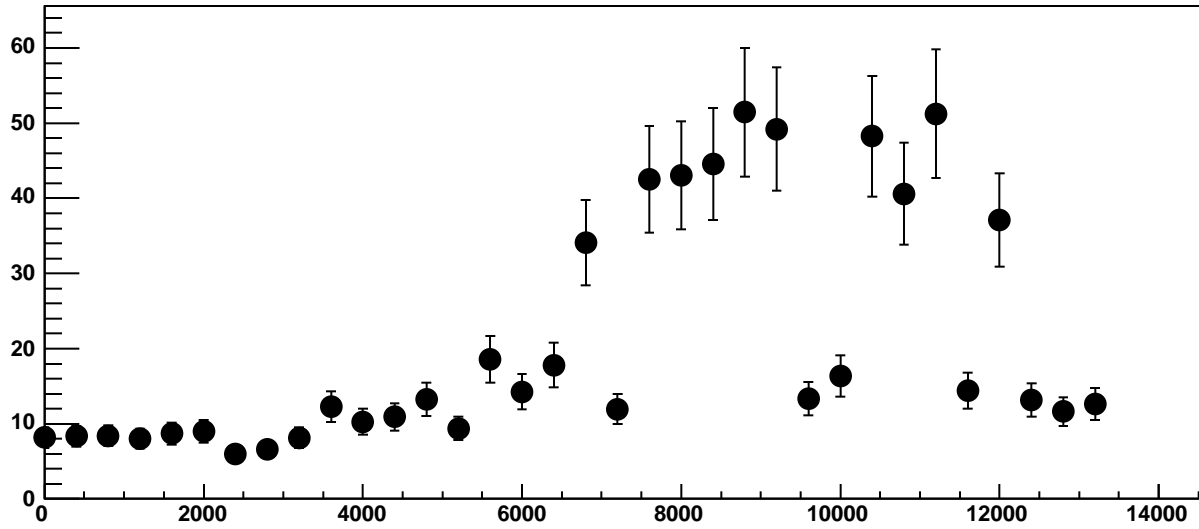
Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



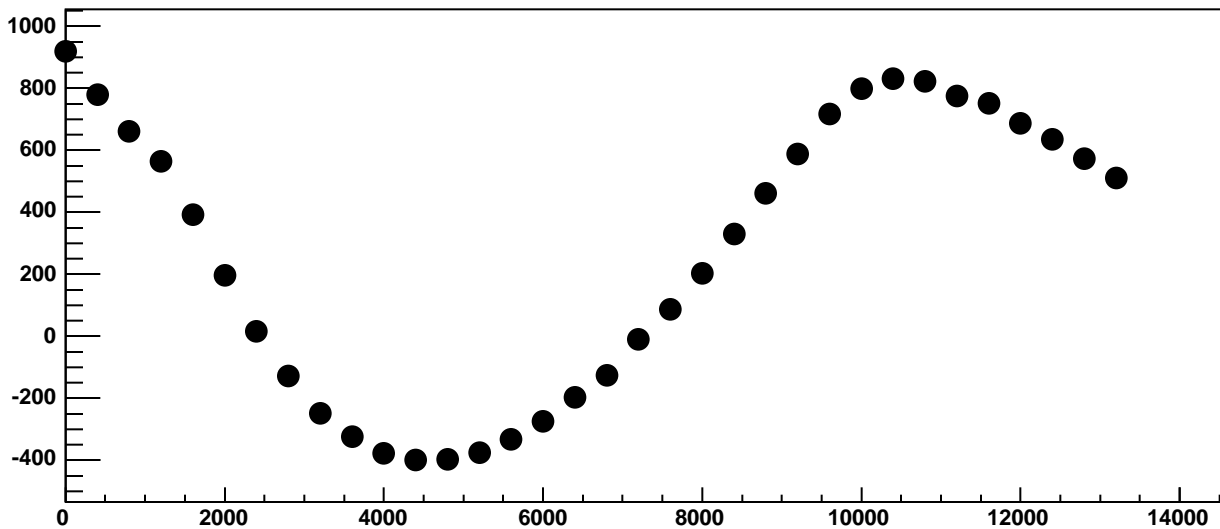
Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



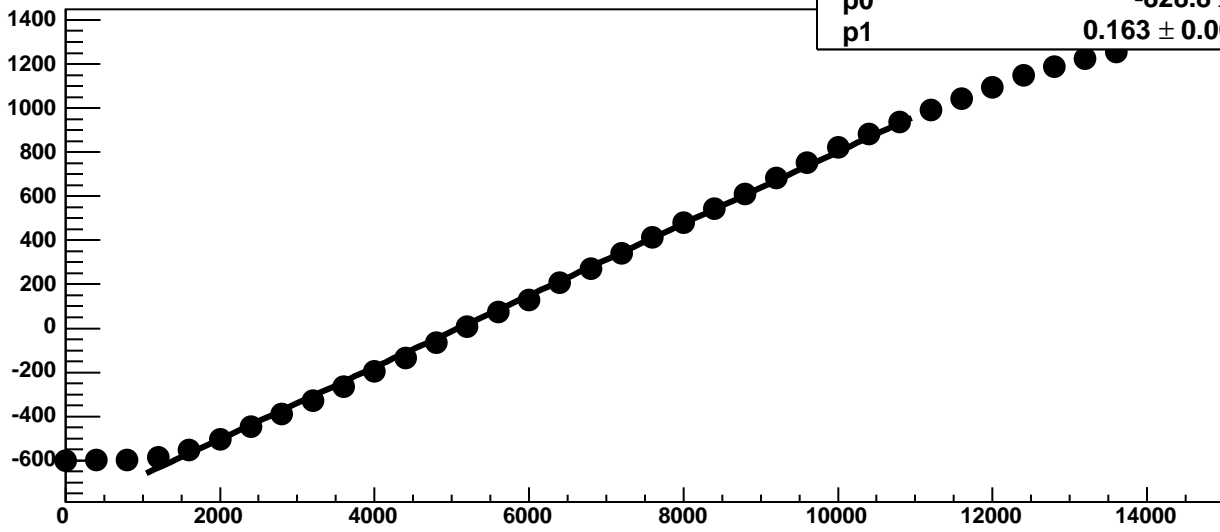
Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



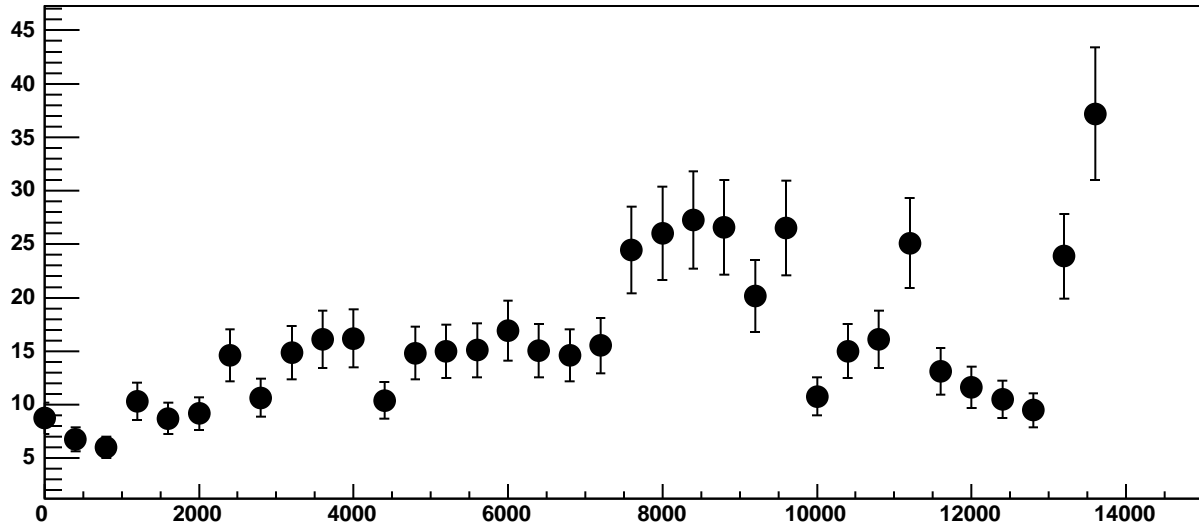
Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC



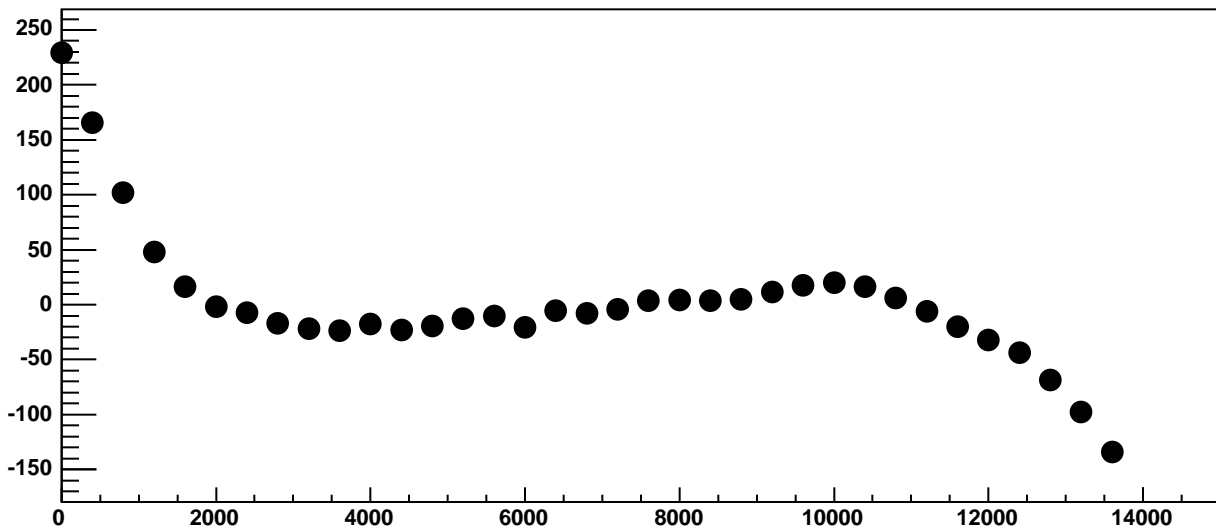
Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



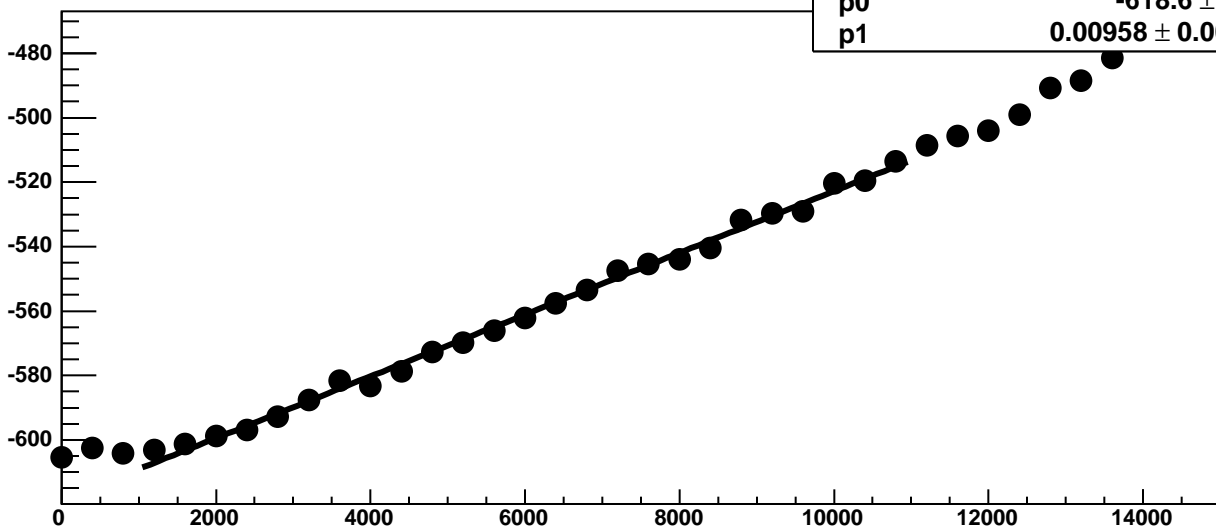
Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

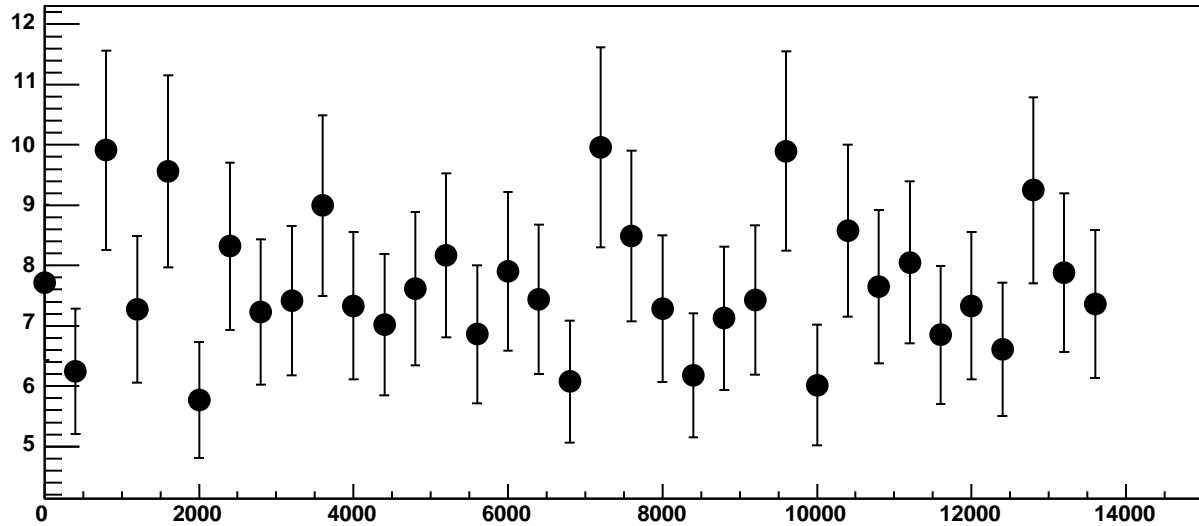


Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

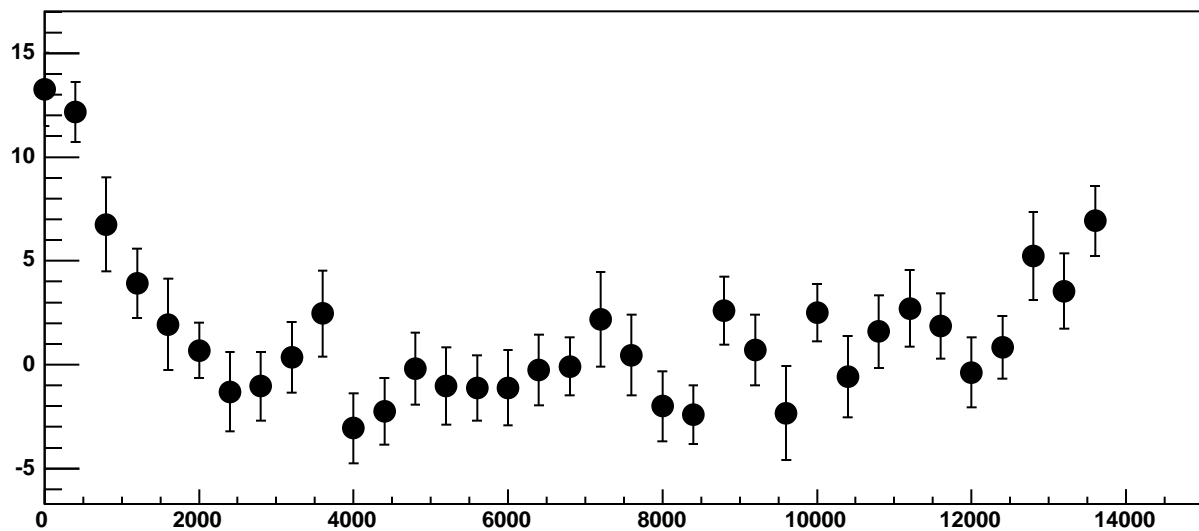


$\chi^2 / \text{ndf}$  28.68 / 23  
p0  $-618.6 \pm 0.7884$   
p1  $0.00958 \pm 0.0001184$

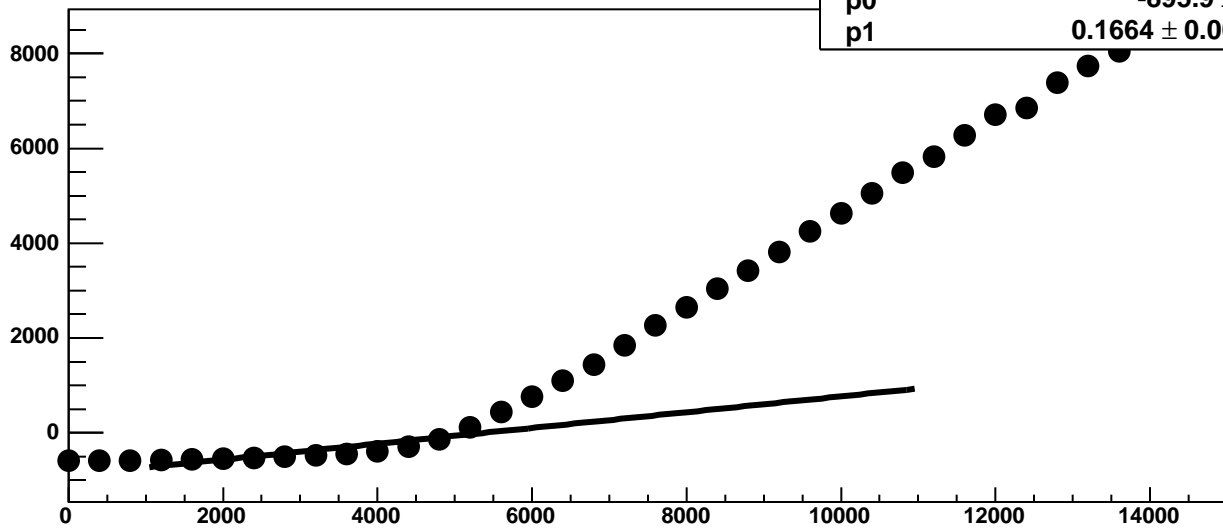
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



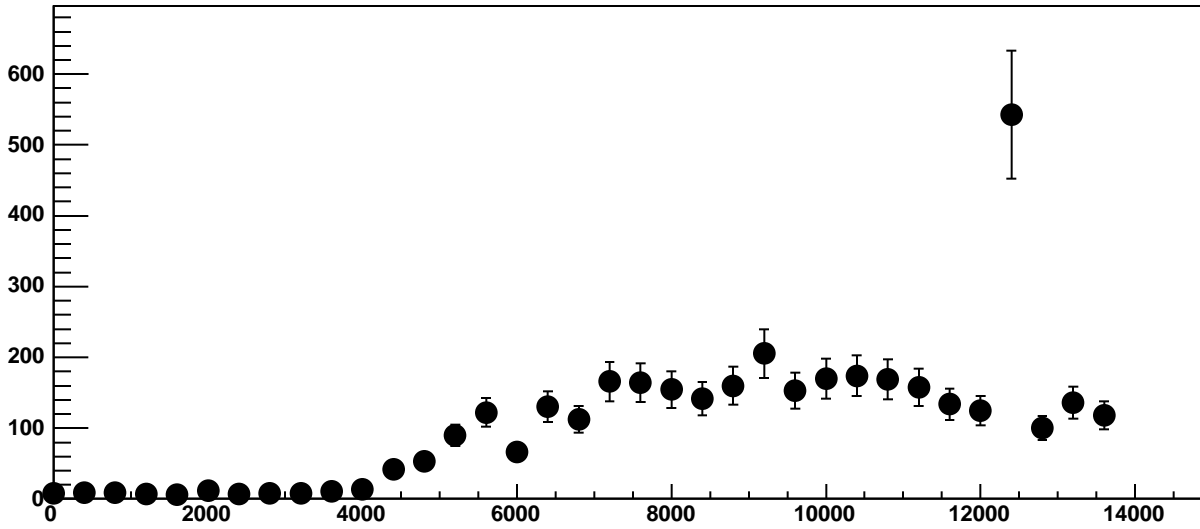
Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



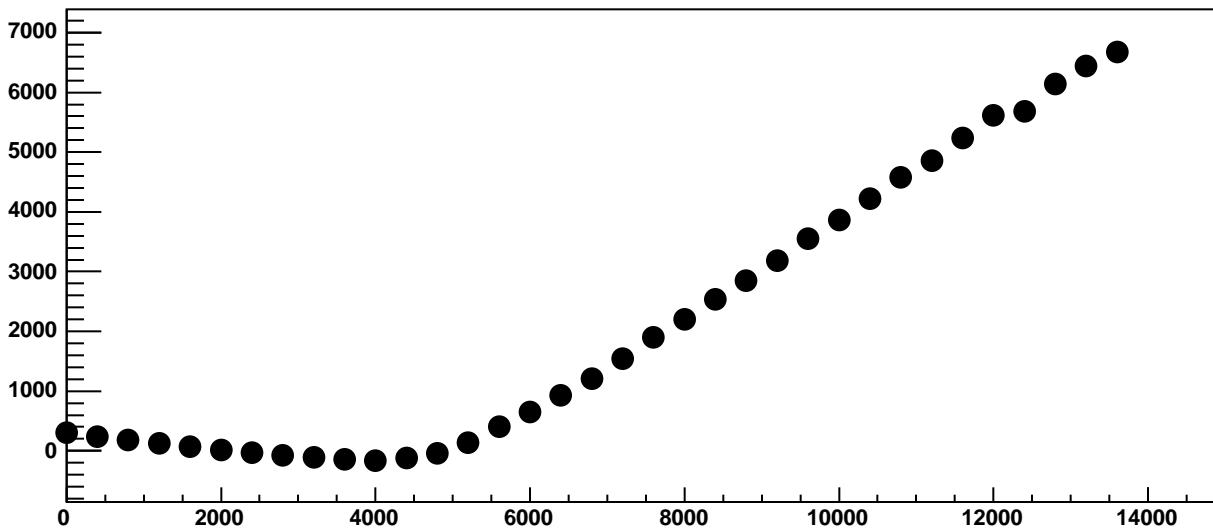
Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



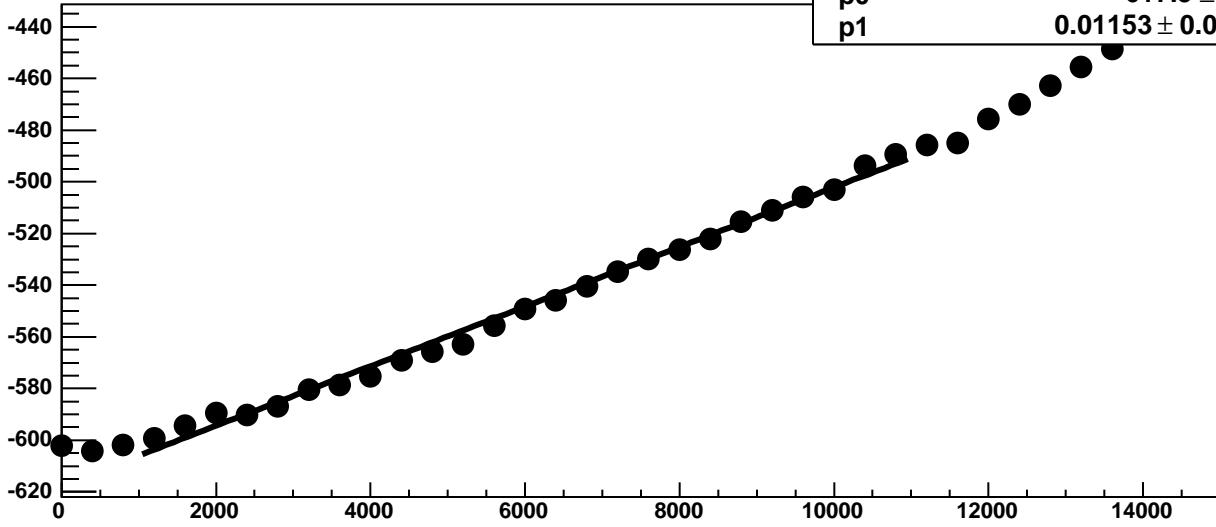
Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



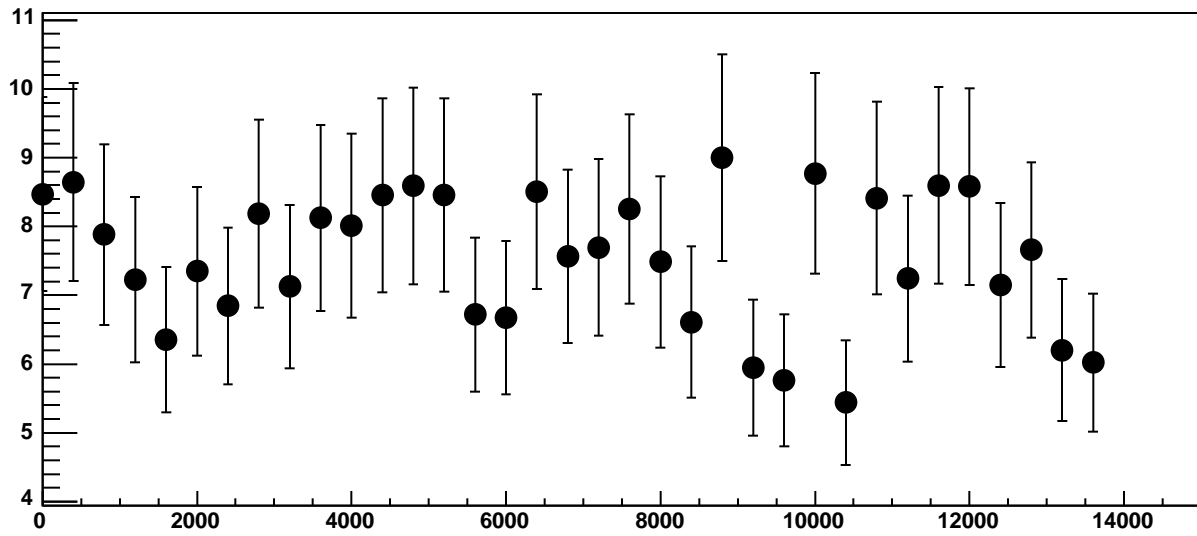
Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



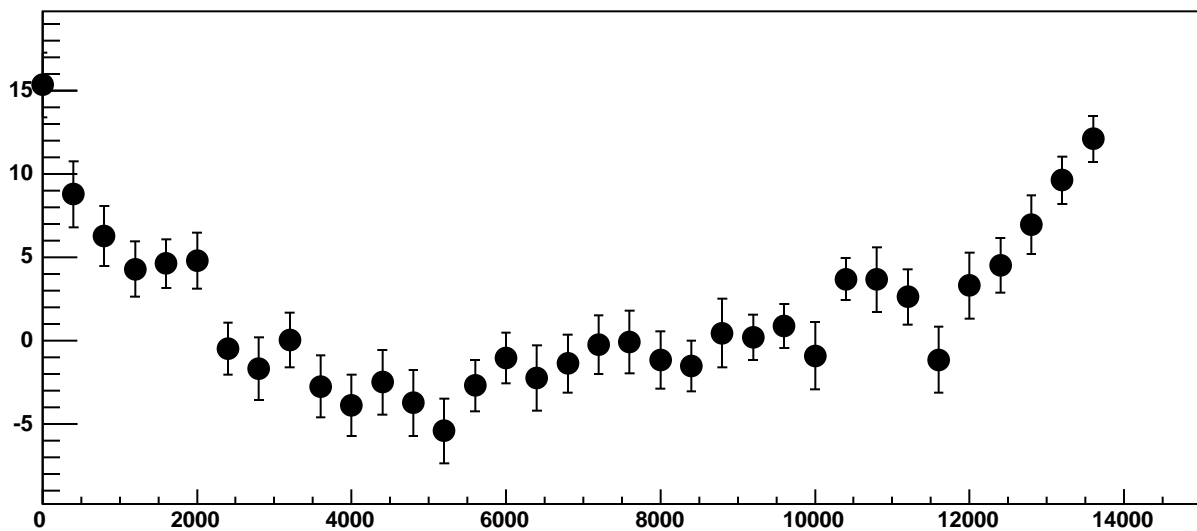
Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



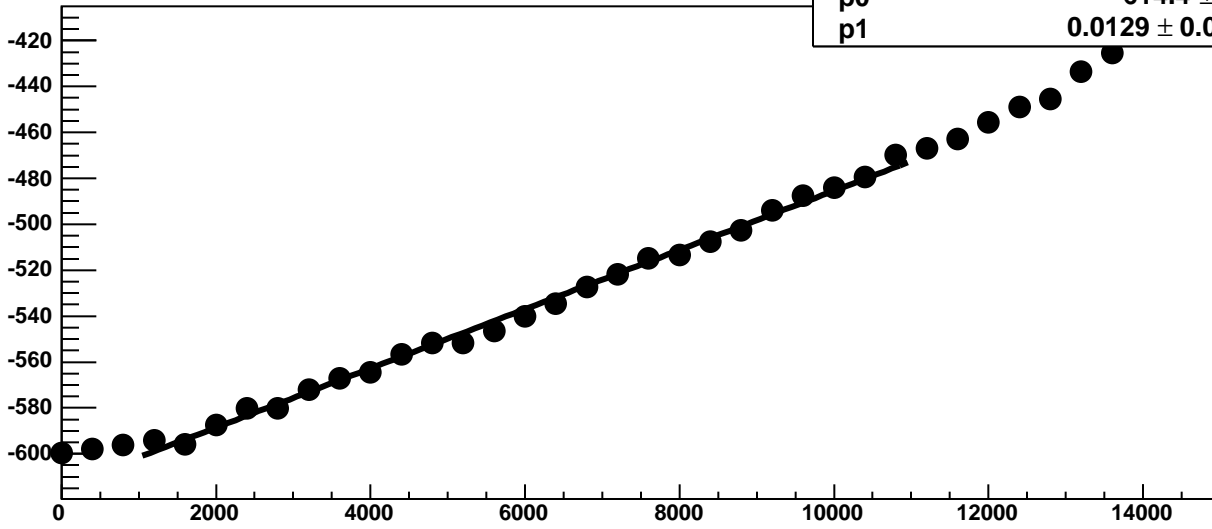
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

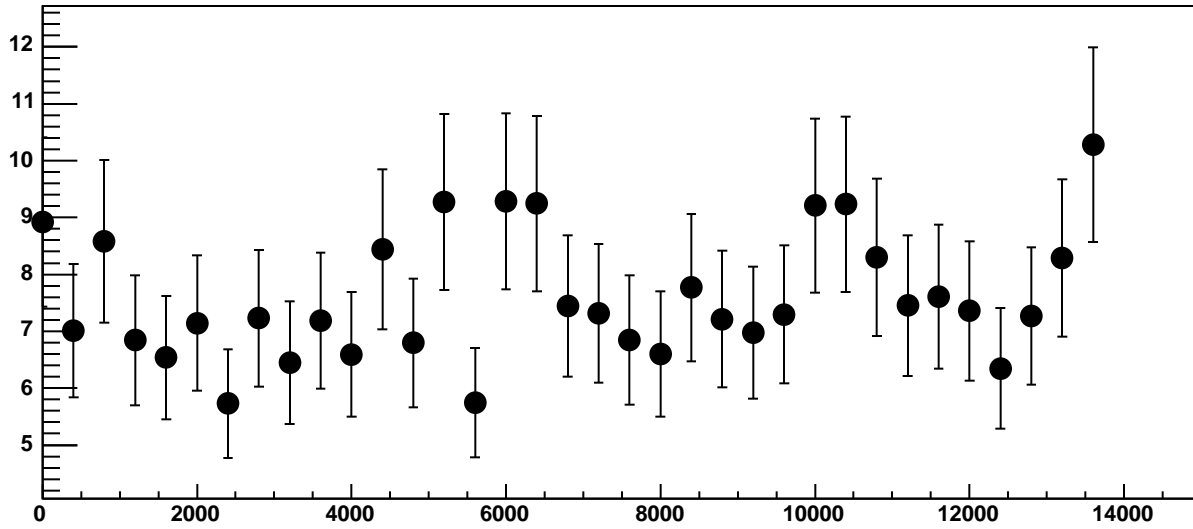


Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

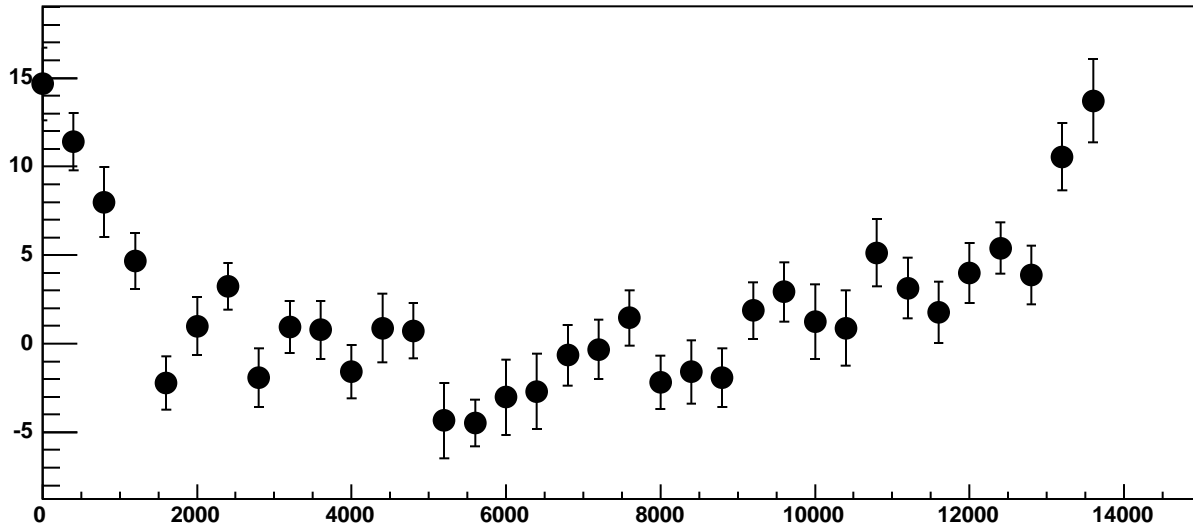


$\chi^2 / \text{ndf}$  57.85 / 23  
p0 -614.4 ± 0.7357  
p1 0.0129 ± 0.0001162

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC





Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

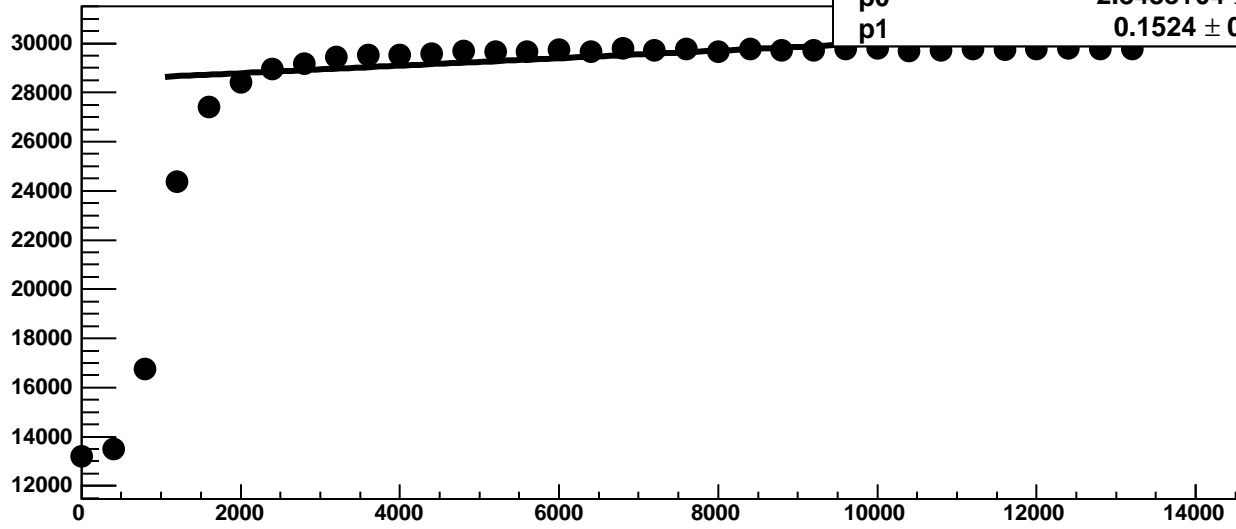
6143 / 23

p0

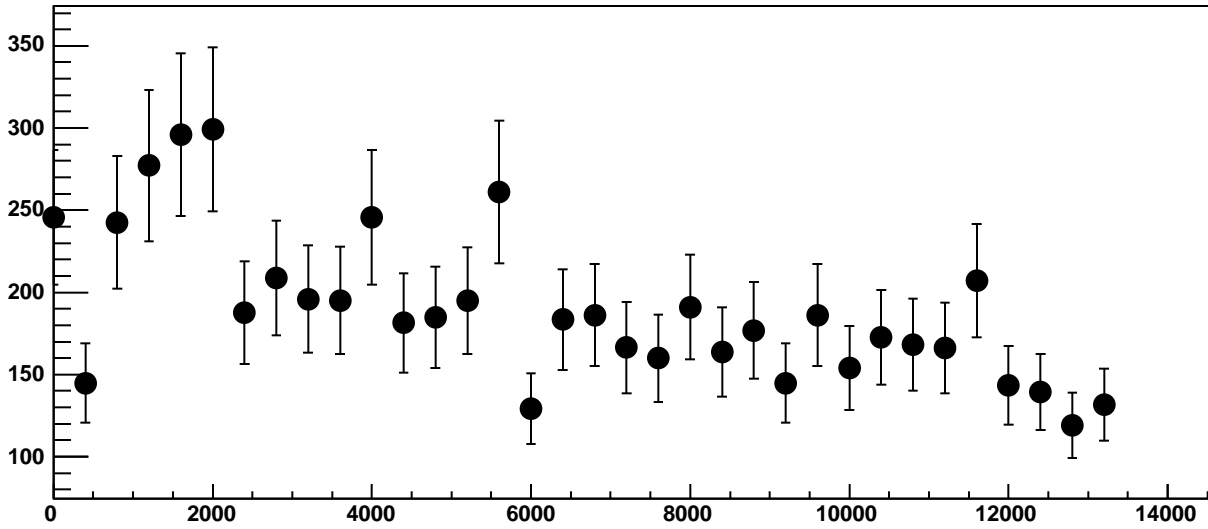
$2.848\text{e}+04 \pm 22.77$

p1

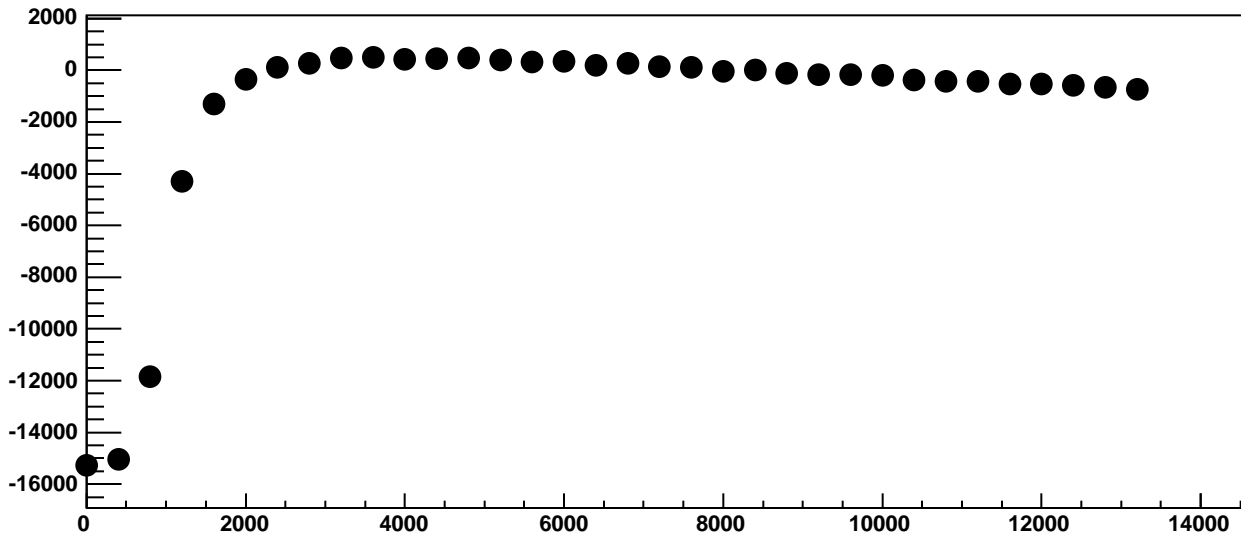
$0.1524 \pm 0.00316$



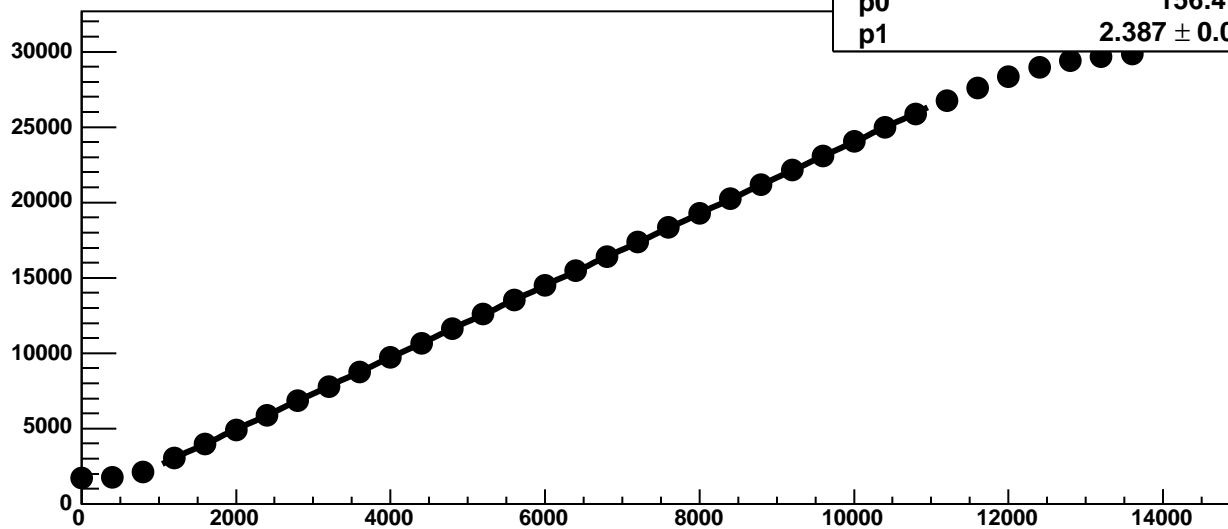
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC

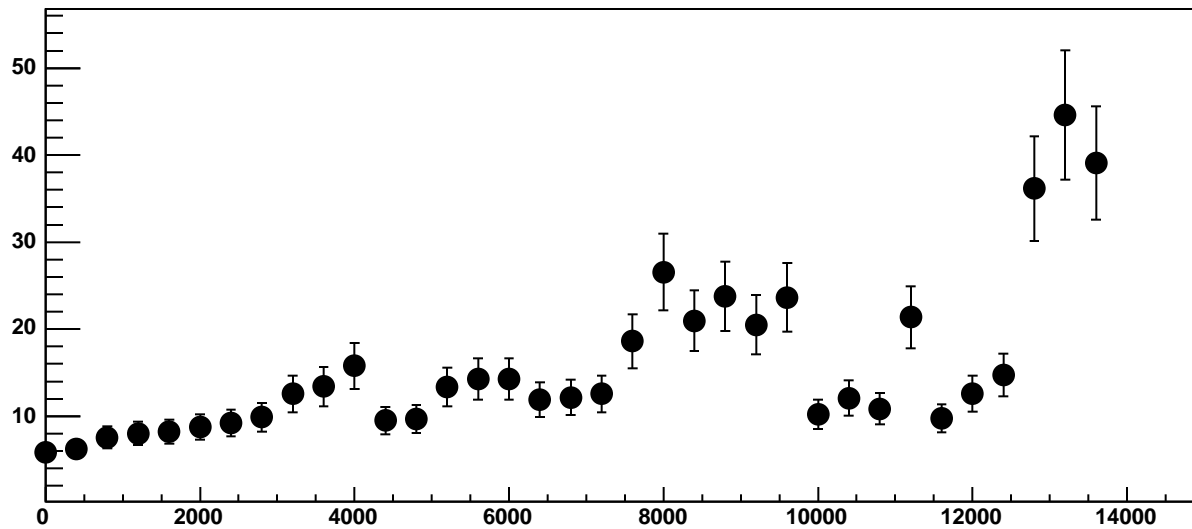


Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC

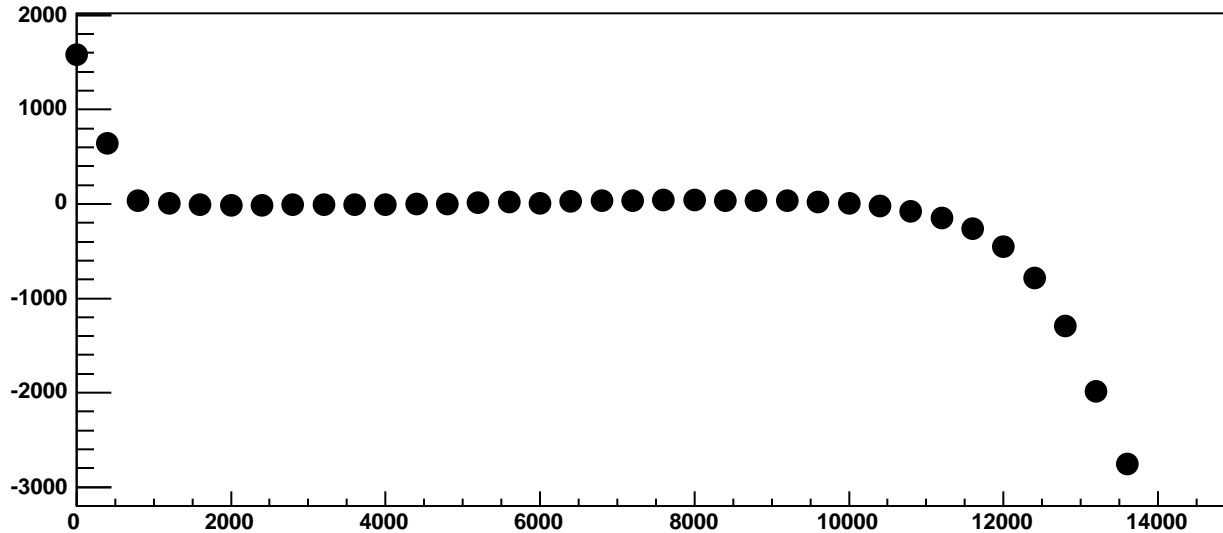


$\chi^2 / \text{ndf}$  1862 / 23  
p0  $156.4 \pm 1.039$   
p1  $2.387 \pm 0.0001789$

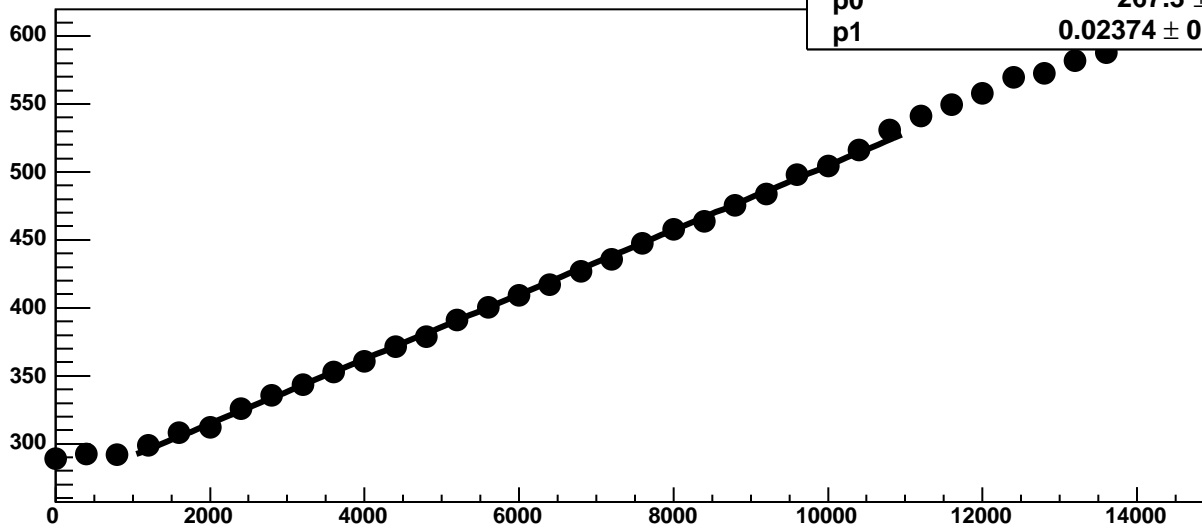
Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

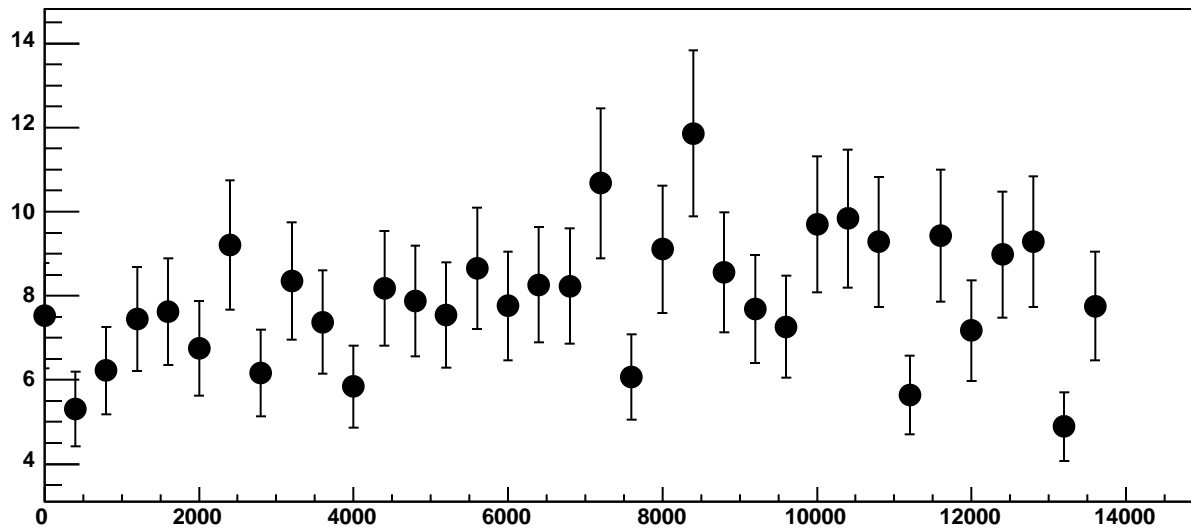


Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

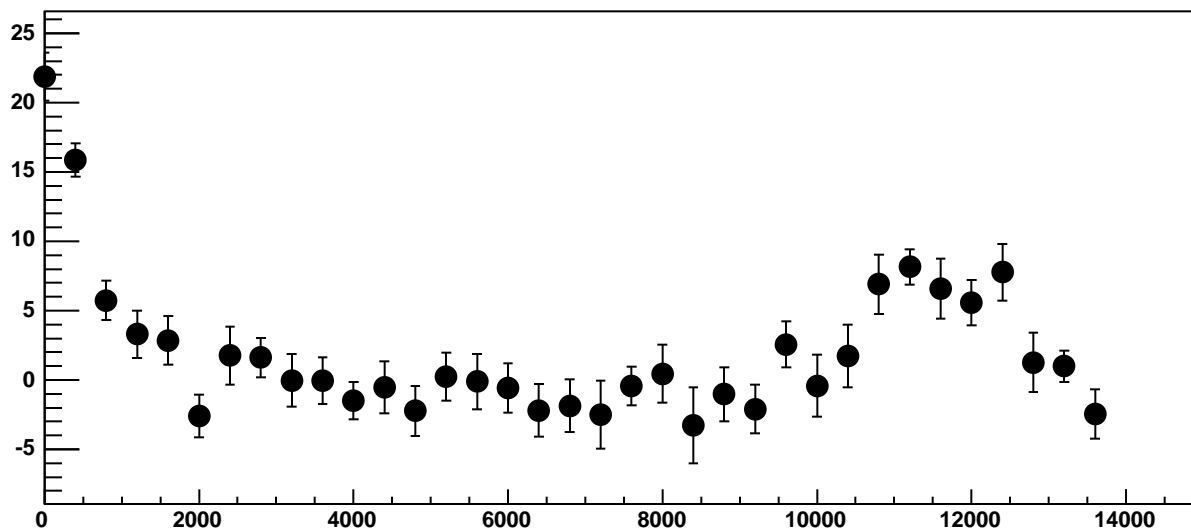


$\chi^2 / \text{ndf}$  34.33 / 23  
p0 267.3 ± 0.8016  
p1 0.02374 ± 0.000128

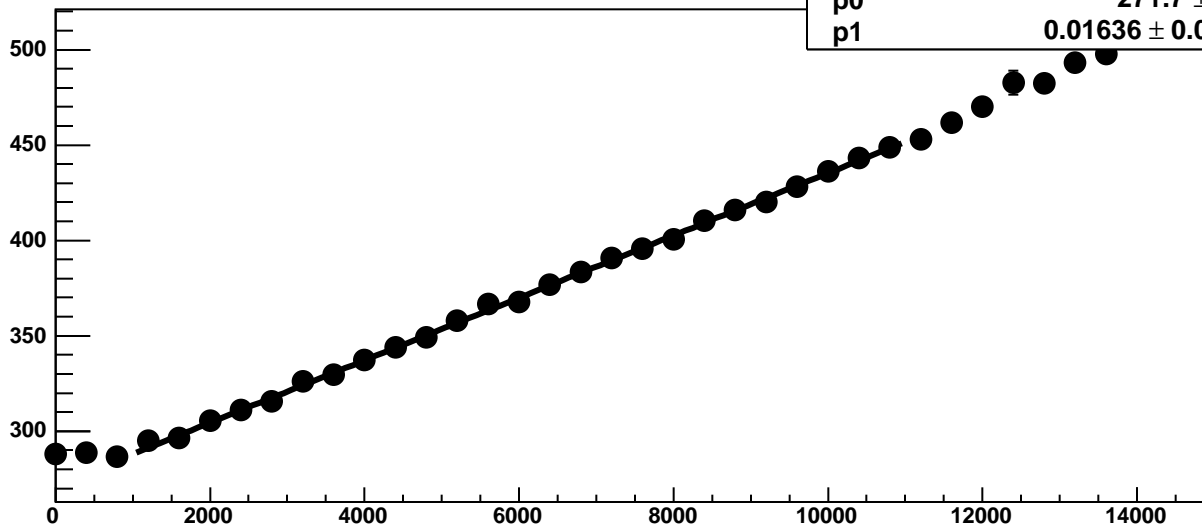
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

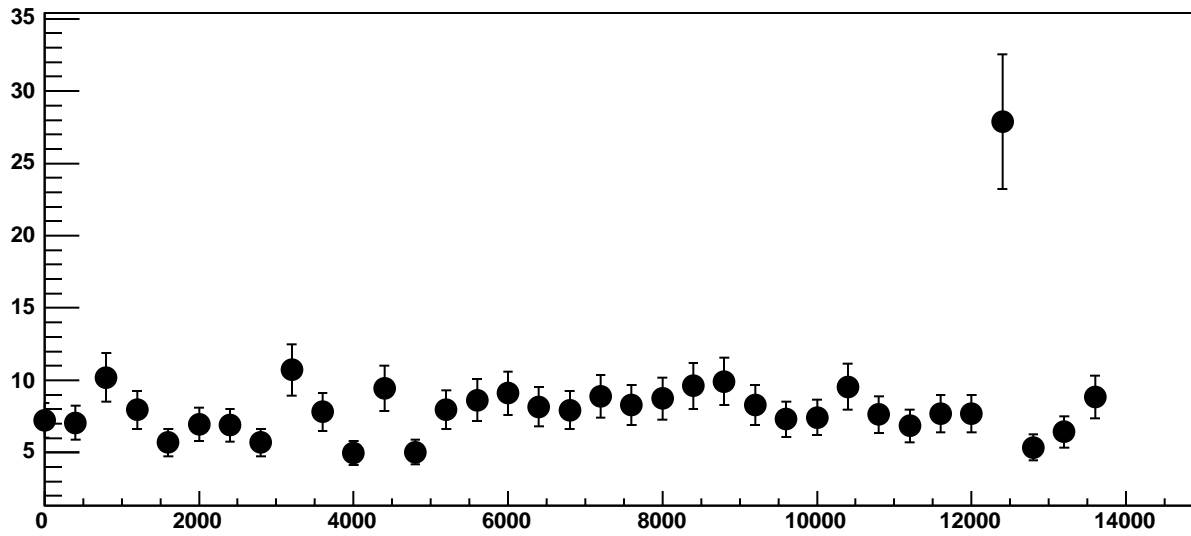


Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

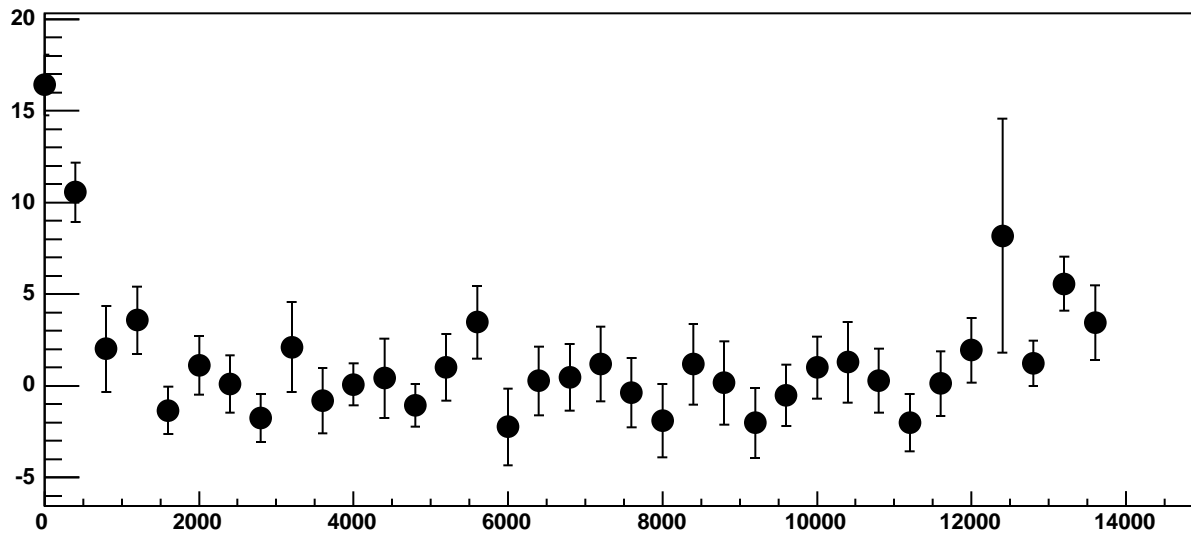


$\chi^2 / \text{ndf}$  17.16 / 23  
p0 271.7 ± 0.7339  
p1 0.01636 ± 0.0001188

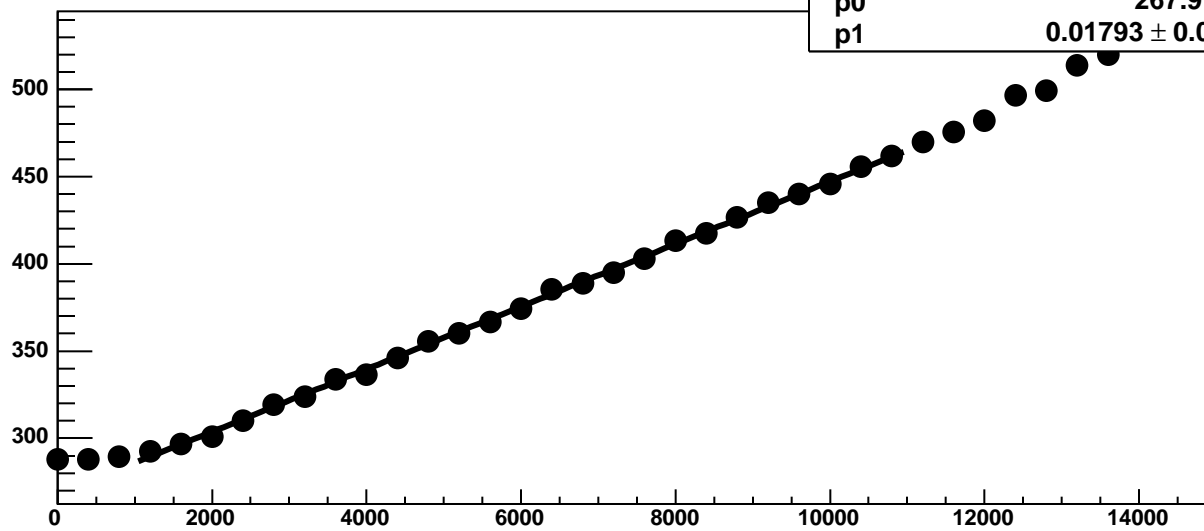
Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

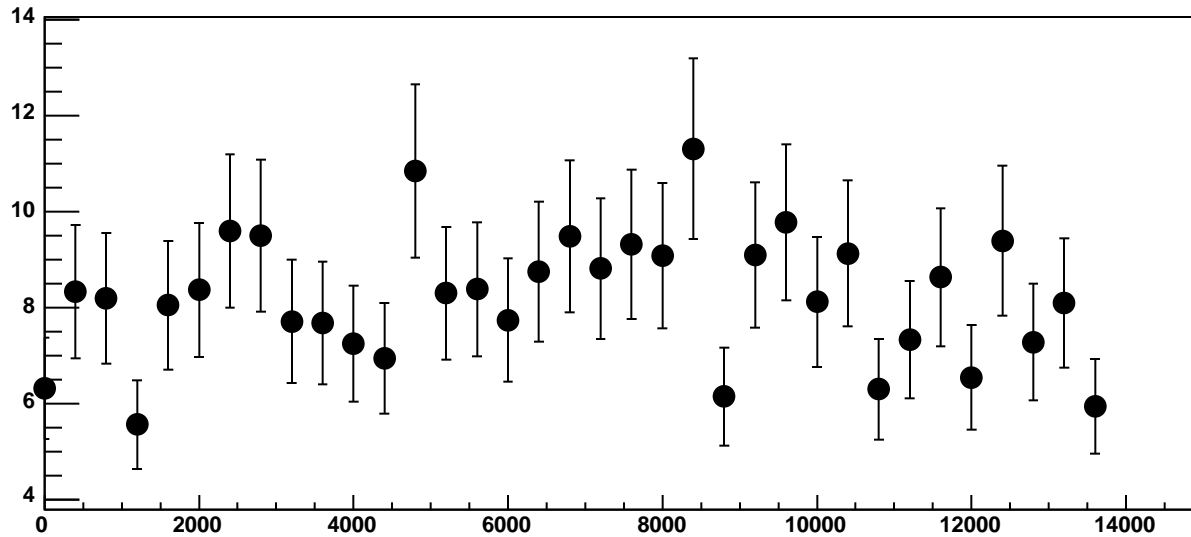


Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

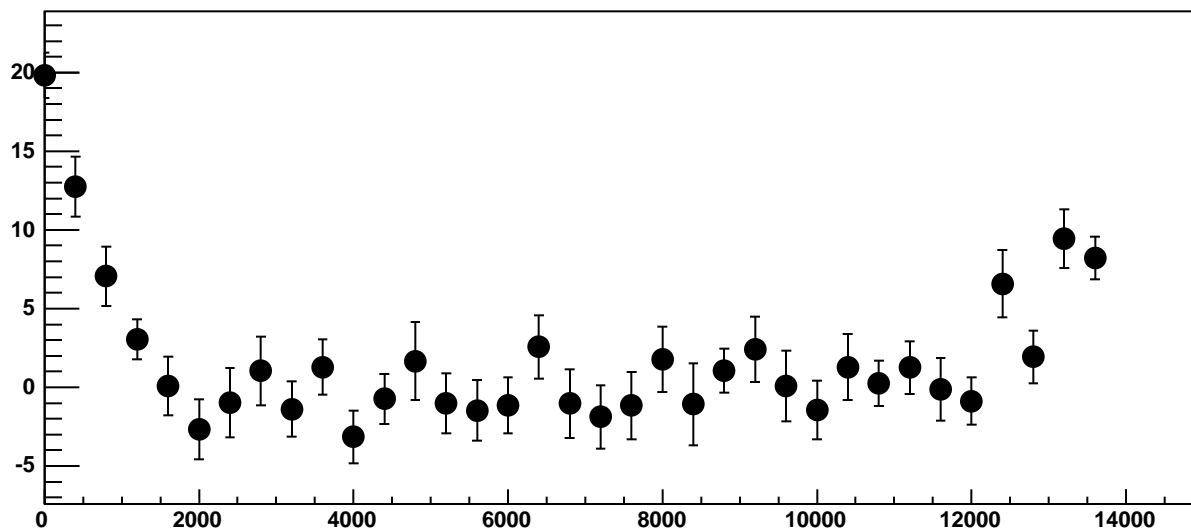


$\chi^2 / \text{ndf}$  21.61 / 23  
p0  $267.9 \pm 0.799$   
p1  $0.01793 \pm 0.0001214$

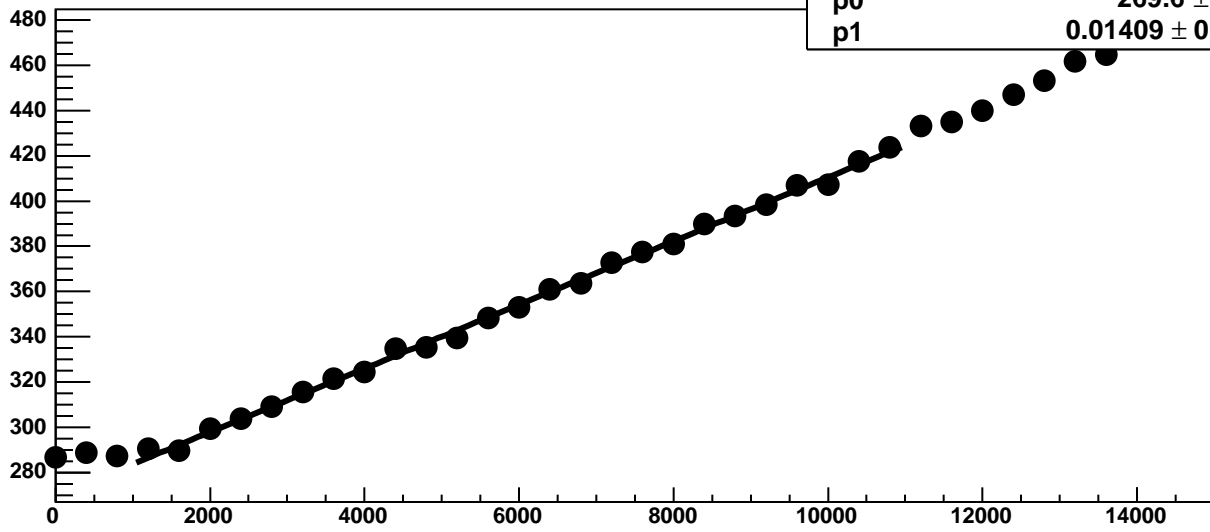
Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

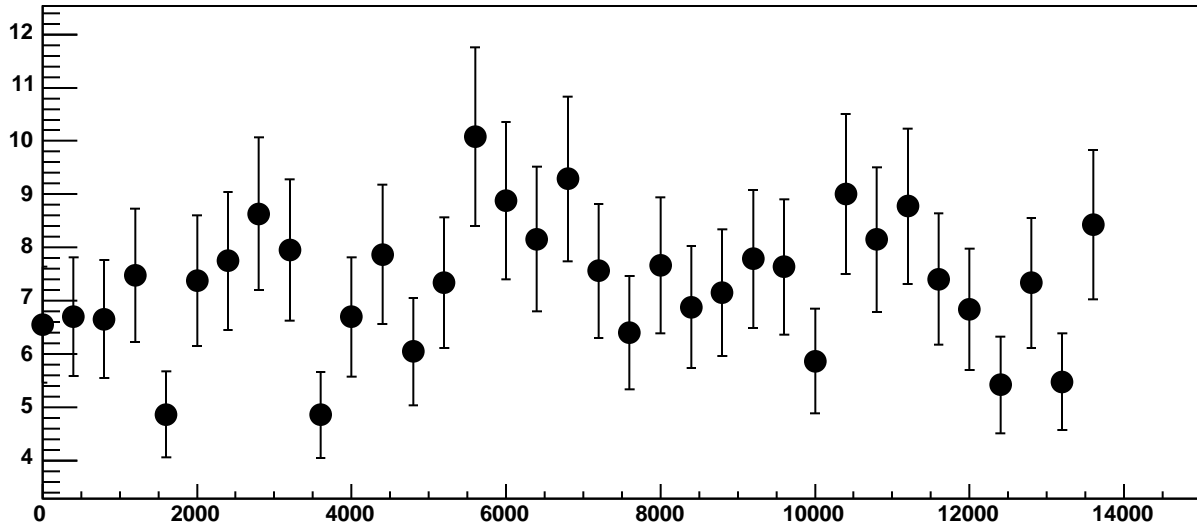


Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

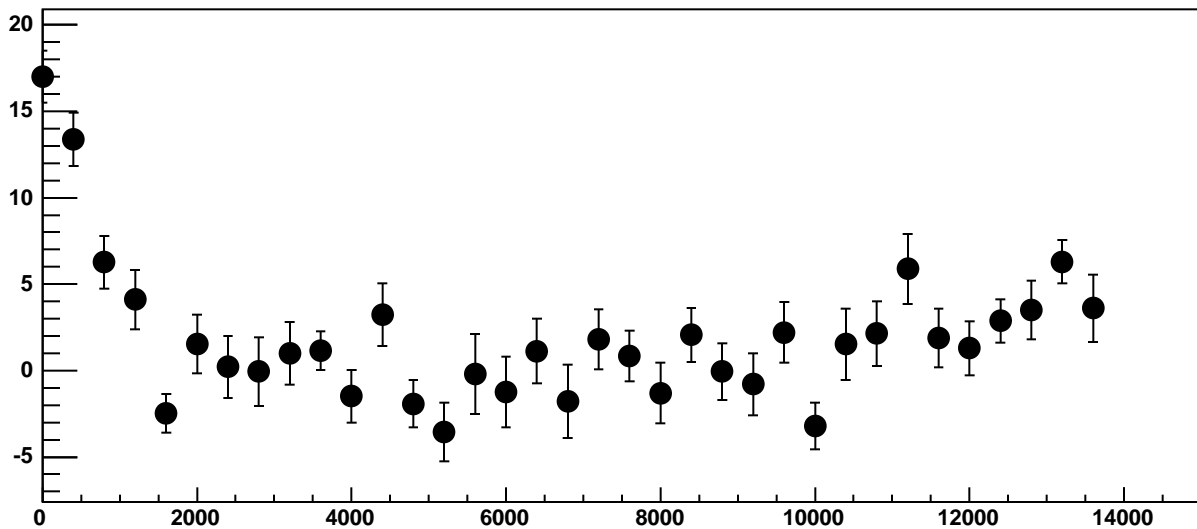


$\chi^2 / \text{ndf}$  37.8 / 23  
p0 269.6 ± 0.7134  
p1 0.01409 ± 0.000111

Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

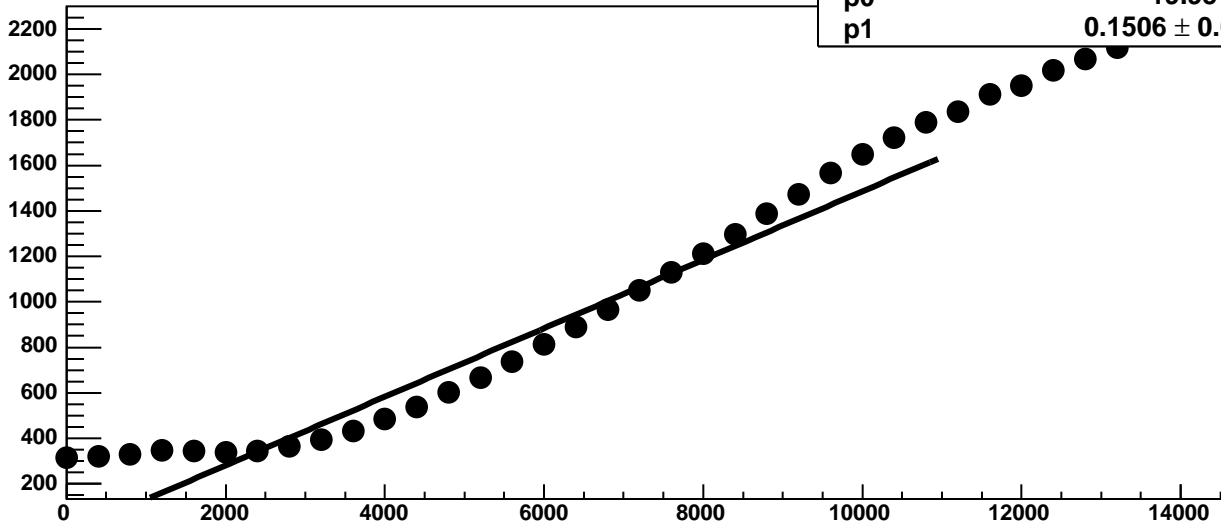


Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

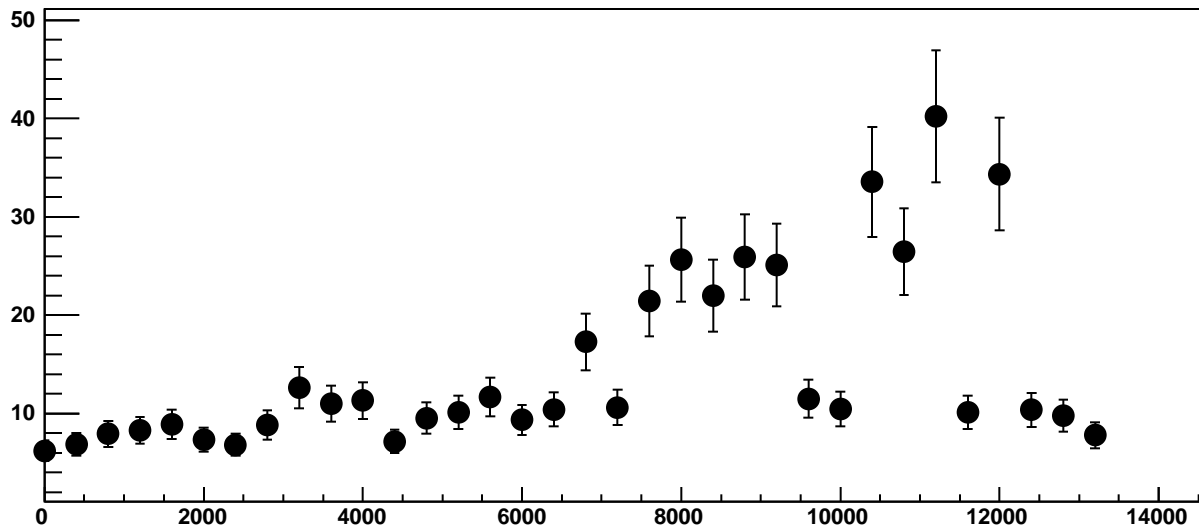


Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

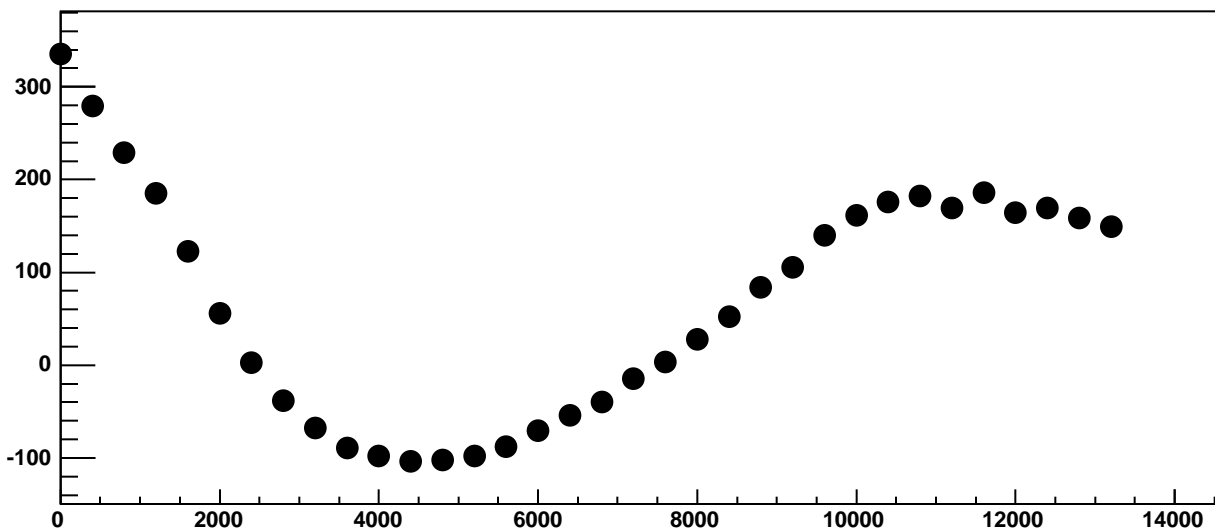
$\chi^2 / \text{ndf}$  3.814e+04 / 23  
p0 -19.95 ± 1.011  
p1 0.1506 ± 0.000195



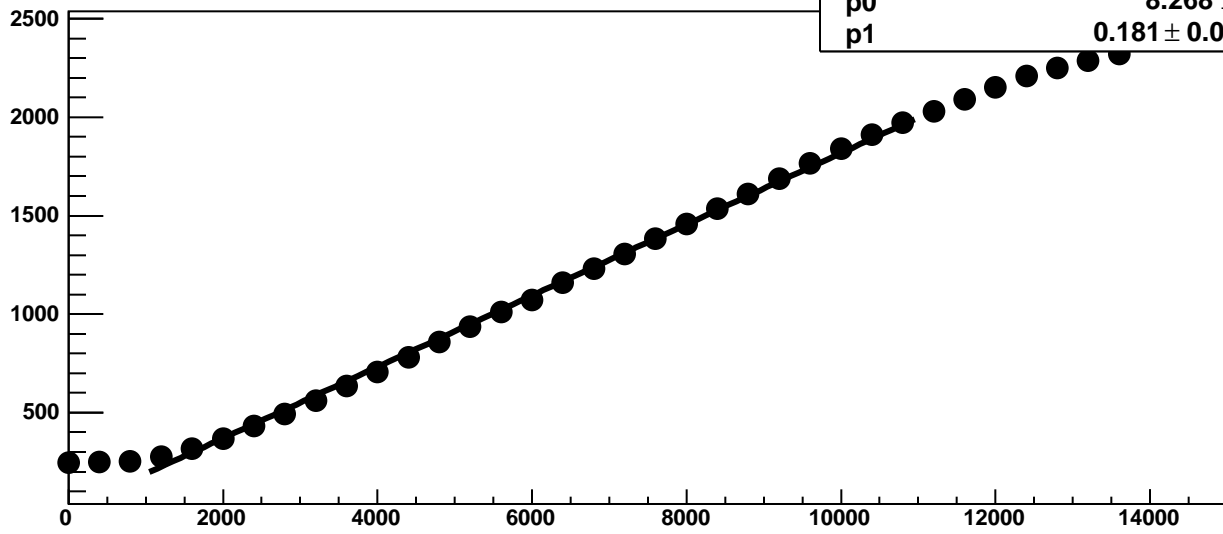
Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



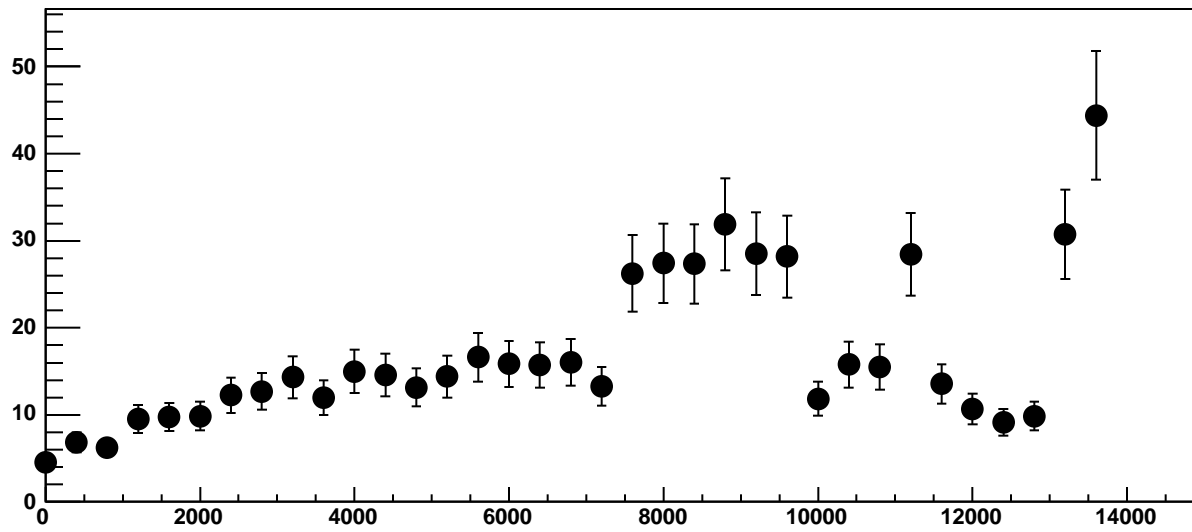
Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



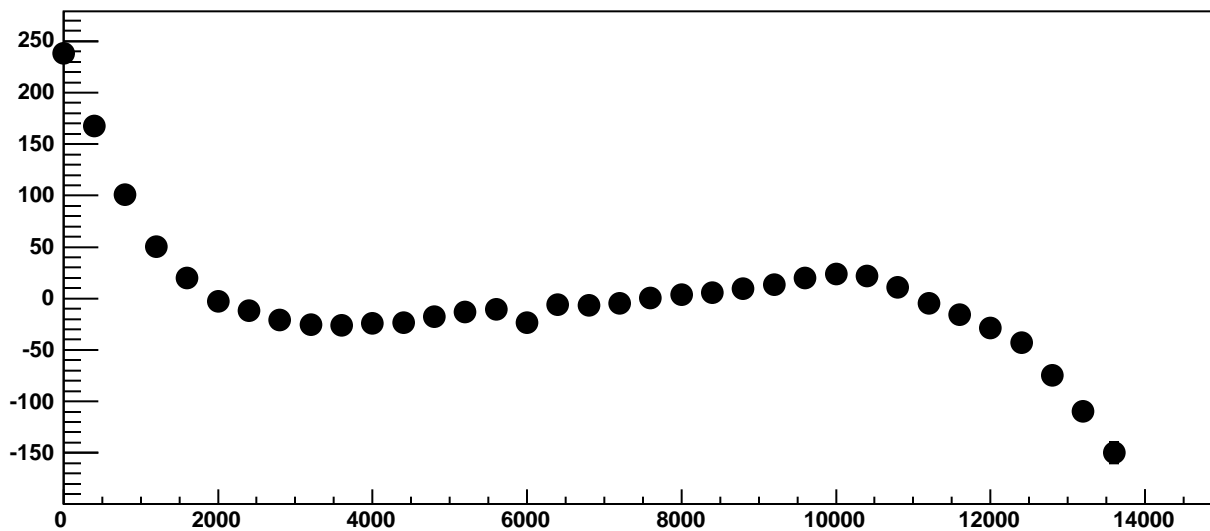
Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

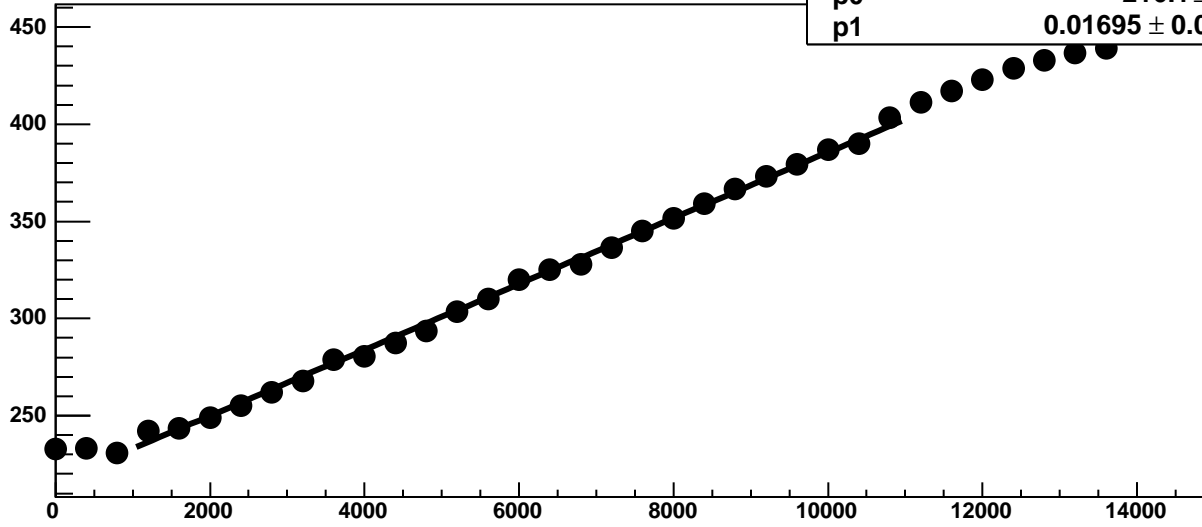


Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC





Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

61.36 / 23

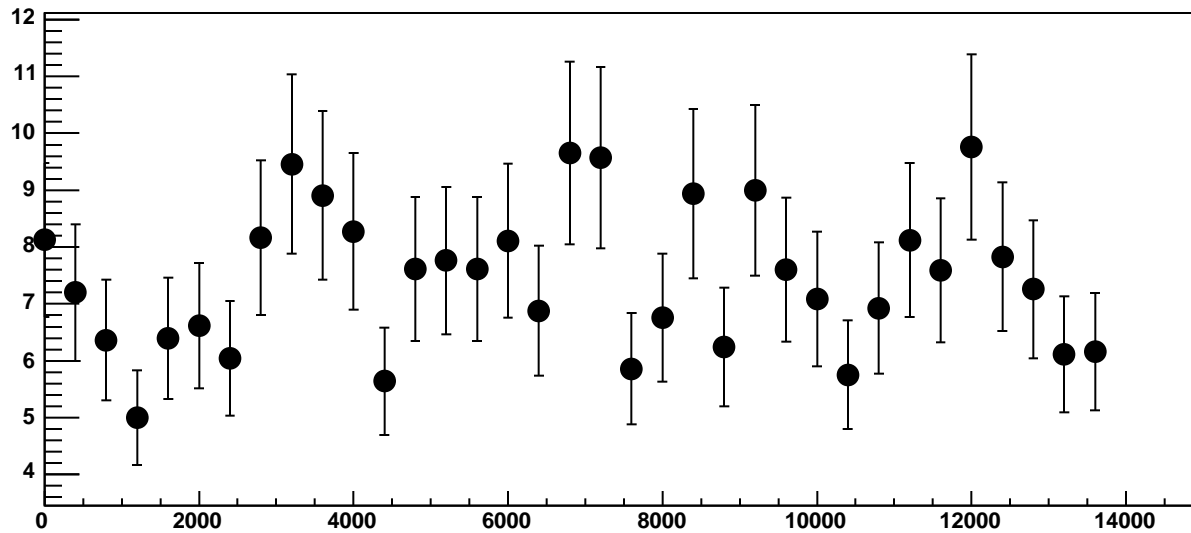
p0

$216.1 \pm 0.6928$

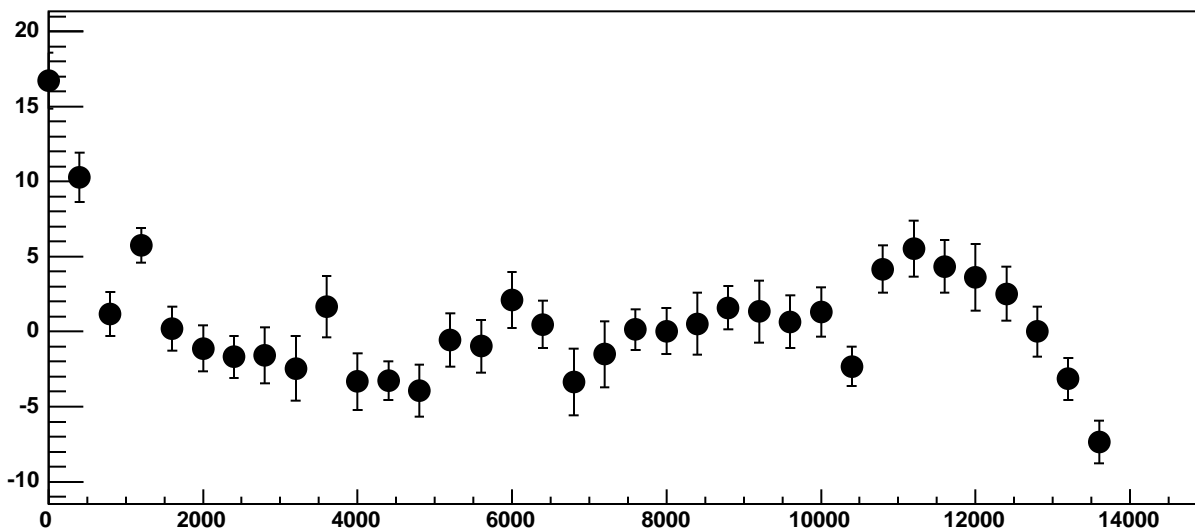
p1

$0.01695 \pm 0.0001049$

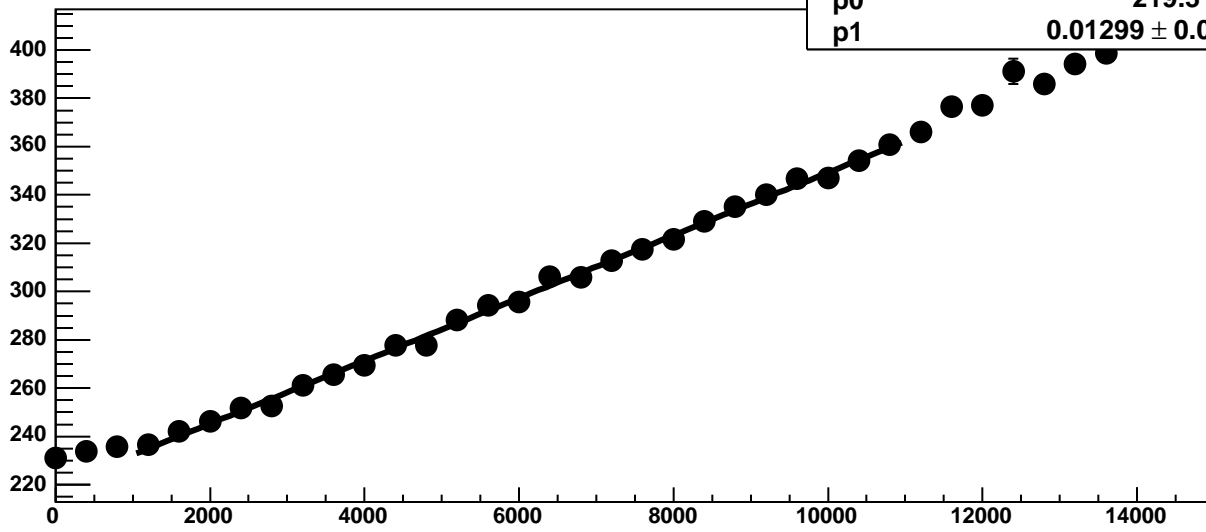
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



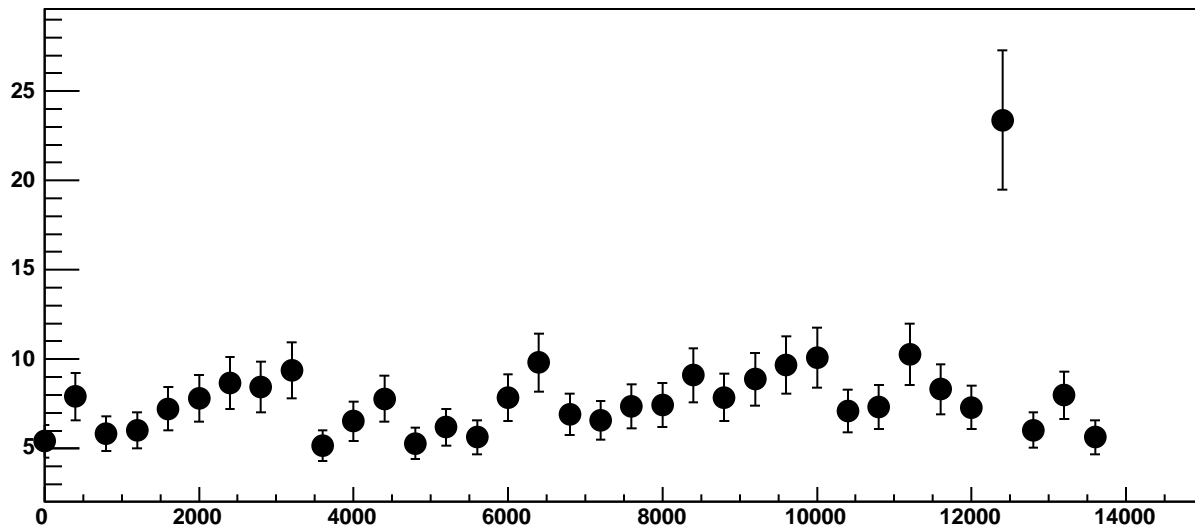
Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



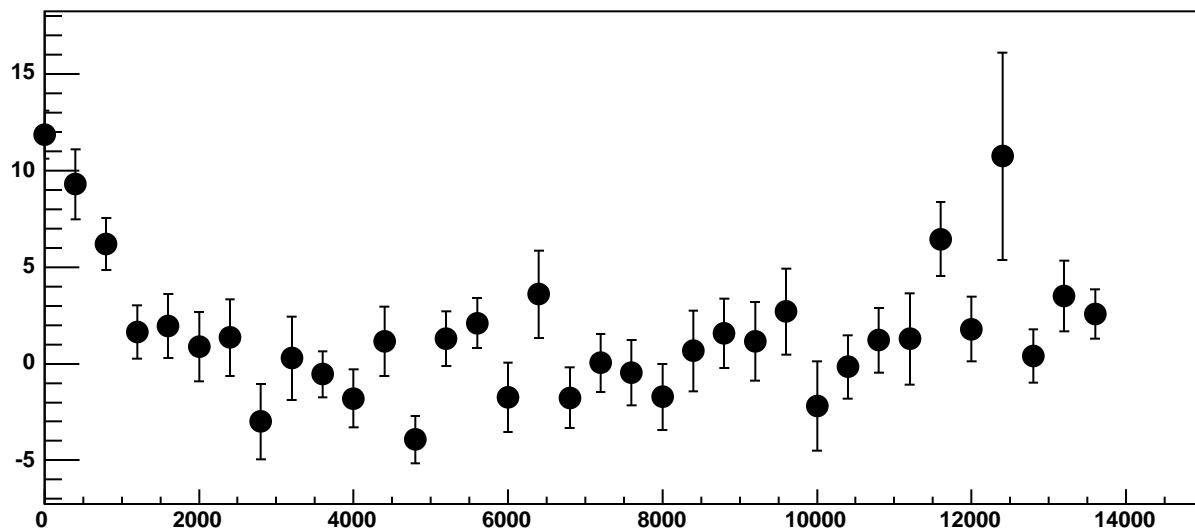
Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



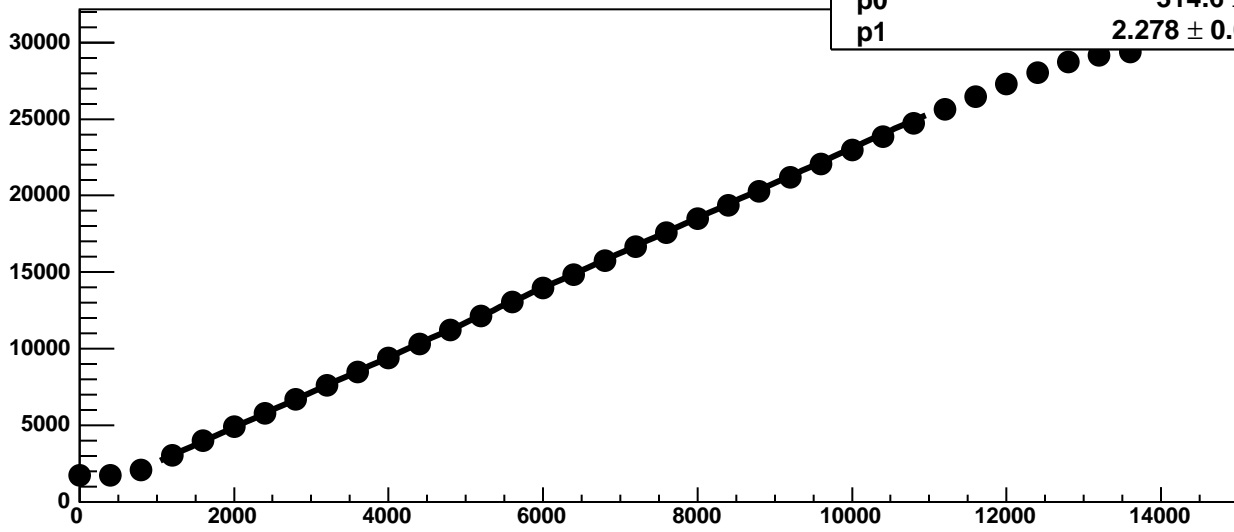
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



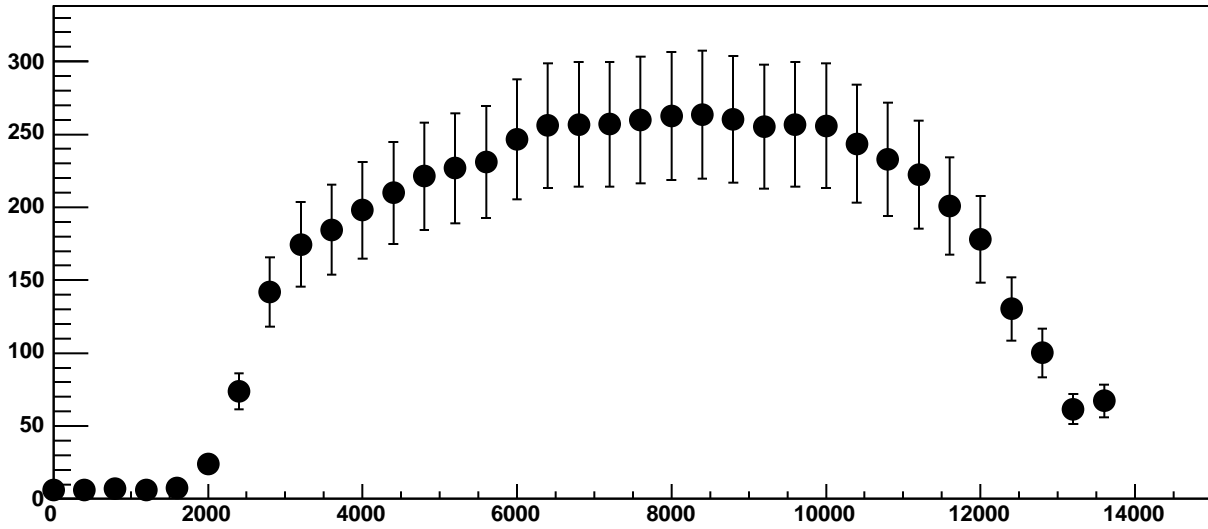
Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



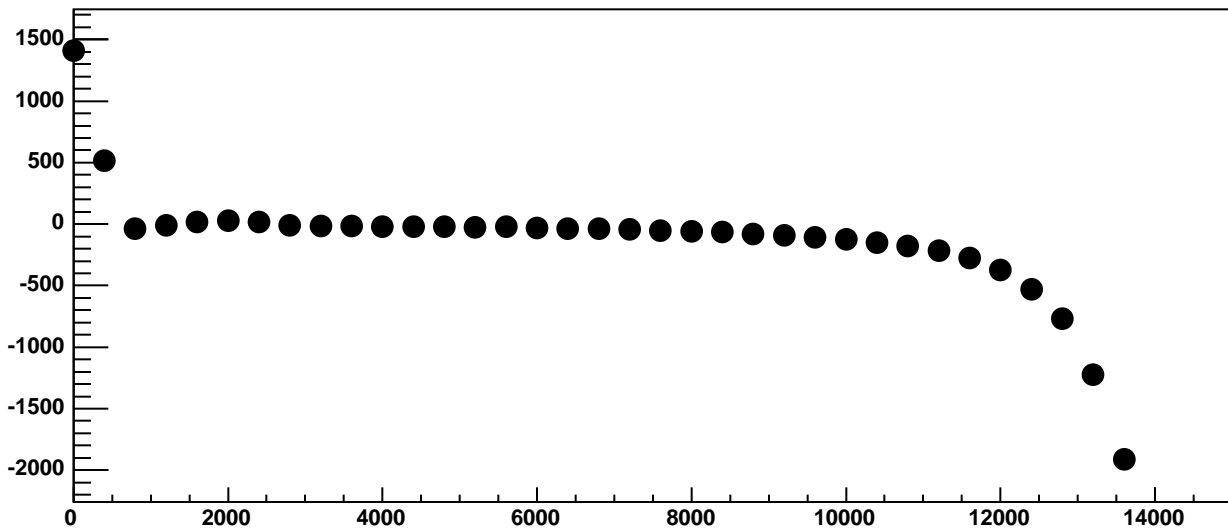
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



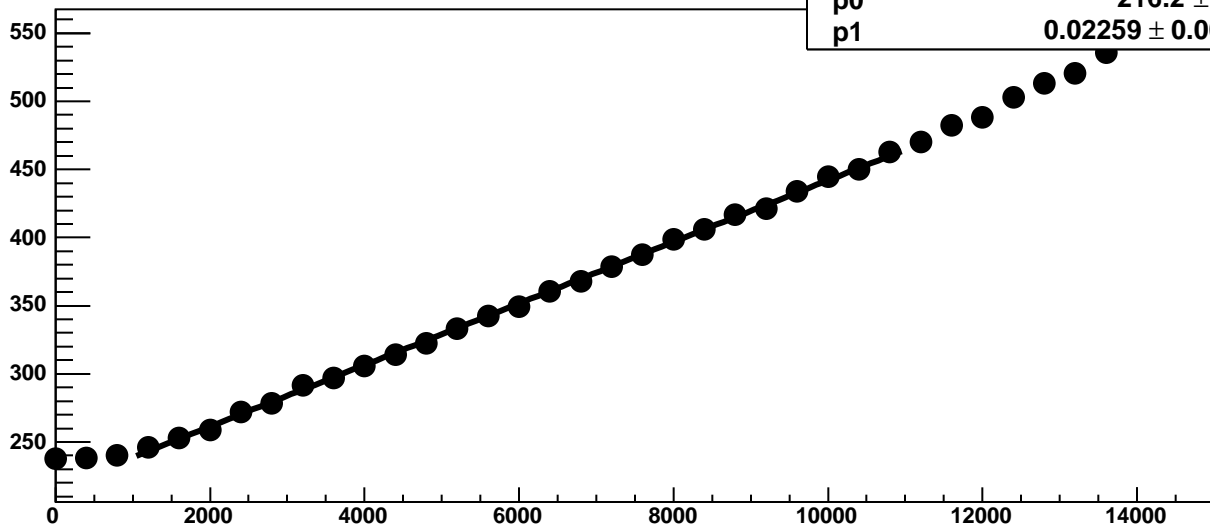
Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

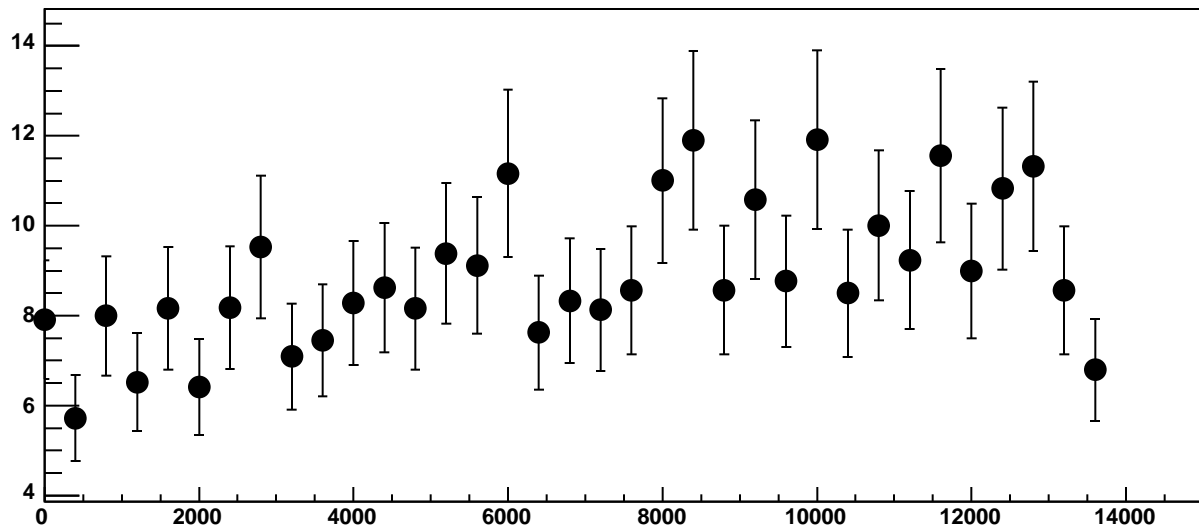


Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

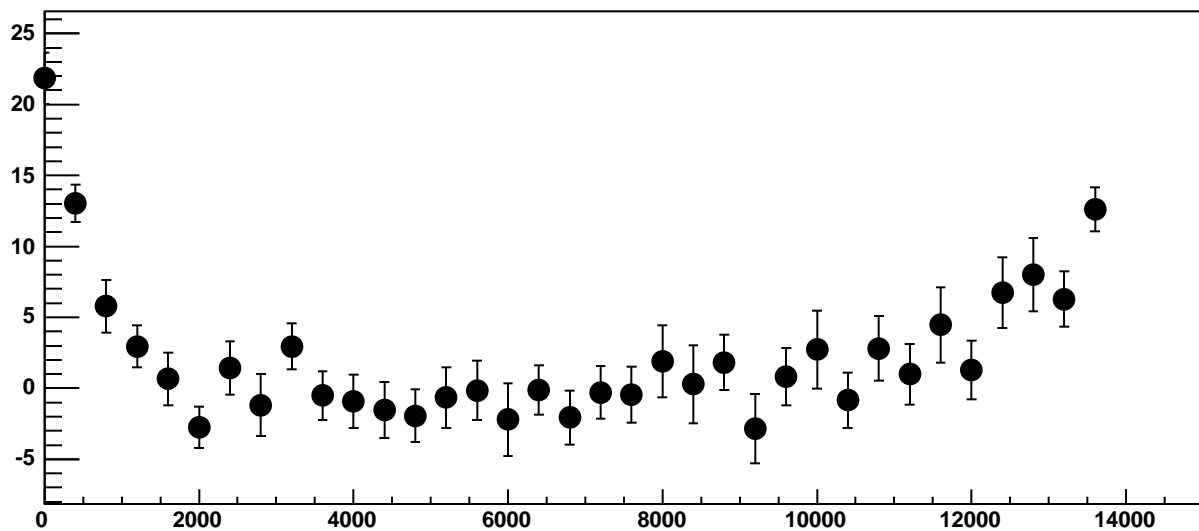


$\chi^2 / \text{ndf}$  21.37 / 23  
p0  $216.2 \pm 0.8255$   
p1  $0.02259 \pm 0.0001345$

Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

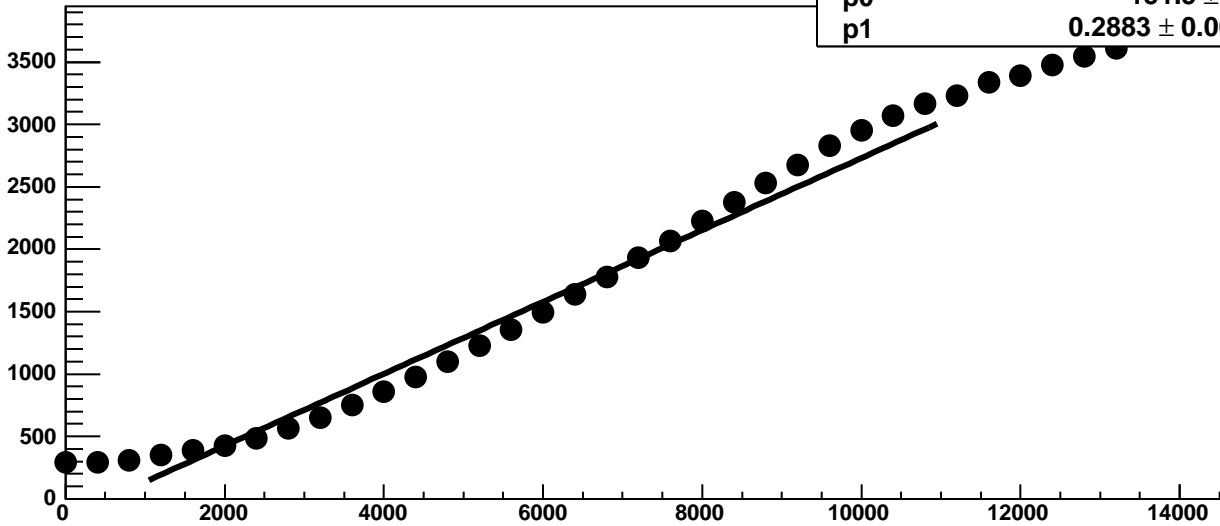


Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

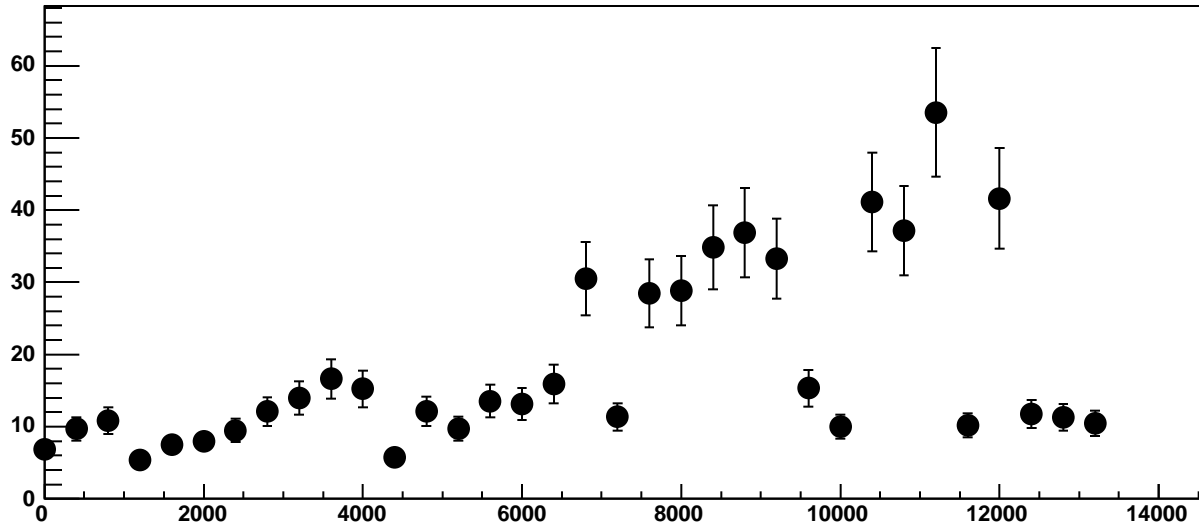


Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

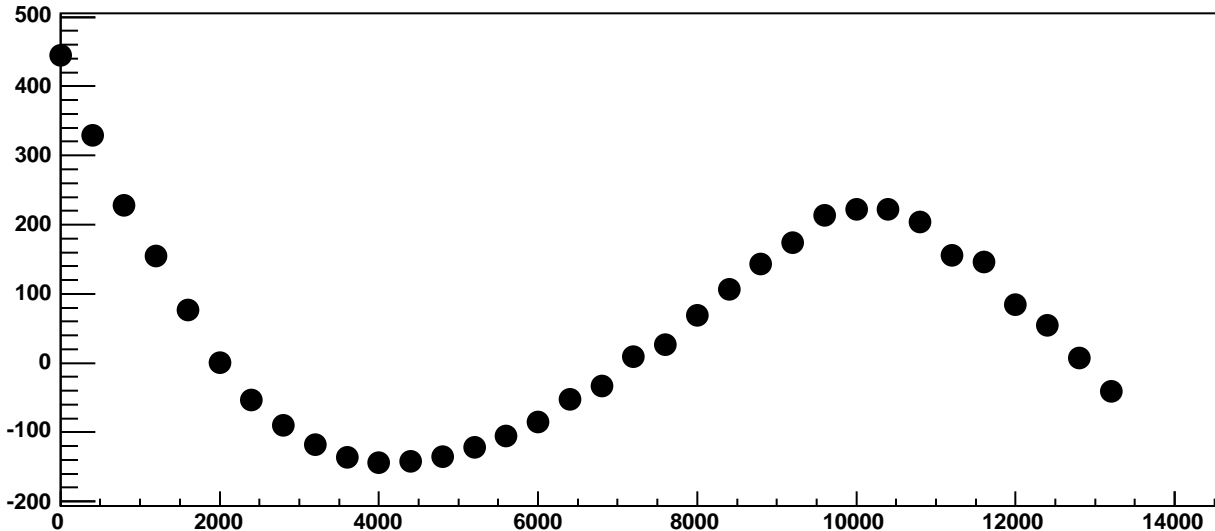
$\chi^2 / \text{ndf}$  5.827e+04 / 23  
p0 -151.5 ± 0.9616  
p1 0.2883 ± 0.0002019



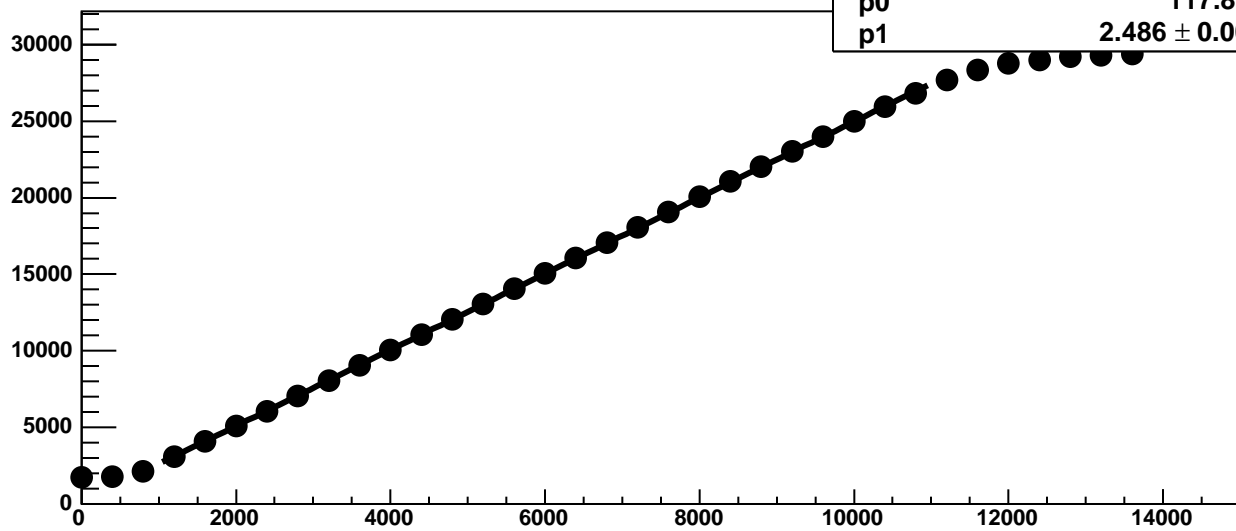
Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



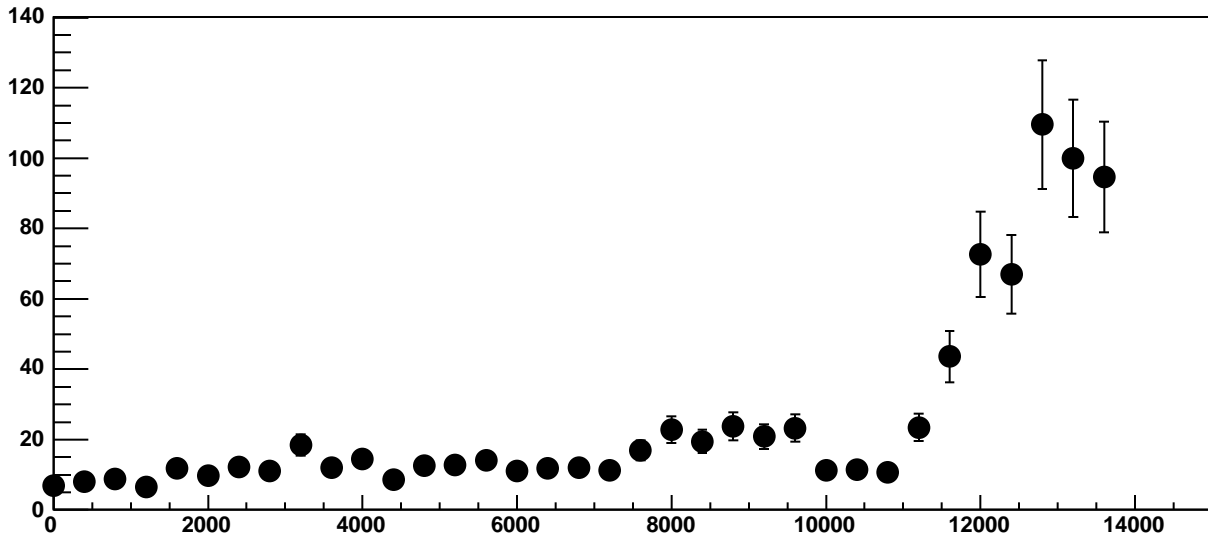
Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



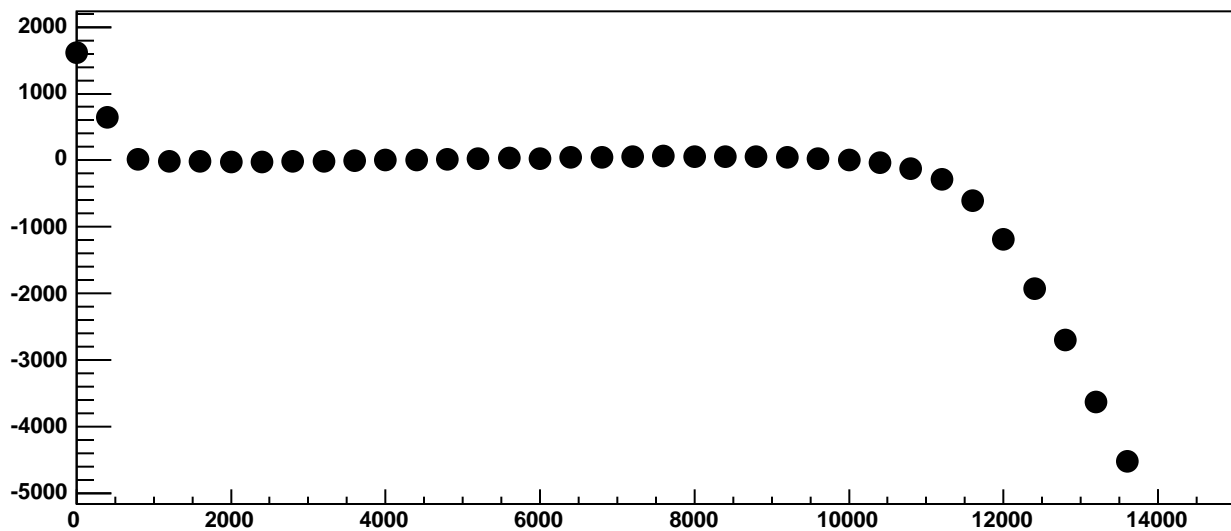
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



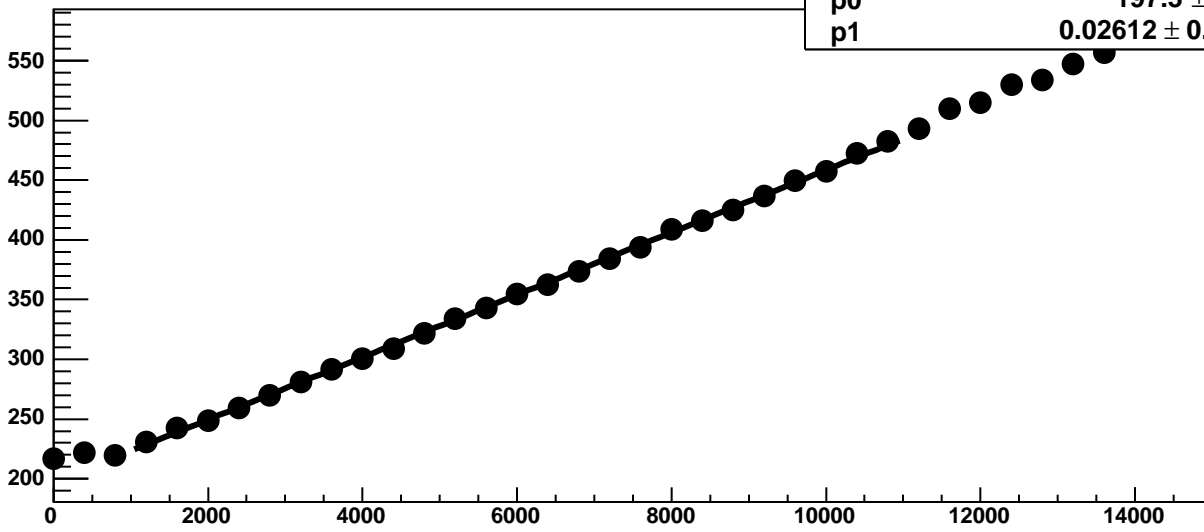
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

28.04 / 23

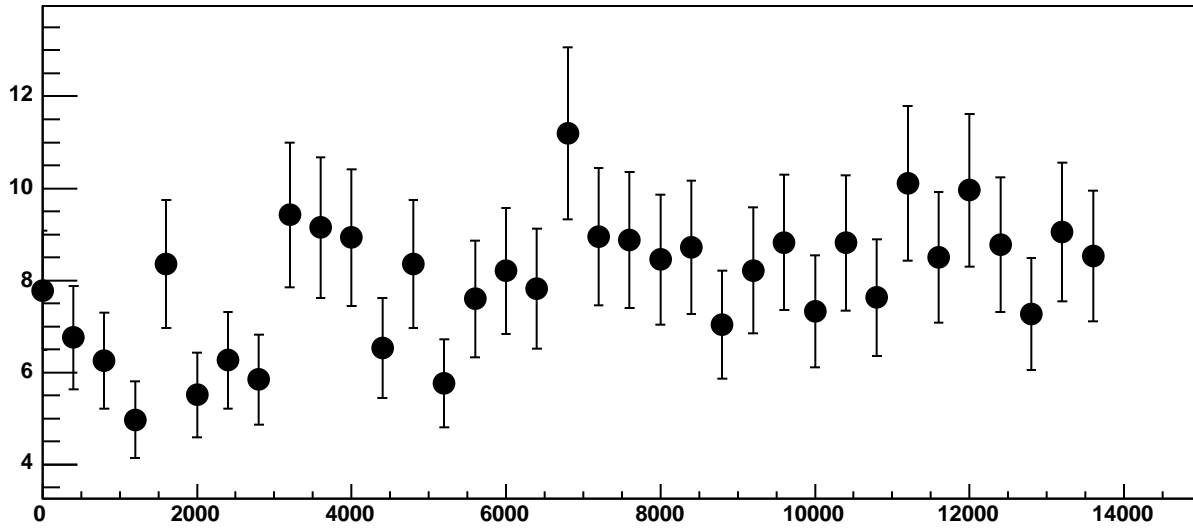
p0

$197.3 \pm 0.6988$

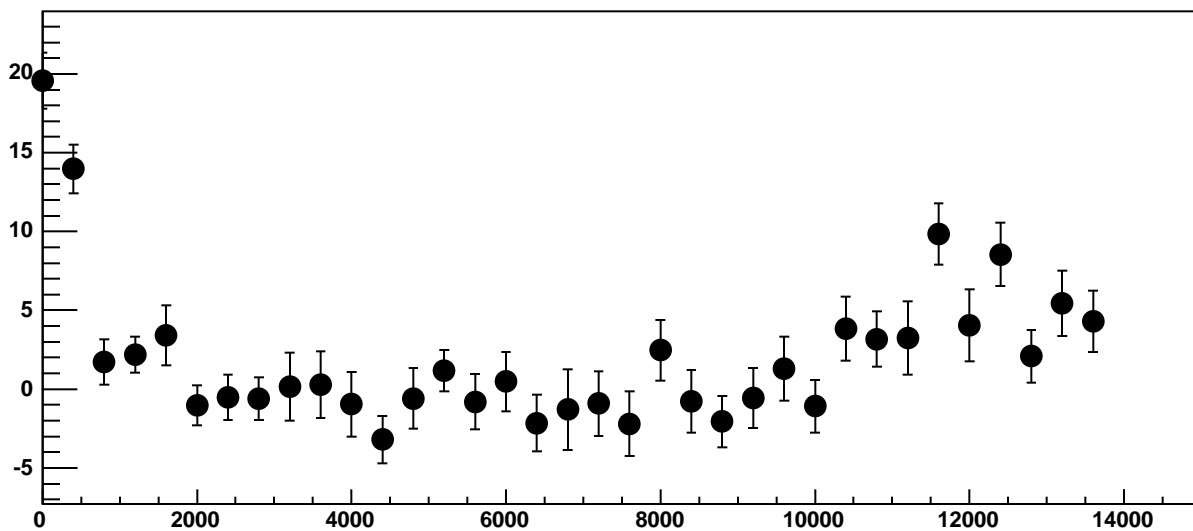
p1

$0.02612 \pm 0.000113$

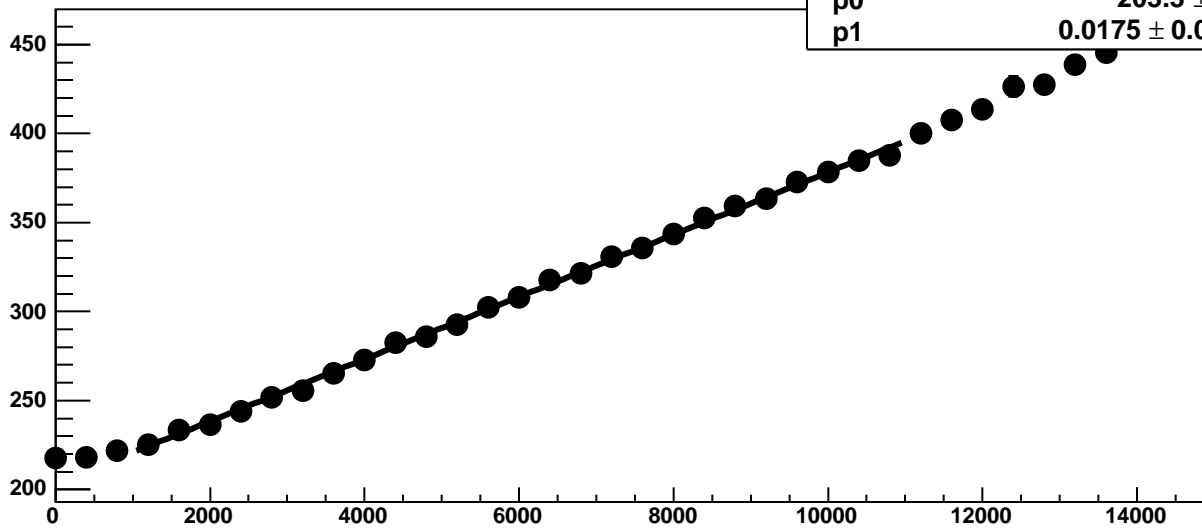
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



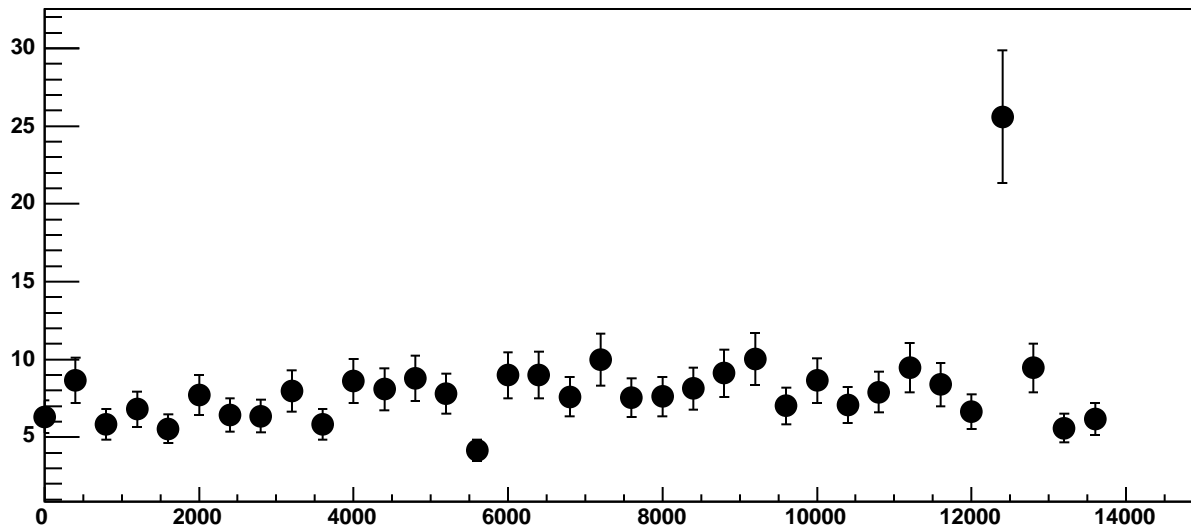
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



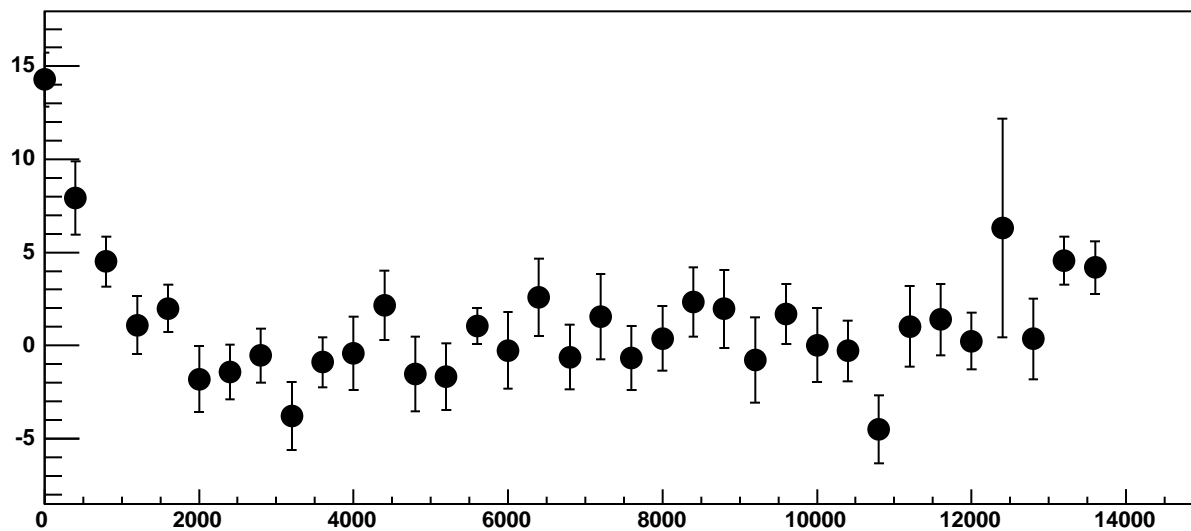
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

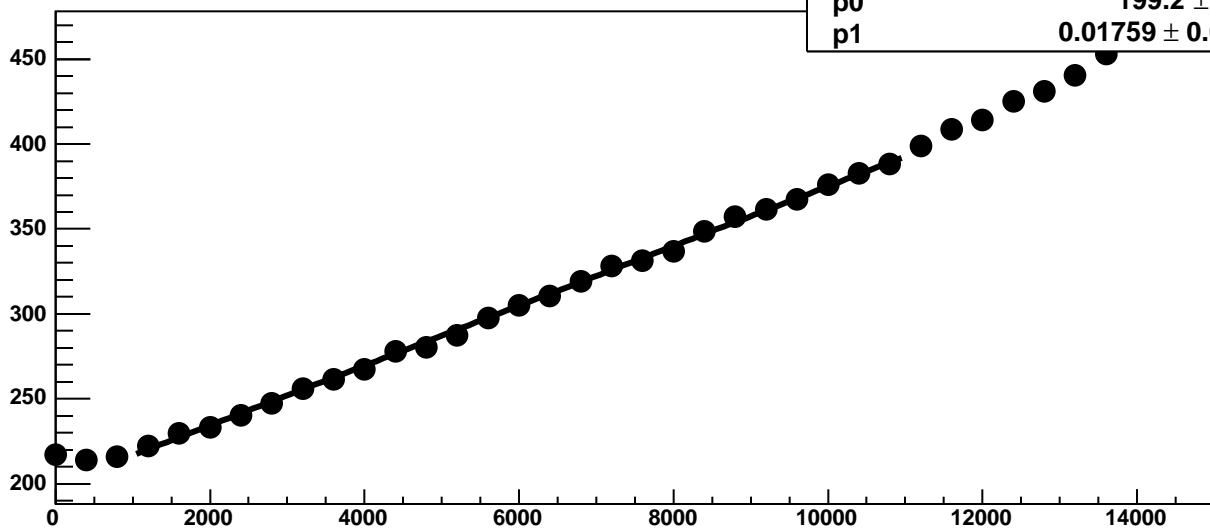


Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



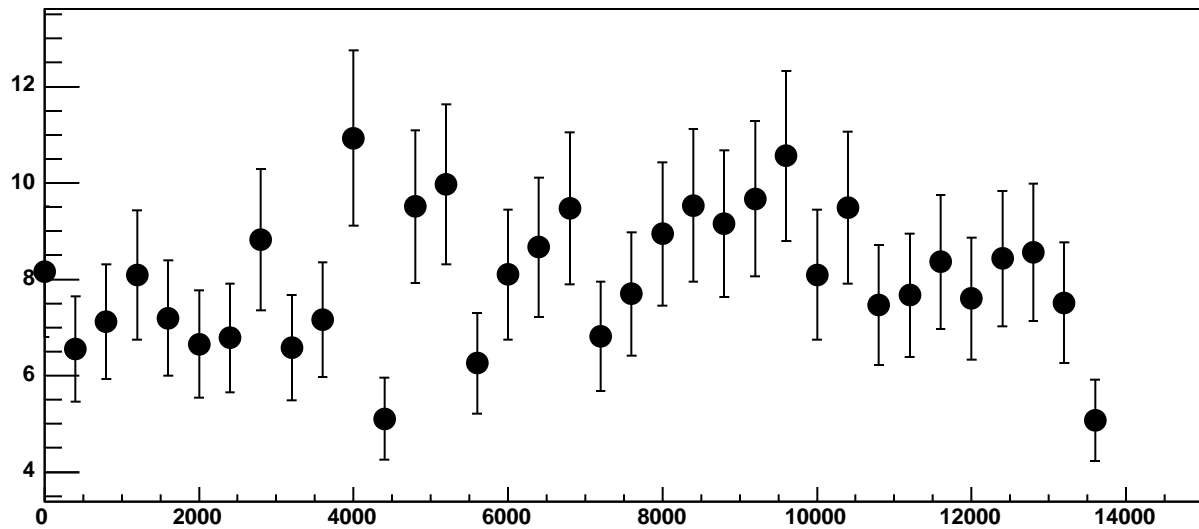


Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

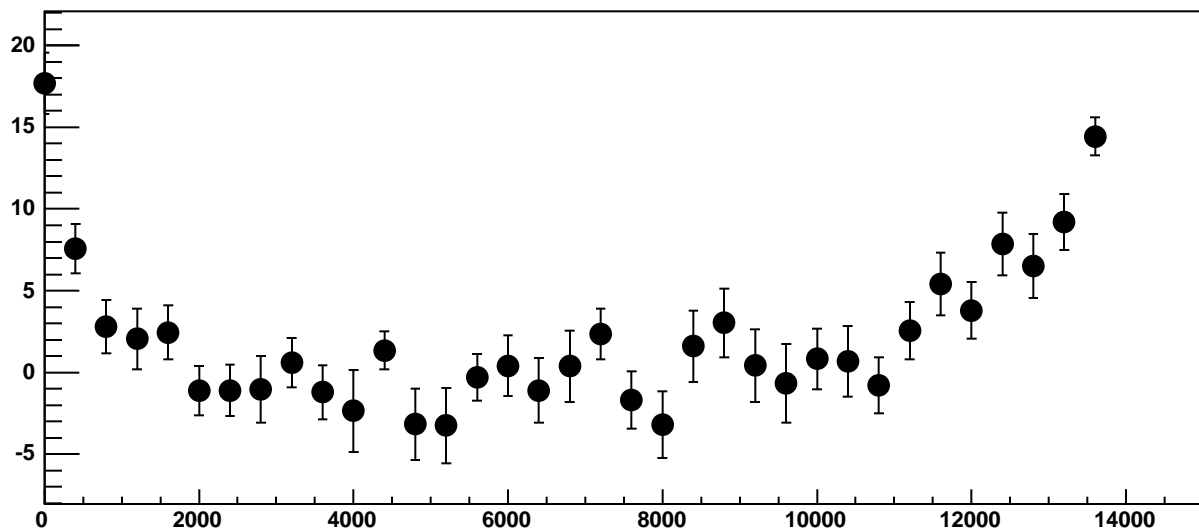


$\chi^2 / \text{ndf}$  21.05 / 23  
p0  $199.2 \pm 0.7936$   
p1  $0.01759 \pm 0.000127$

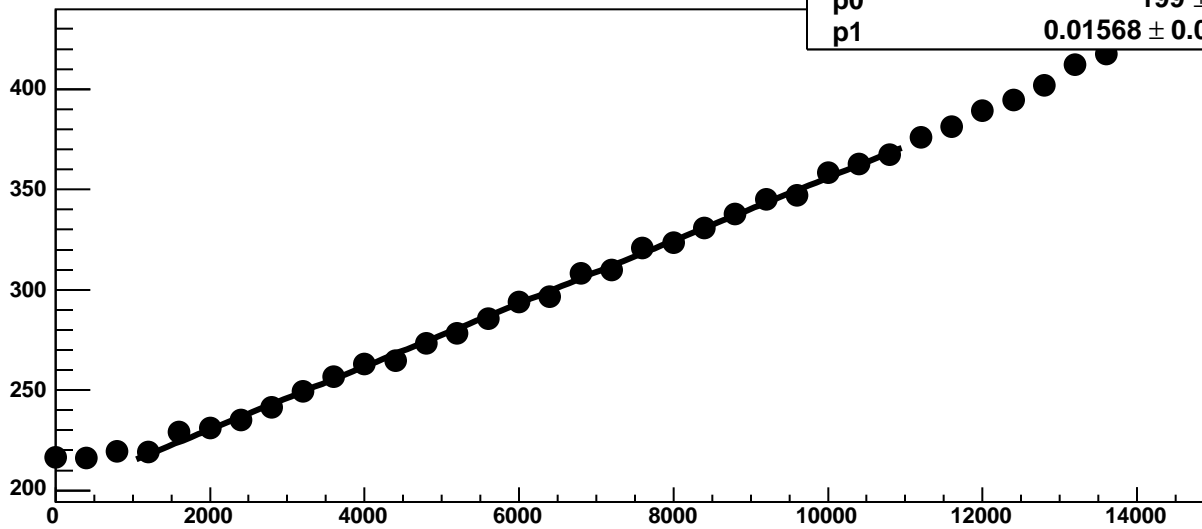
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

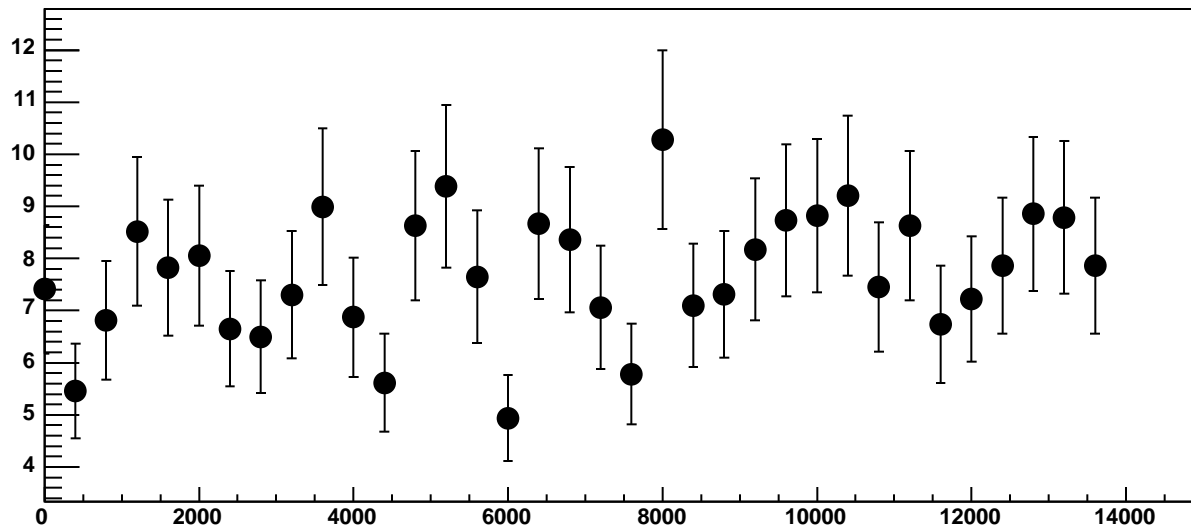


Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

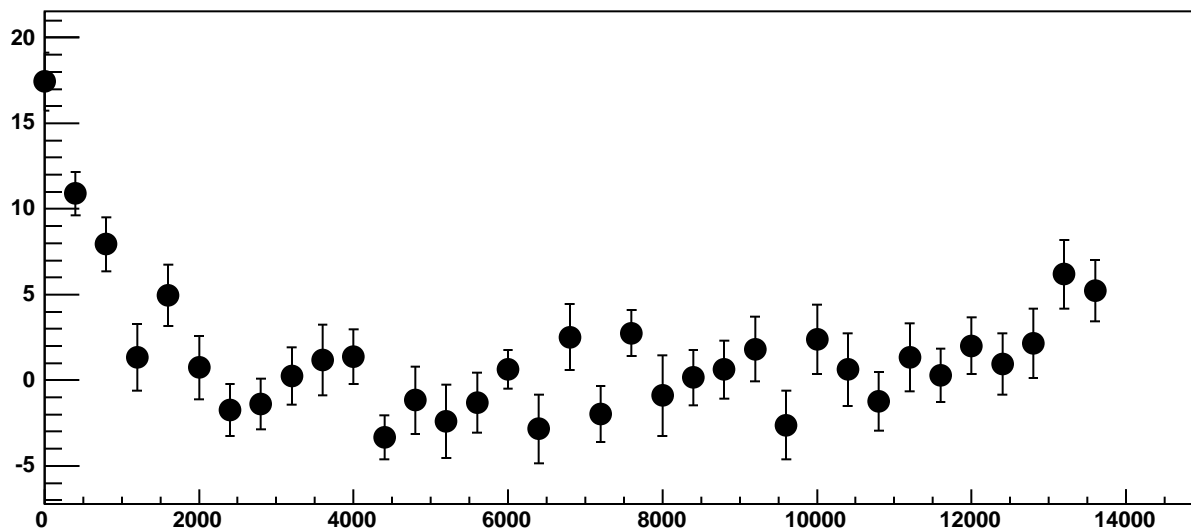


$\chi^2 / \text{ndf}$  35.1 / 23  
p0  $199 \pm 0.8022$   
p1  $0.01568 \pm 0.0001243$

Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

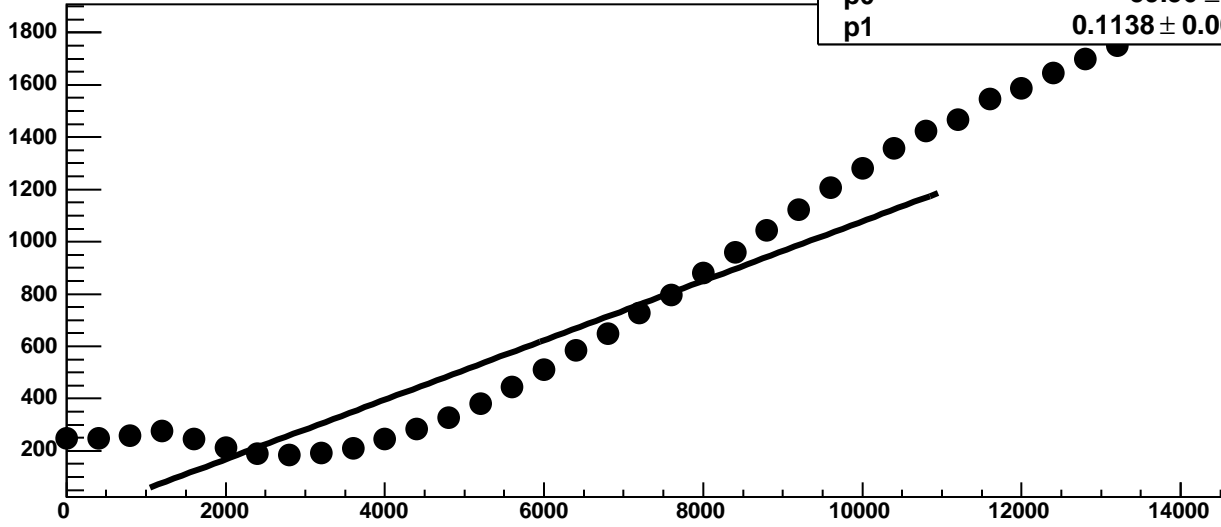


Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

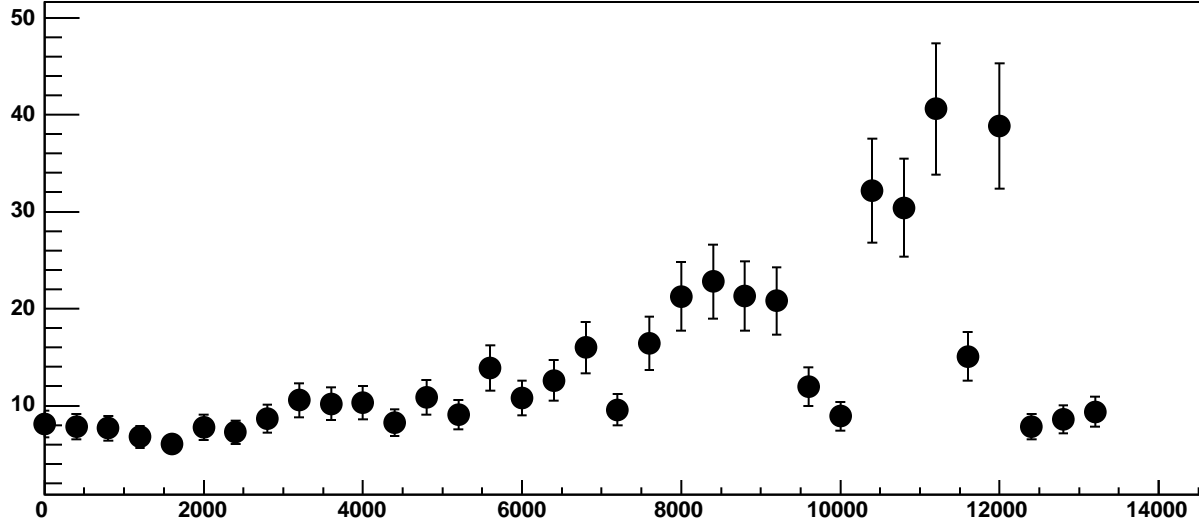


Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

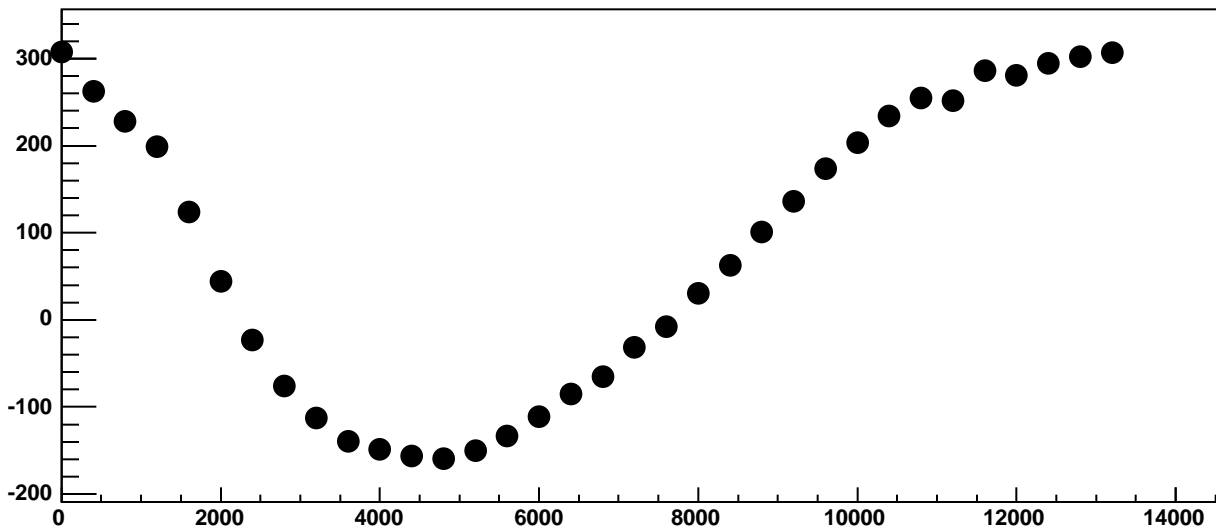
$\chi^2 / \text{ndf}$  7.54e+04 / 23  
p0 -59.56 ± 0.9026  
p1 0.1138 ± 0.0001764



Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

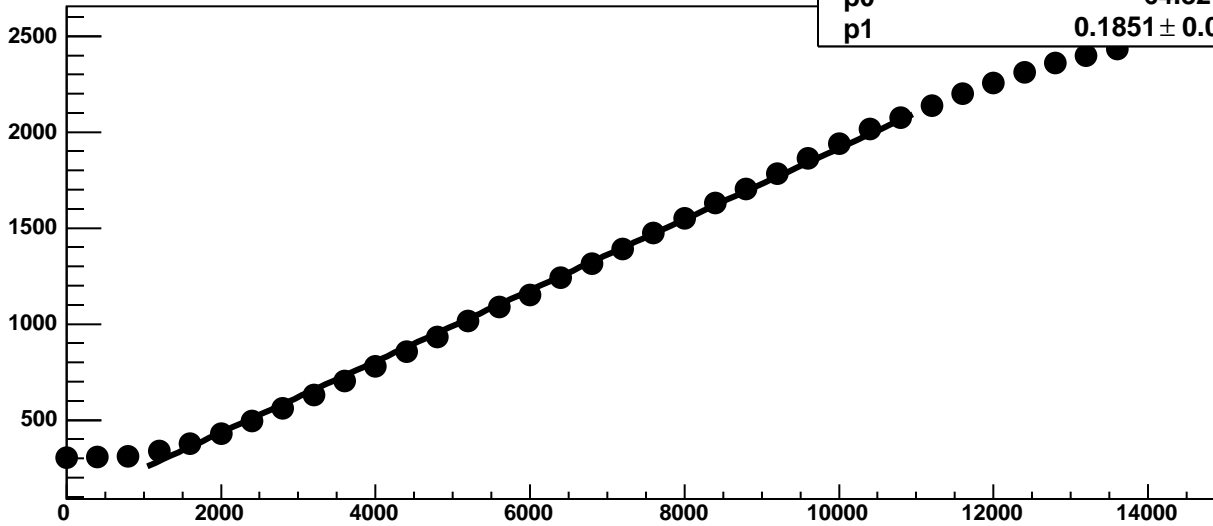


Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

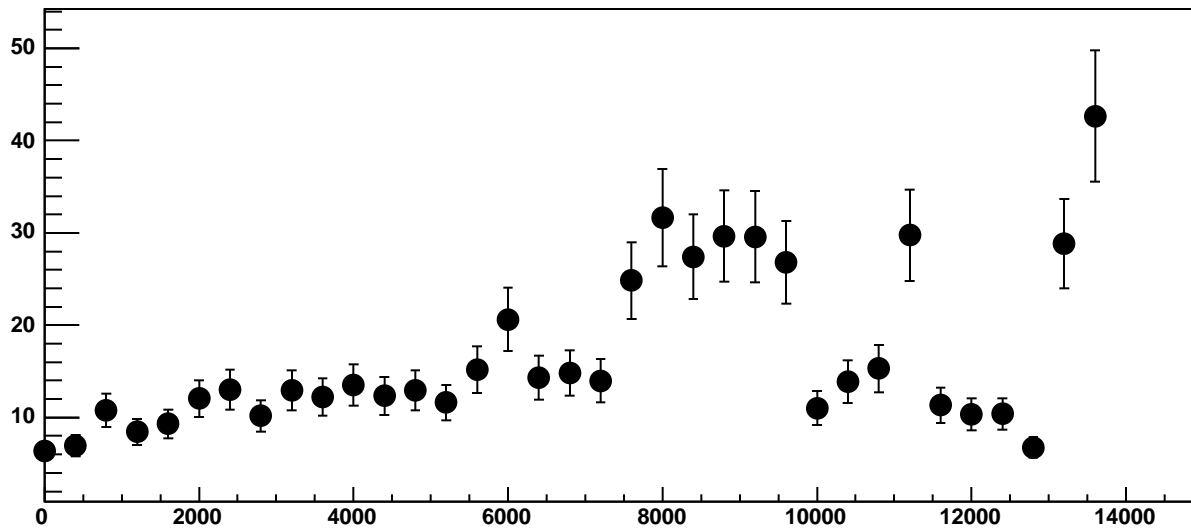


Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

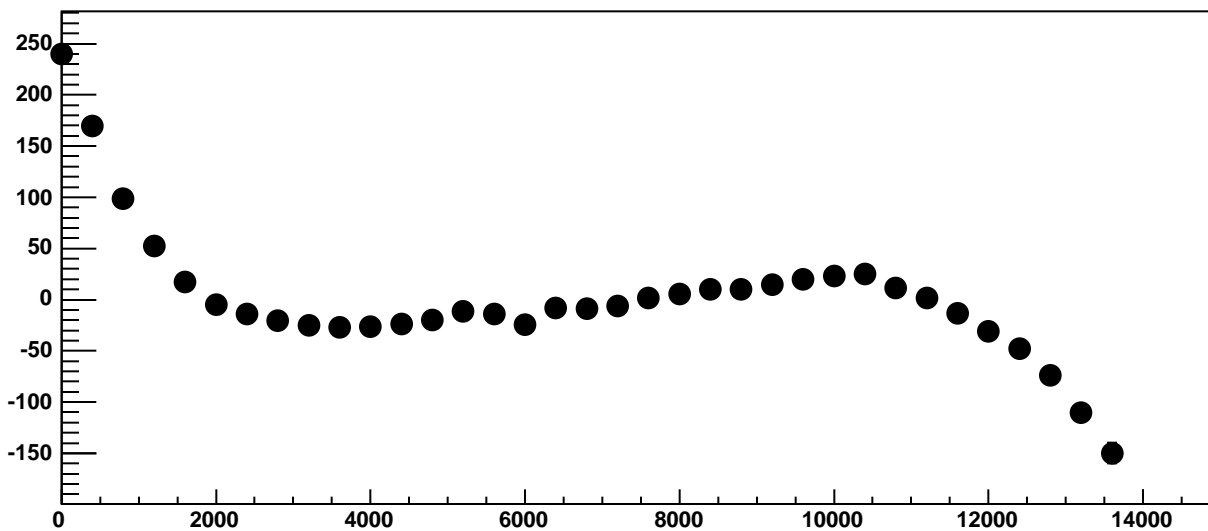
$\chi^2 / \text{ndf}$  1503 / 23  
p0  $64.82 \pm 1.196$   
p1  $0.1851 \pm 0.0002117$



Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

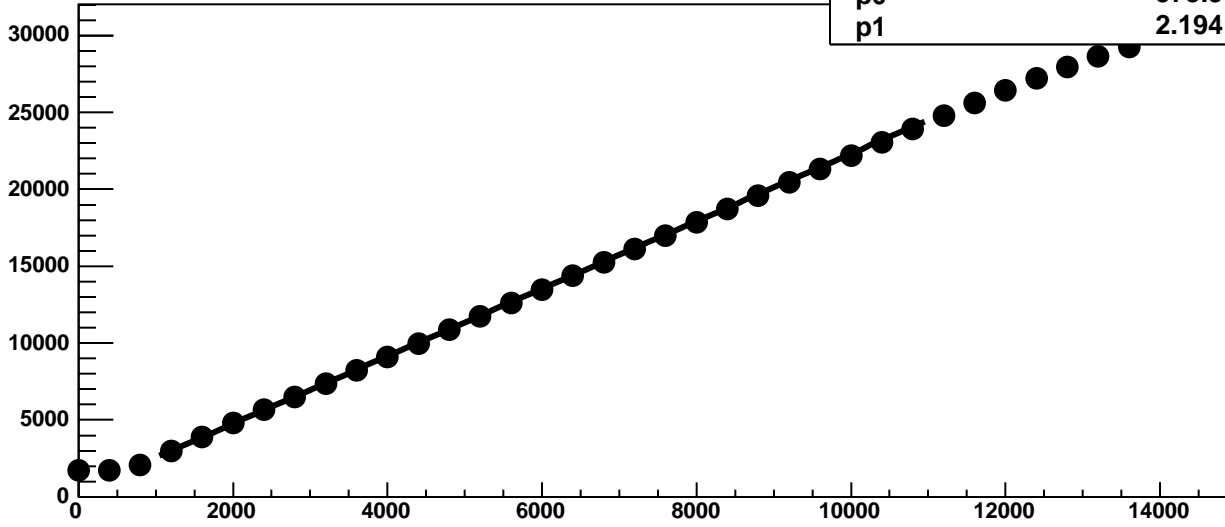


Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

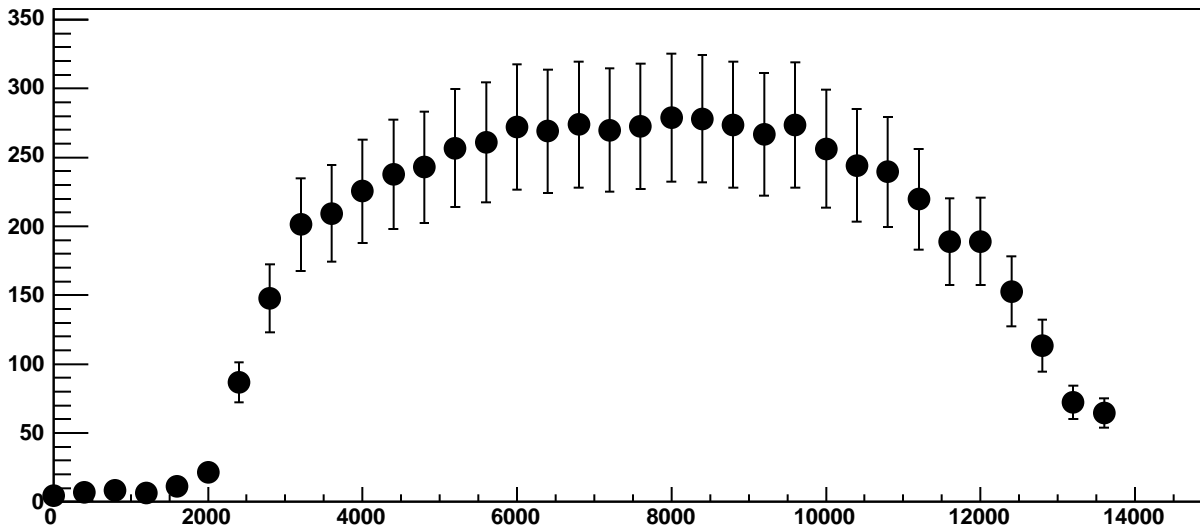


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

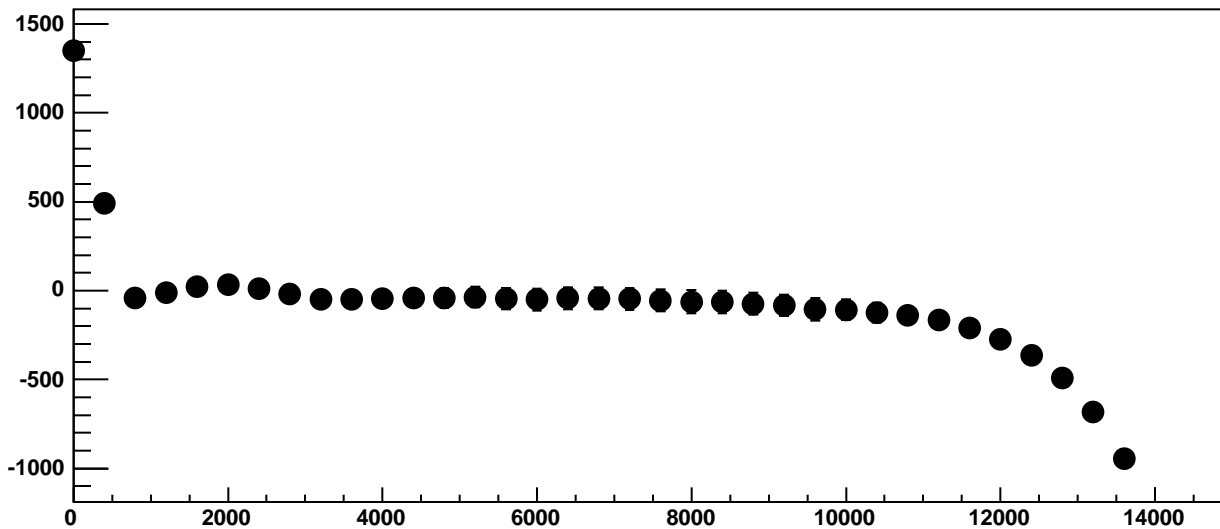
$\chi^2 / \text{ndf}$  202.9 / 23  
p0  $373.5 \pm 3.045$   
p1  $2.194 \pm 0.002$



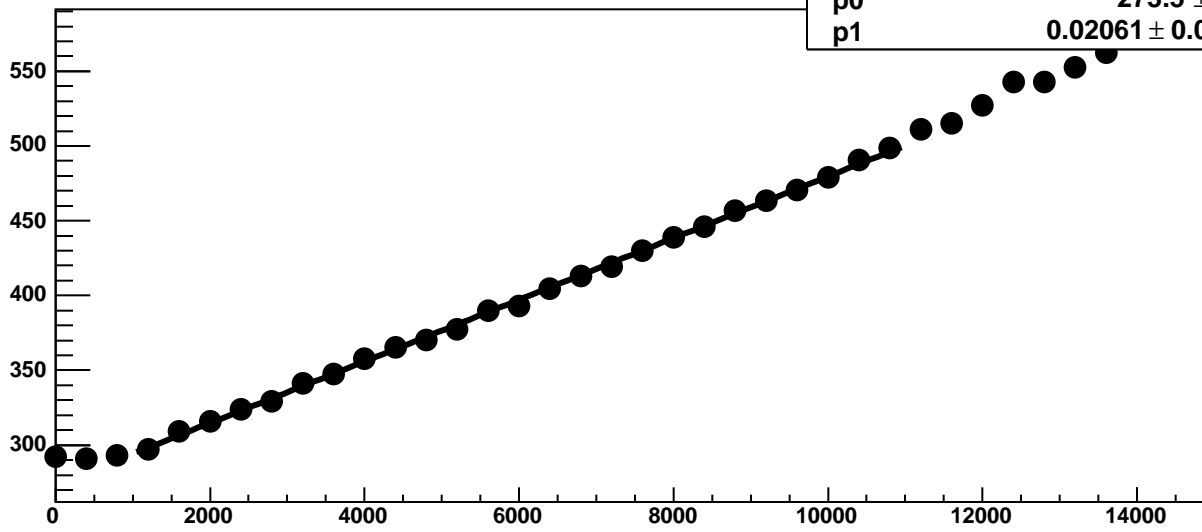
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

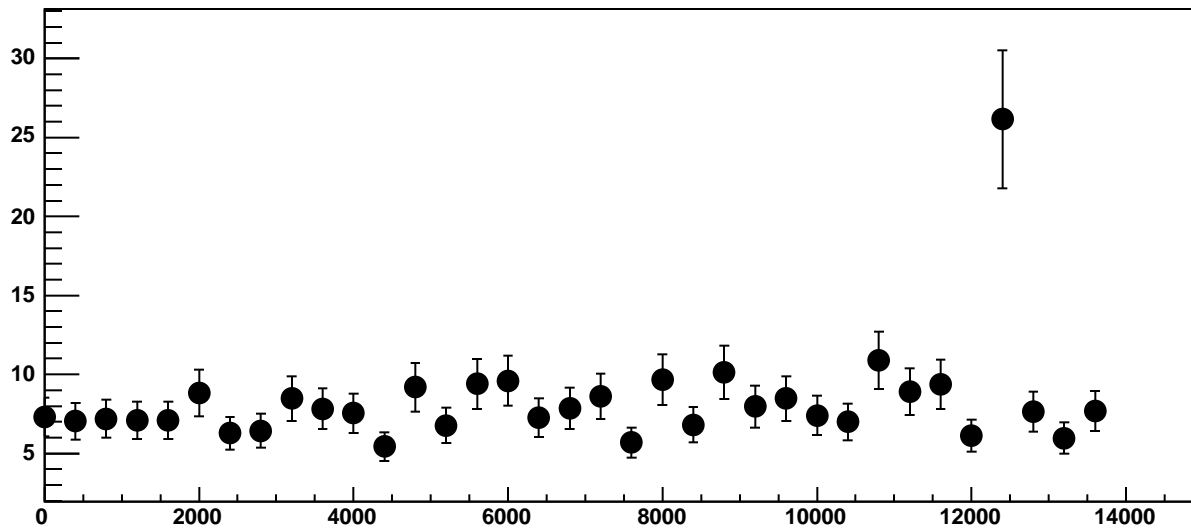


Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

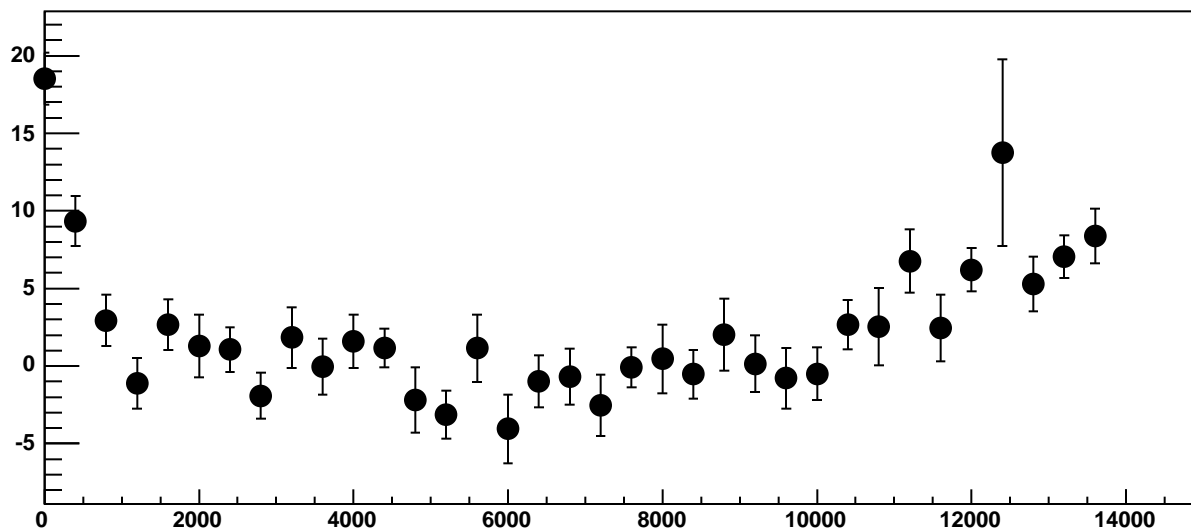


$\chi^2 / \text{ndf}$  24.26 / 23  
p0  $273.5 \pm 0.7786$   
p1  $0.02061 \pm 0.0001215$

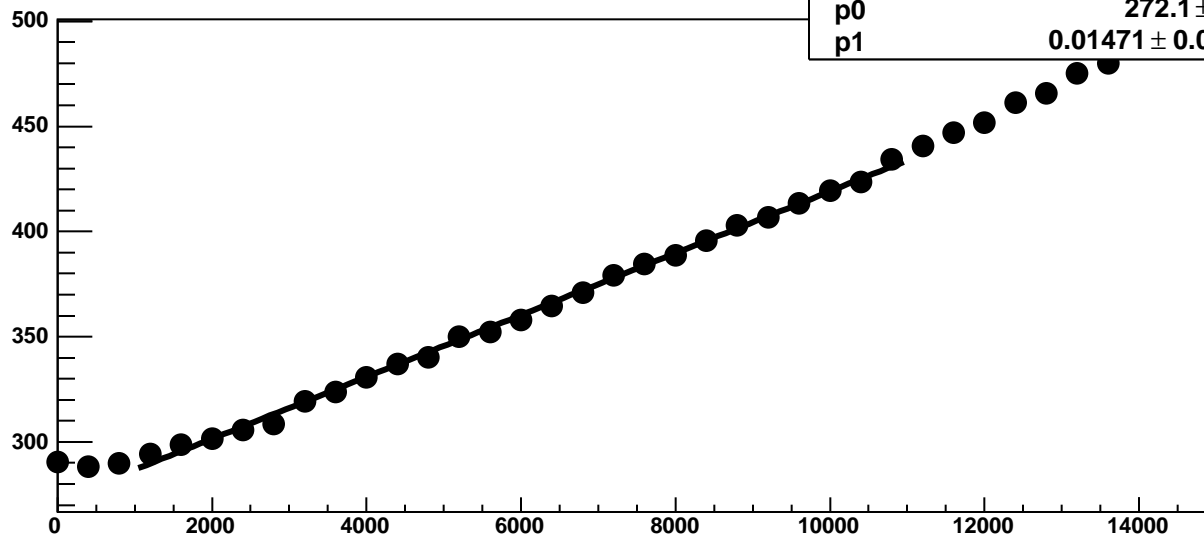
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

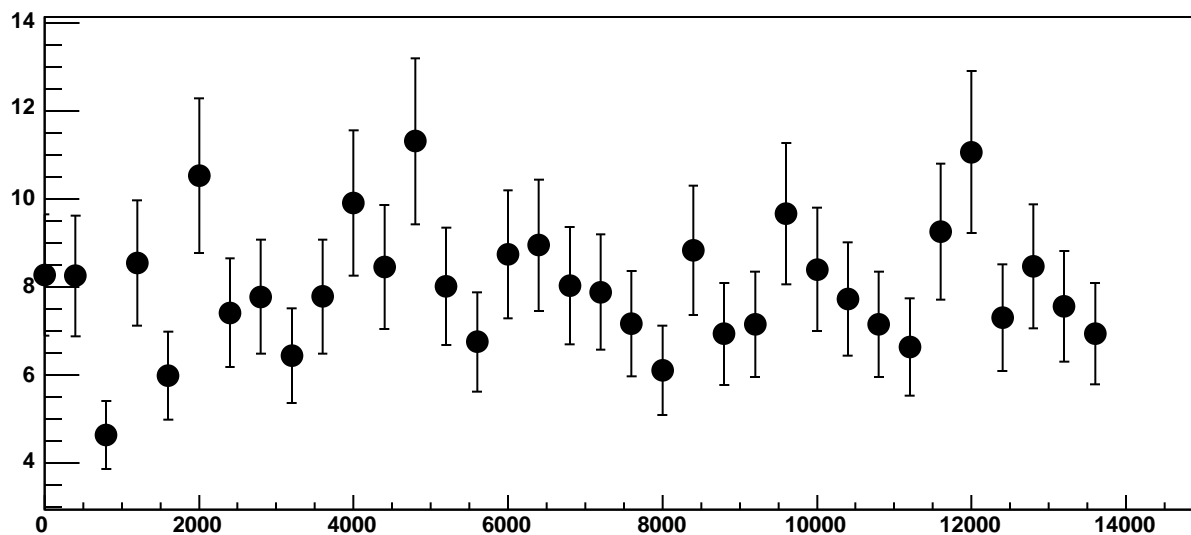


Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

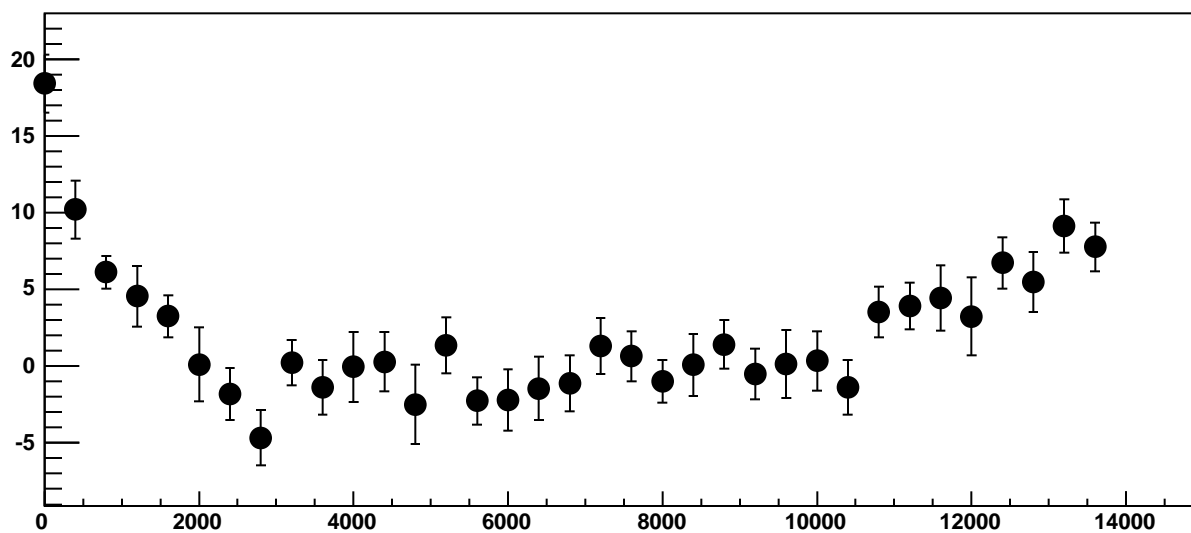


$\chi^2 / \text{ndf}$  32.61 / 23  
p0  $272.1 \pm 0.8155$   
p1  $0.01471 \pm 0.0001216$

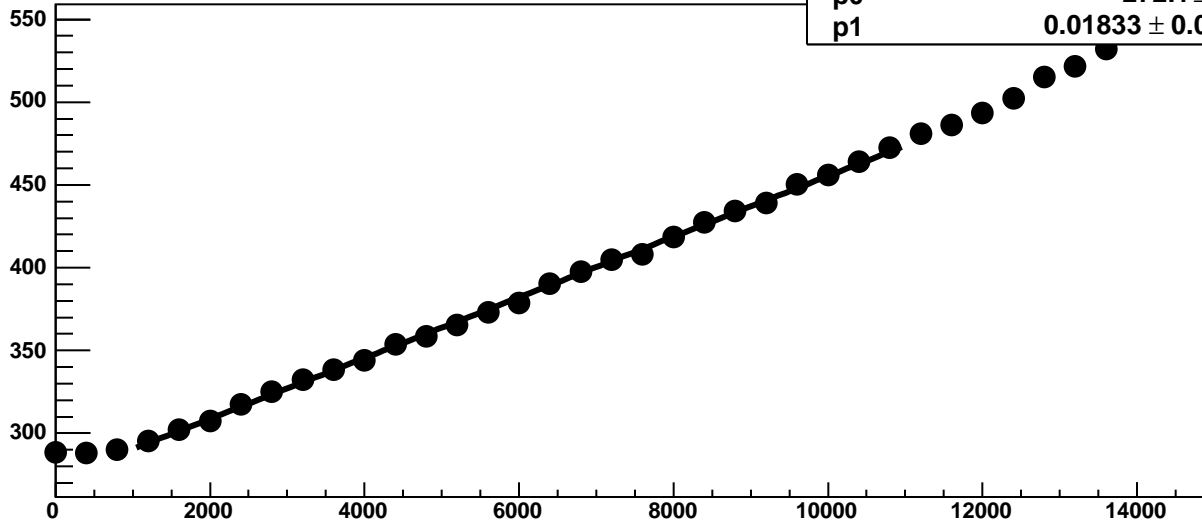
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

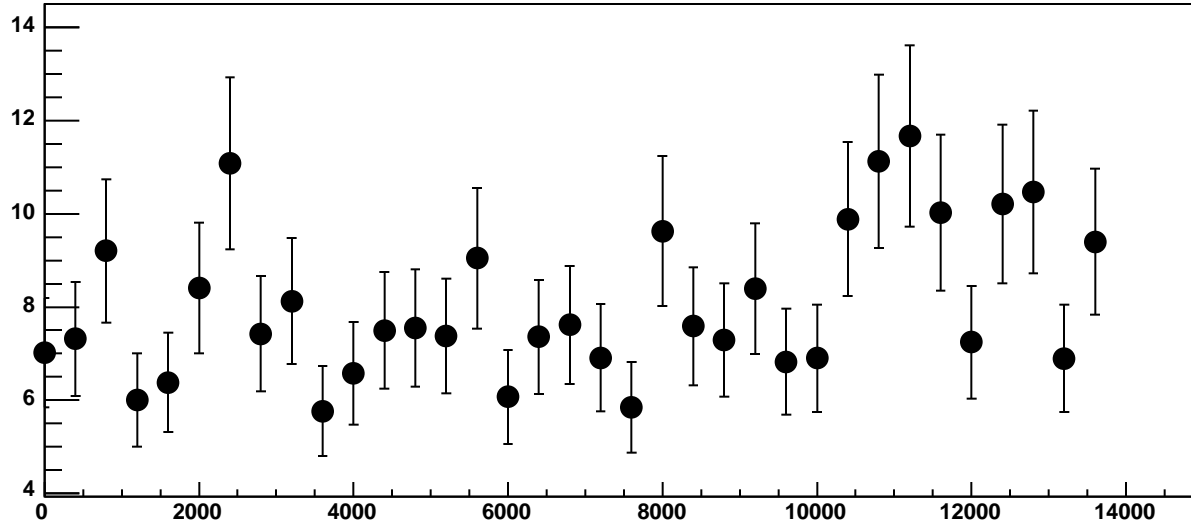


Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

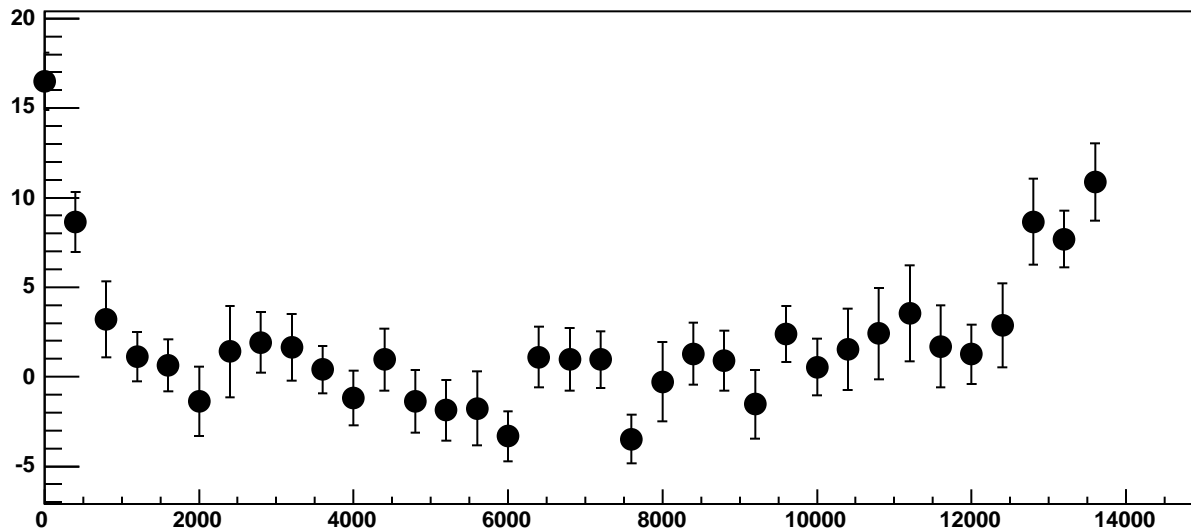


$\chi^2 / \text{ndf}$  26.04 / 23  
p0  $272.1 \pm 0.7707$   
p1  $0.01833 \pm 0.0001205$

Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



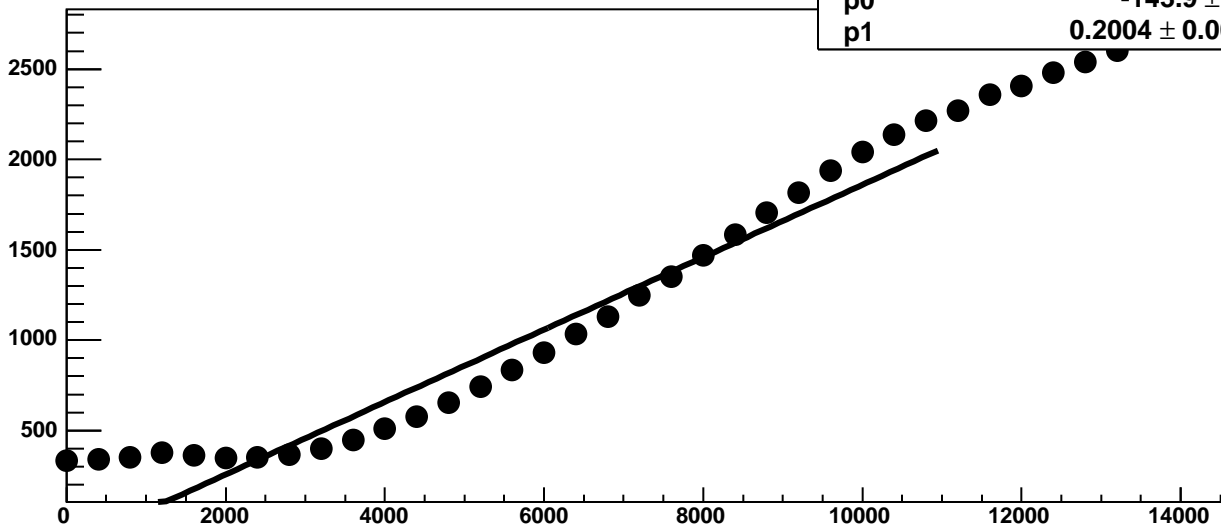
Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



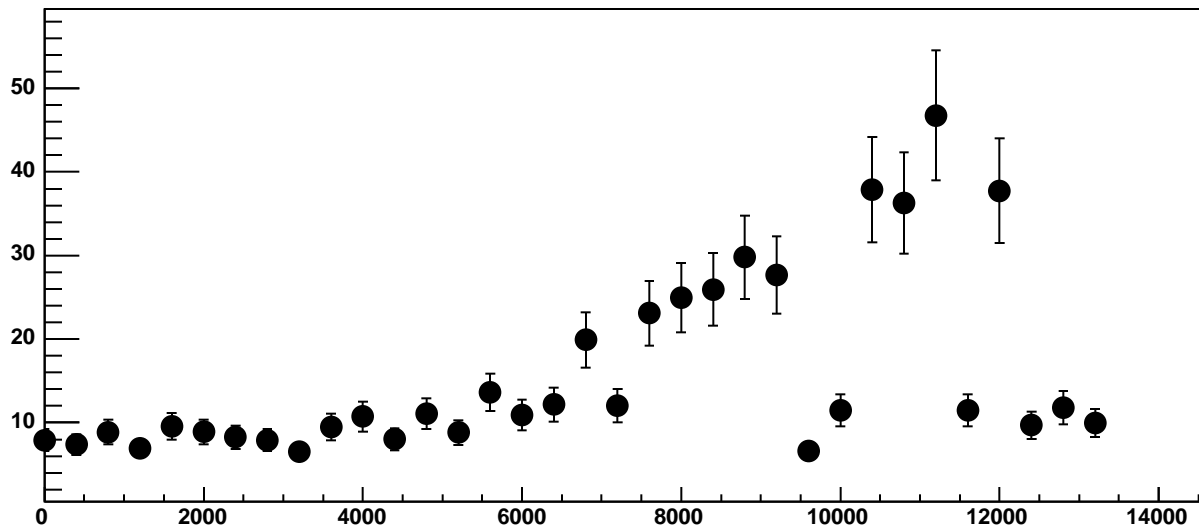


Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

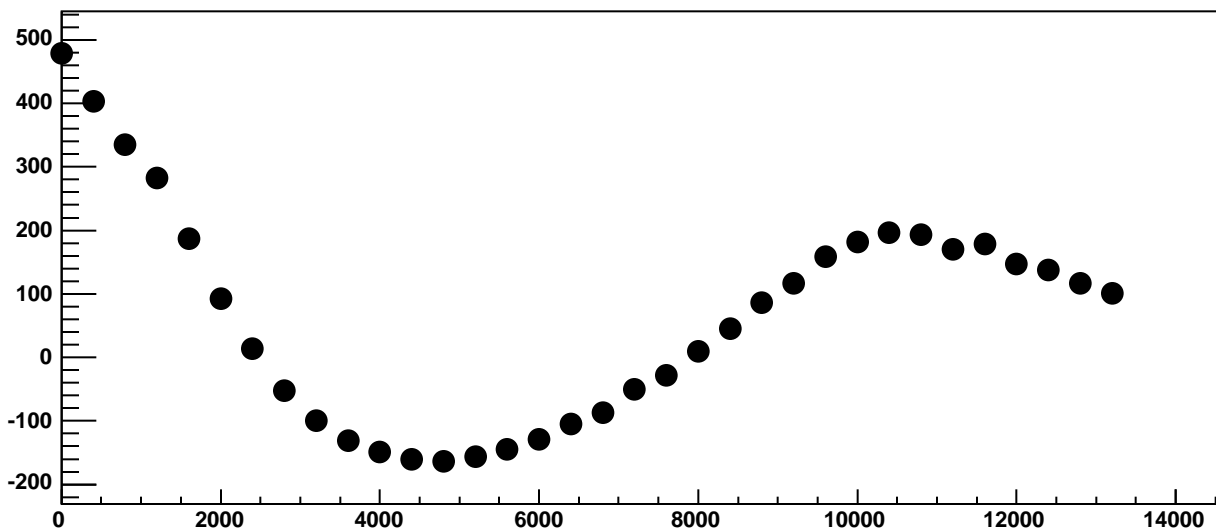
$\chi^2 / \text{ndf}$  9.575e+04 / 23  
p0 -143.9 ± 0.9516  
p1 0.2004 ± 0.0001775



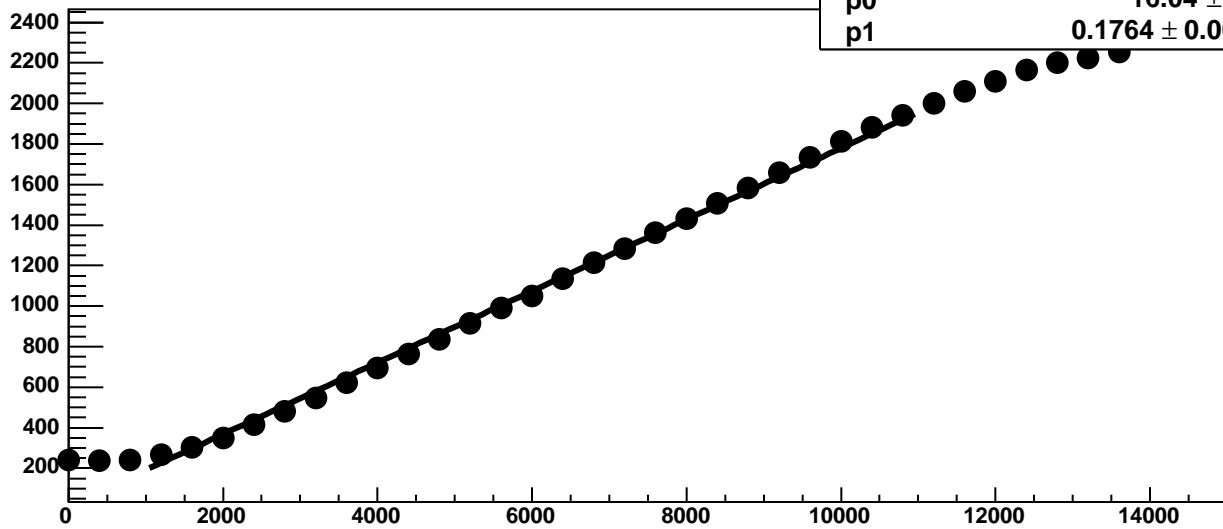
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

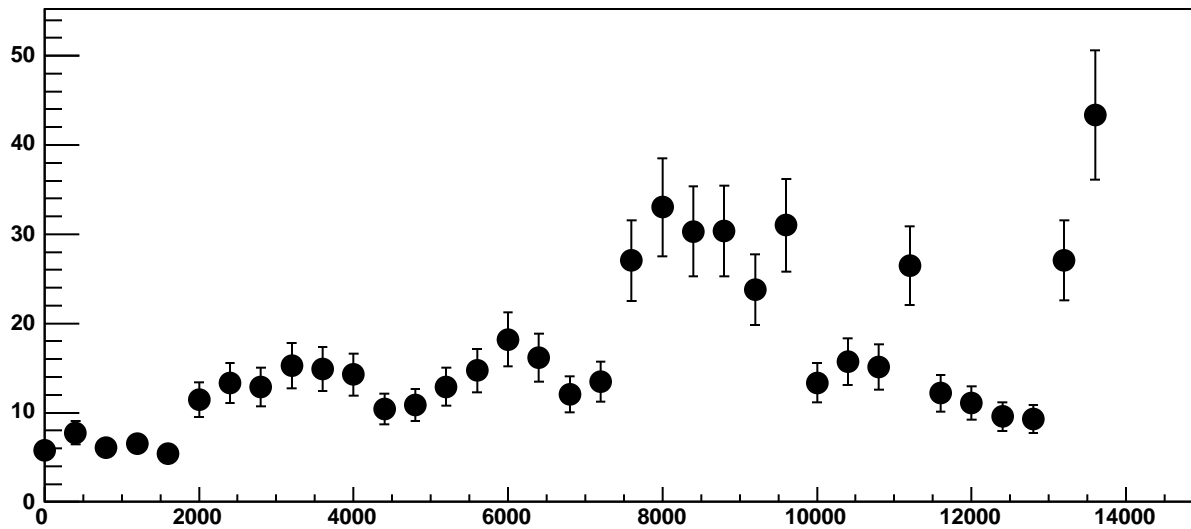


Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

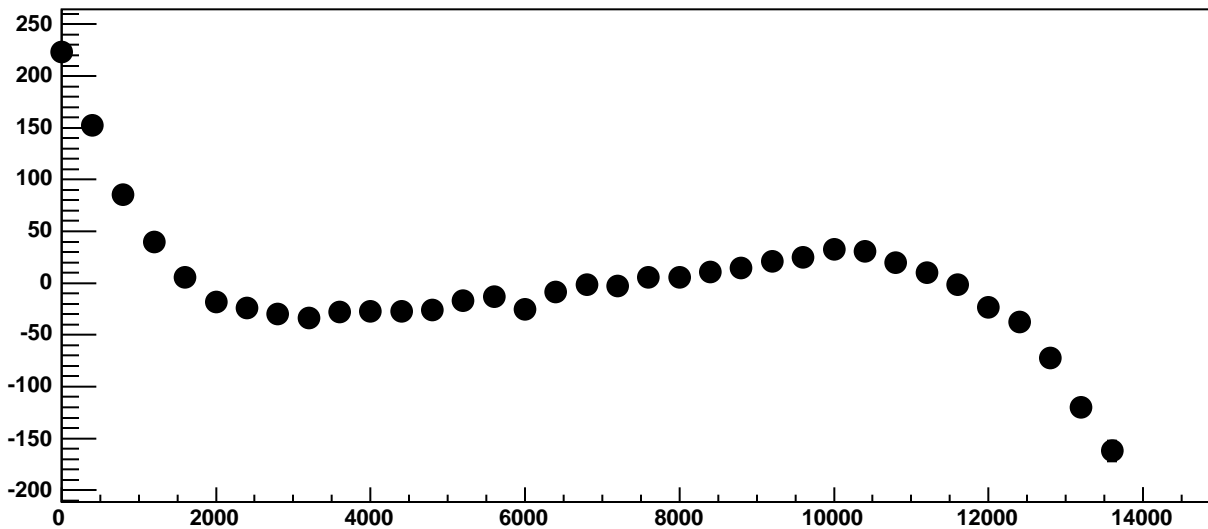


$\chi^2 / \text{ndf}$  1749 / 23  
p0  $16.04 \pm 0.9962$   
p1  $0.1764 \pm 0.0001985$

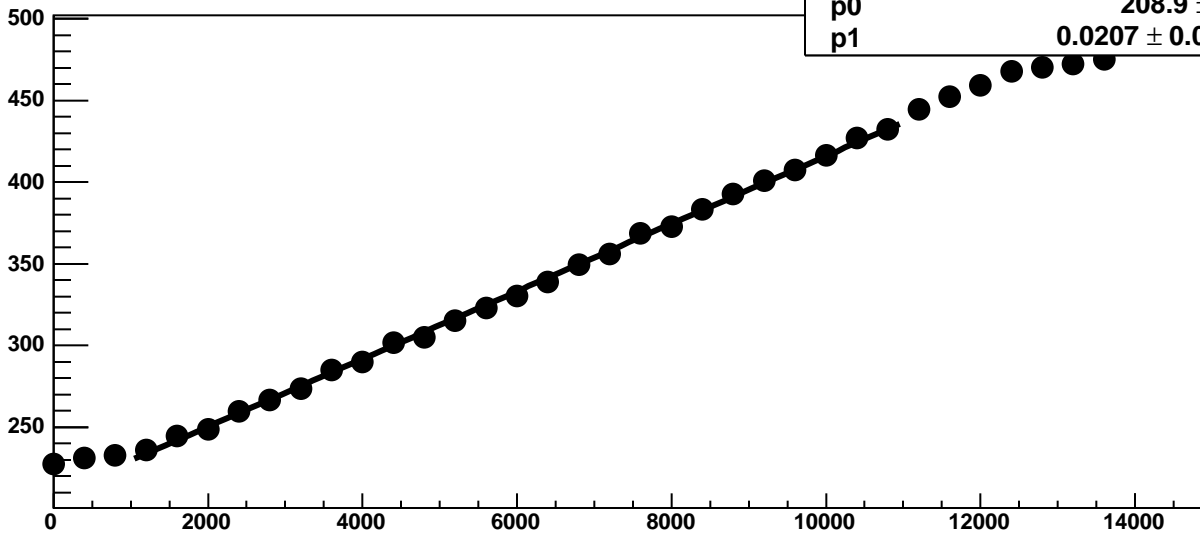
Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

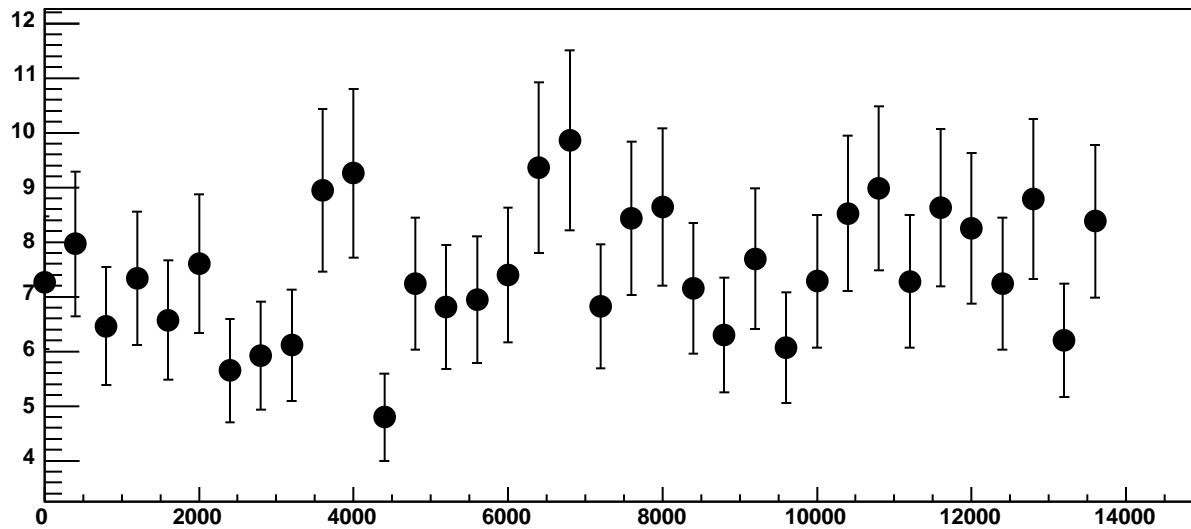


Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

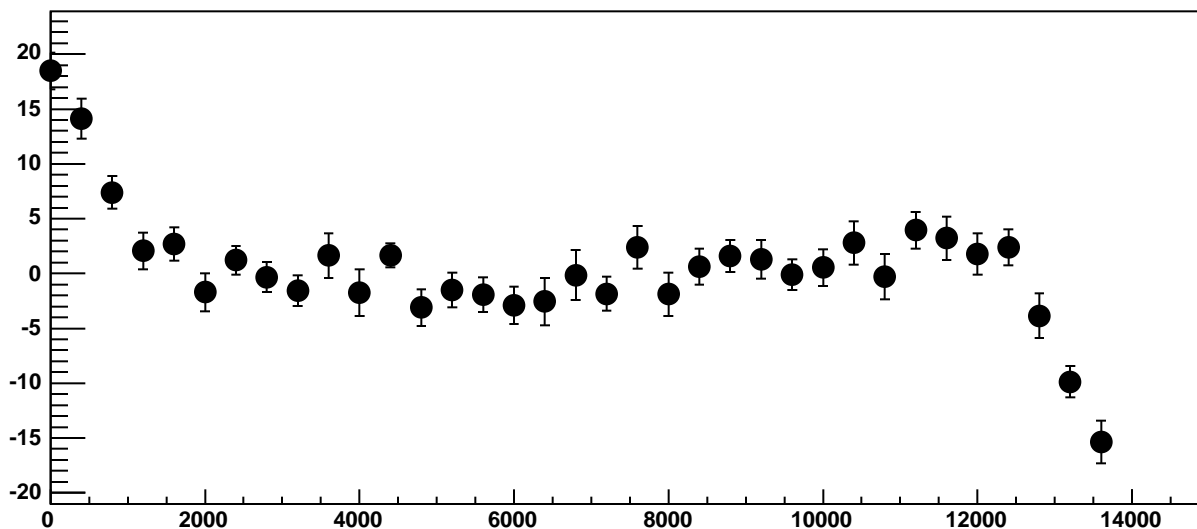


$\chi^2 / \text{ndf}$  29.5 / 23  
p0  $208.9 \pm 0.7211$   
p1  $0.0207 \pm 0.0001133$

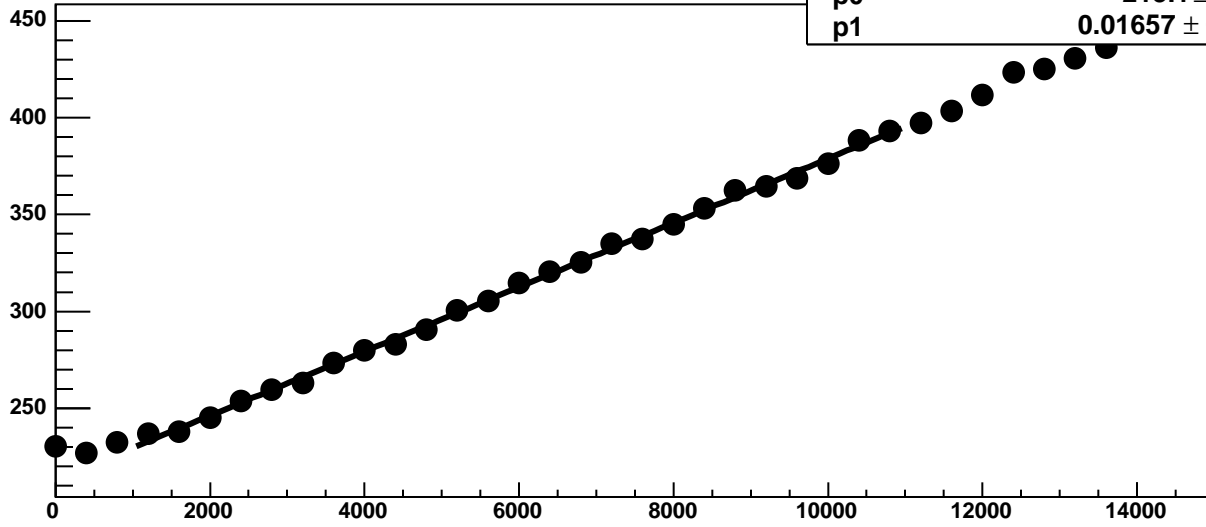
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

41.22 / 23

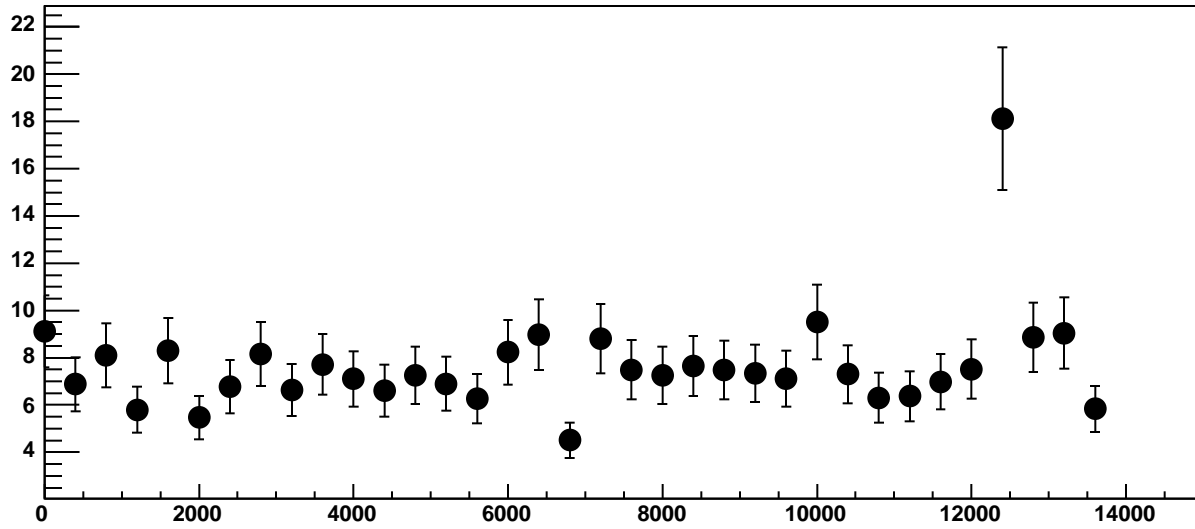
p0

$213.1 \pm 0.7135$

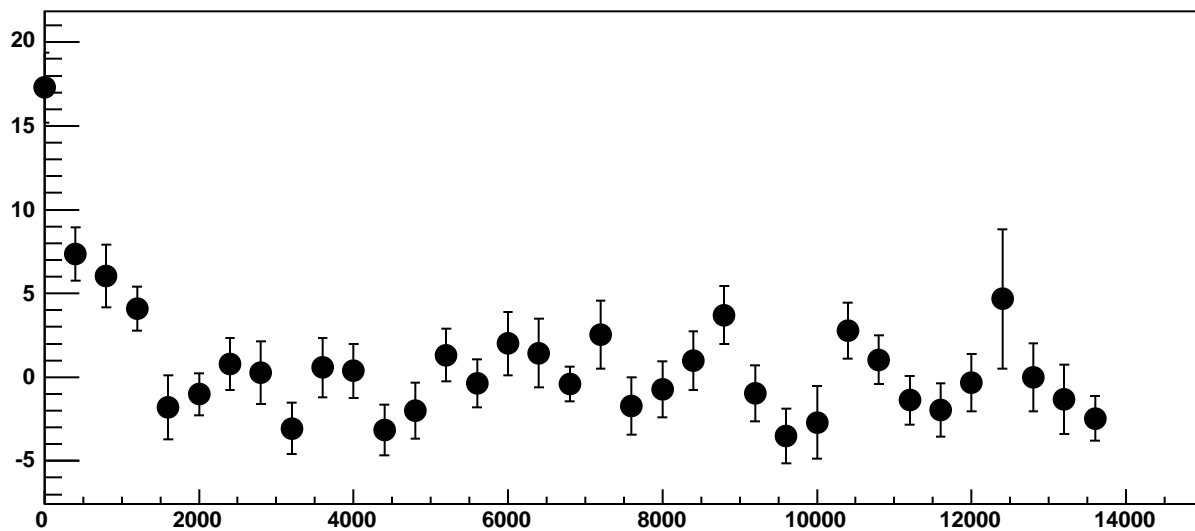
p1

$0.01657 \pm 0.00011$

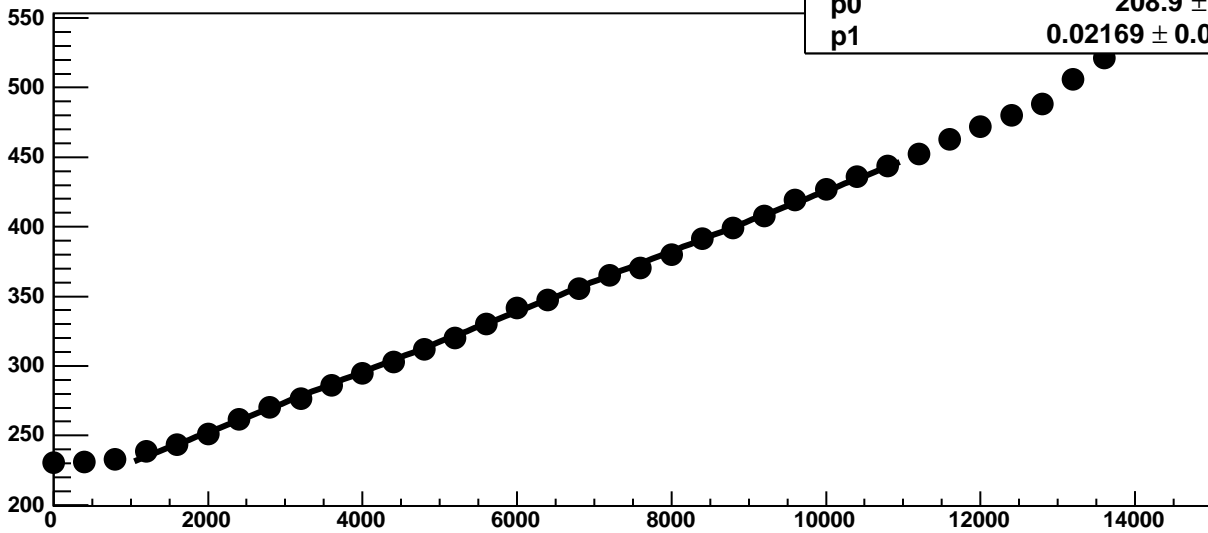
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

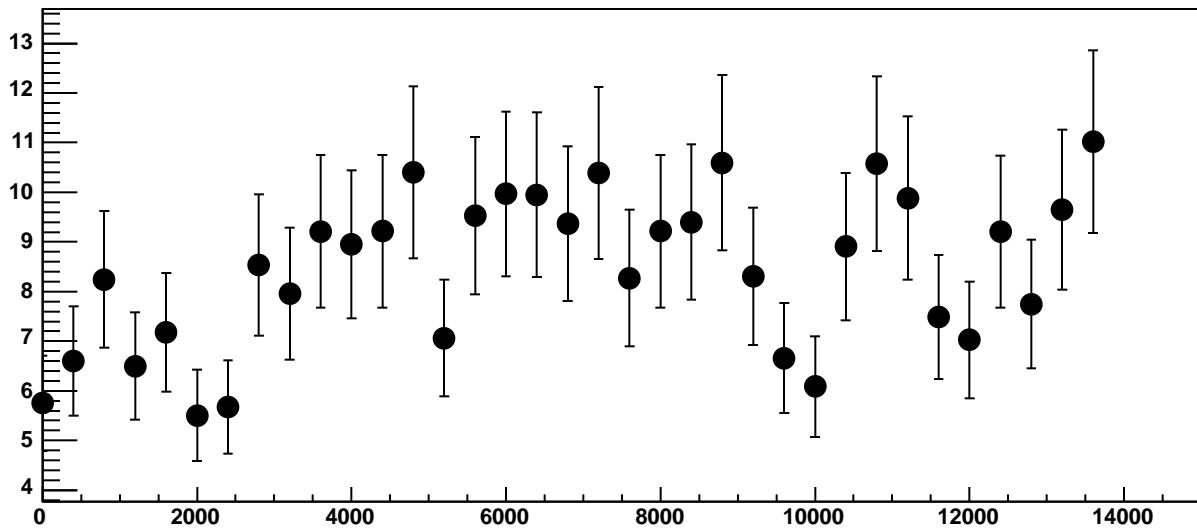


Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

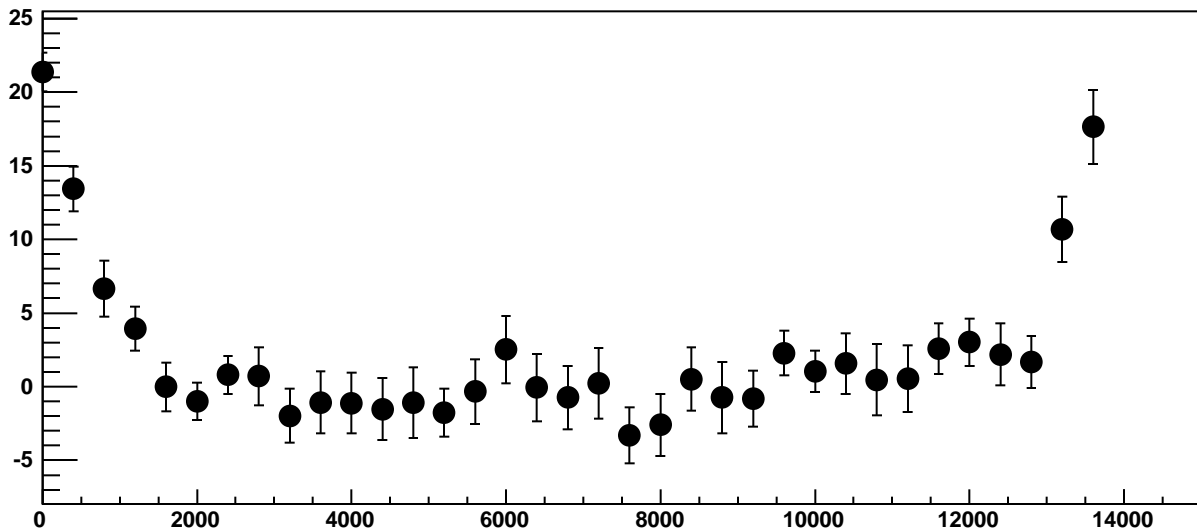


$\chi^2 / \text{ndf}$  21.32 / 23  
p0  $208.9 \pm 0.7513$   
p1  $0.02169 \pm 0.0001181$

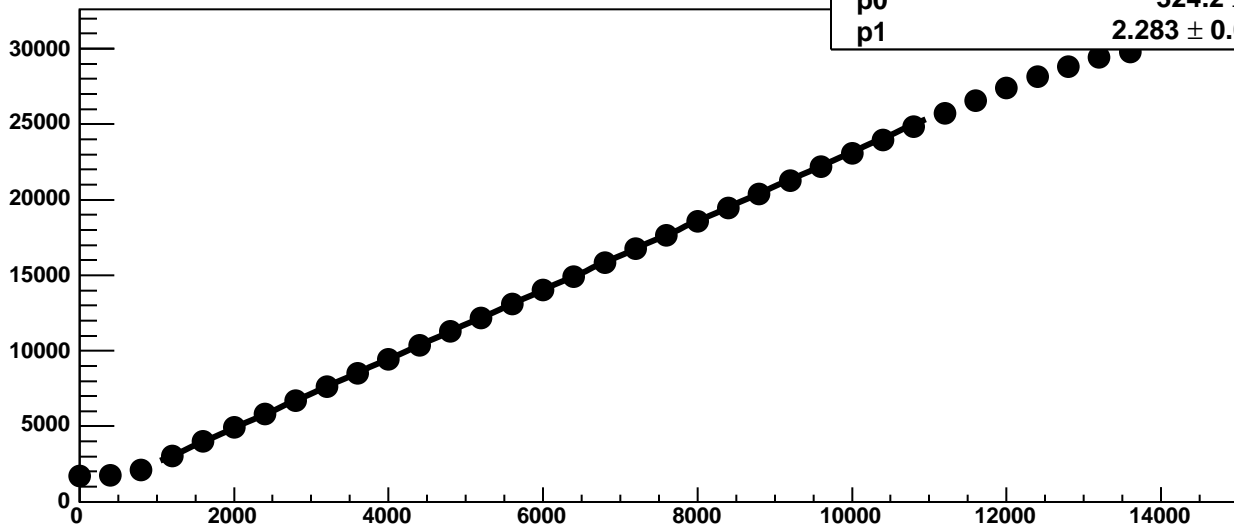
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

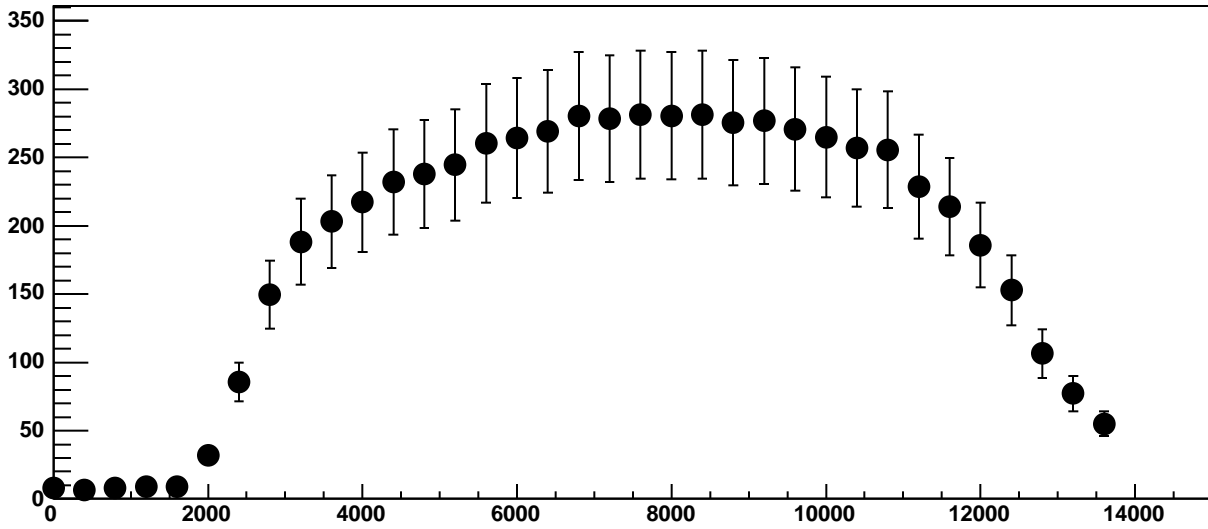


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

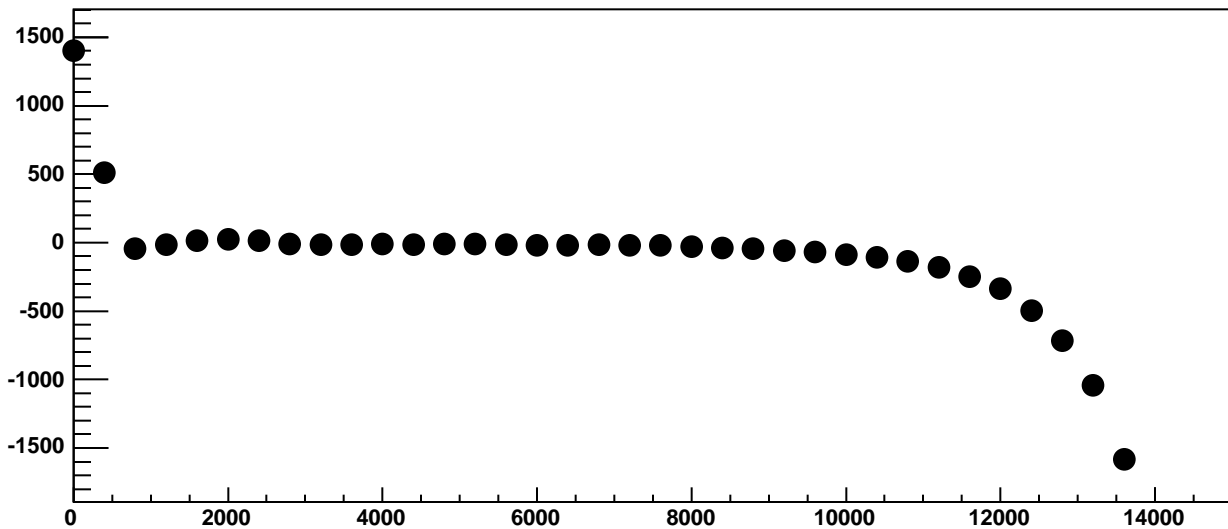


$\chi^2 / \text{ndf}$	112.1 / 23
p0	$324.2 \pm 3.495$
p1	$2.283 \pm 0.002123$

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC

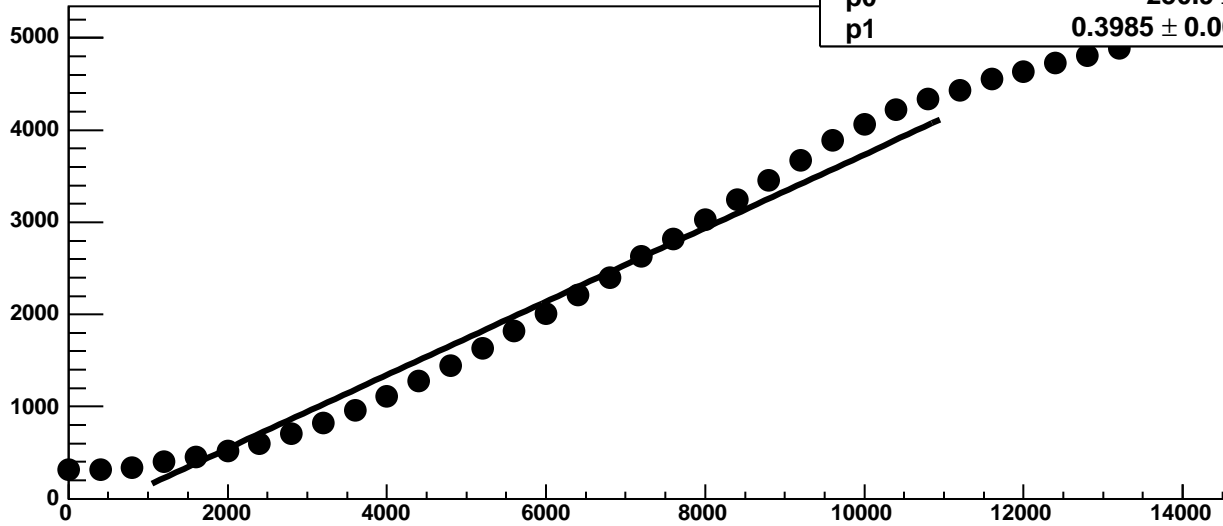


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

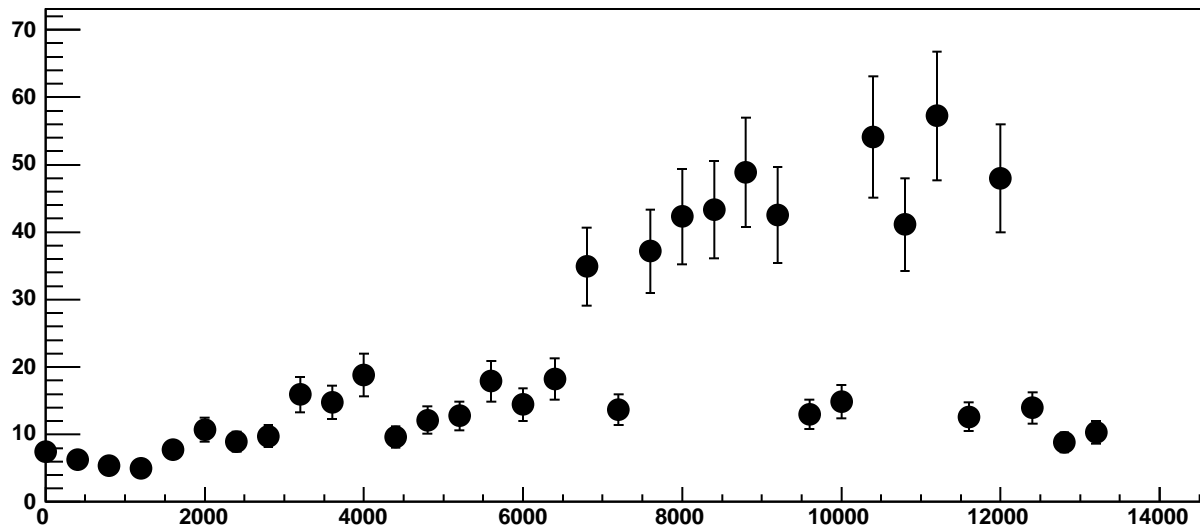


Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

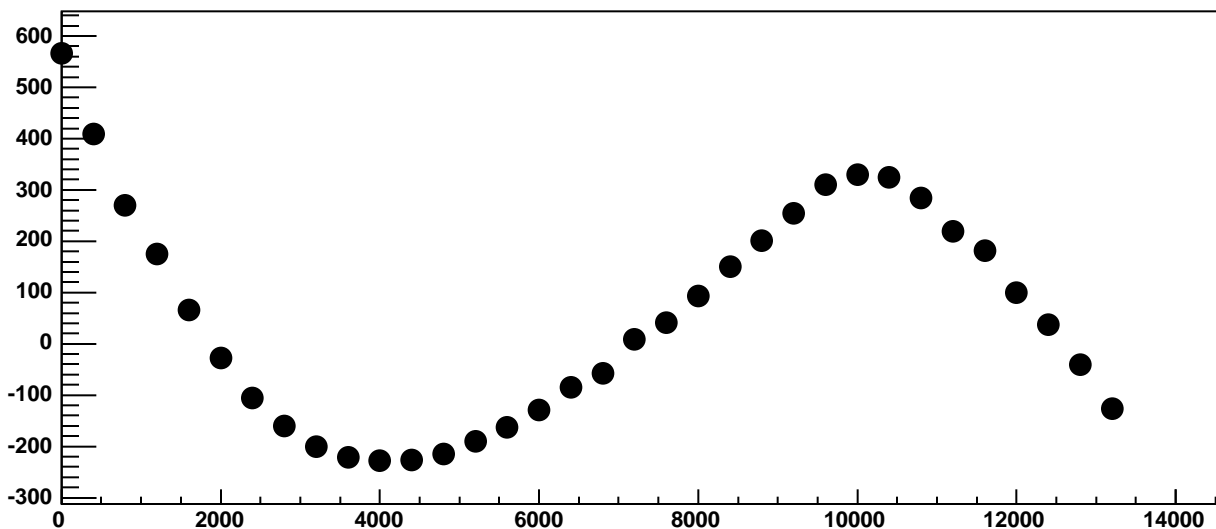
$\chi^2 / \text{ndf}$  9.007e+04 / 23  
p0 -250.9 ± 0.992  
p1 0.3985 ± 0.0002263



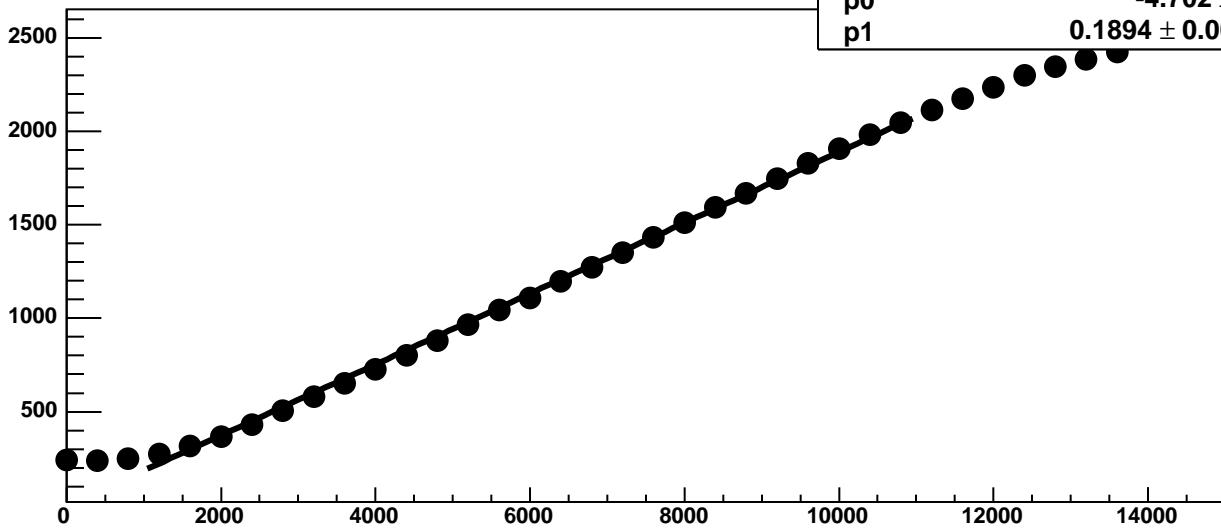
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

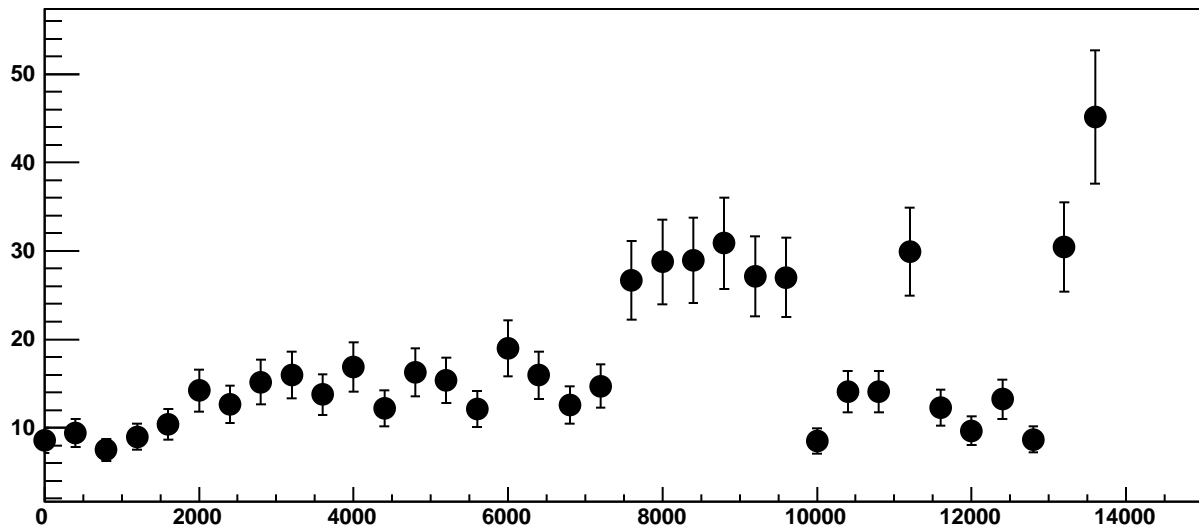


Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

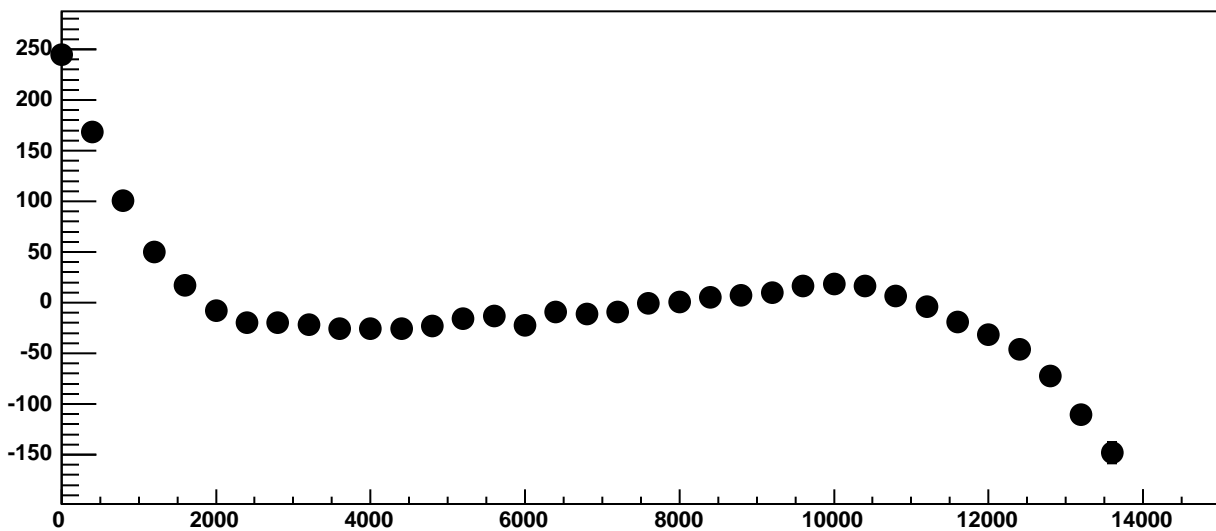


$\chi^2 / \text{ndf}$  1218 / 23  
p0  $-4.702 \pm 1.295$   
p1  $0.1894 \pm 0.0002072$

Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

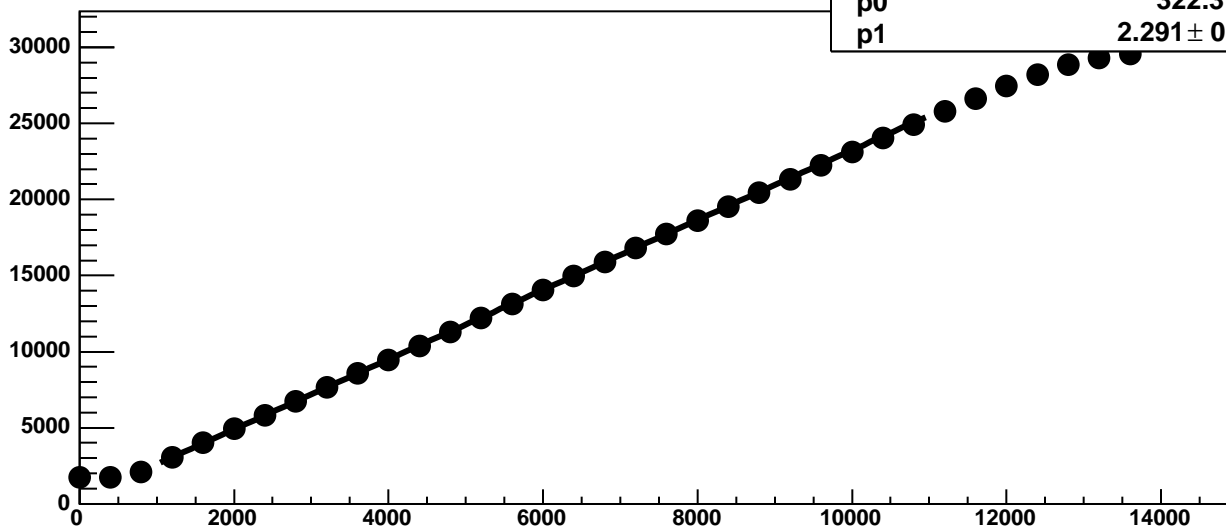


Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

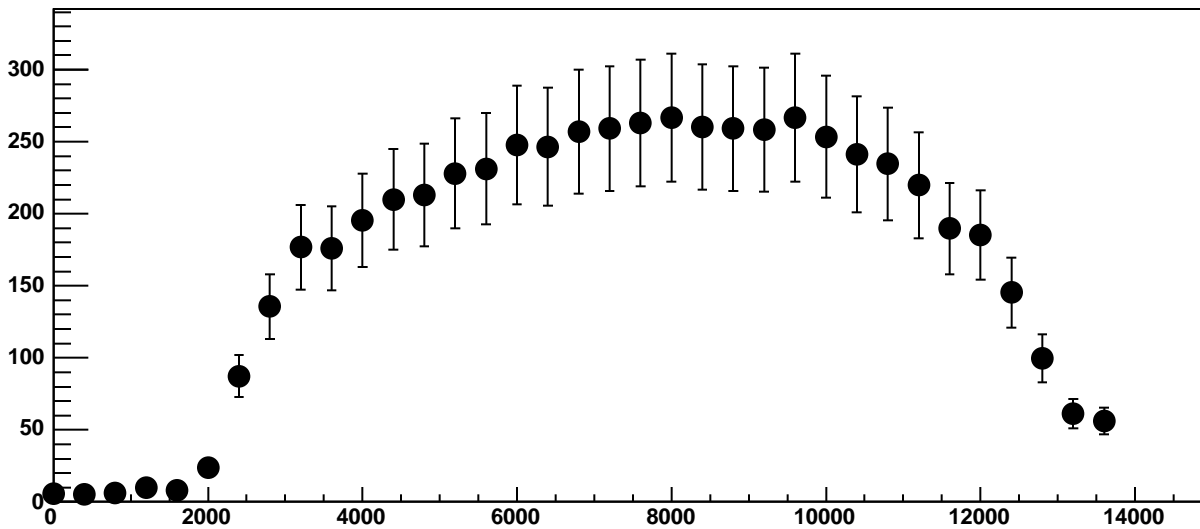




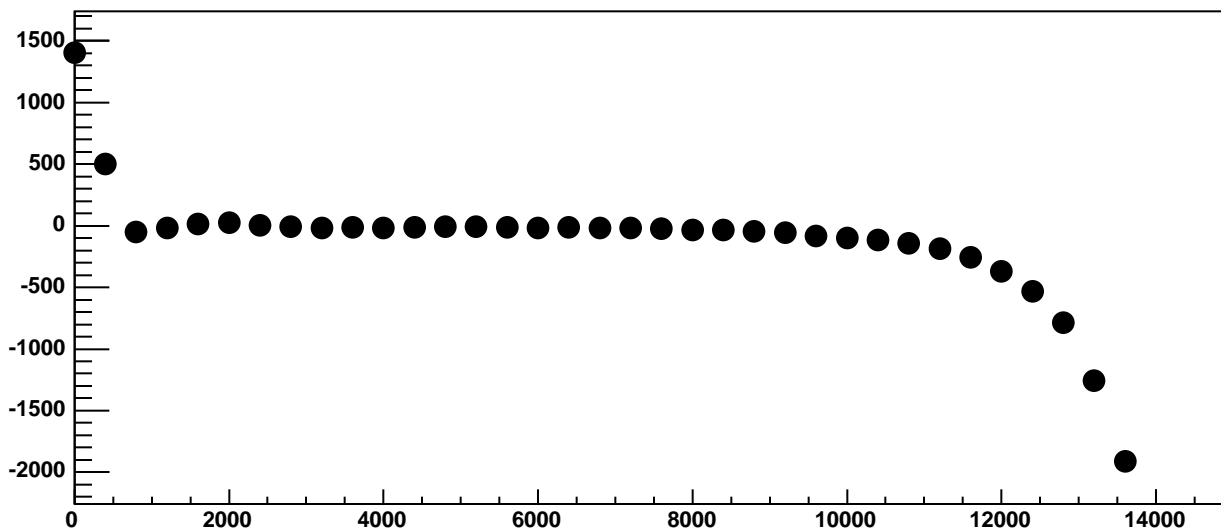
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



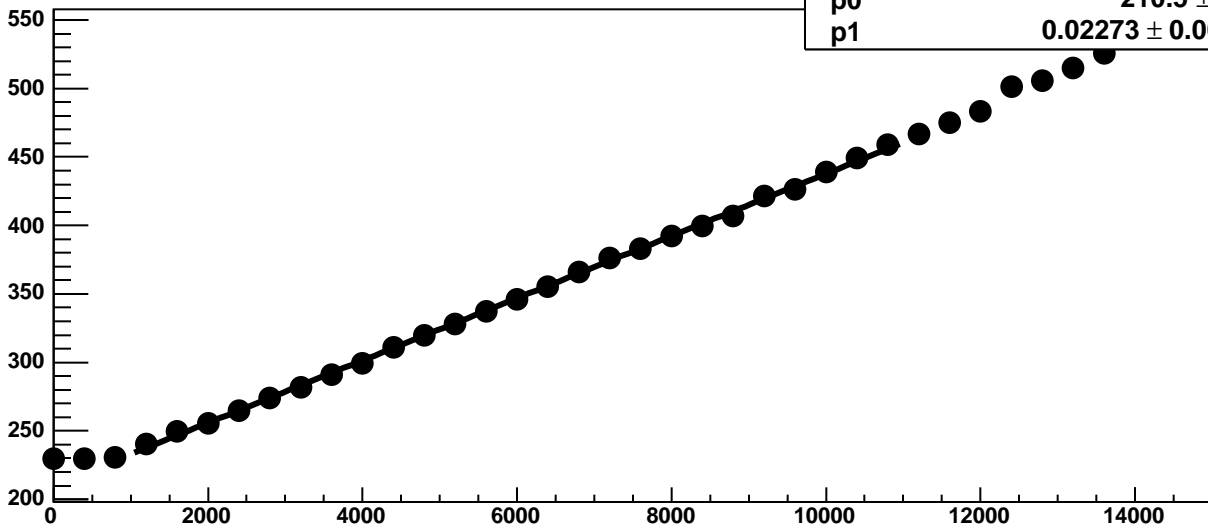
Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

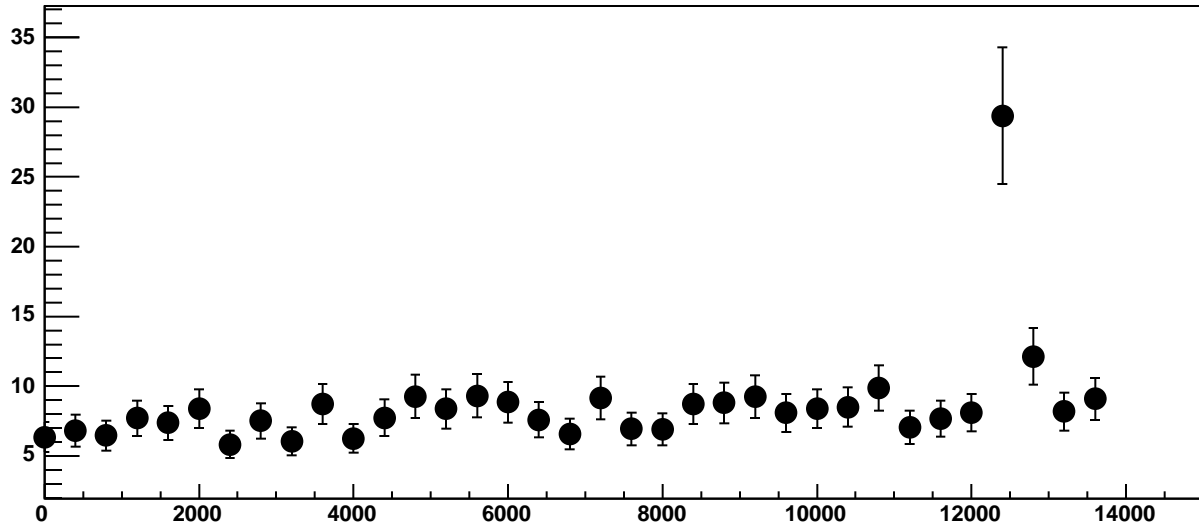


Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

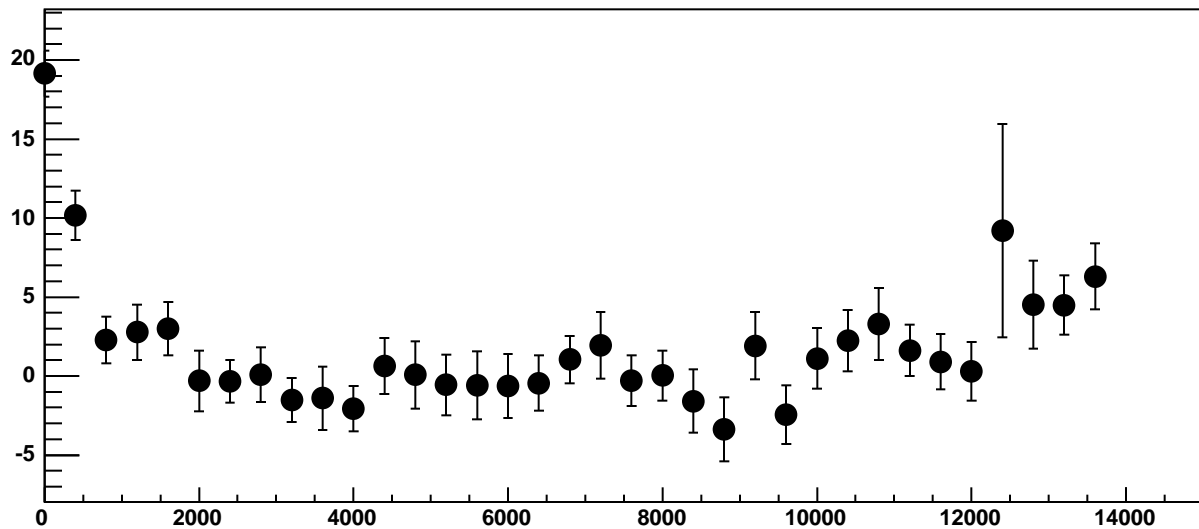


$\chi^2 / \text{ndf}$  20.97 / 23  
p0 210.5 ± 0.7911  
p1 0.02273 ± 0.0001256

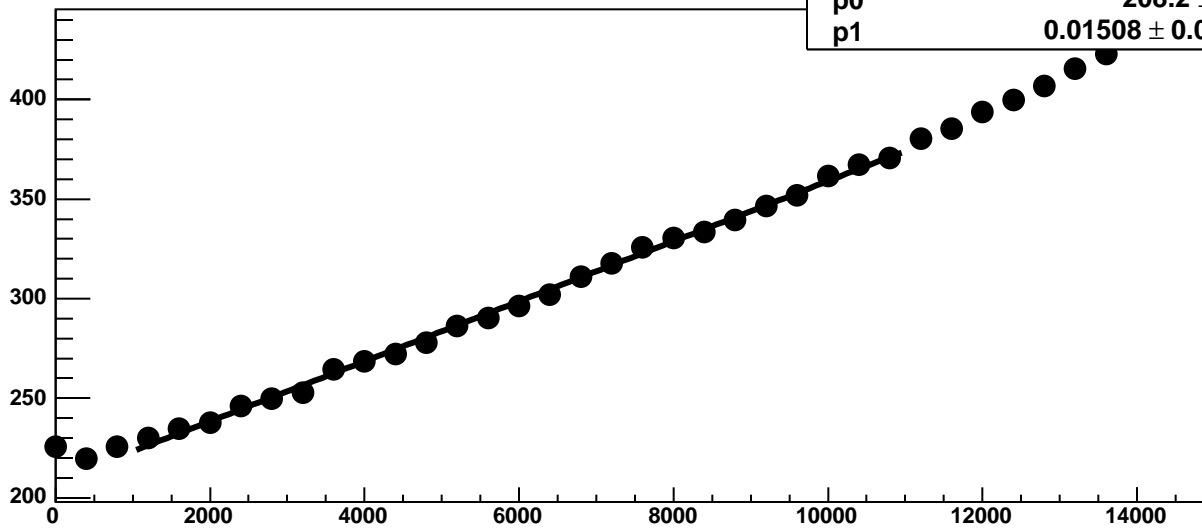
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

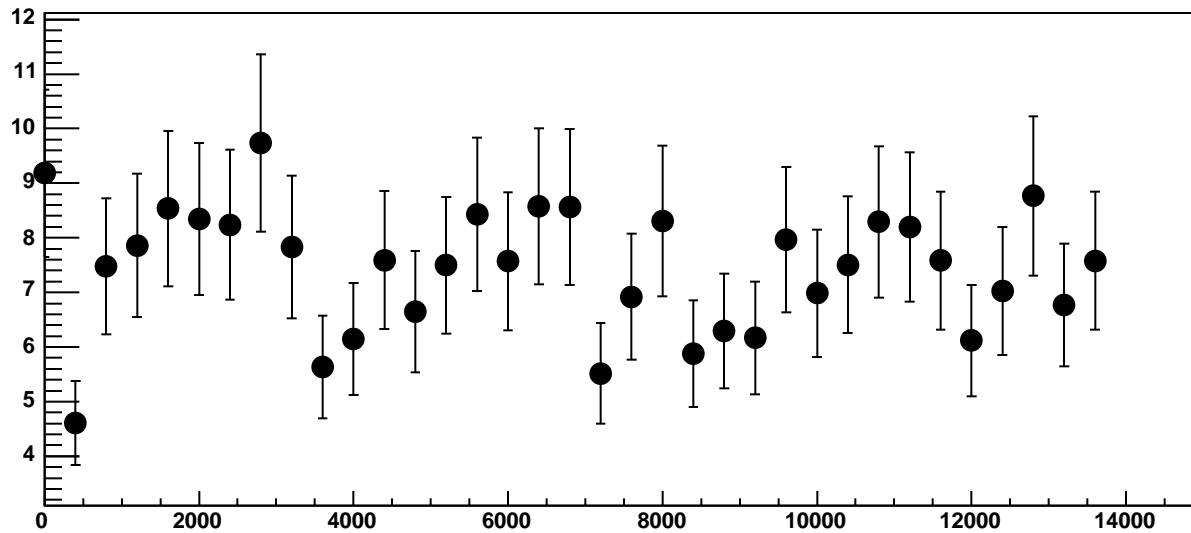


Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

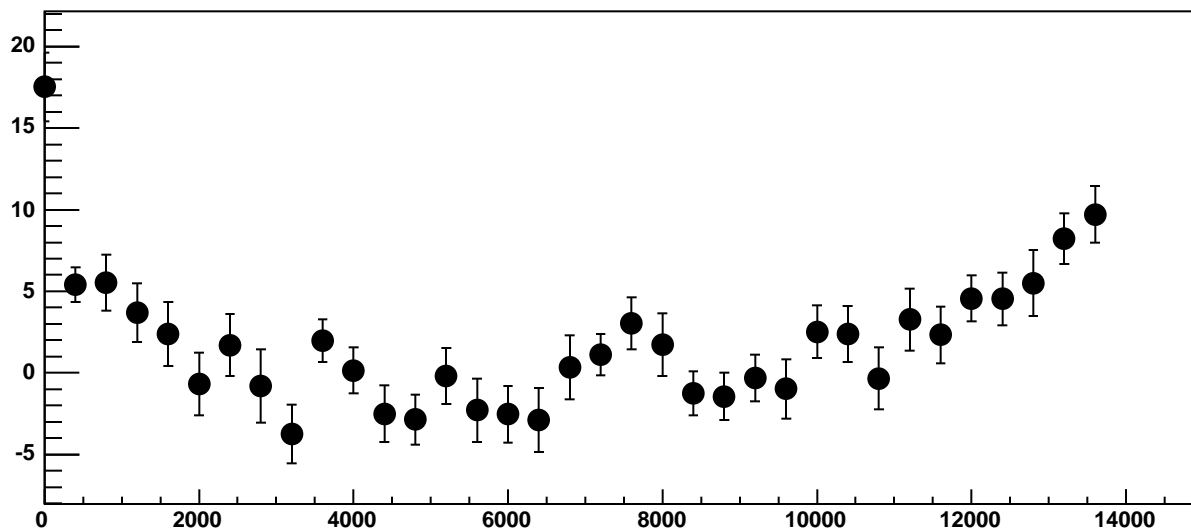


$\chi^2 / \text{ndf}$  36.53 / 23  
p0 208.2 ± 0.8141  
p1 0.01508 ± 0.0001198

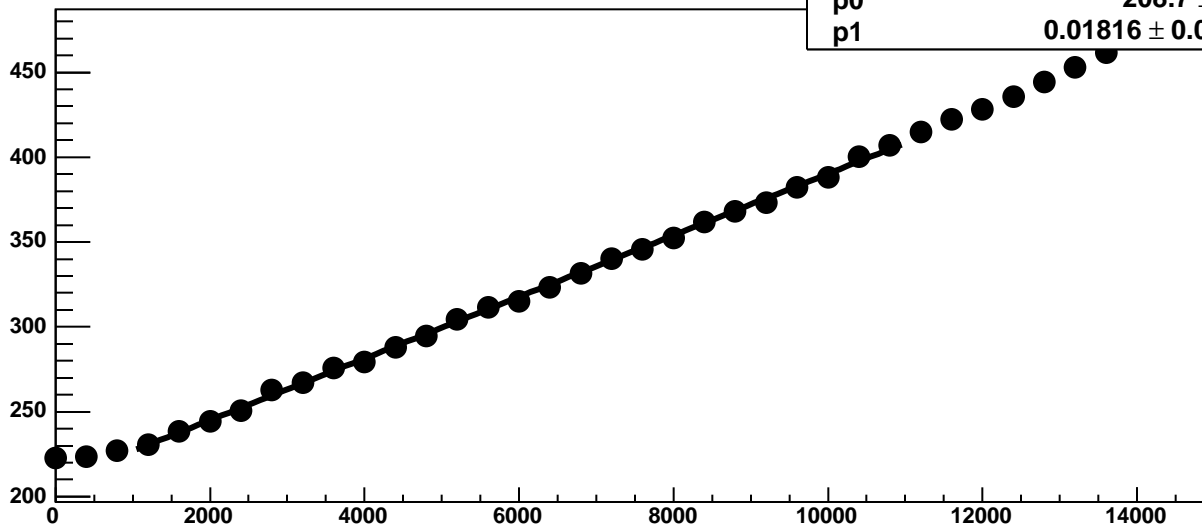
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

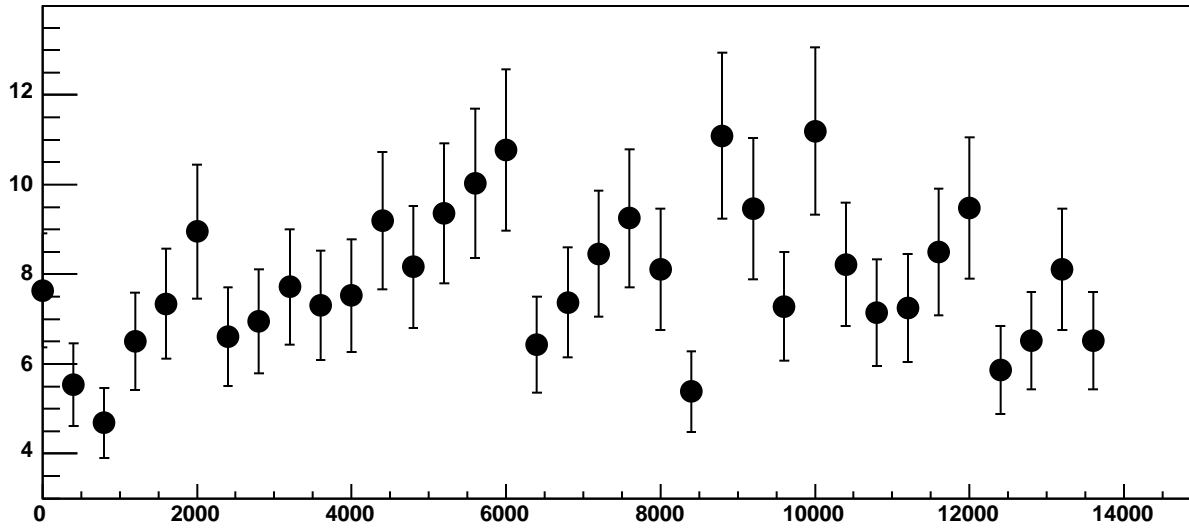


Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

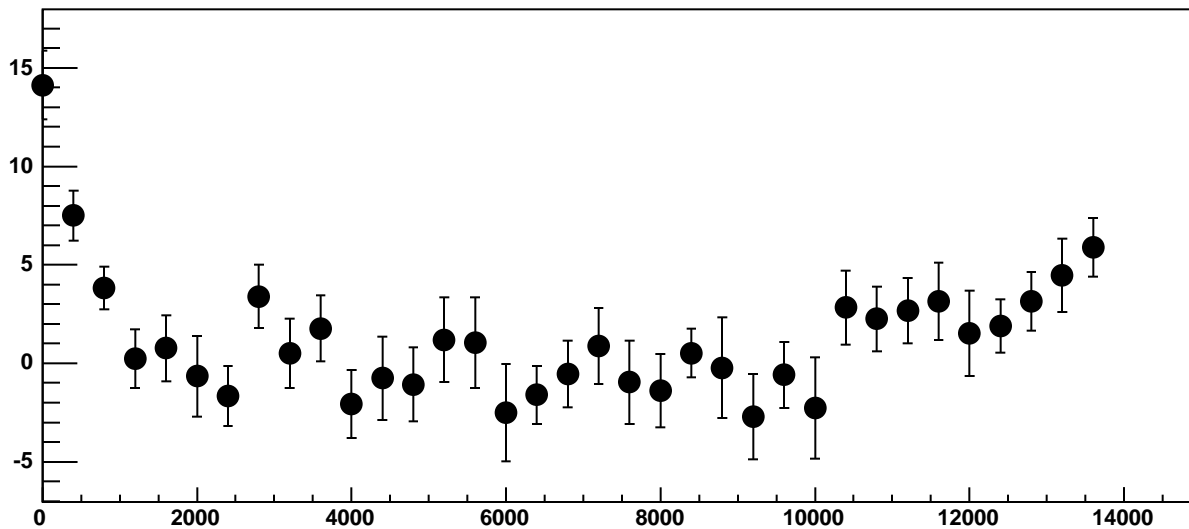


$\chi^2 / \text{ndf}$  19.7 / 23  
p0  $208.7 \pm 0.7901$   
p1  $0.01816 \pm 0.0001212$

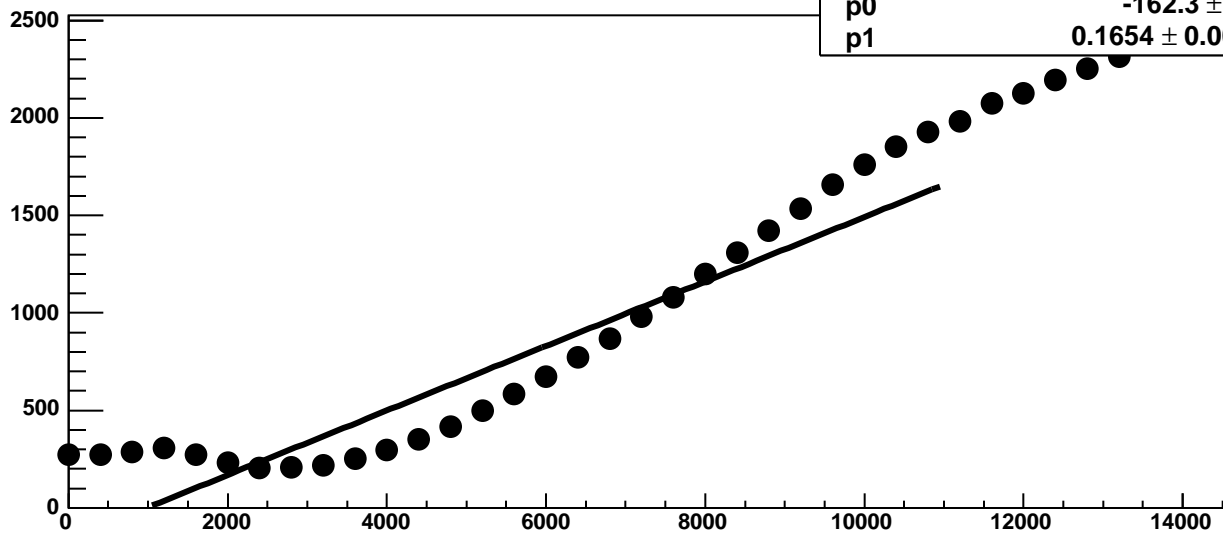
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

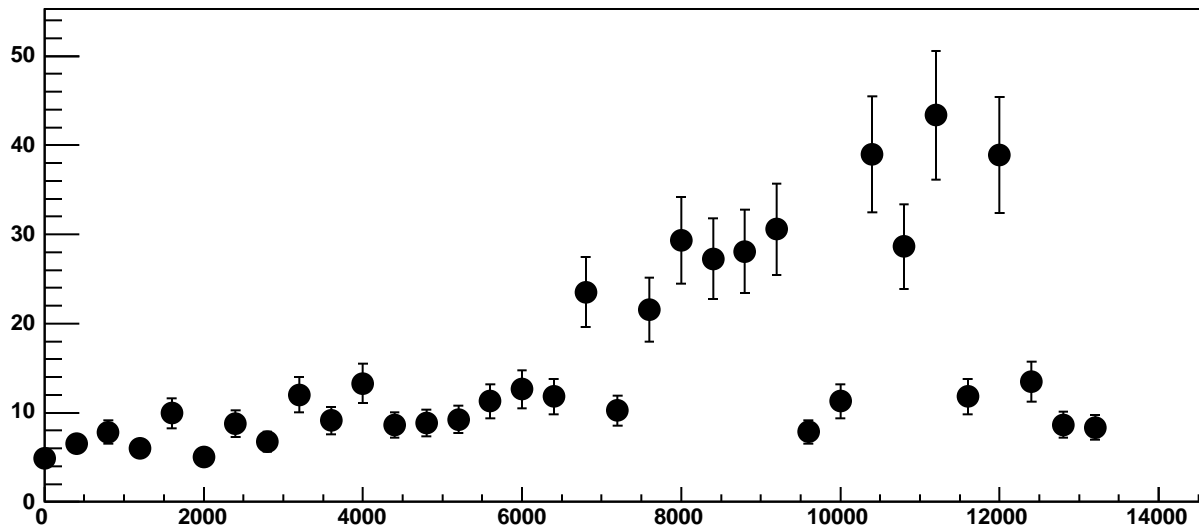


Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

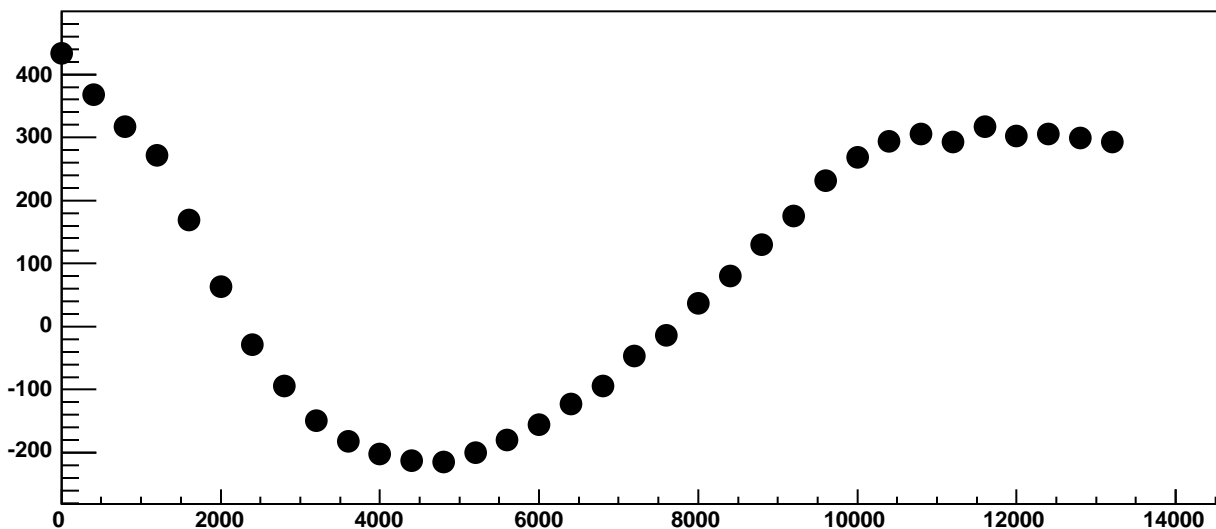


$\chi^2 / \text{ndf}$  1.398e+05 / 23  
p0 -162.3 ± 0.8727  
p1 0.1654 ± 0.0001749

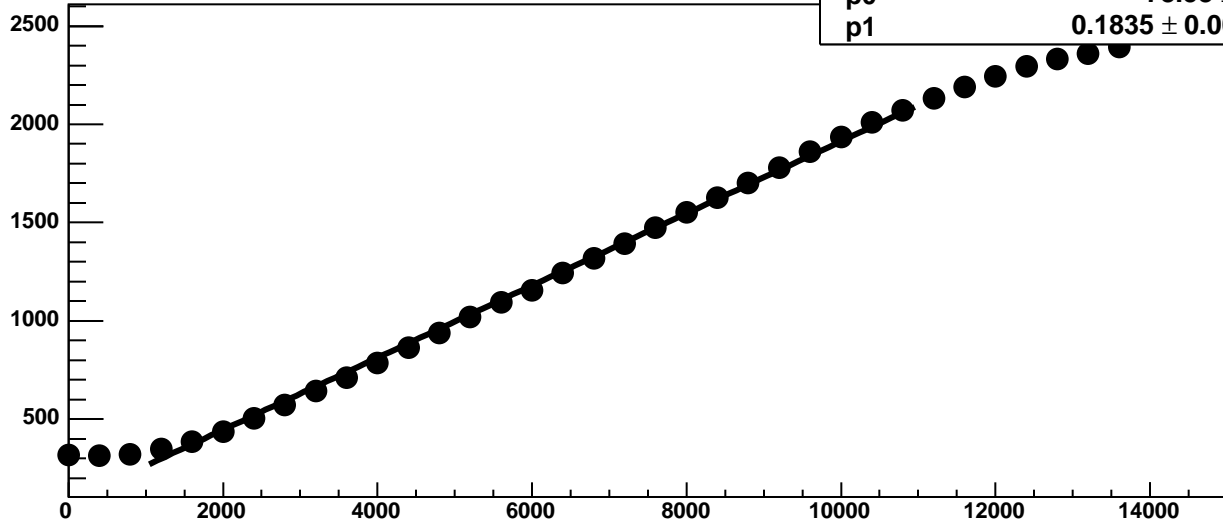
Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



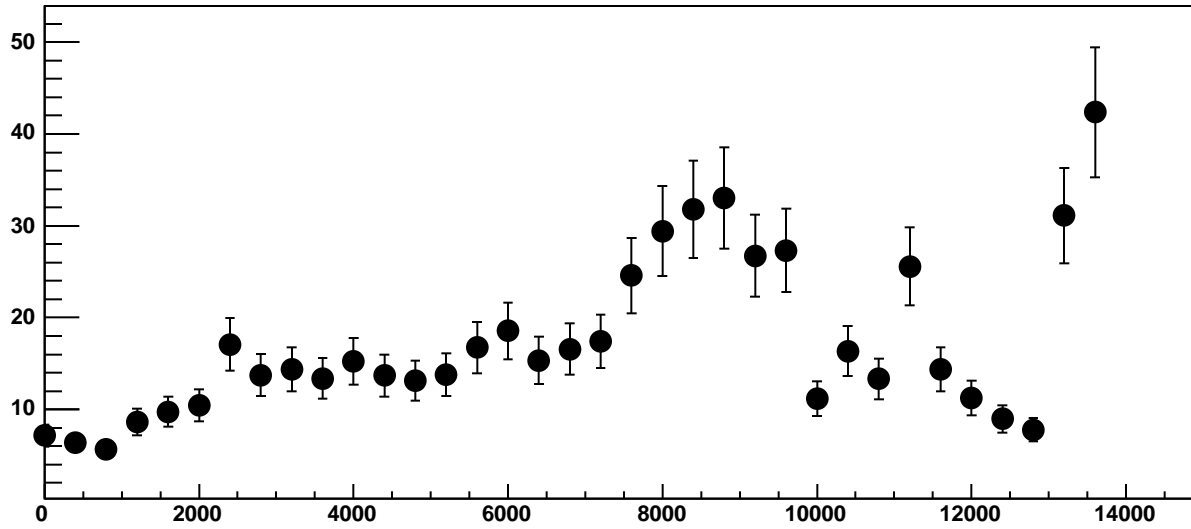
Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



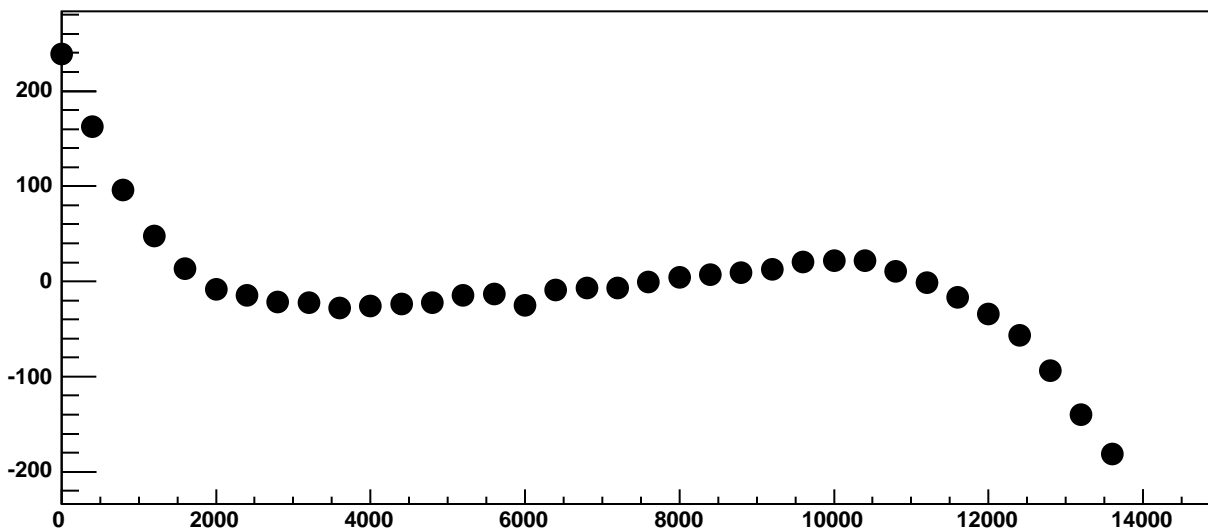
Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



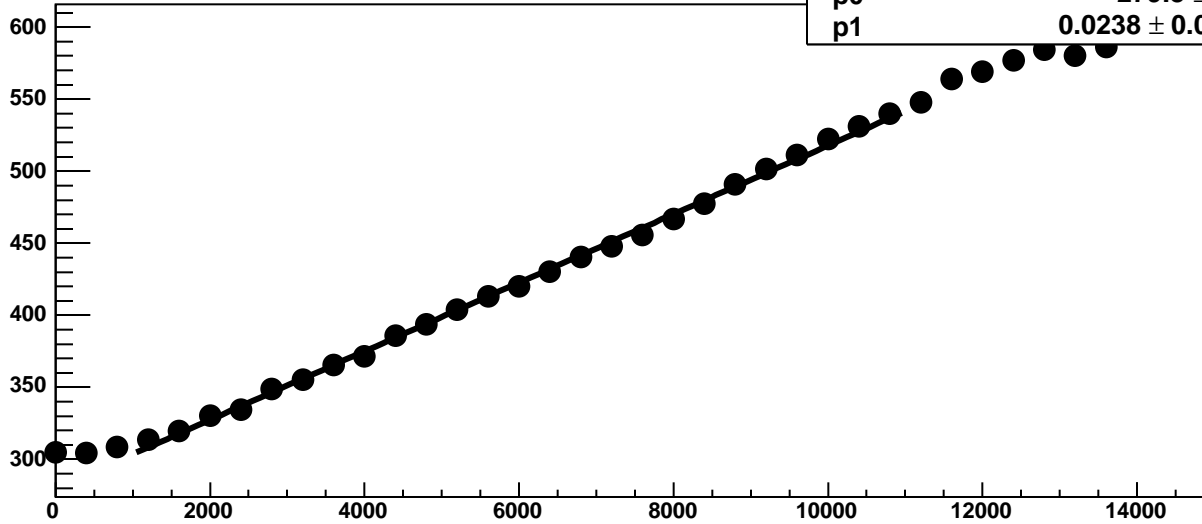
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



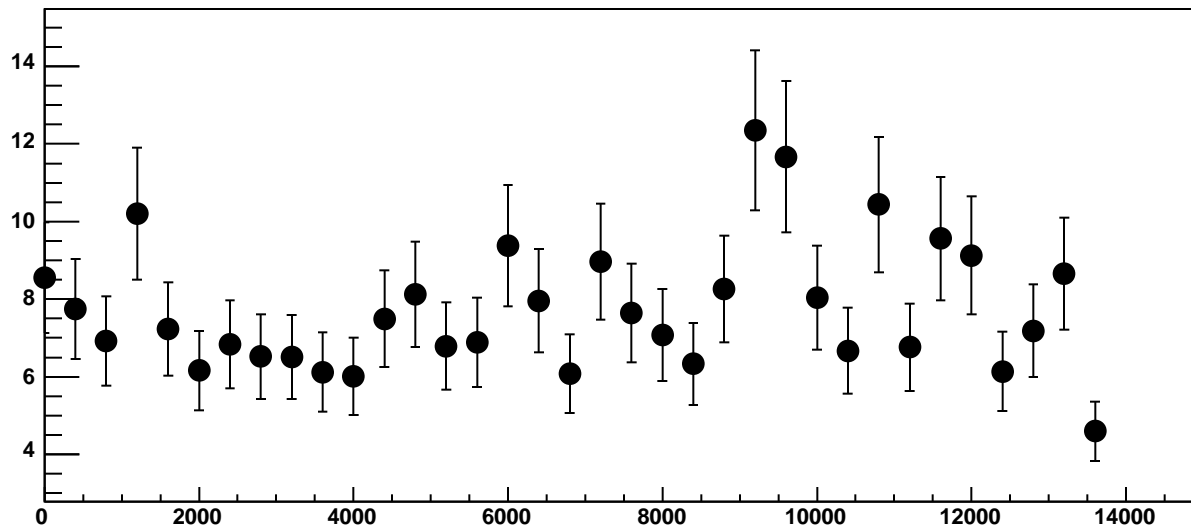
Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



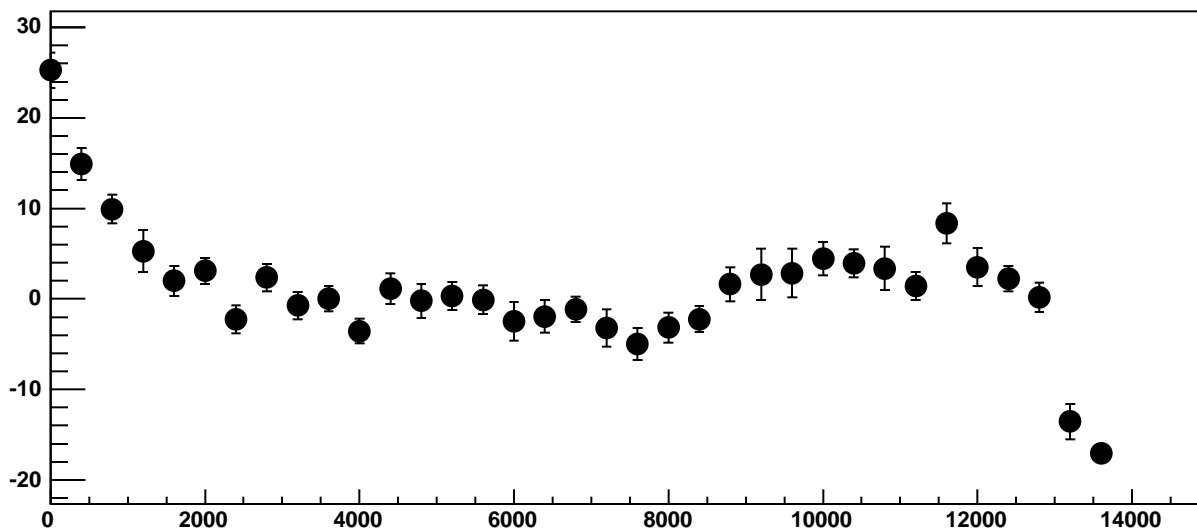
Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



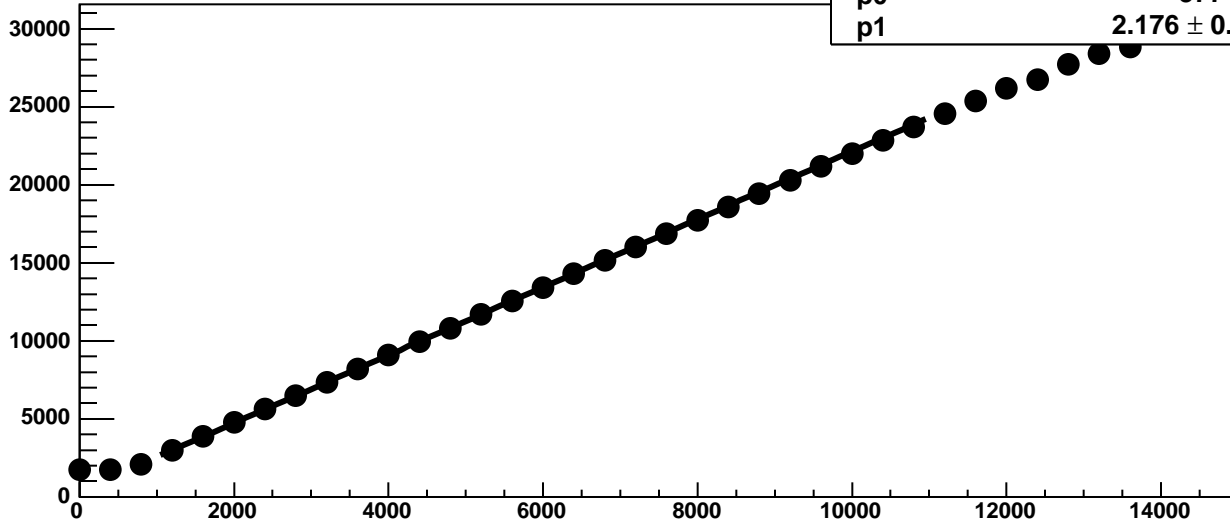
Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

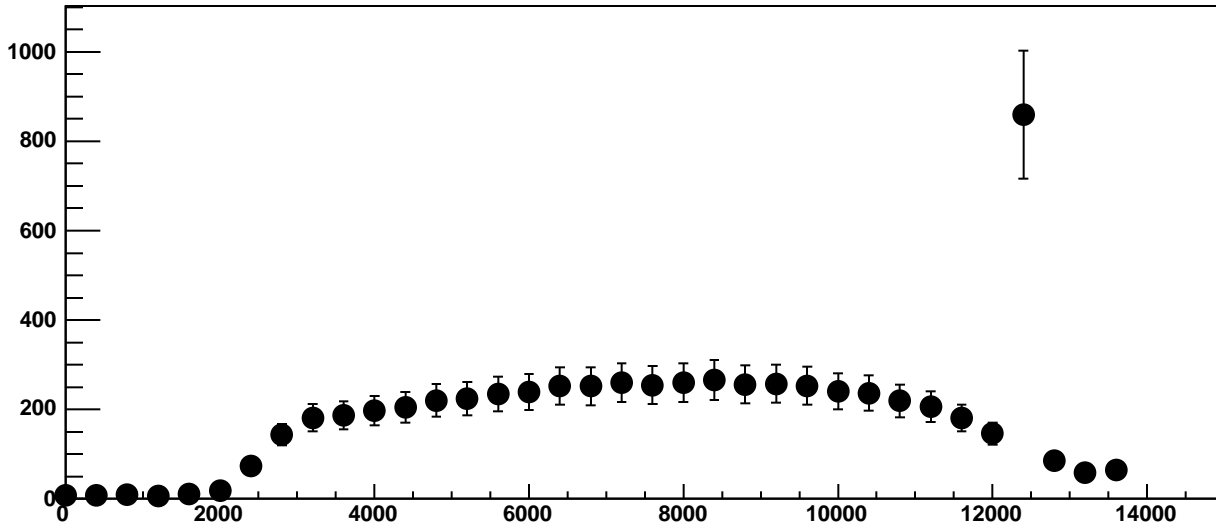


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

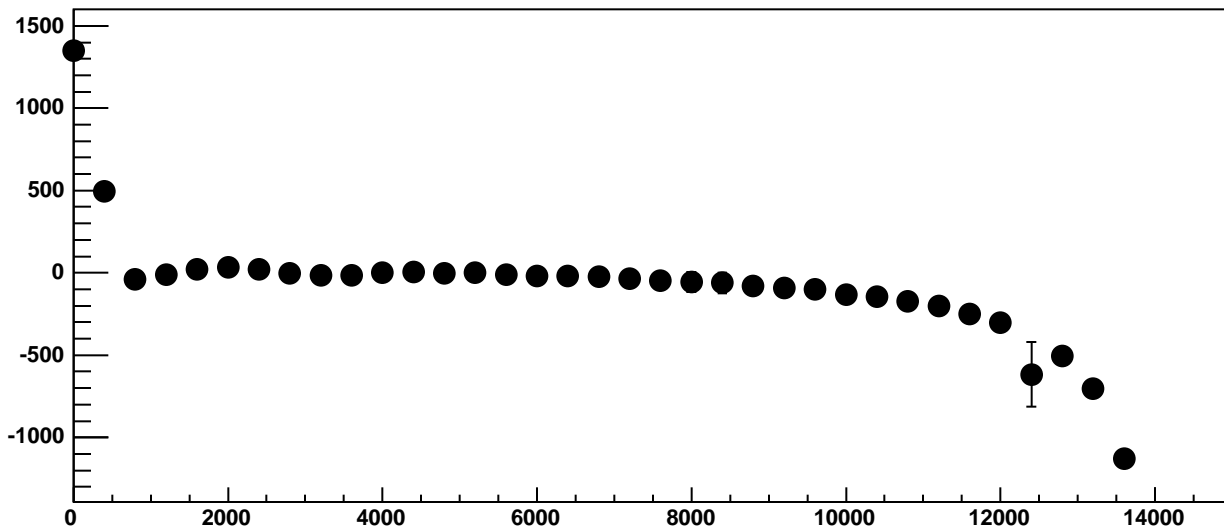


$\chi^2 / \text{ndf}$  193.6 / 23  
p0  $377 \pm 2.906$   
p1  $2.176 \pm 0.001872$

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

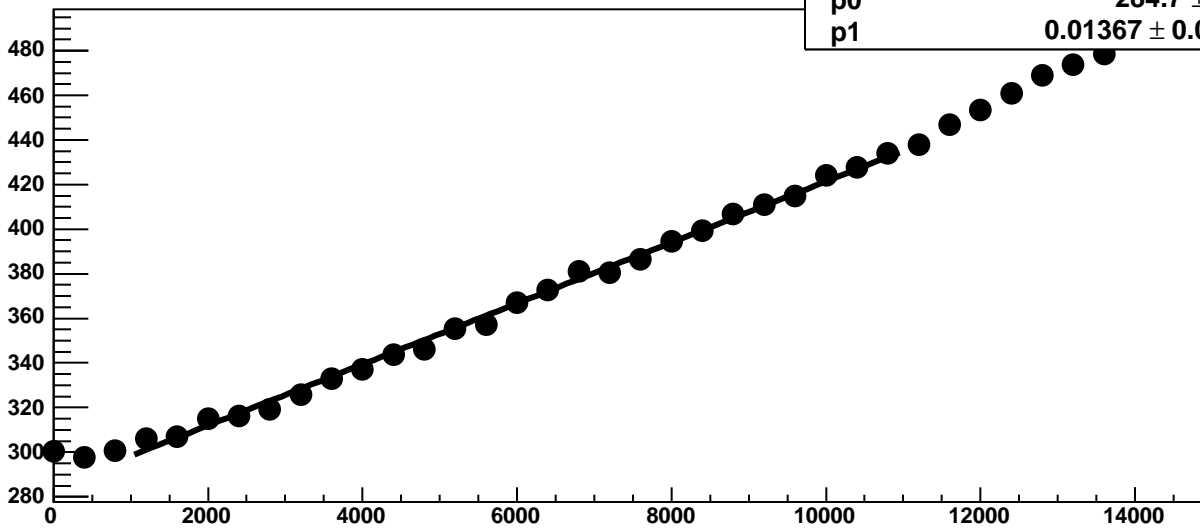


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



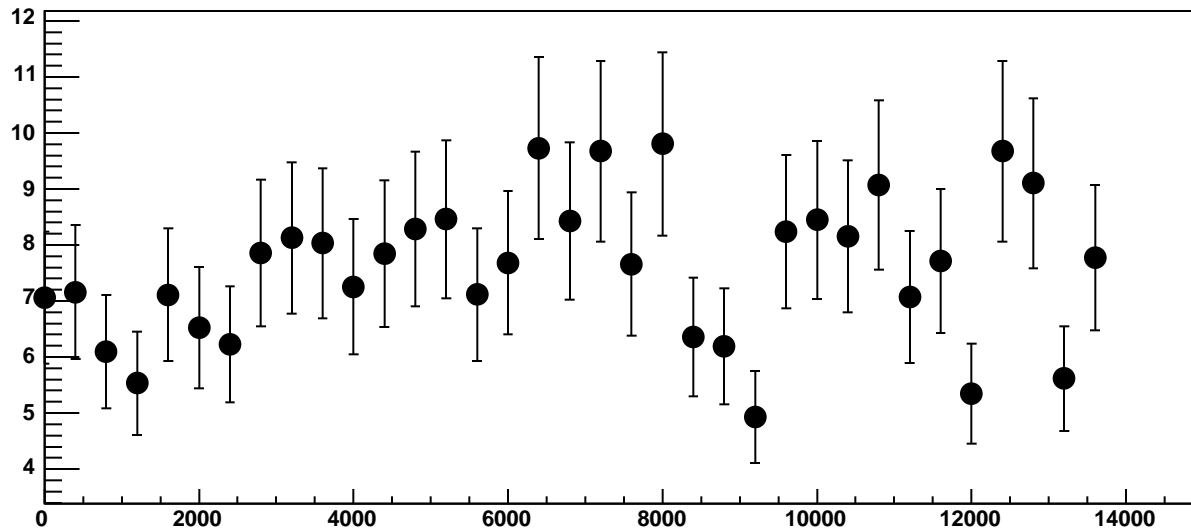


Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

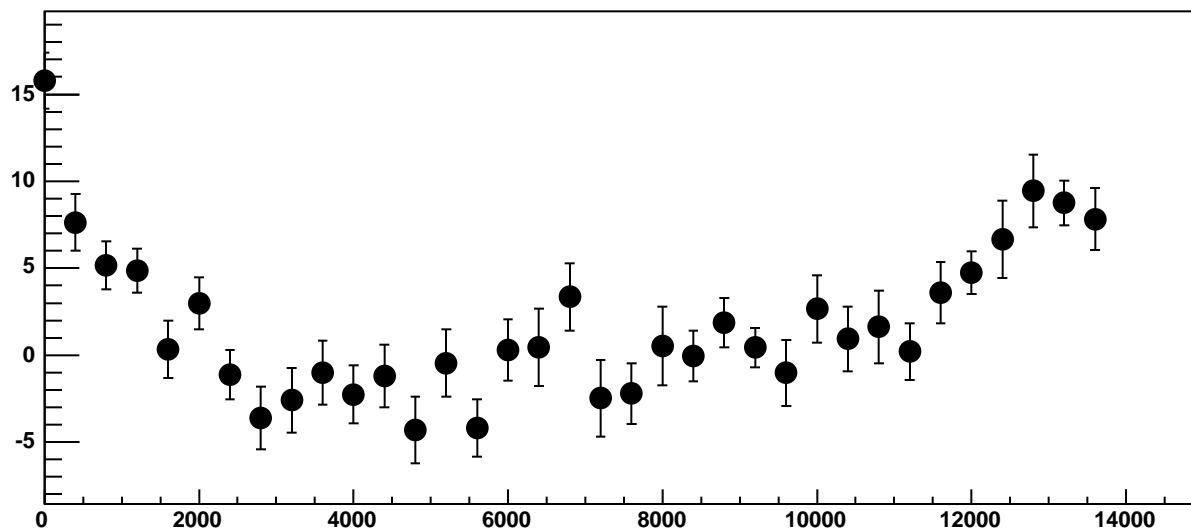


$\chi^2 / \text{ndf}$  50.39 / 23  
p0  $284.7 \pm 0.7316$   
p1  $0.01367 \pm 0.0001117$

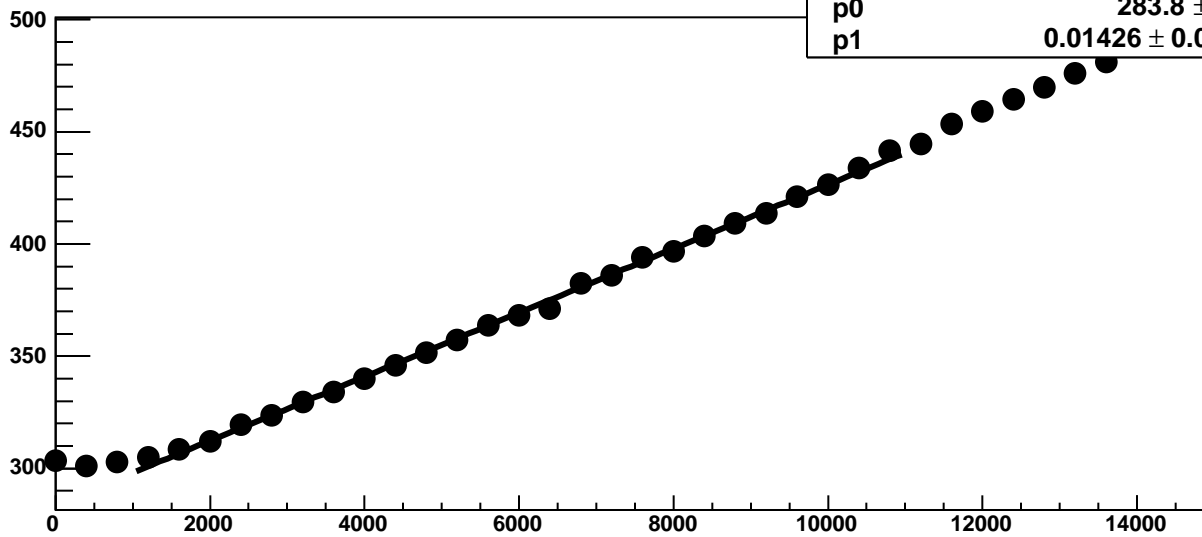
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

29.28 / 23

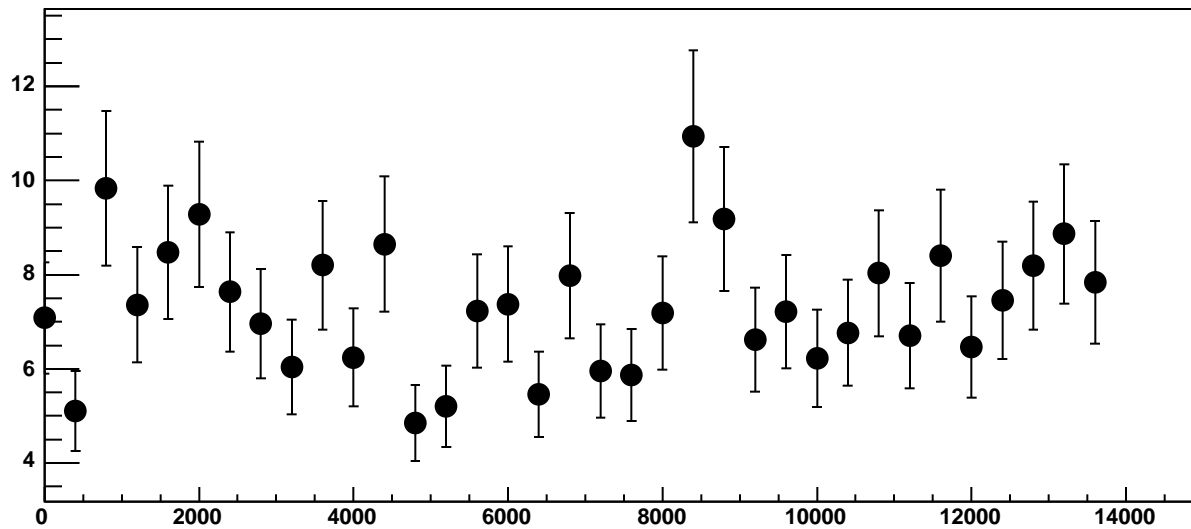
p0

$283.8 \pm 0.7712$

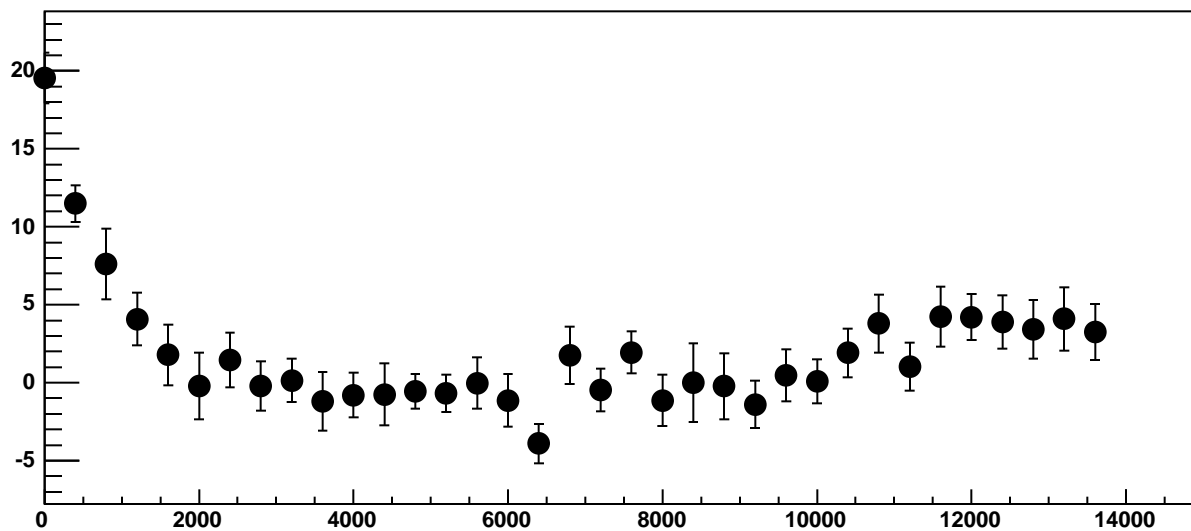
p1

$0.01426 \pm 0.0001167$

Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

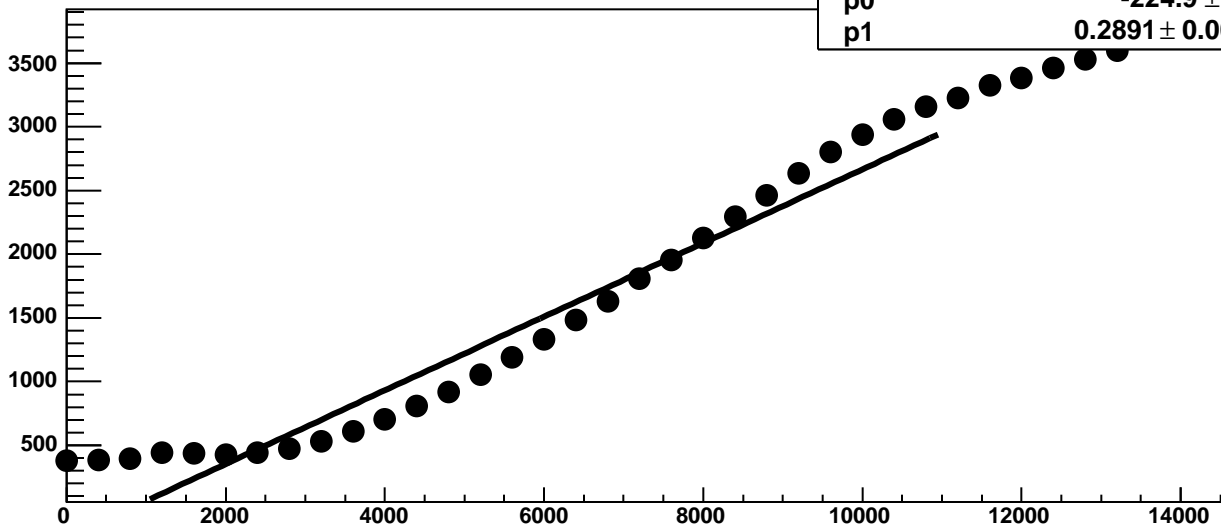


Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

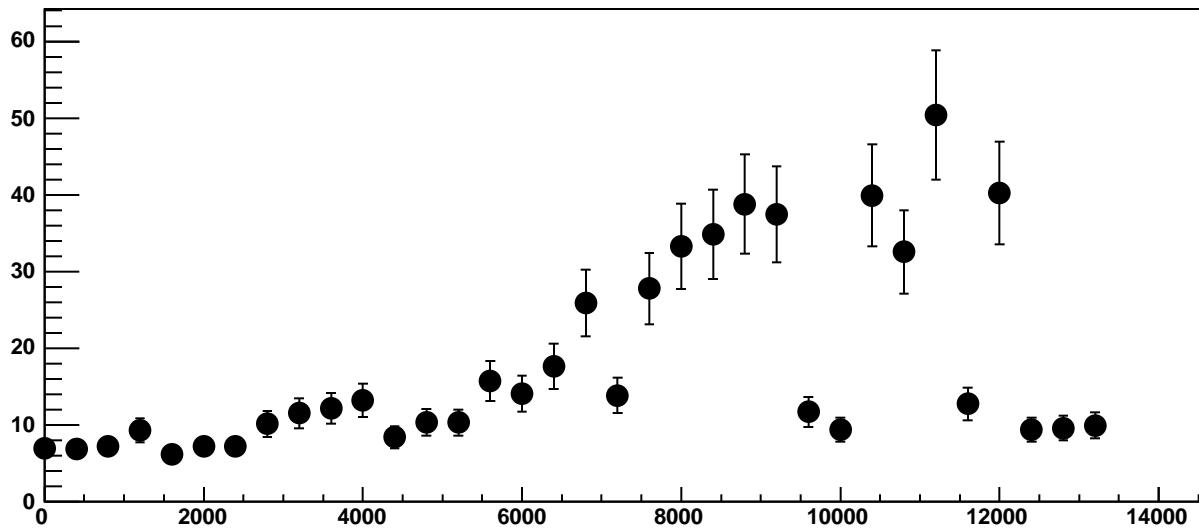


Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

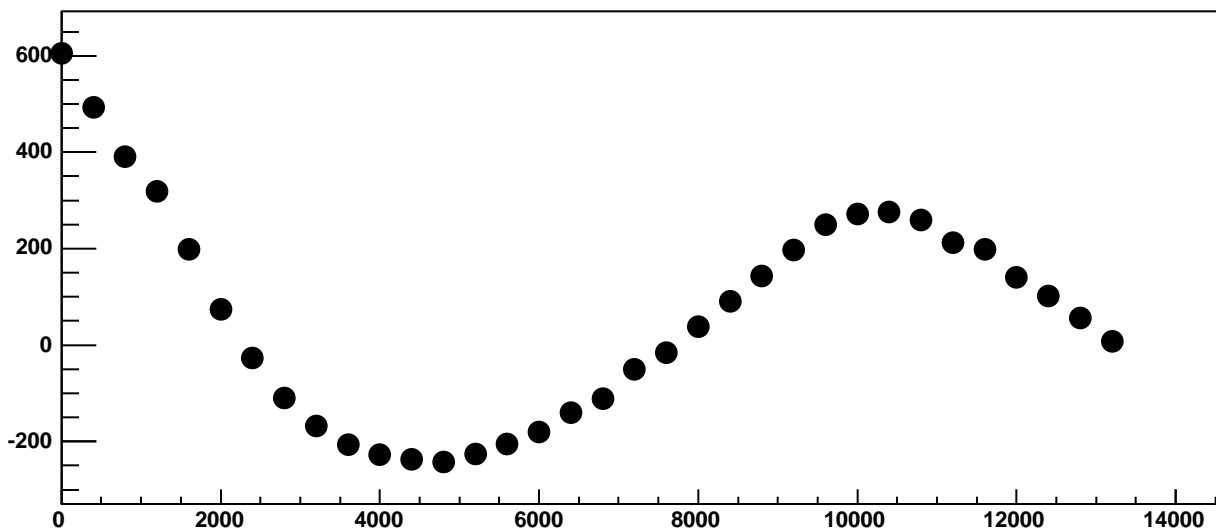
$\chi^2 / \text{ndf}$  1.312e+05 / 23  
p0 -224.9 ± 0.9909  
p1 0.2891 ± 0.0002012



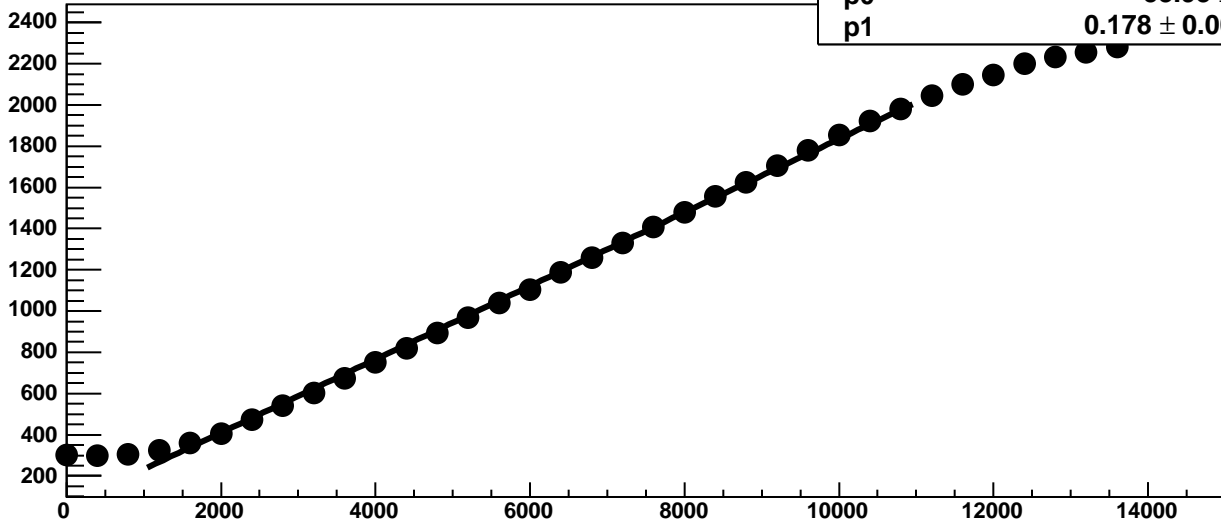
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

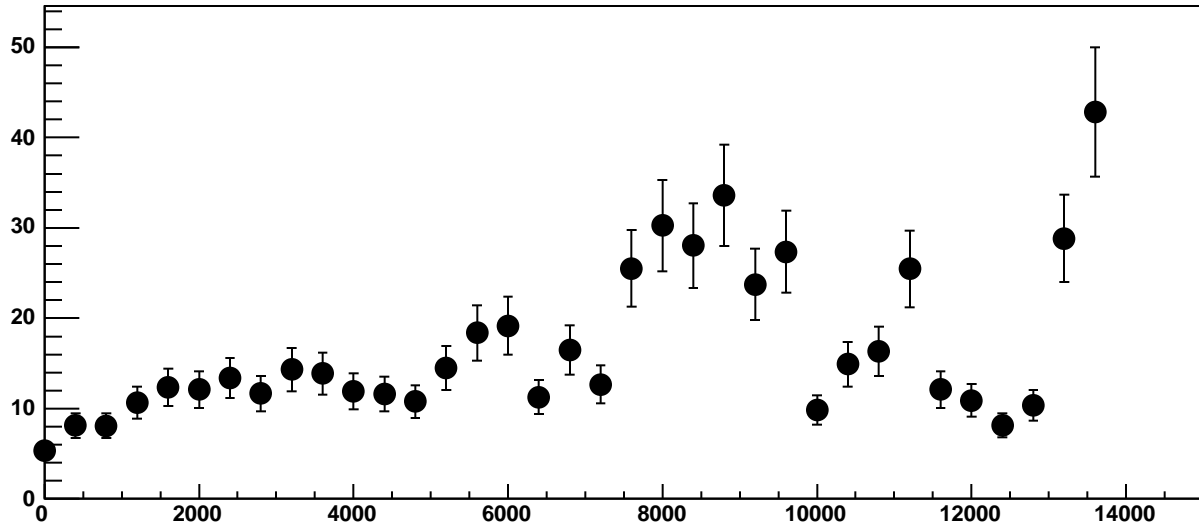


Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

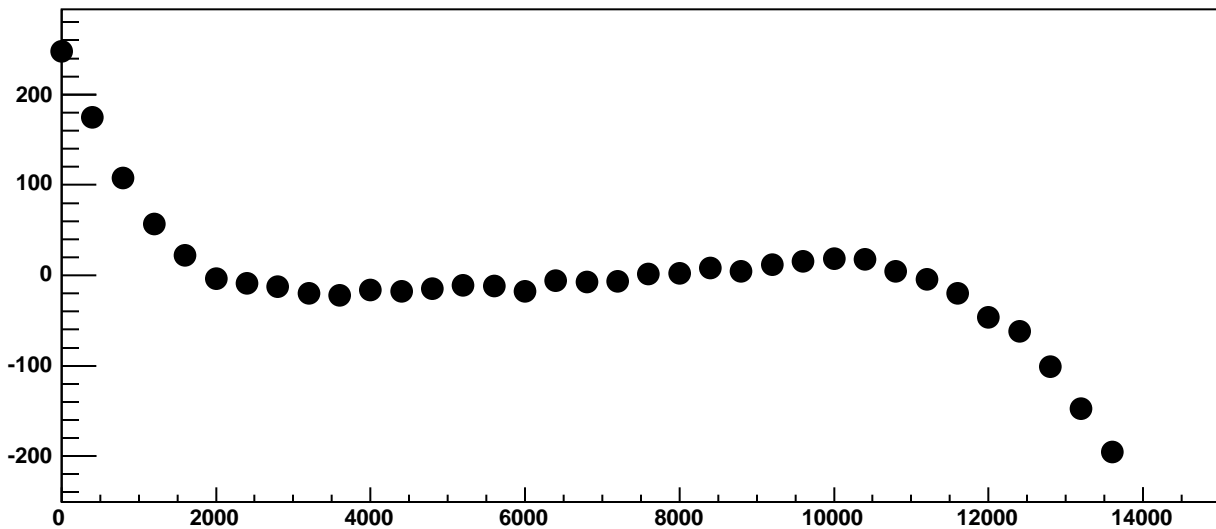


$\chi^2 / \text{ndf}$  989.2 / 23  
p0  $53.93 \pm 1.339$   
p1  $0.178 \pm 0.0002238$

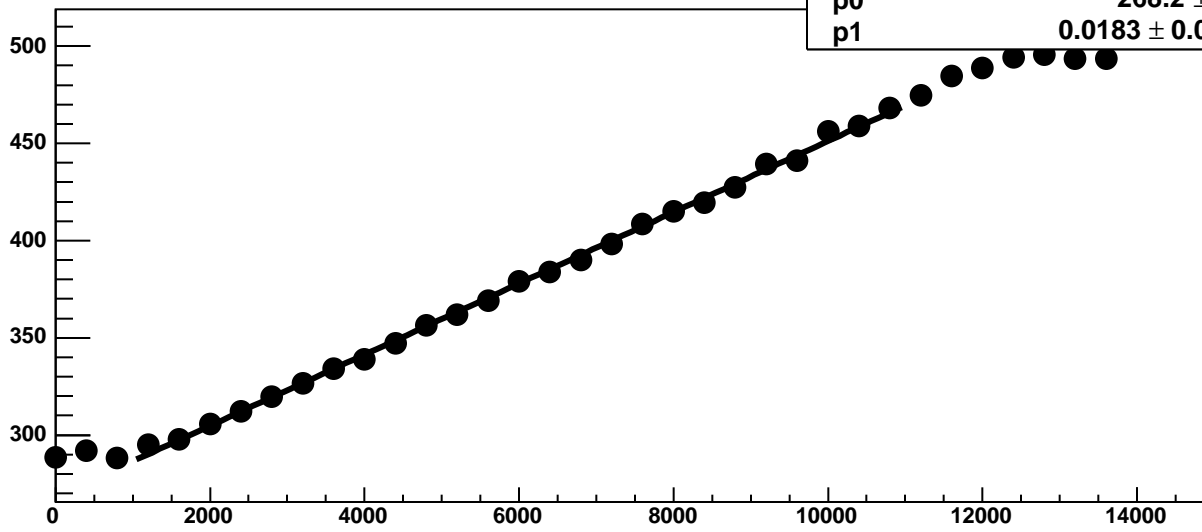
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



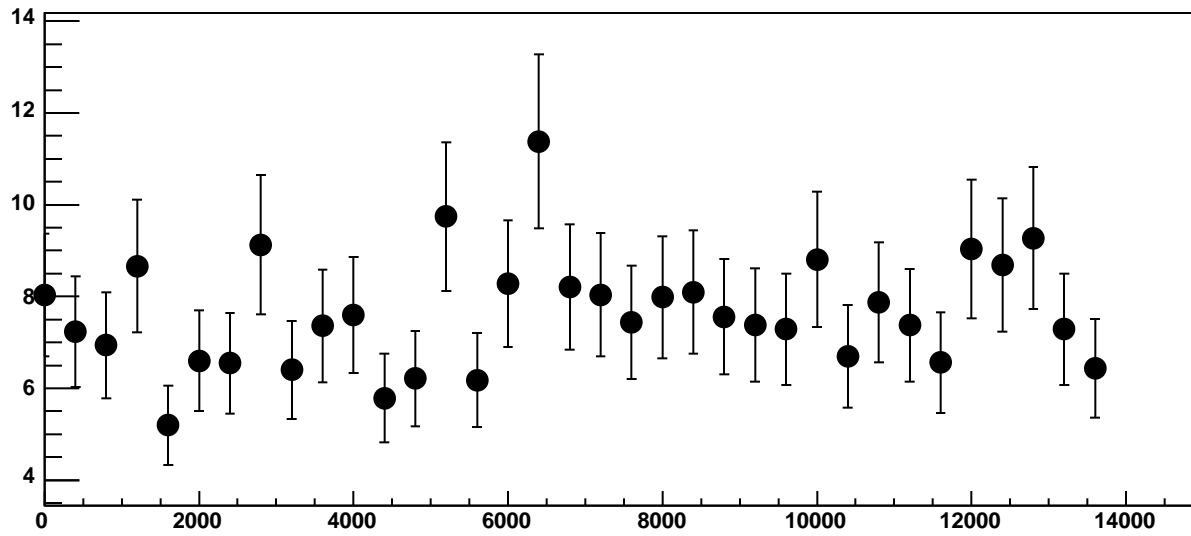
Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



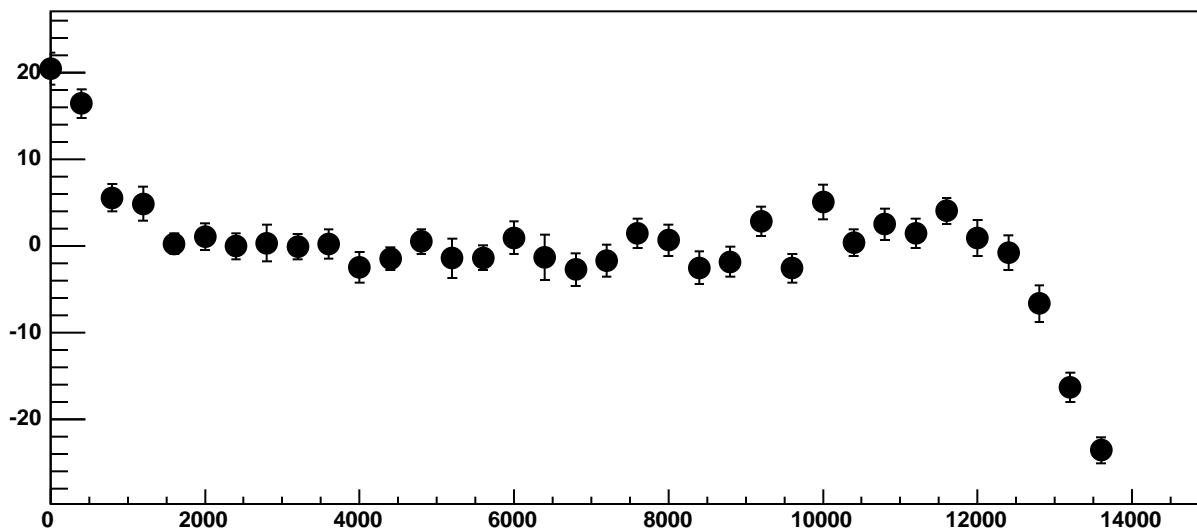
Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



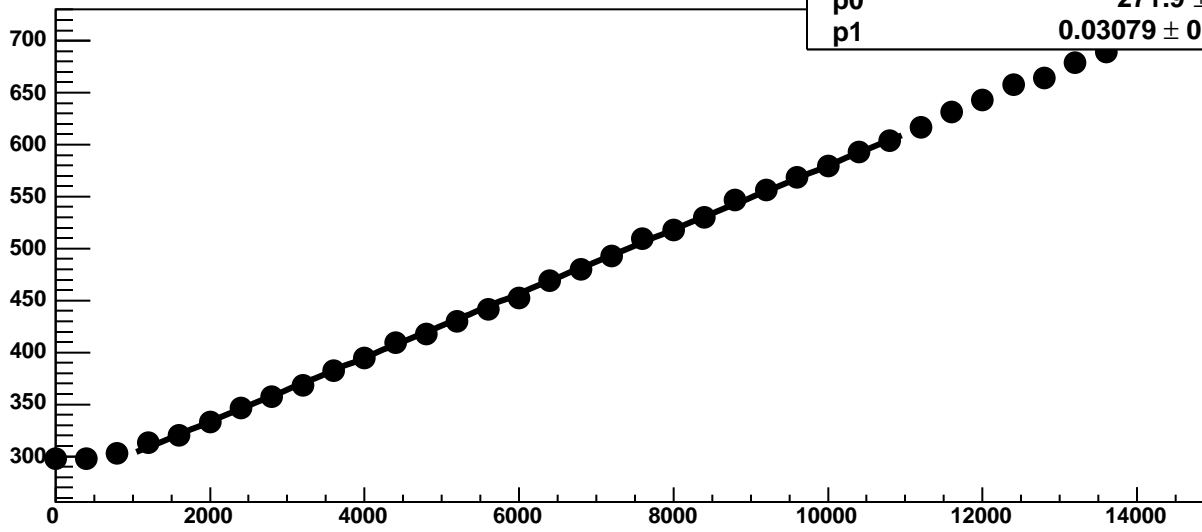
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

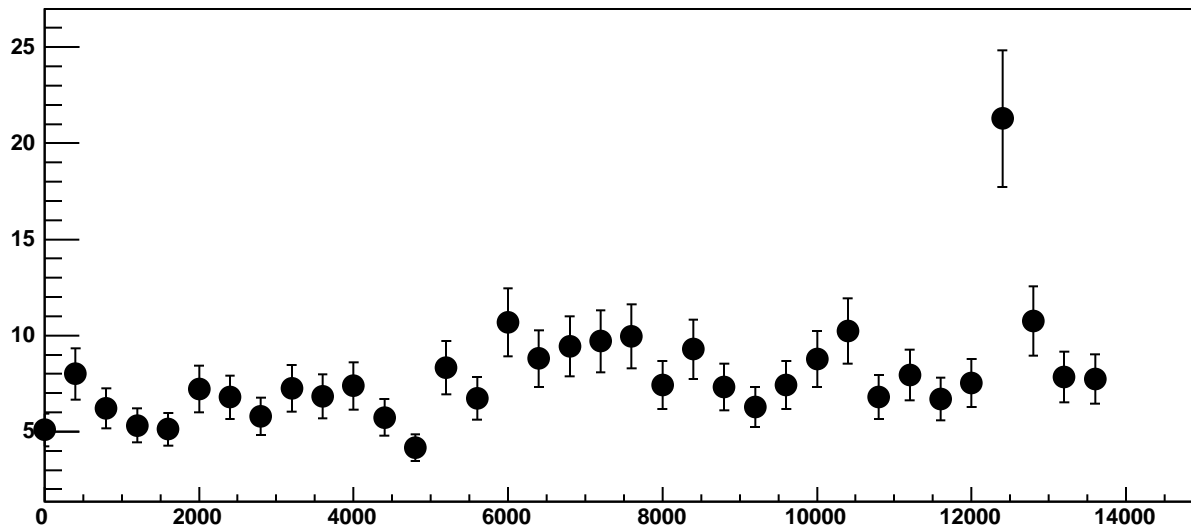


Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

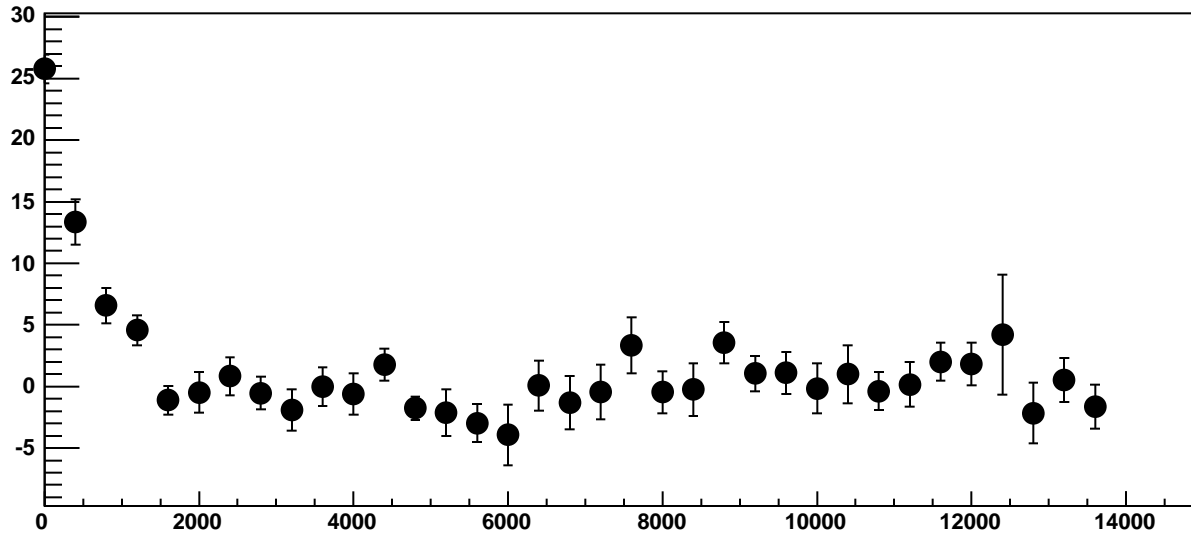


$\chi^2 / \text{ndf}$  37.9 / 23  
p0  $271.9 \pm 0.6597$   
p1  $0.03079 \pm 0.000109$

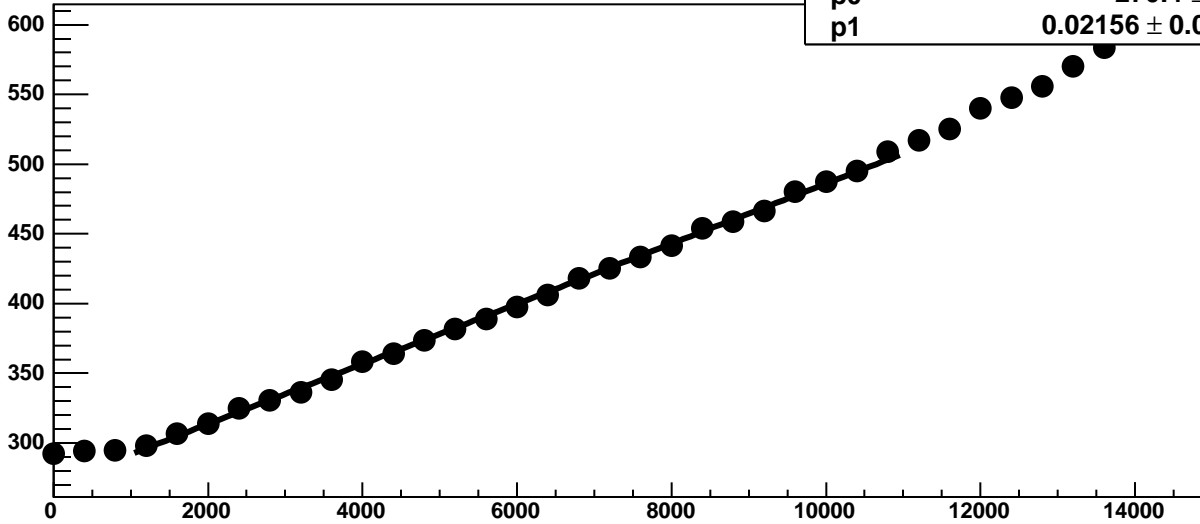
Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

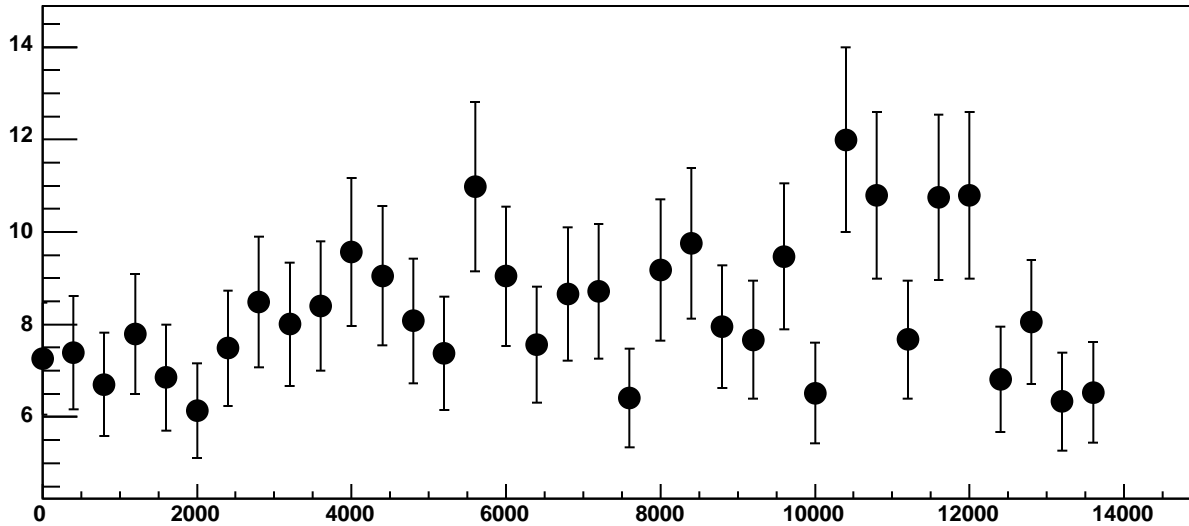


Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

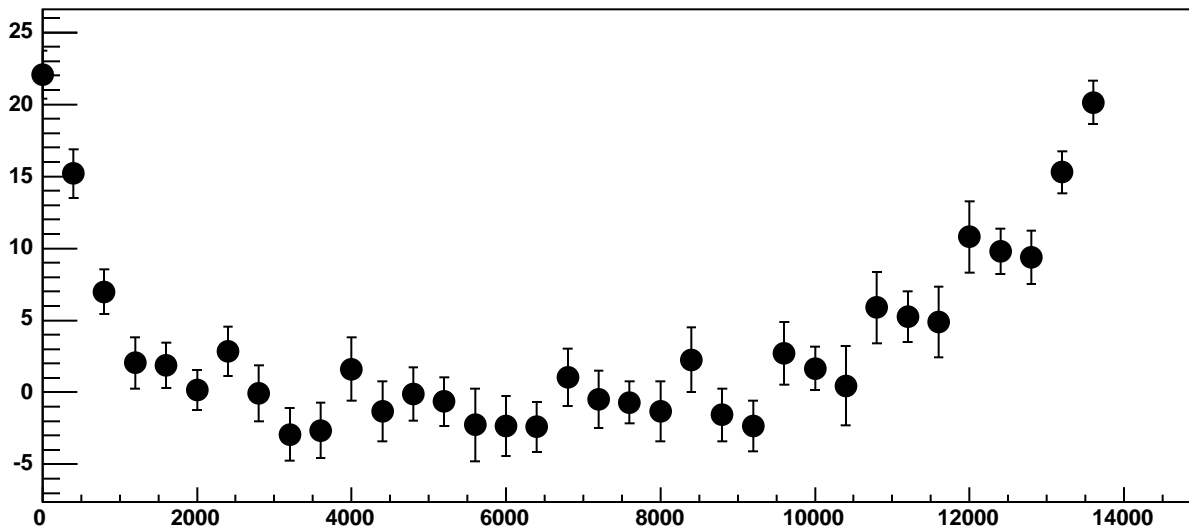


$\chi^2 / \text{ndf}$  28 / 23  
p0  $270.4 \pm 0.8178$   
p1  $0.02156 \pm 0.0001279$

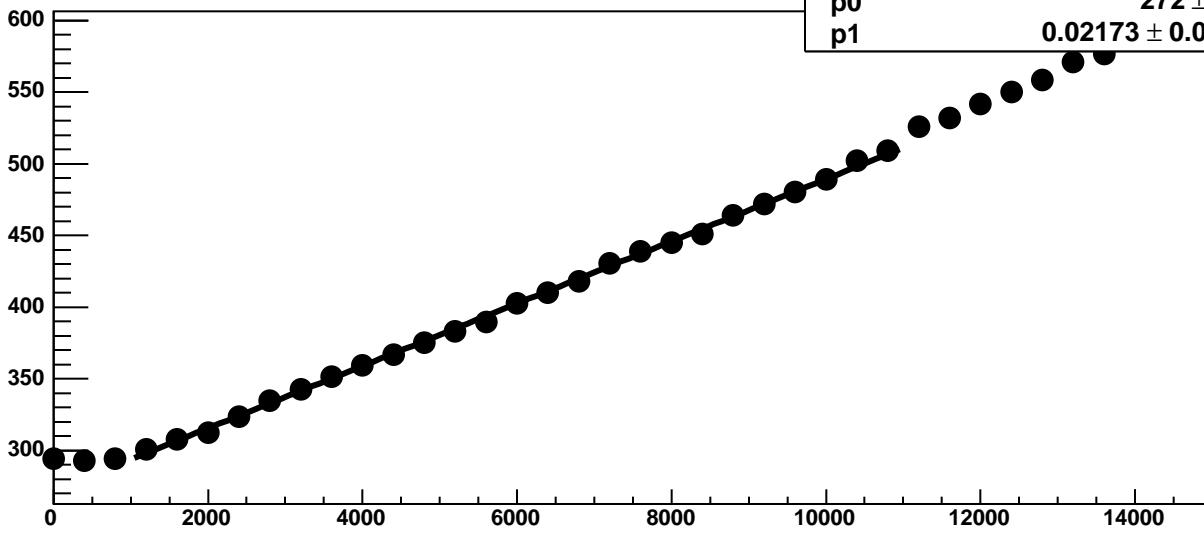
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

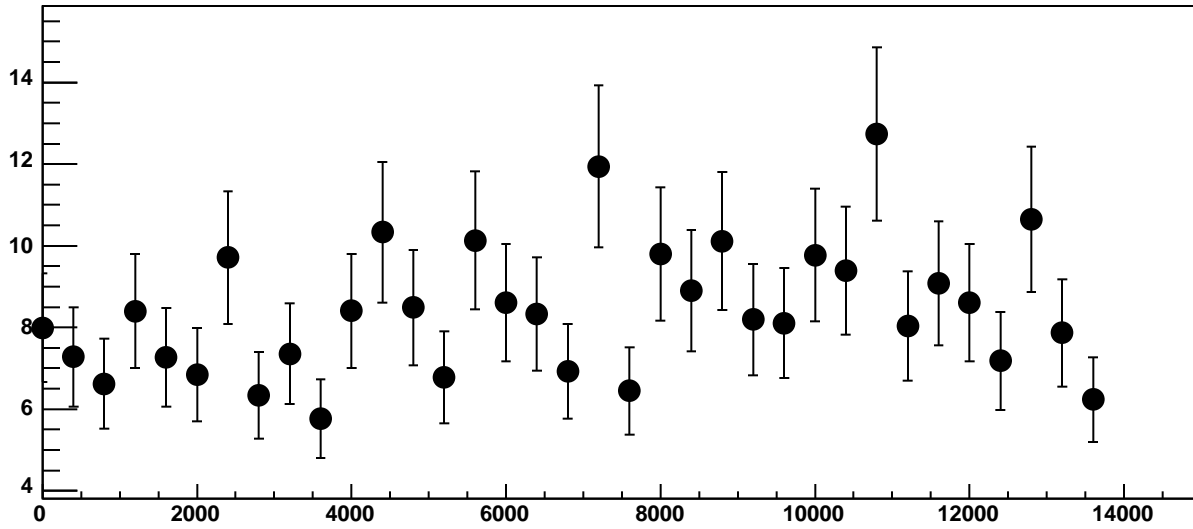


Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

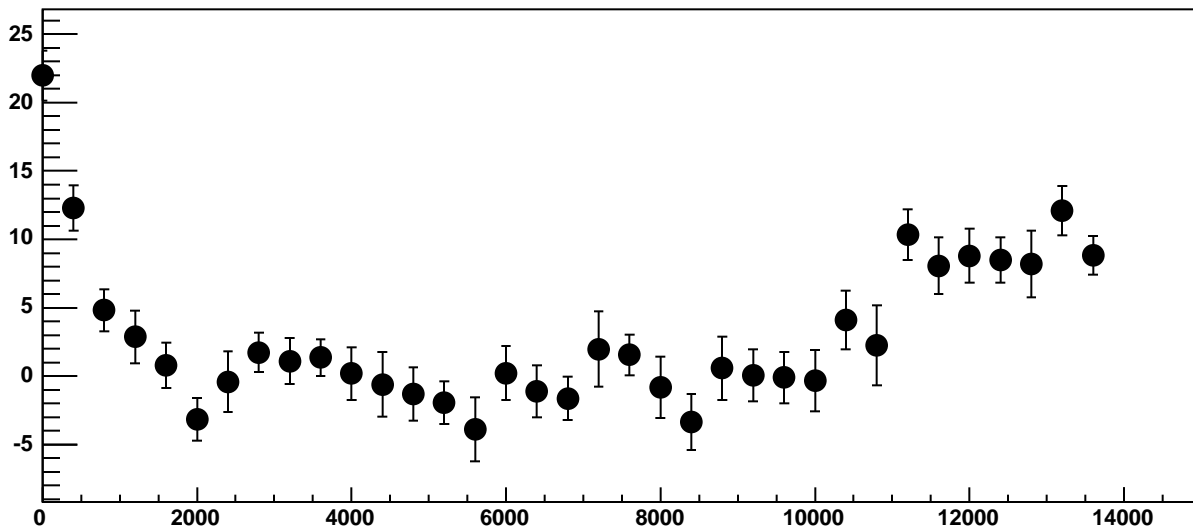


$\chi^2 / \text{ndf}$  24.41 / 23  
p0  $272 \pm 0.8272$   
p1  $0.02173 \pm 0.0001342$

Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

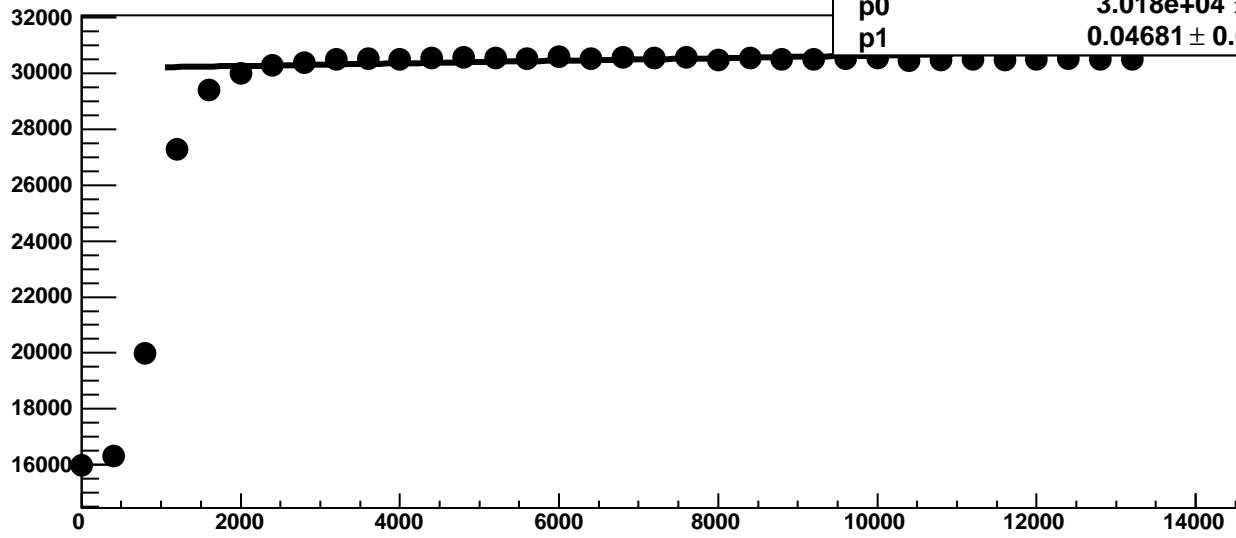


Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

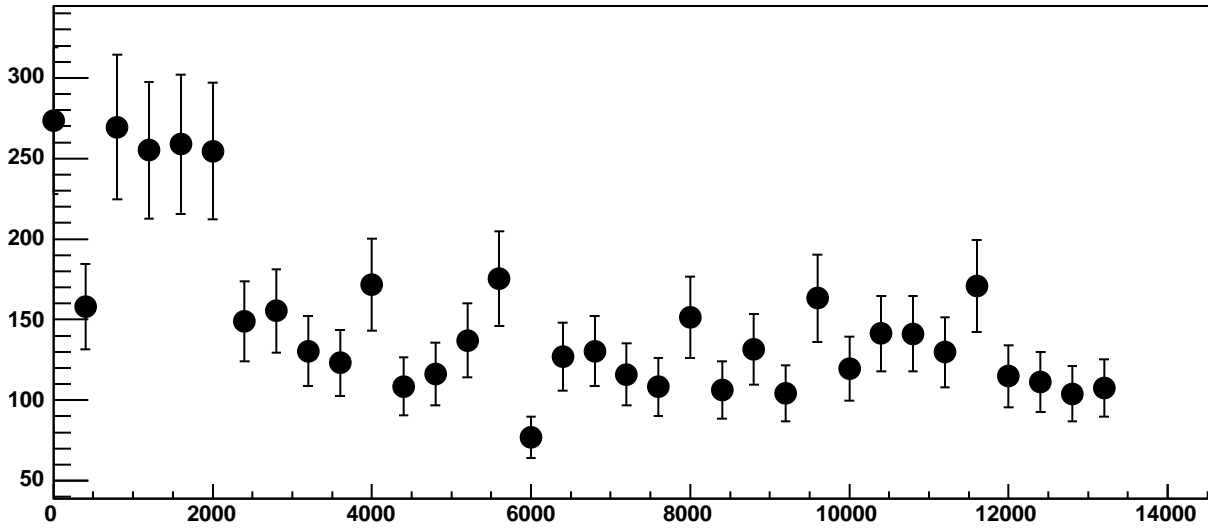




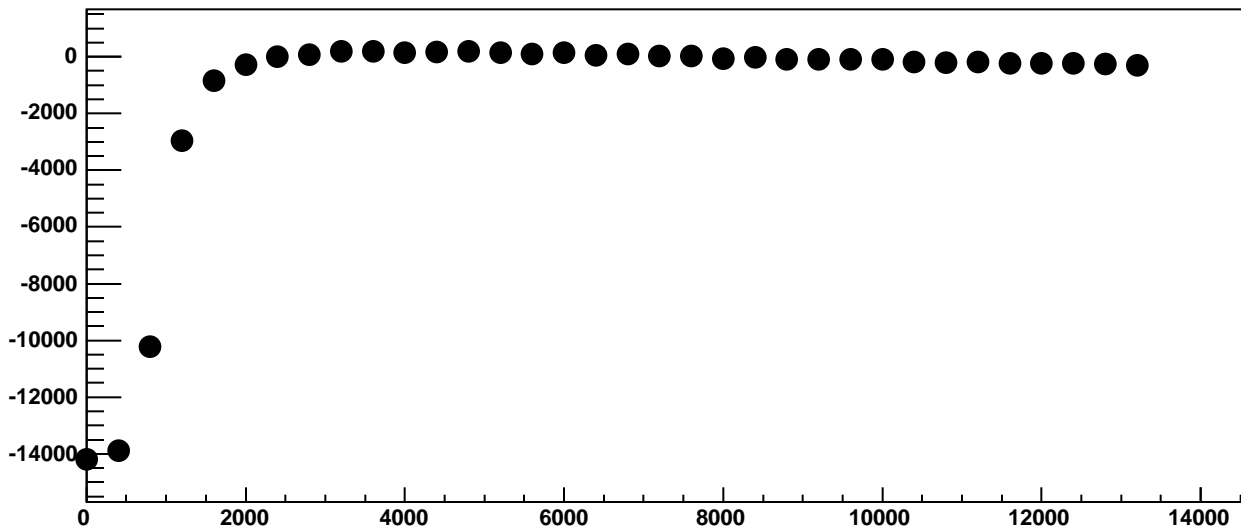
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



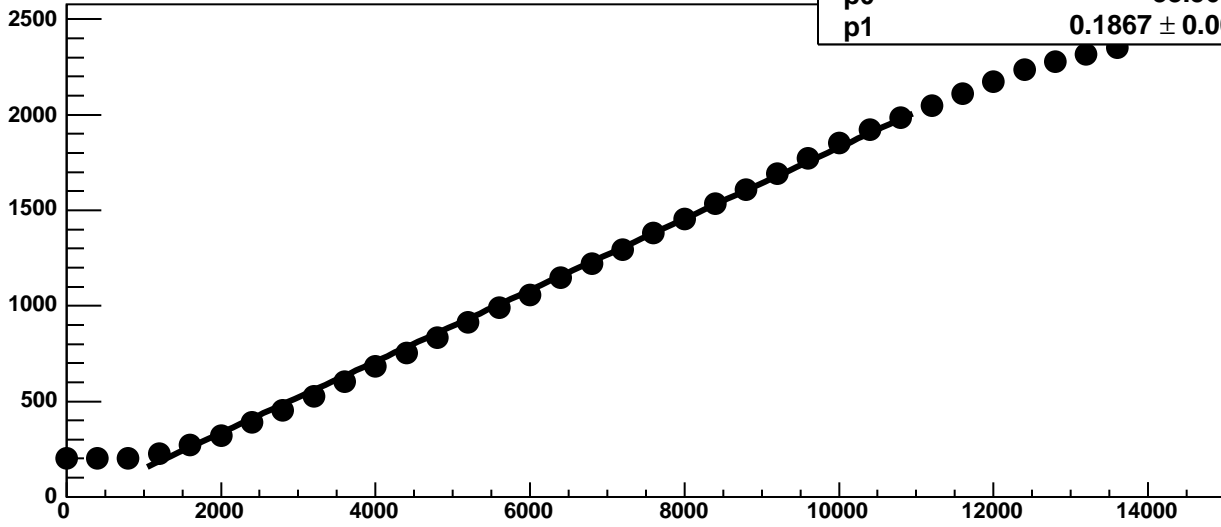
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



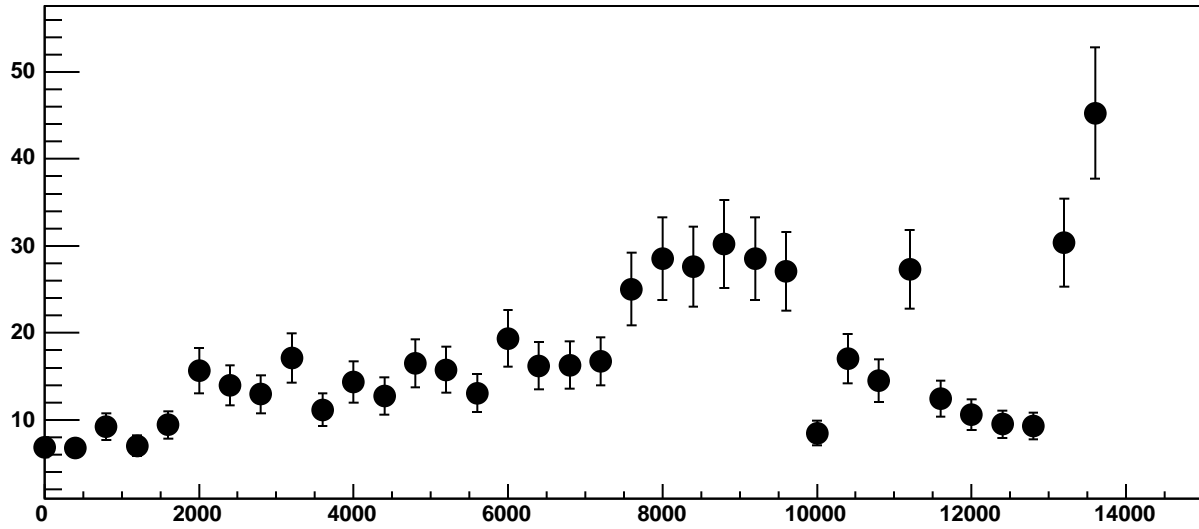
Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC



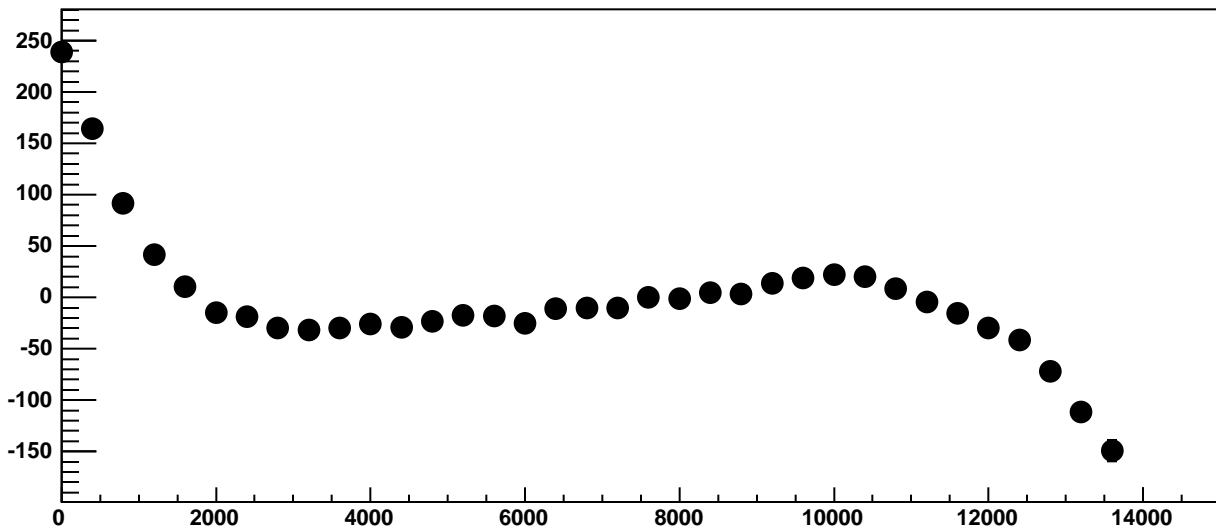
Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



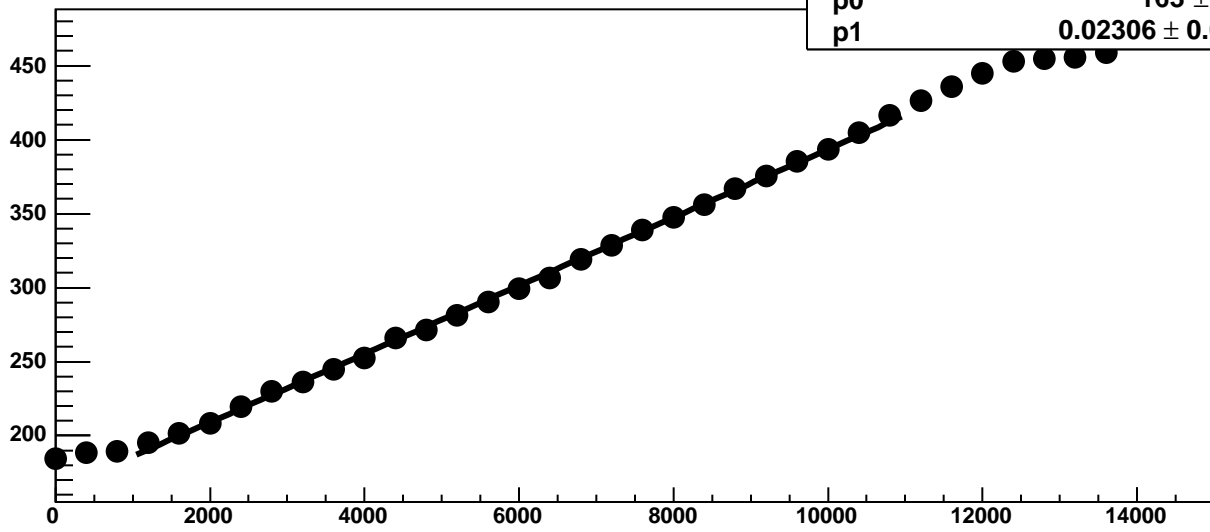
Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

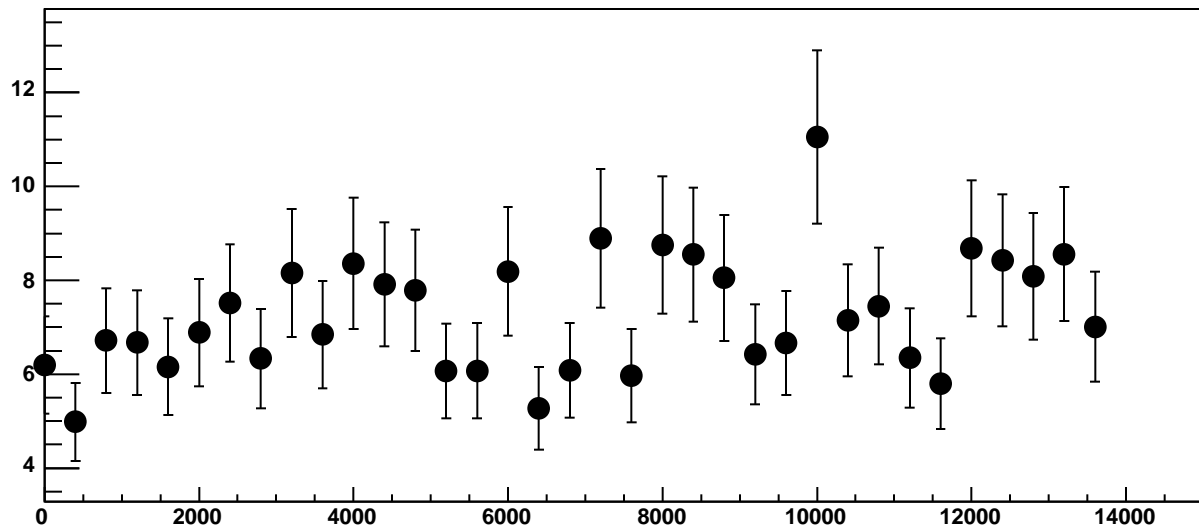


Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

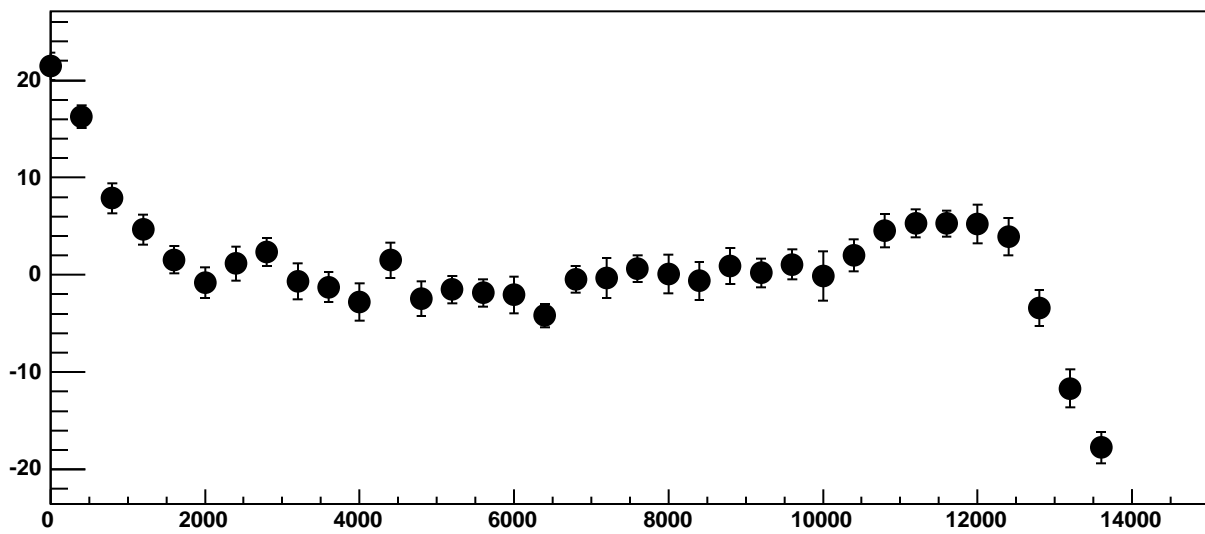


$\chi^2 / \text{ndf}$  45.27 / 23  
p0  $163 \pm 0.7385$   
p1  $0.02306 \pm 0.000114$

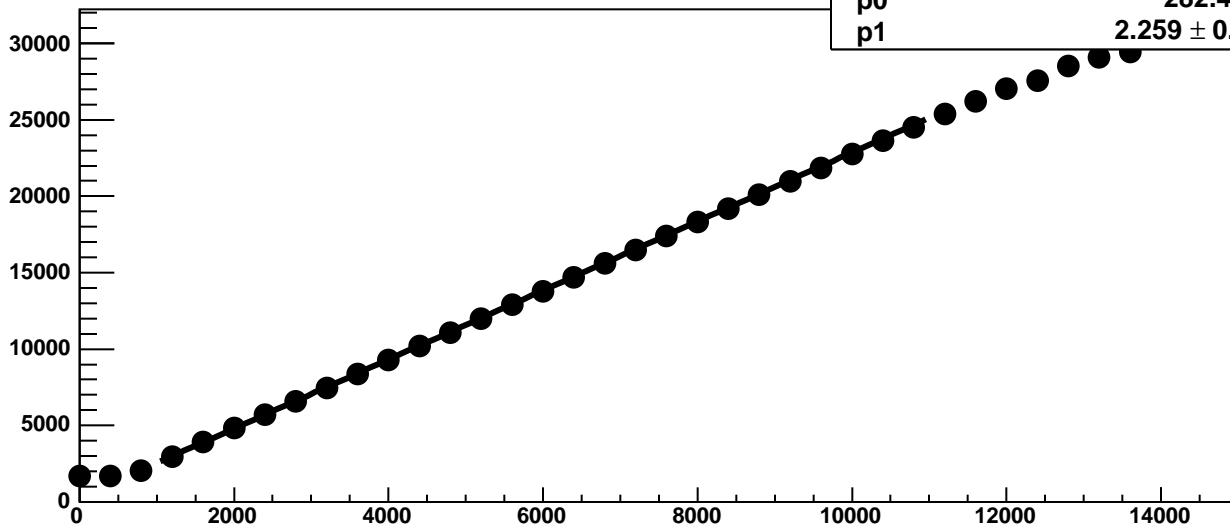
Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

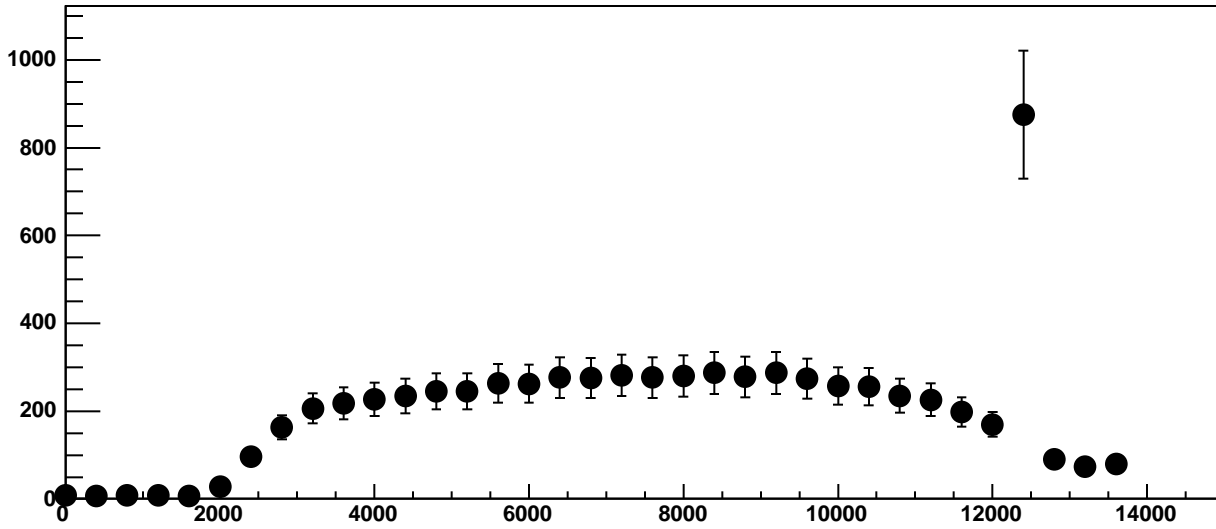


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

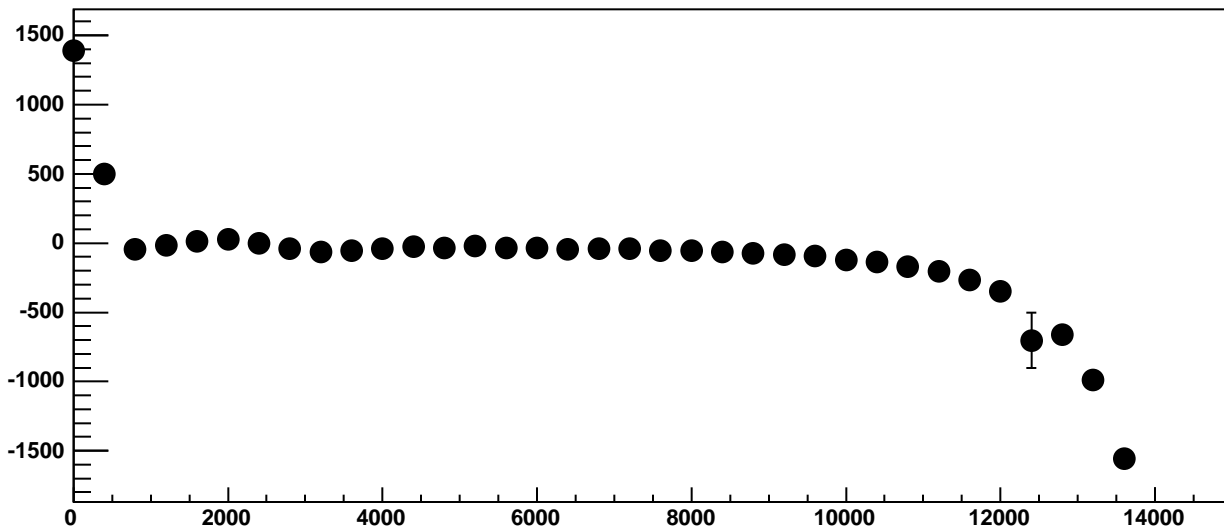


$\chi^2 / \text{ndf}$  216.6 / 23  
p0 282.4 ± 3.26  
p1 2.259 ± 0.002061

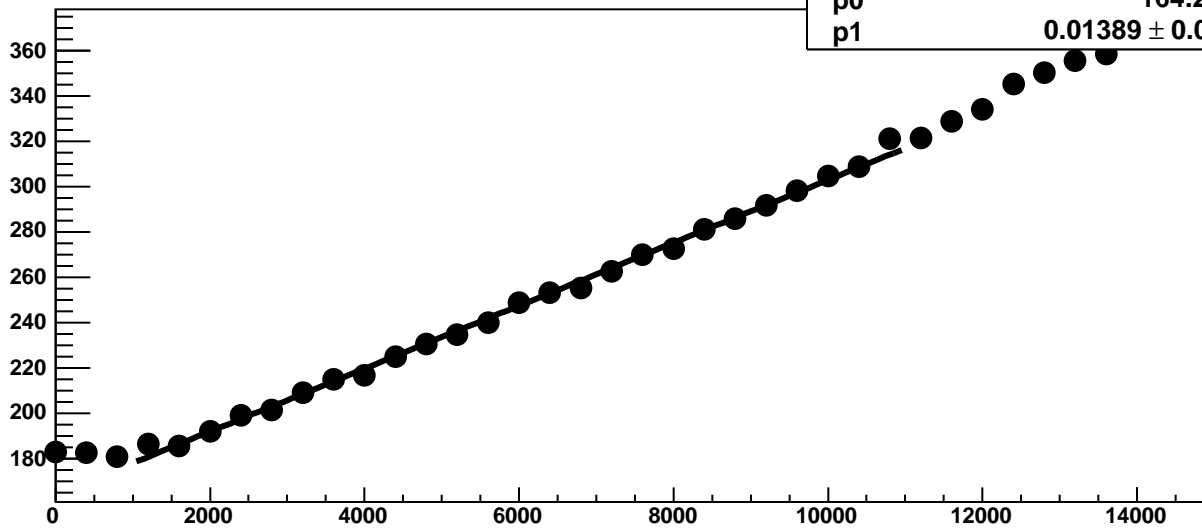
Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

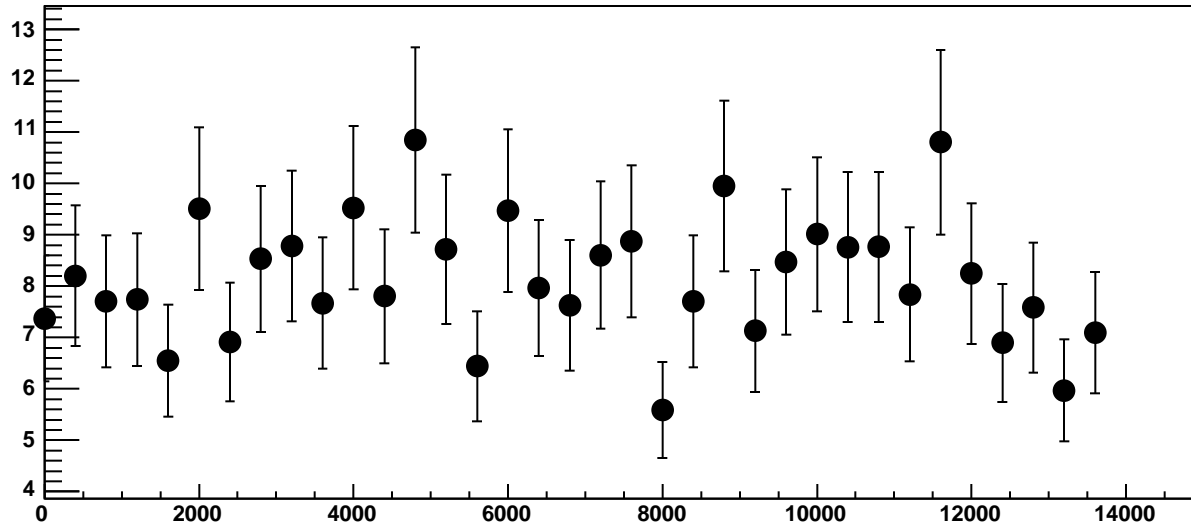


Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

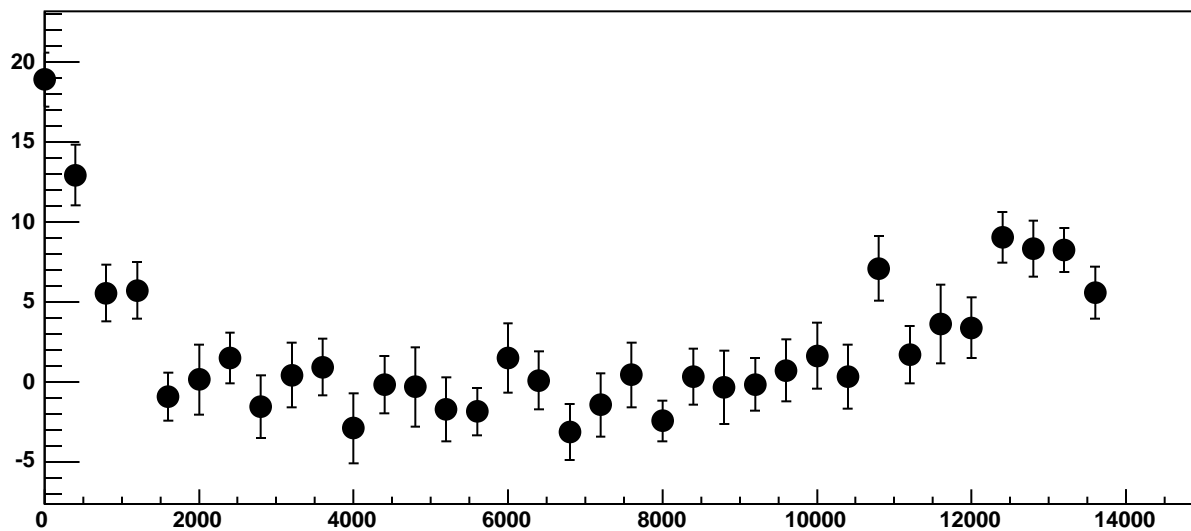


$\chi^2 / \text{ndf}$  37.88 / 23  
p0  $164.2 \pm 0.84$   
p1  $0.01389 \pm 0.0001276$

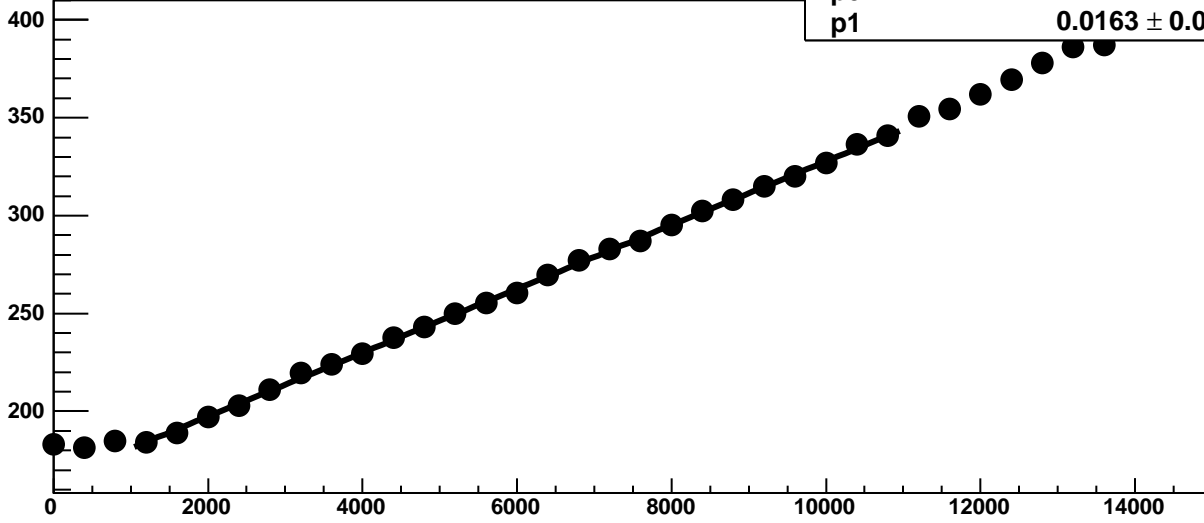
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

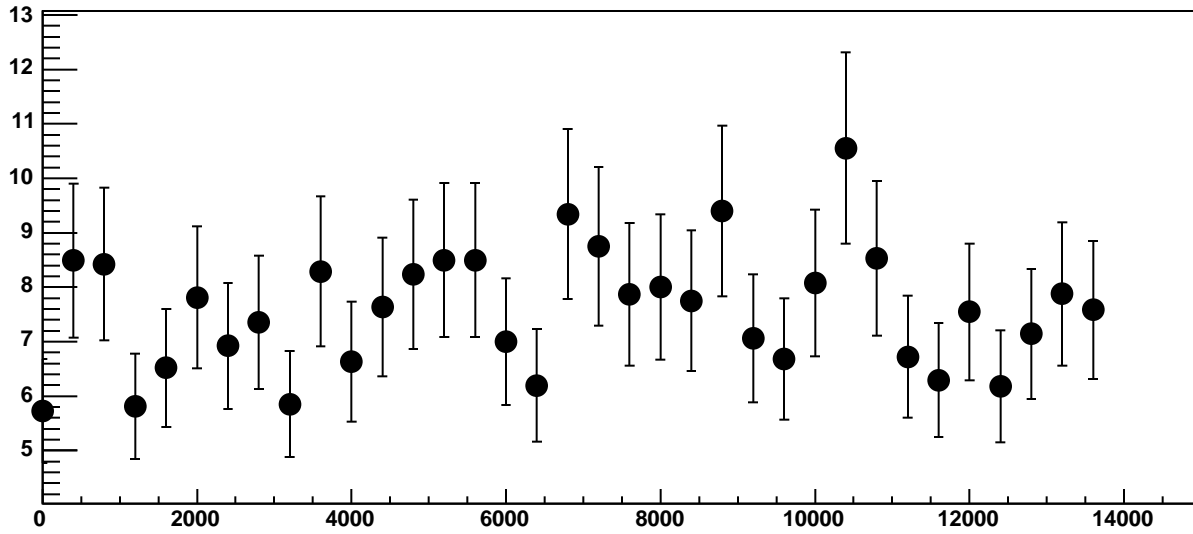


Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

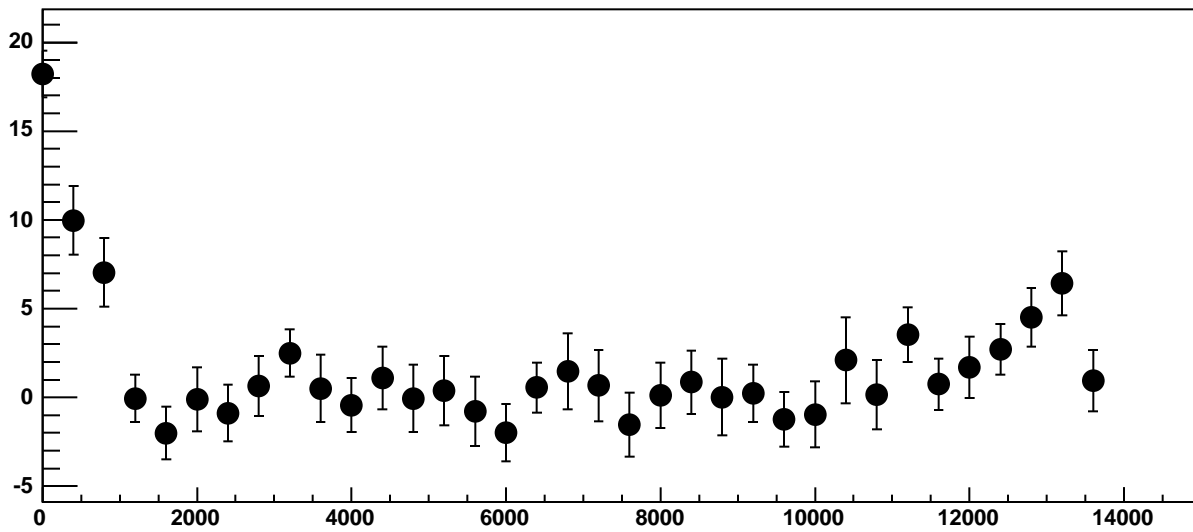


$\chi^2 / \text{ndf}$  11.36 / 23  
p0  $164.8 \pm 0.739$   
p1  $0.0163 \pm 0.0001176$

Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

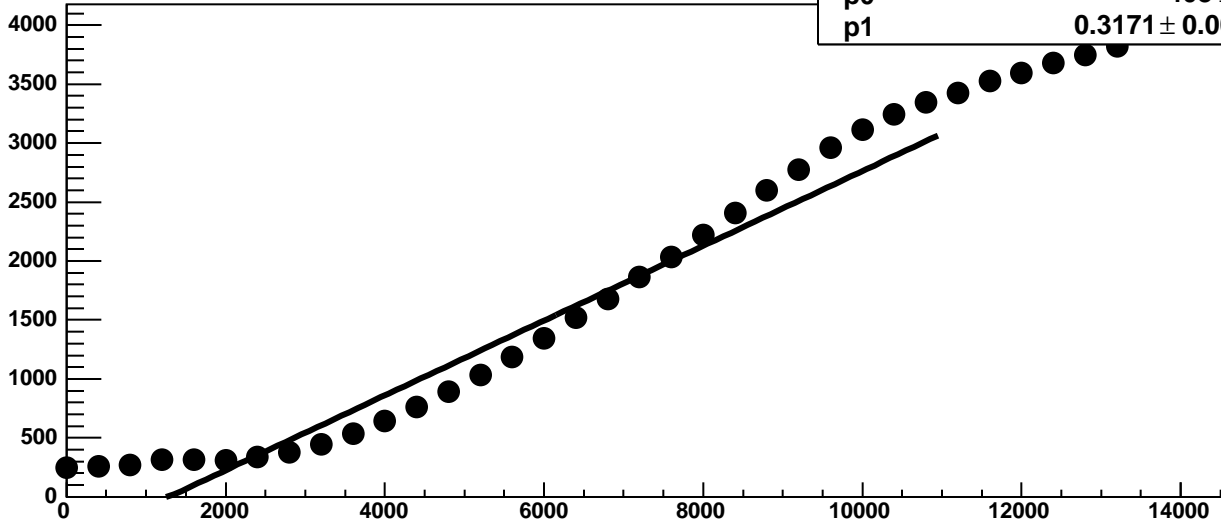


Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

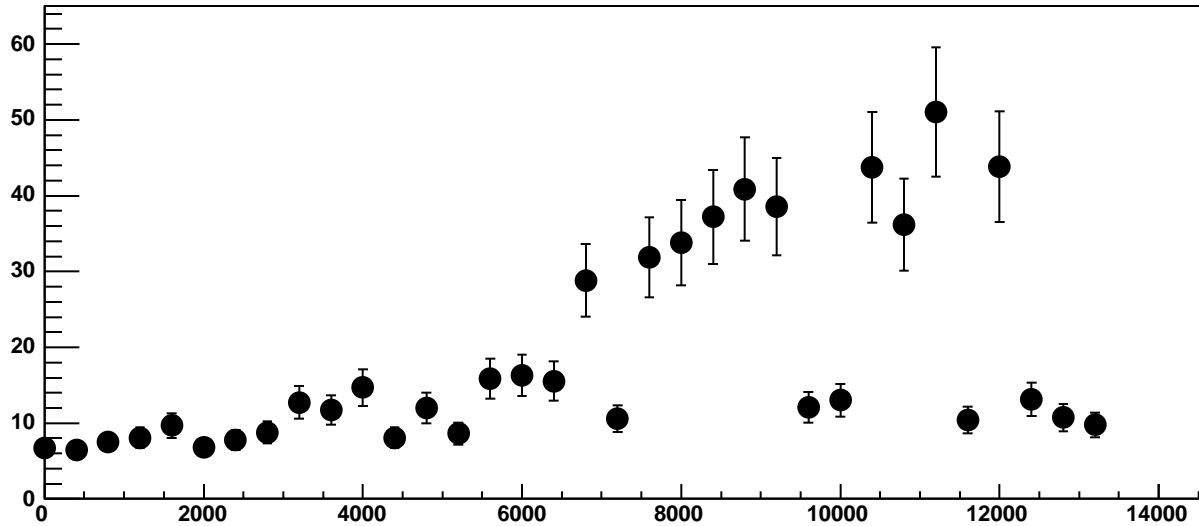


Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

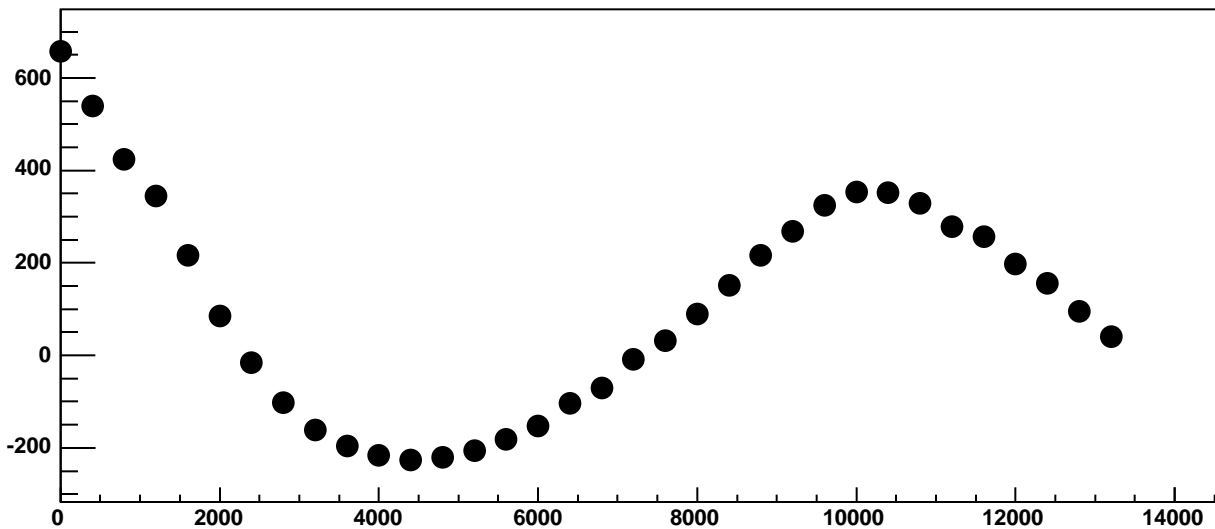
$\chi^2 / \text{ndf}$  1.318e+05 / 23  
p0 -408 ± 1.072  
p1 0.3171 ± 0.0002224



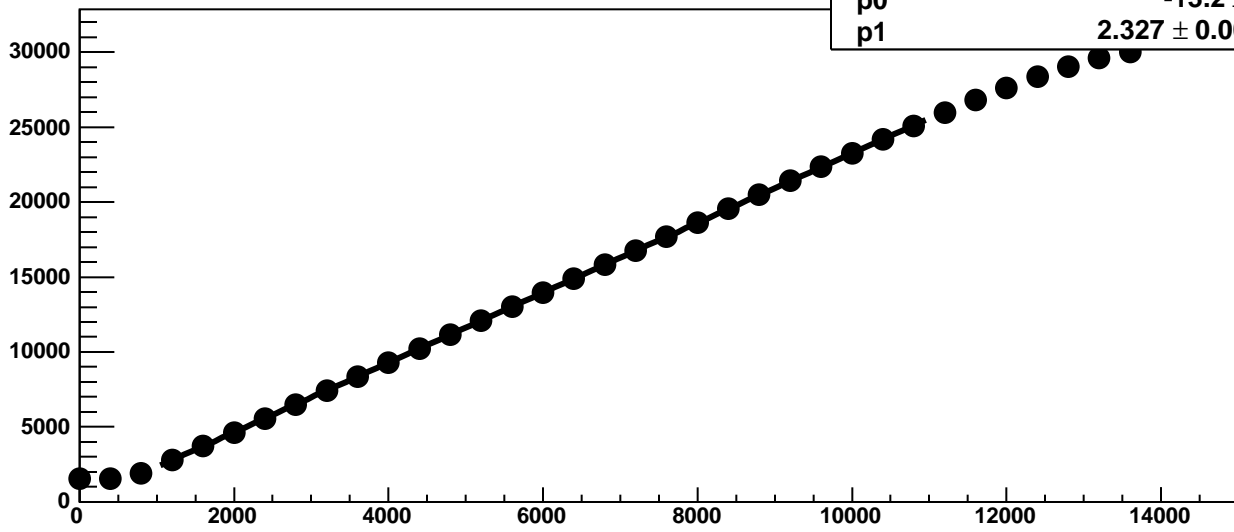
Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

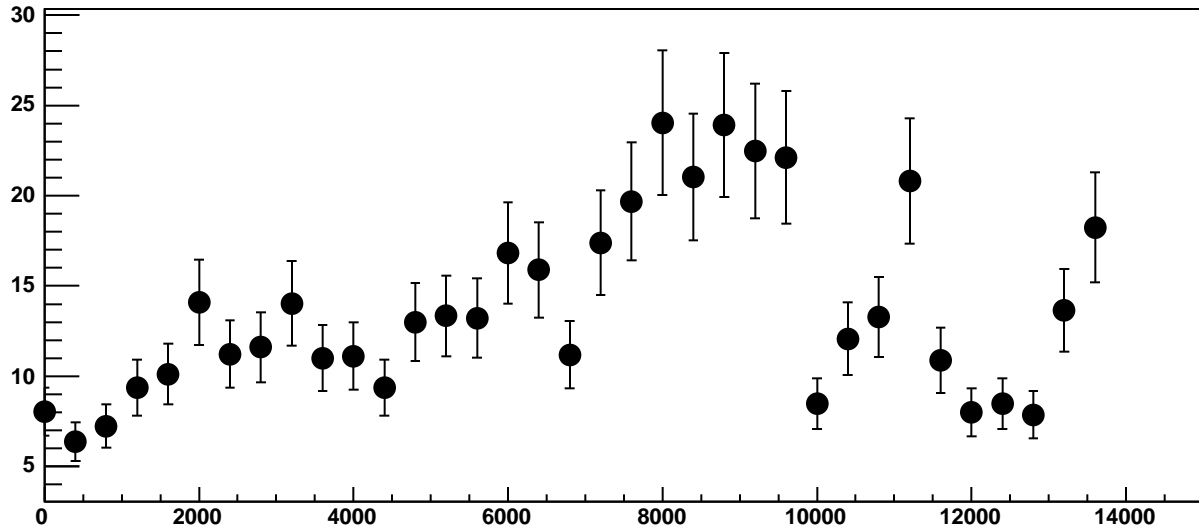


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC

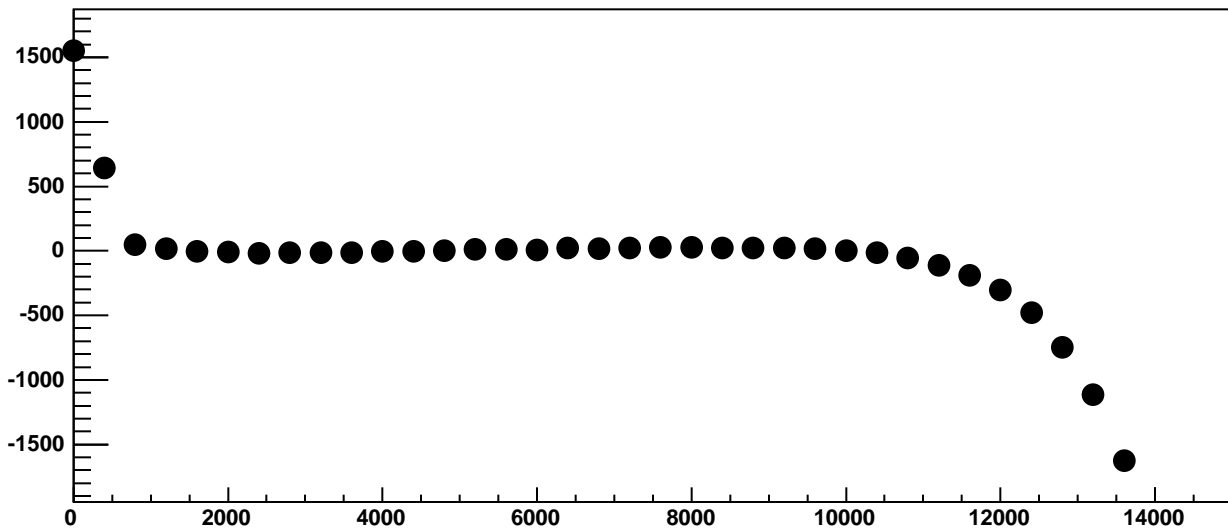


$\chi^2 / \text{ndf}$  862.5 / 23  
p0 -13.2 ± 1.198  
p1 2.327 ± 0.0001942

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

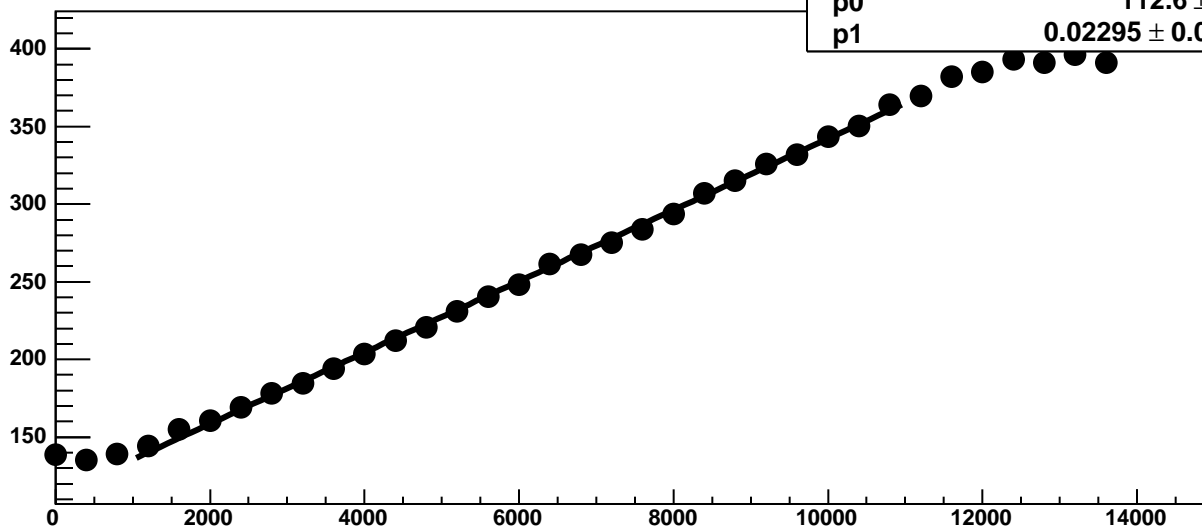


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

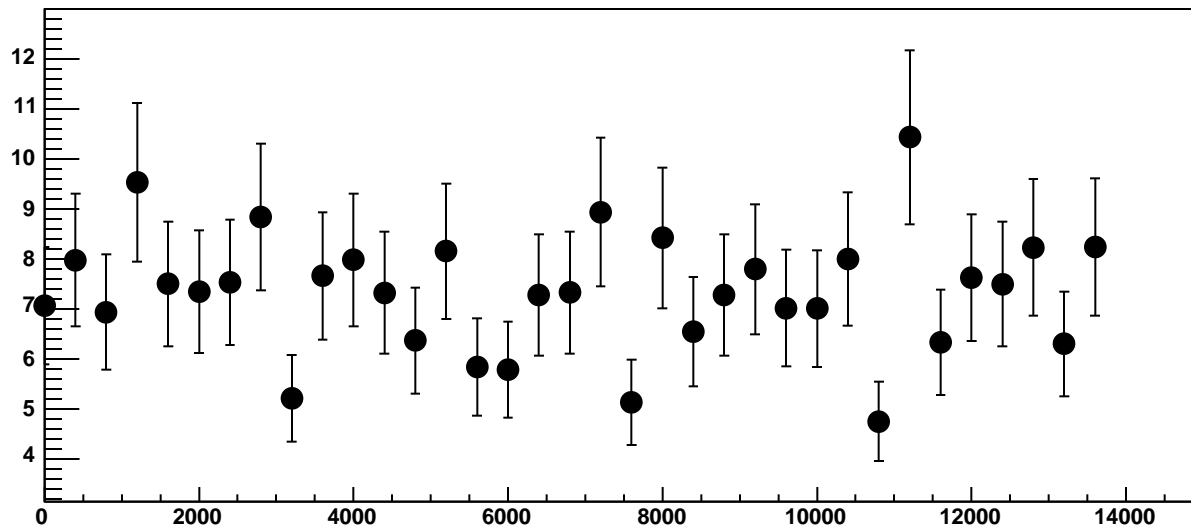




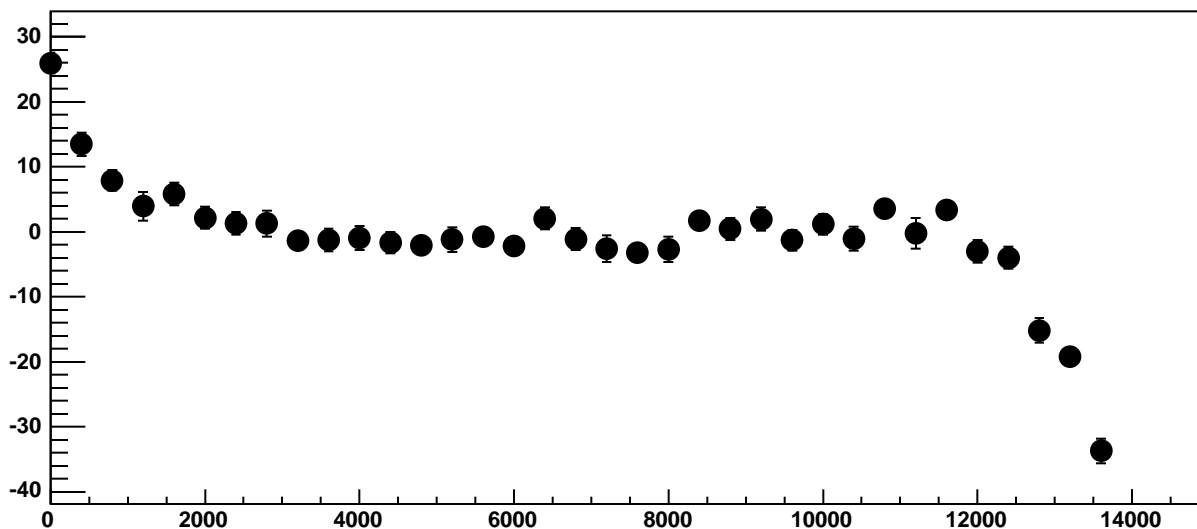
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



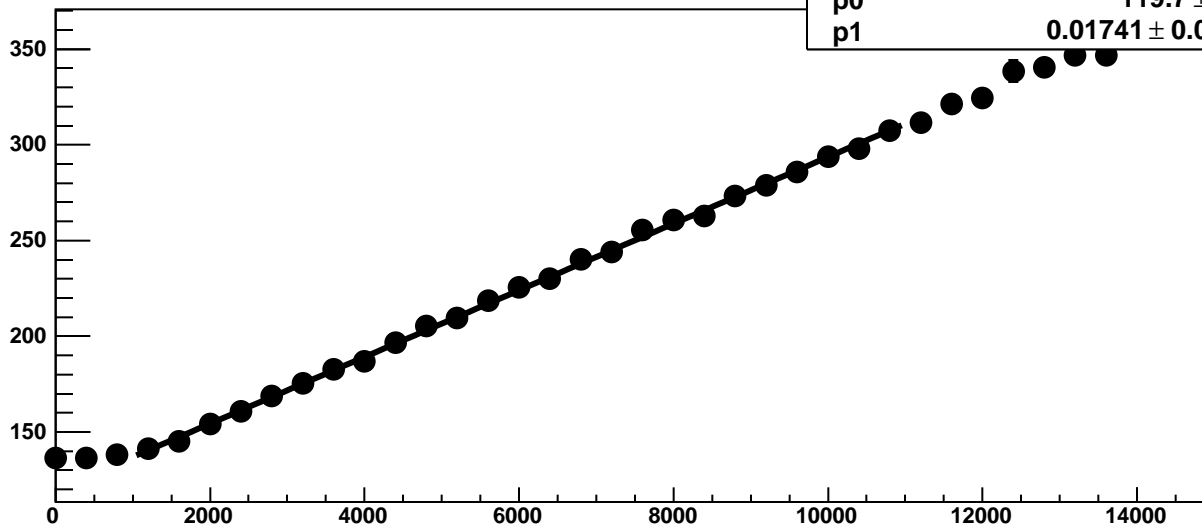
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

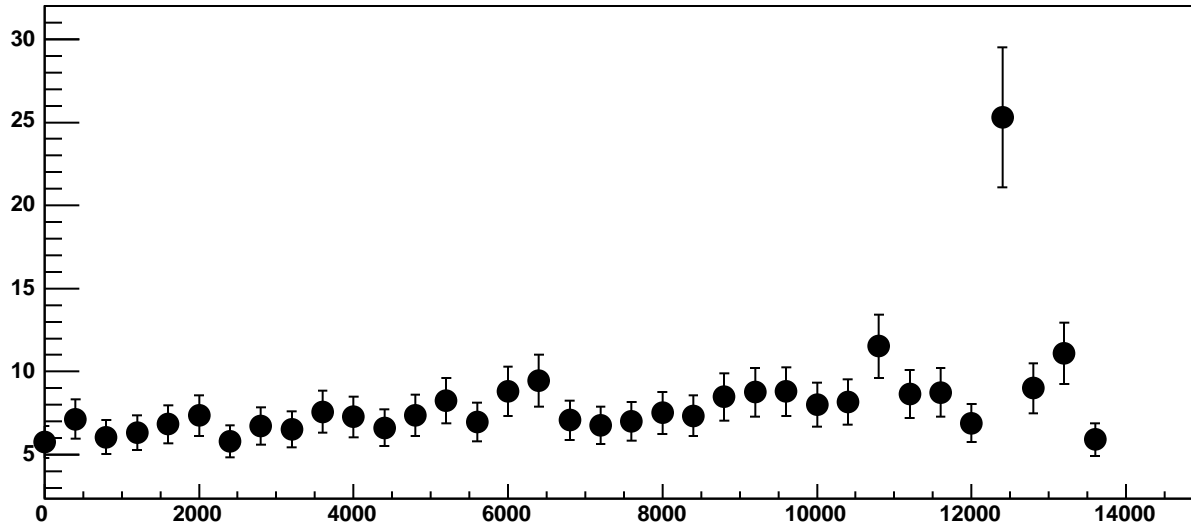


Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

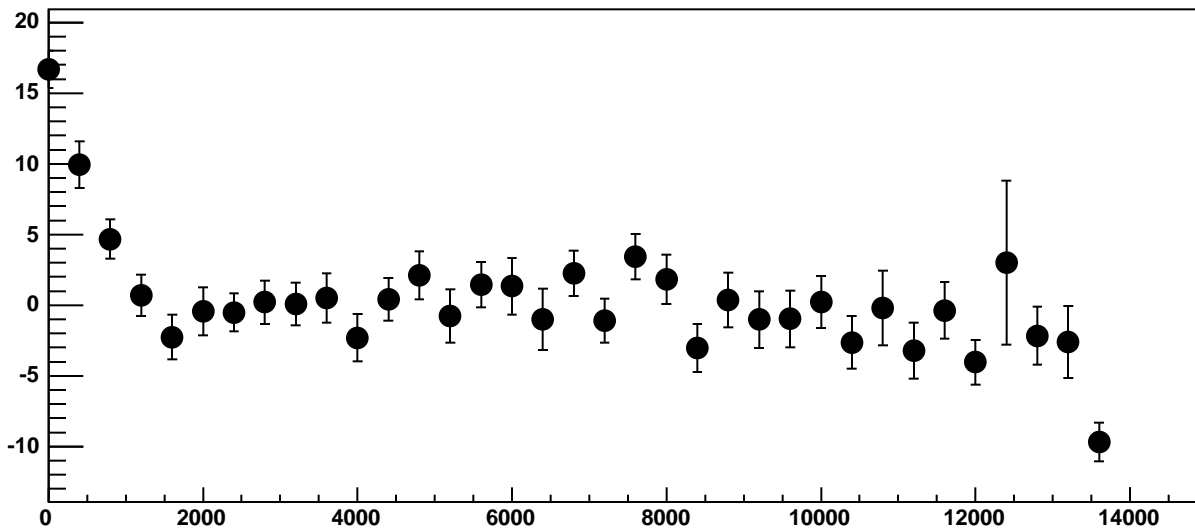


$\chi^2 / \text{ndf}$  21.69 / 23  
p0 119.7 ± 0.7415  
p1 0.01741 ± 0.0001204

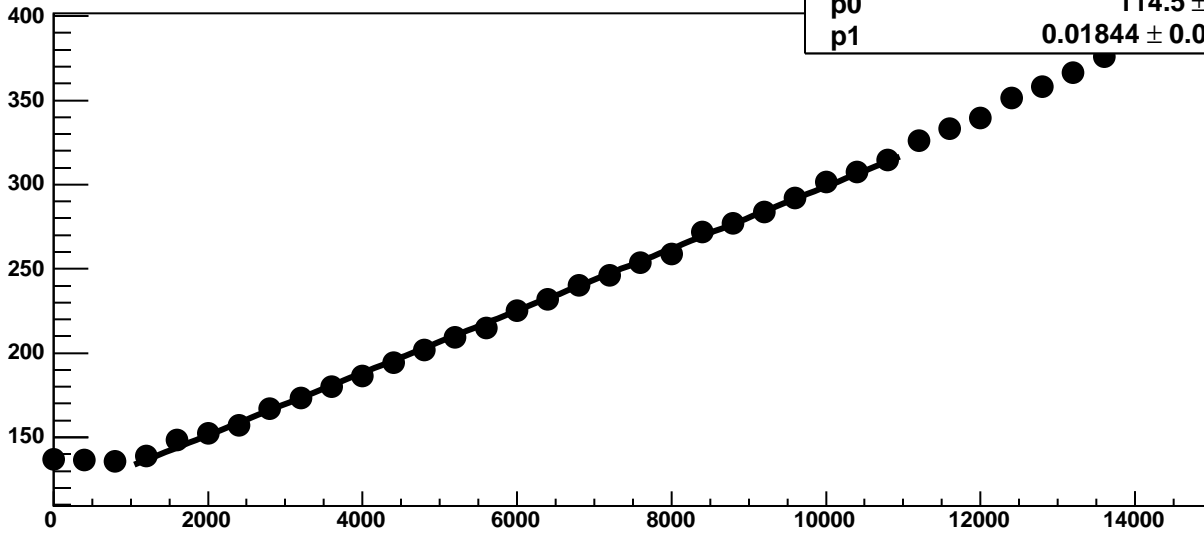
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

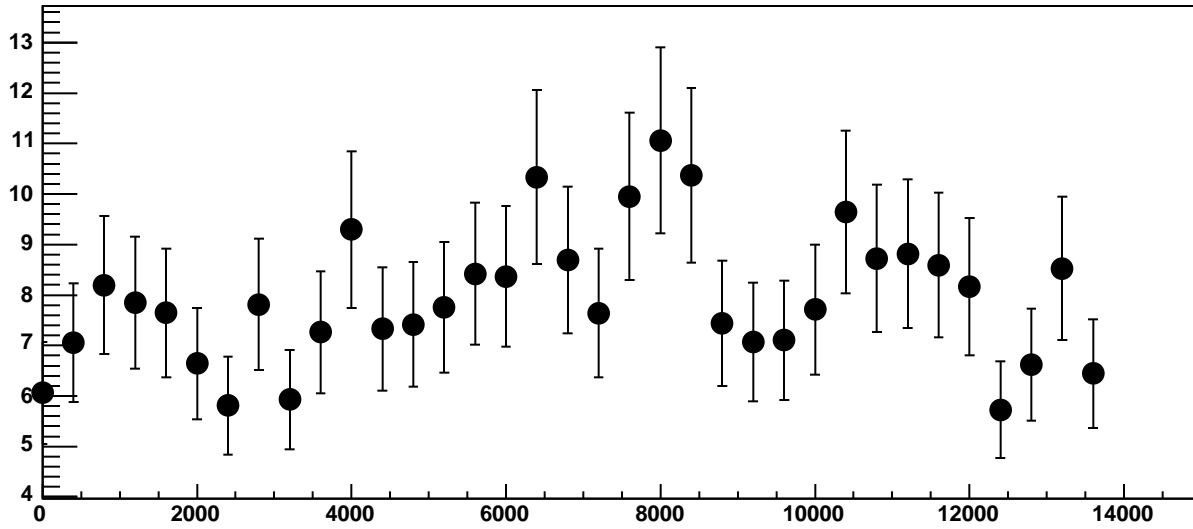


Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

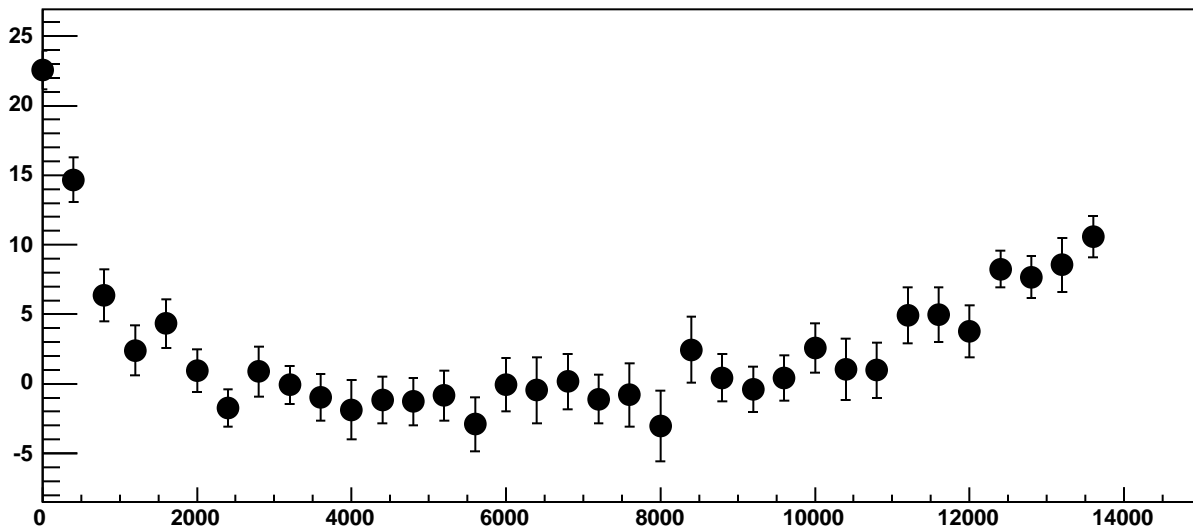


$\chi^2 / \text{ndf}$  20.65 / 23  
p0 114.5 ± 0.7697  
p1 0.01844 ± 0.0001219

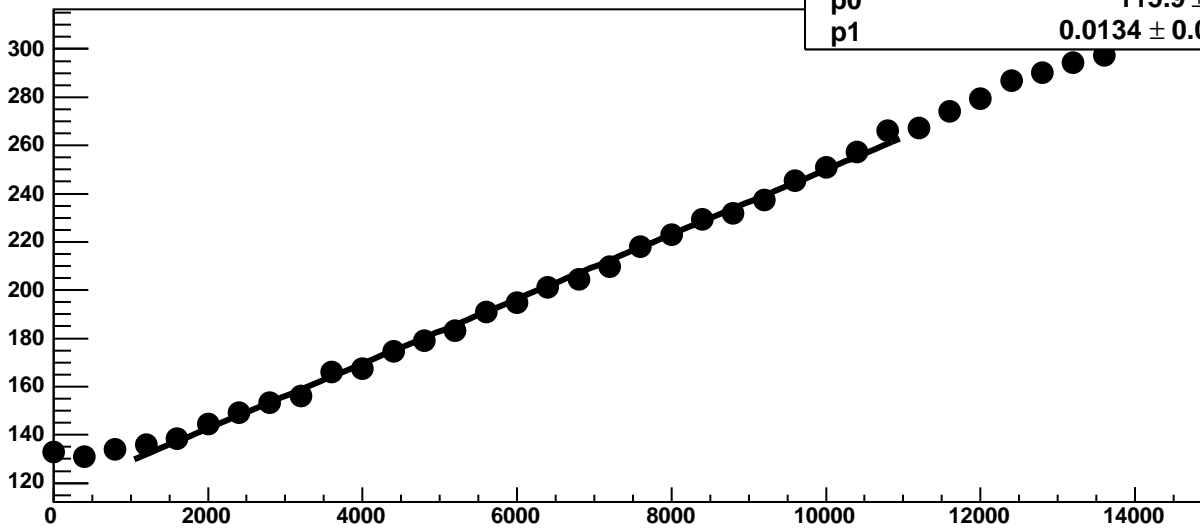
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

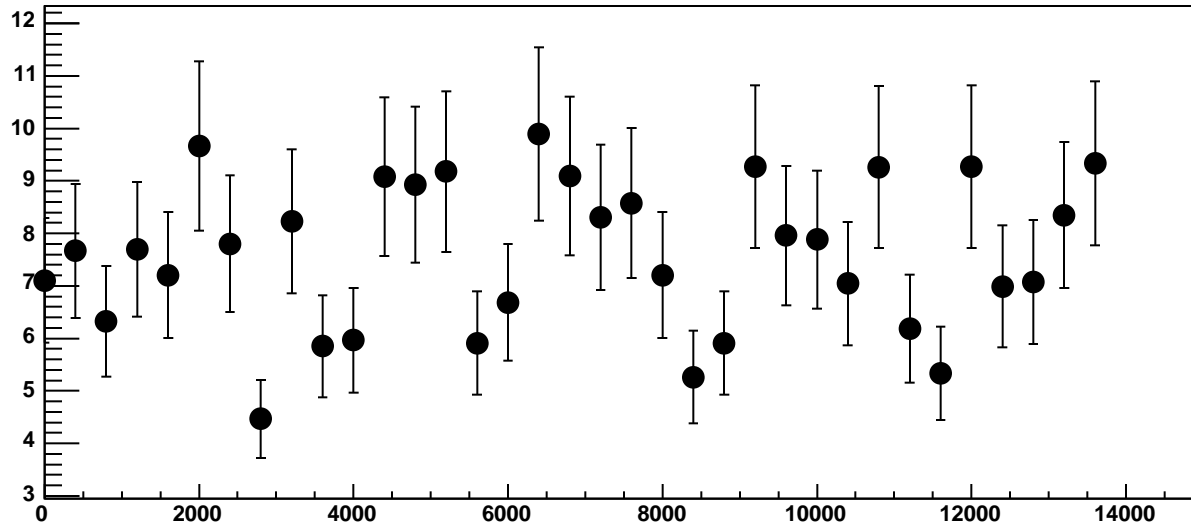


Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

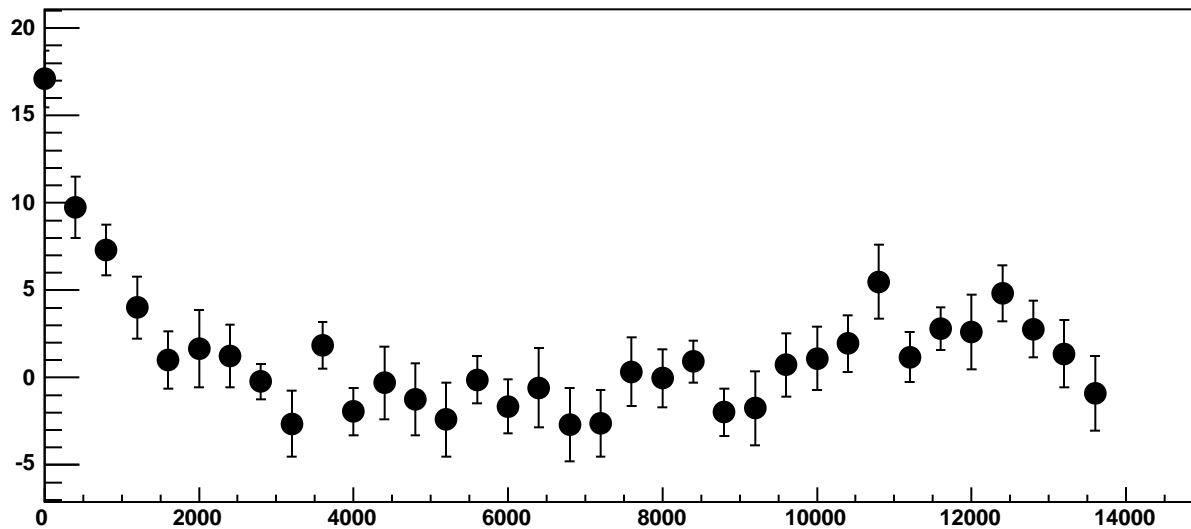


$\chi^2 / \text{ndf}$  31 / 23  
p0  $115.9 \pm 0.7506$   
p1  $0.0134 \pm 0.0001159$

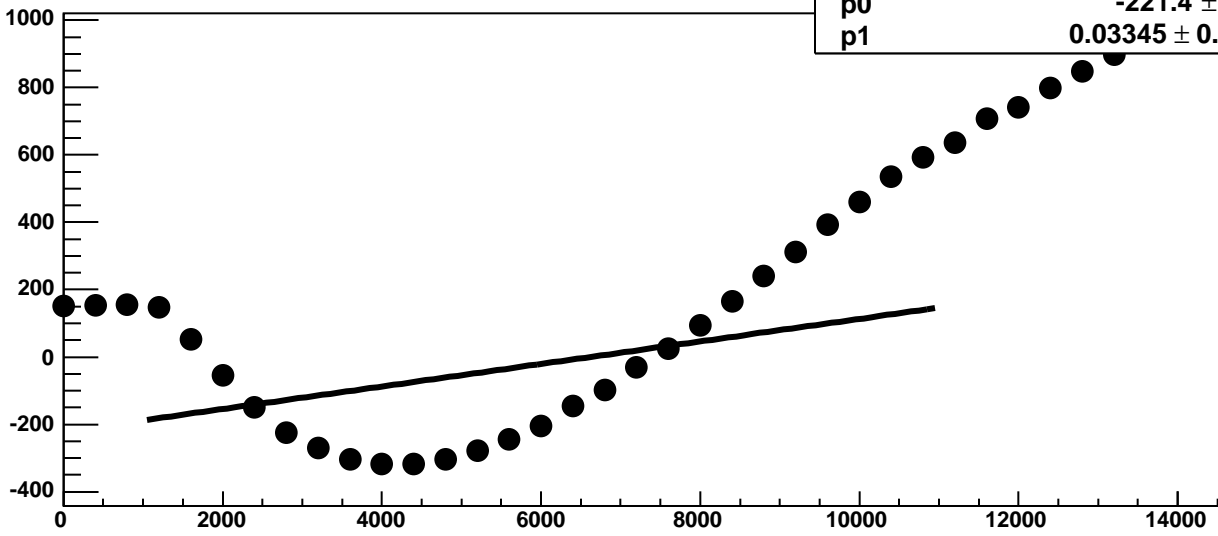
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



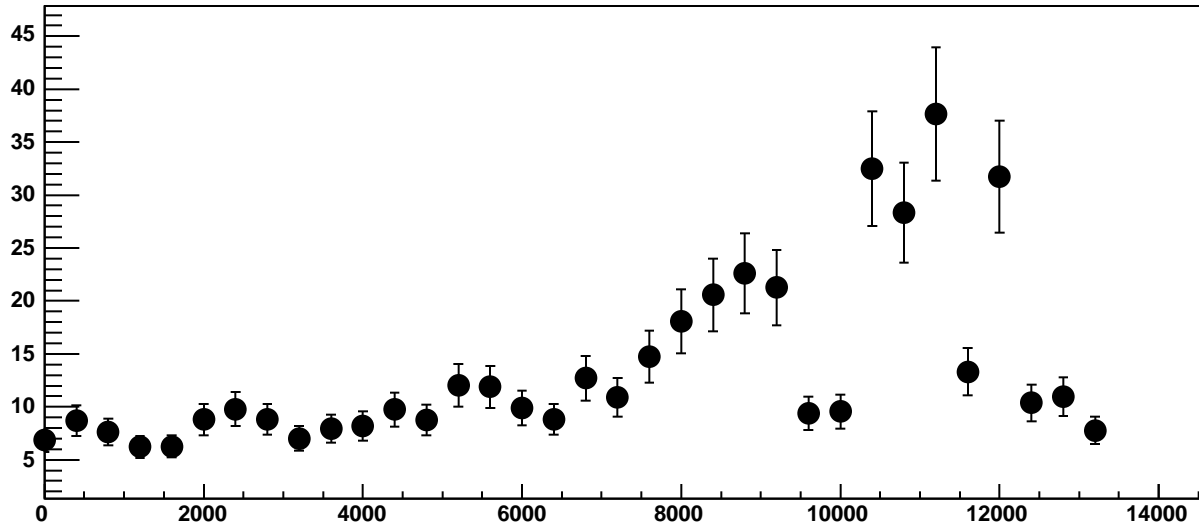
Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



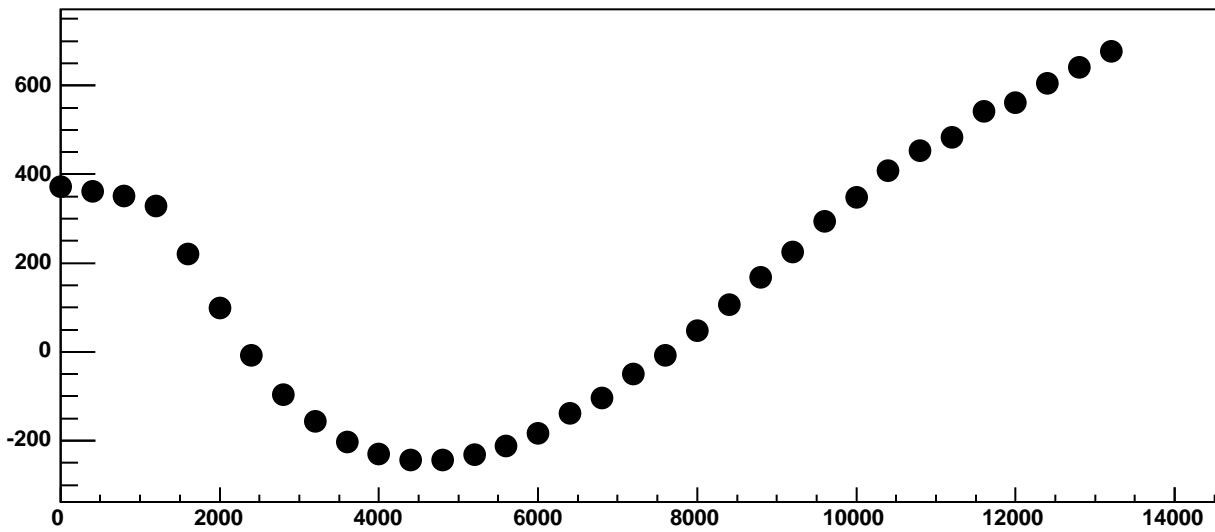
Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



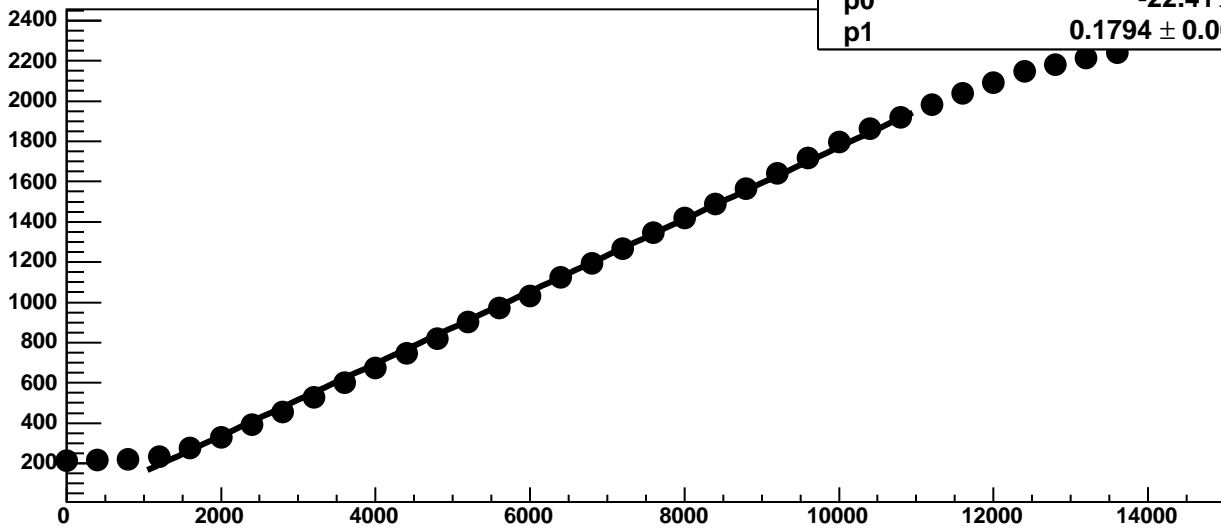
Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



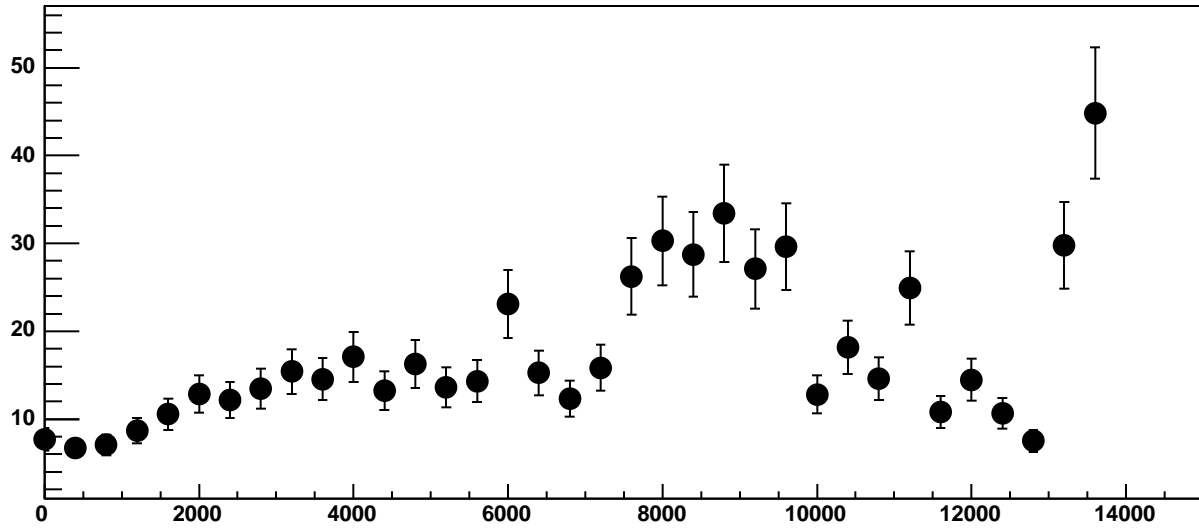
Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



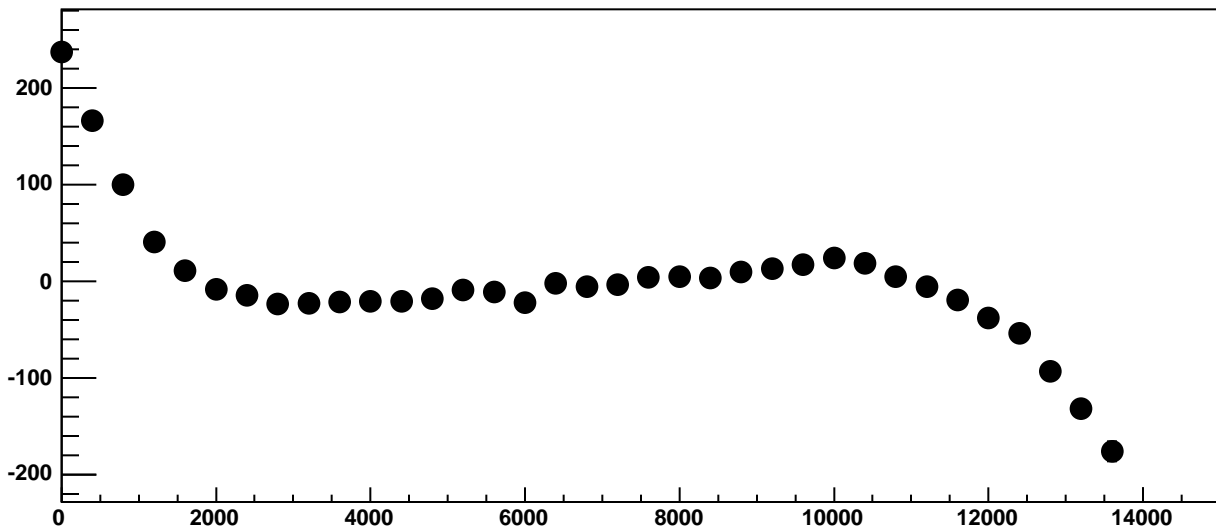
Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



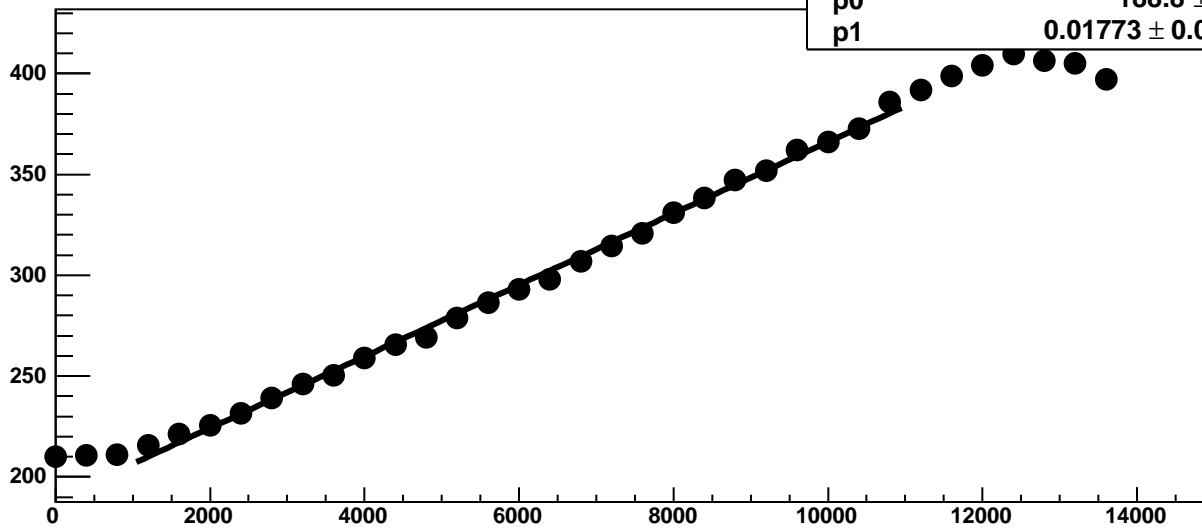
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



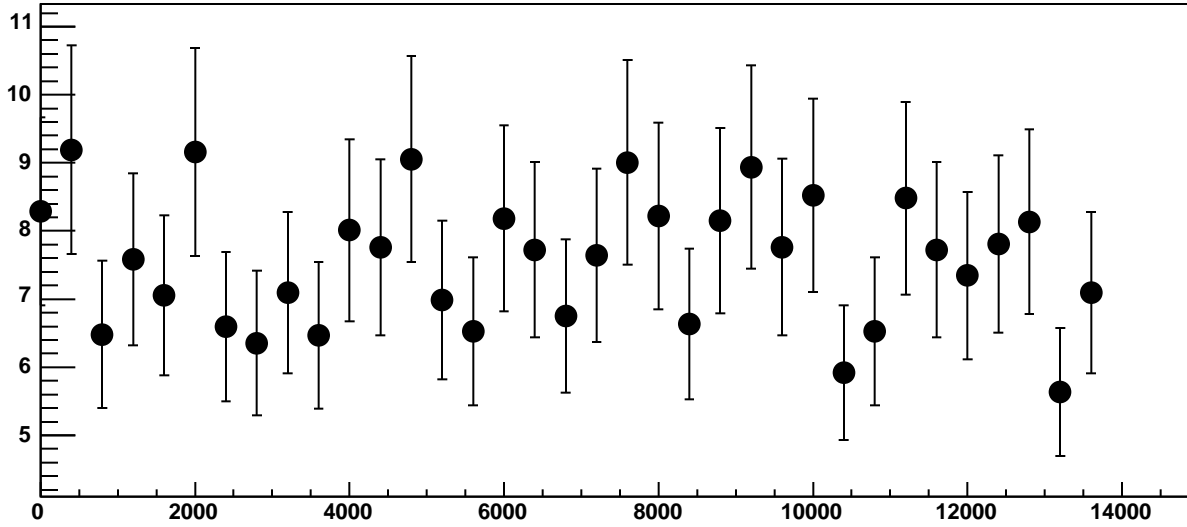
Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



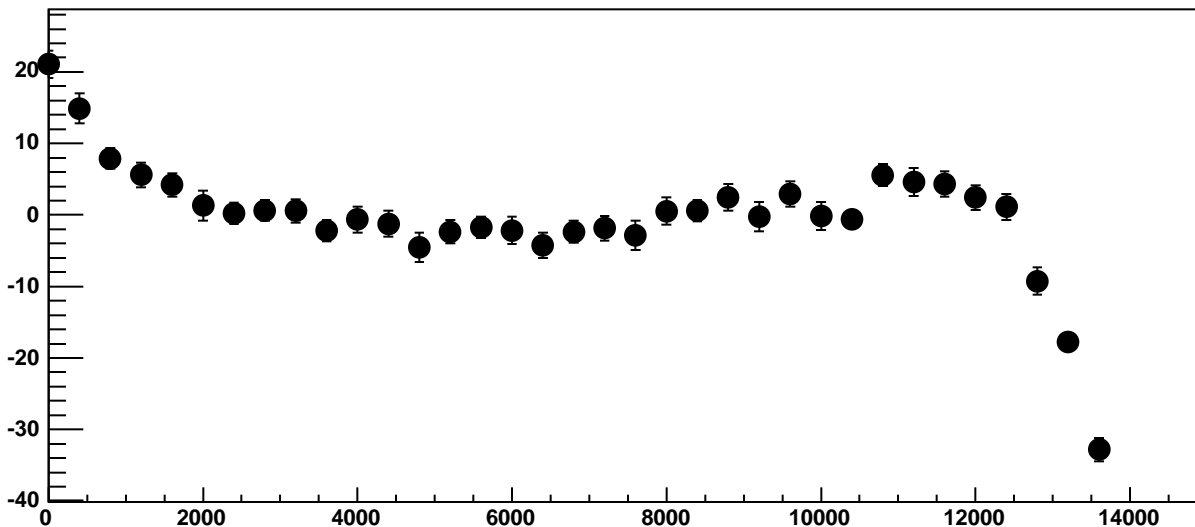
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



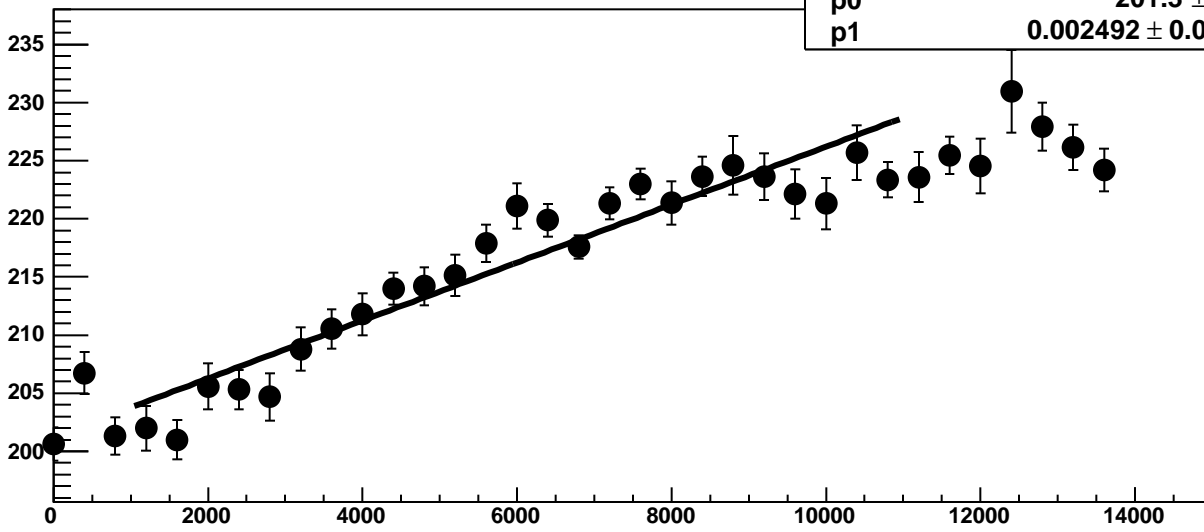
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



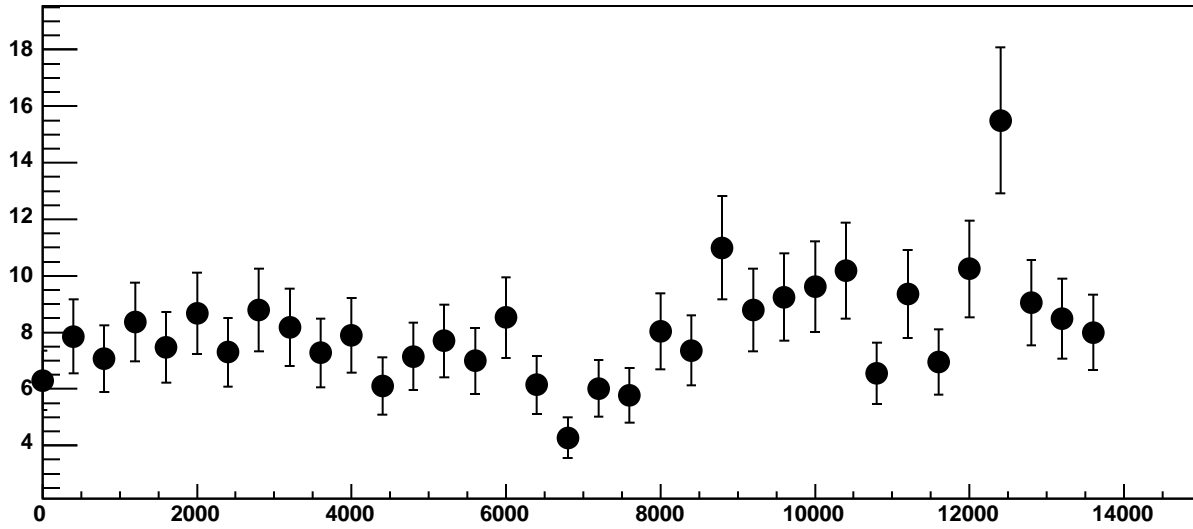
Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



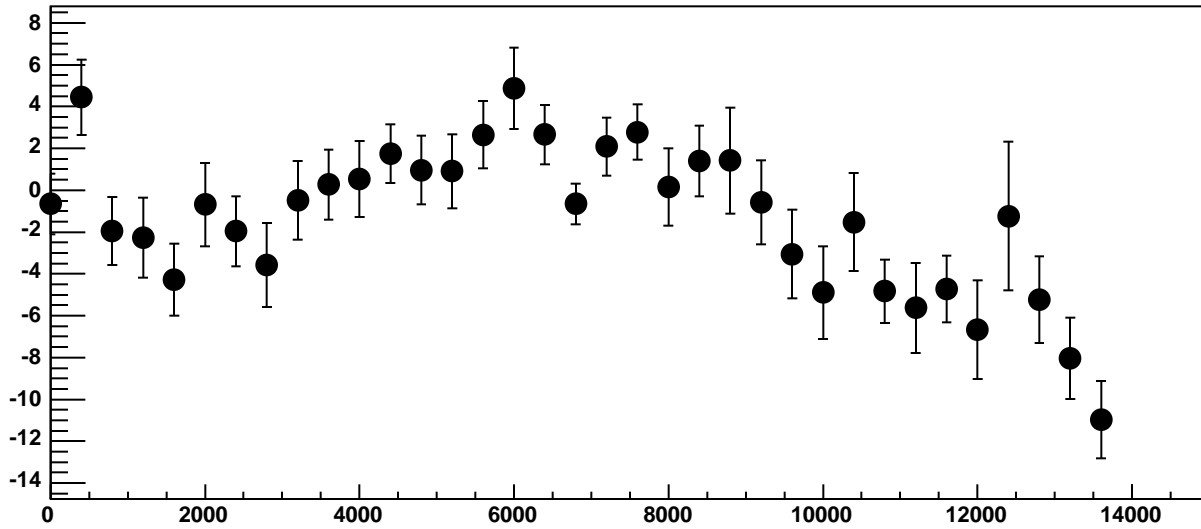
Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

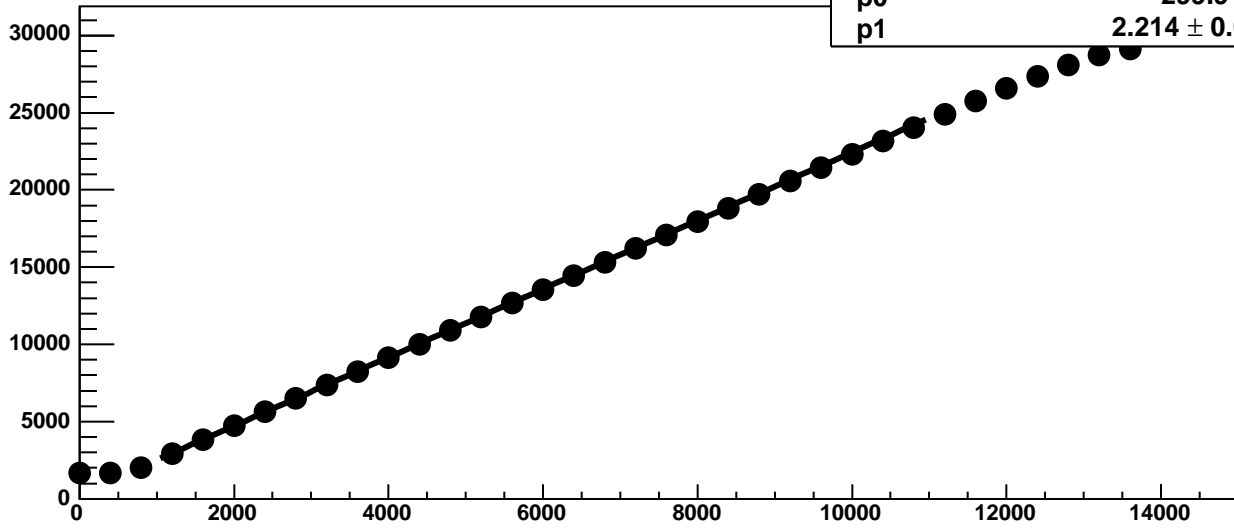


Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

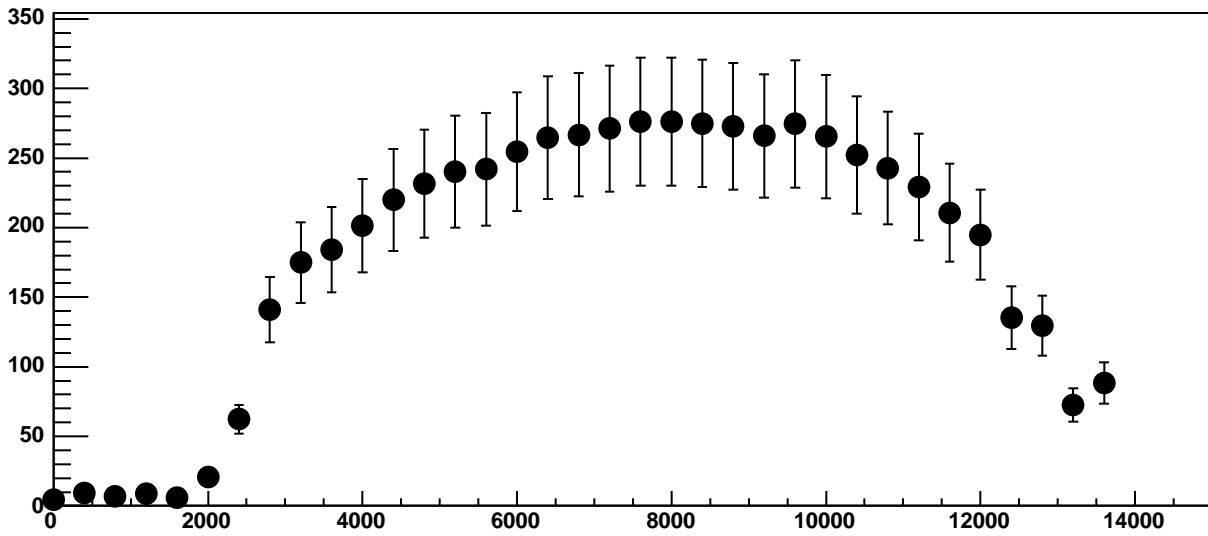




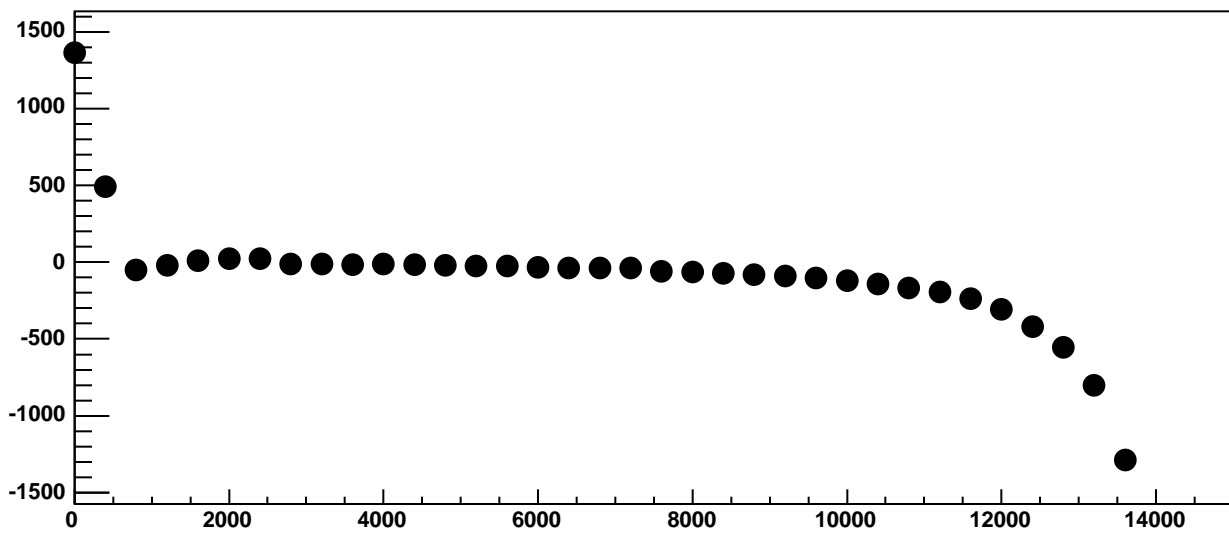
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



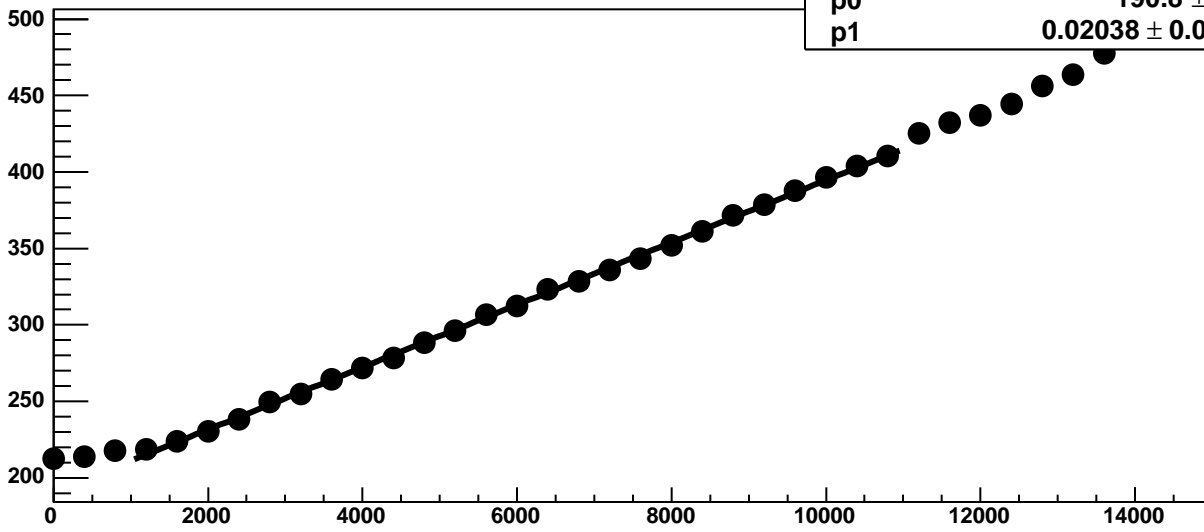
Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

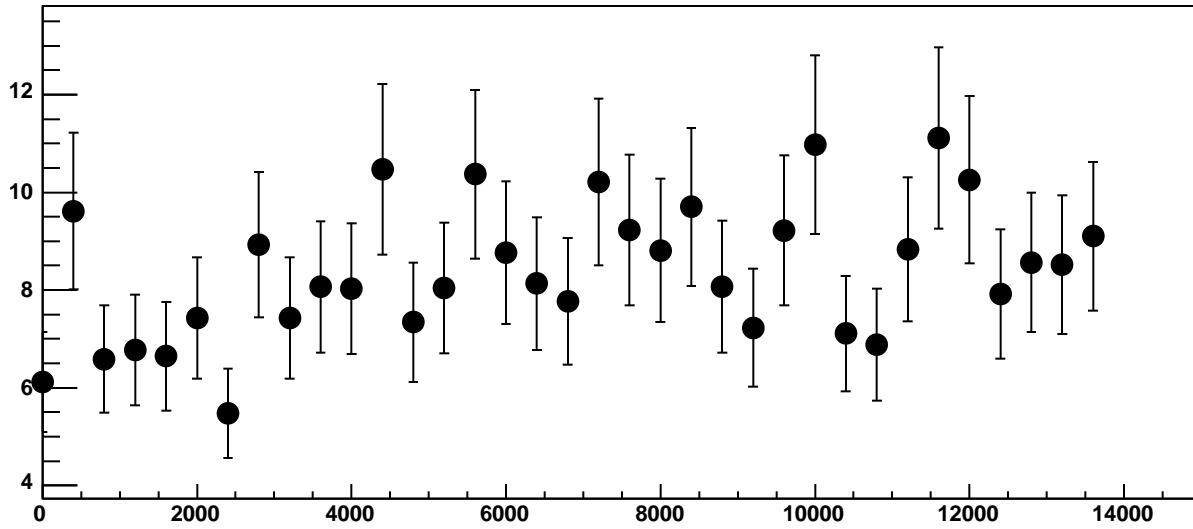


Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

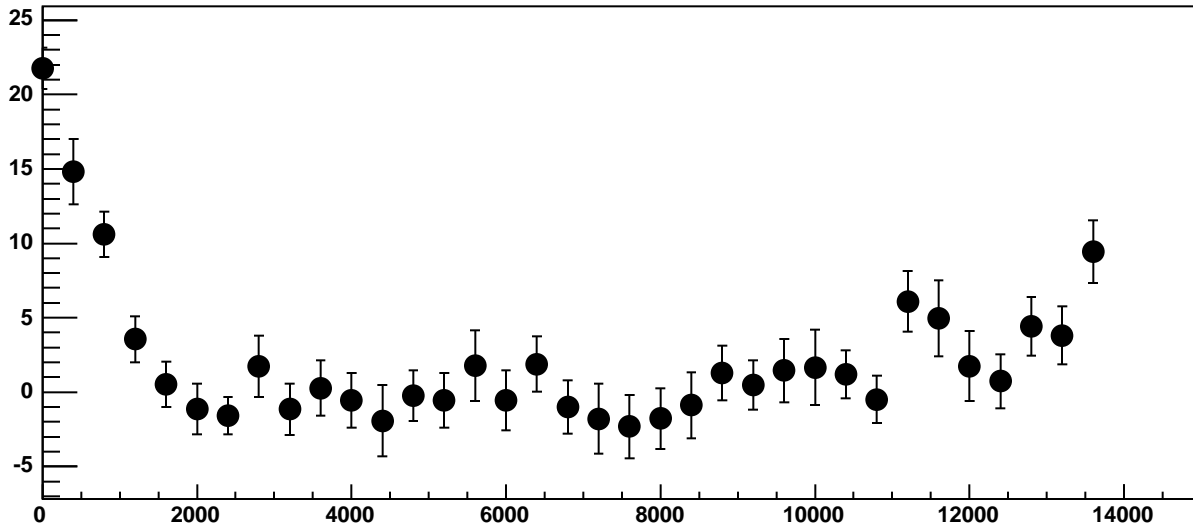


$\chi^2 / \text{ndf}$  16.2 / 23  
p0  $190.8 \pm 0.7633$   
p1  $0.02038 \pm 0.0001187$

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

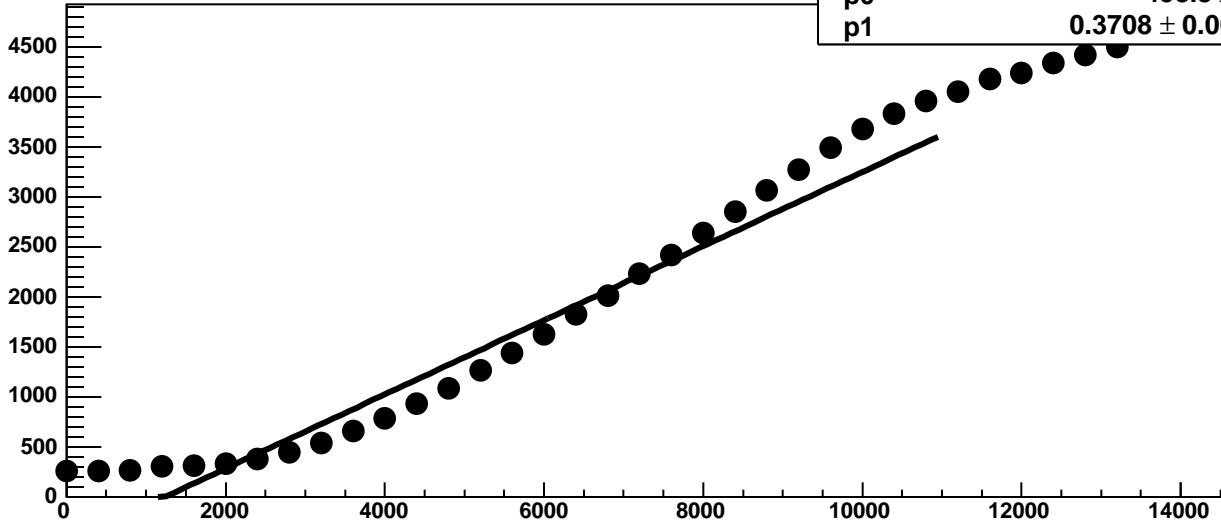


Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

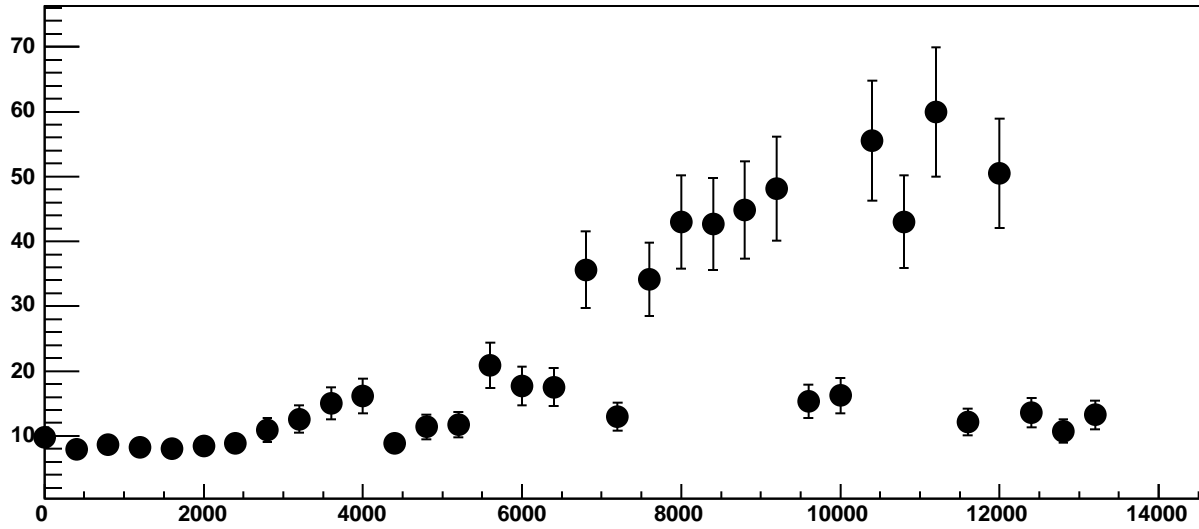


Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

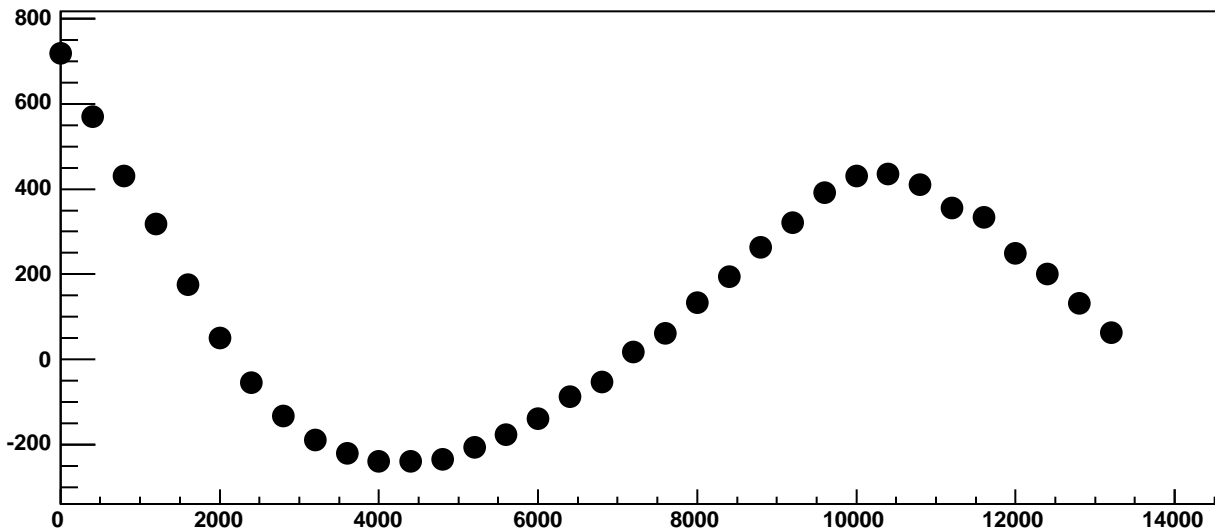
$\chi^2 / \text{ndf}$  1.159e+05 / 23  
p0 -458.8 ± 1.173  
p1 0.3708 ± 0.0002579



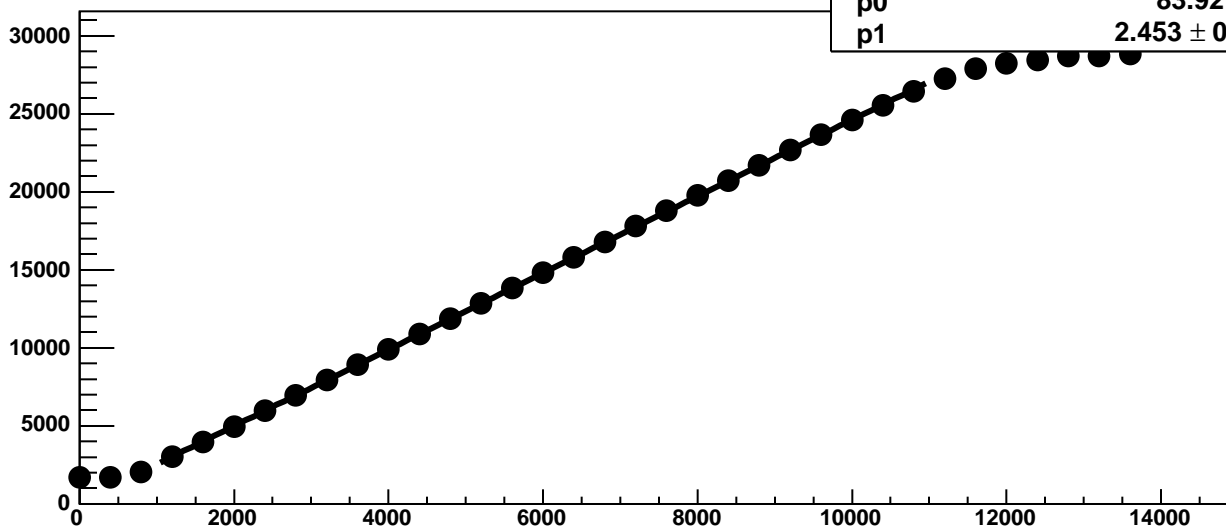
Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



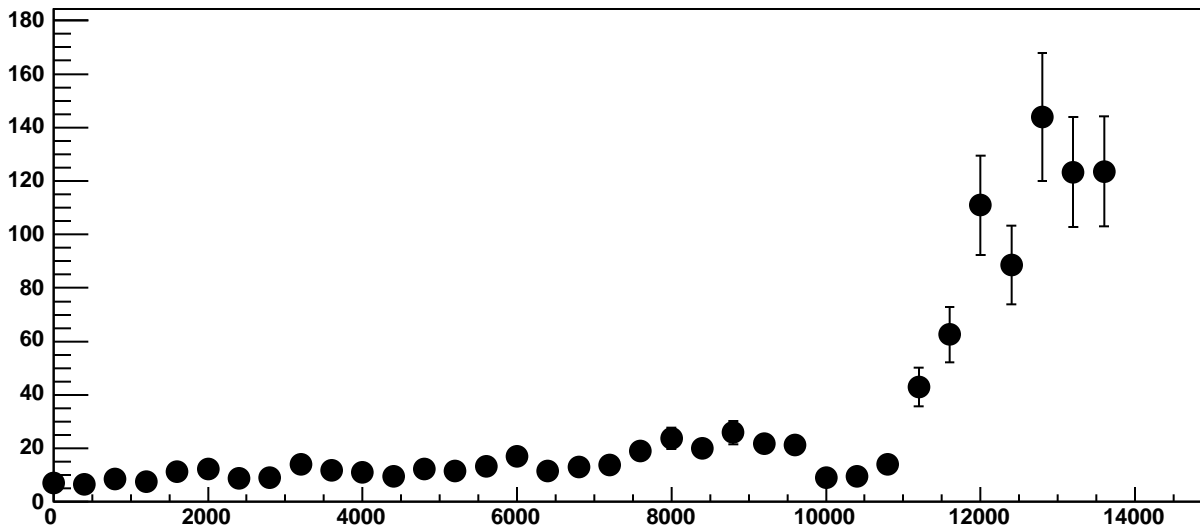
Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



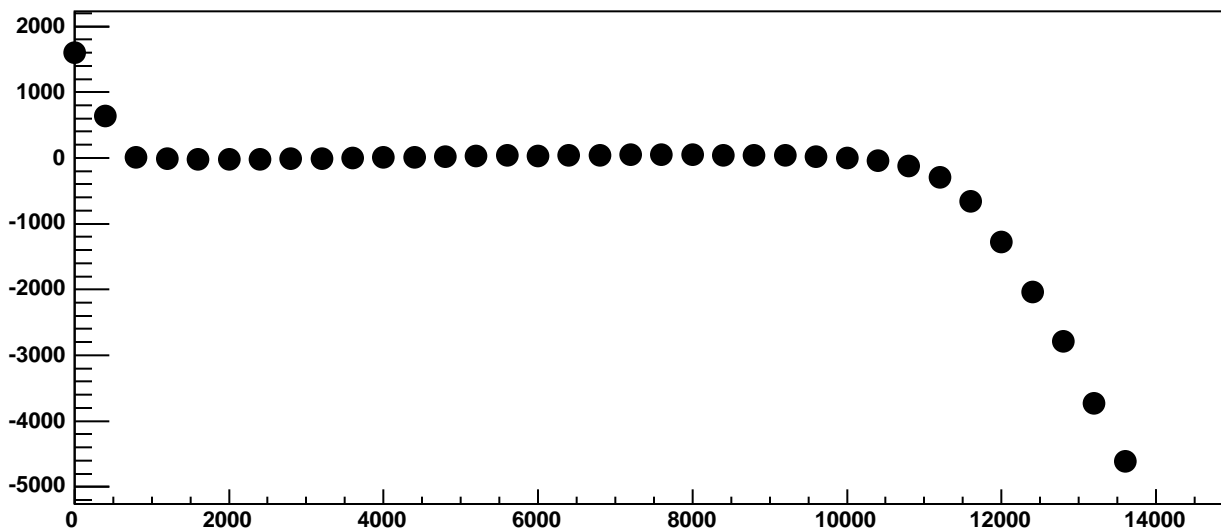
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



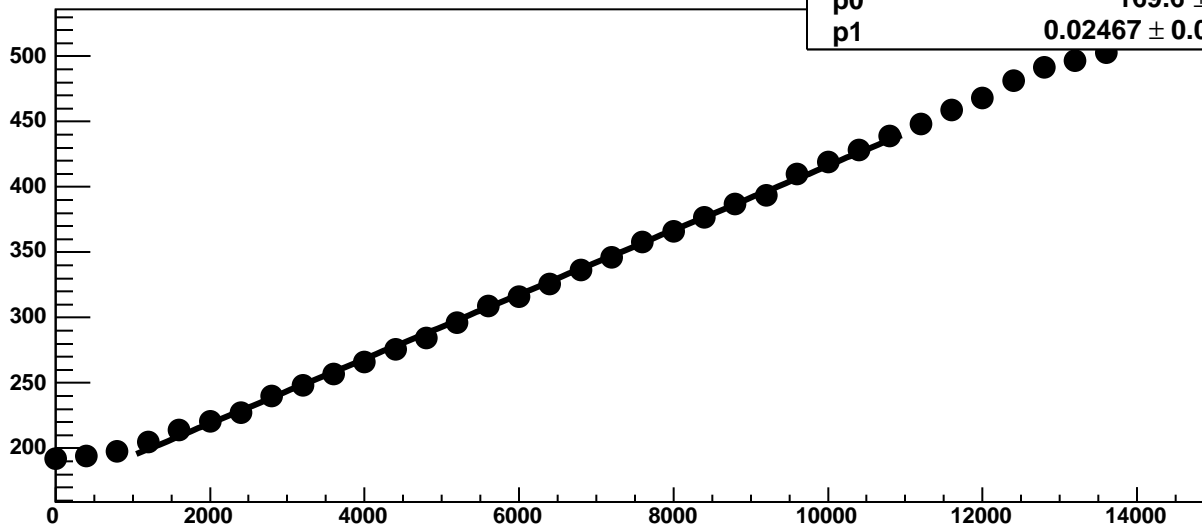
Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

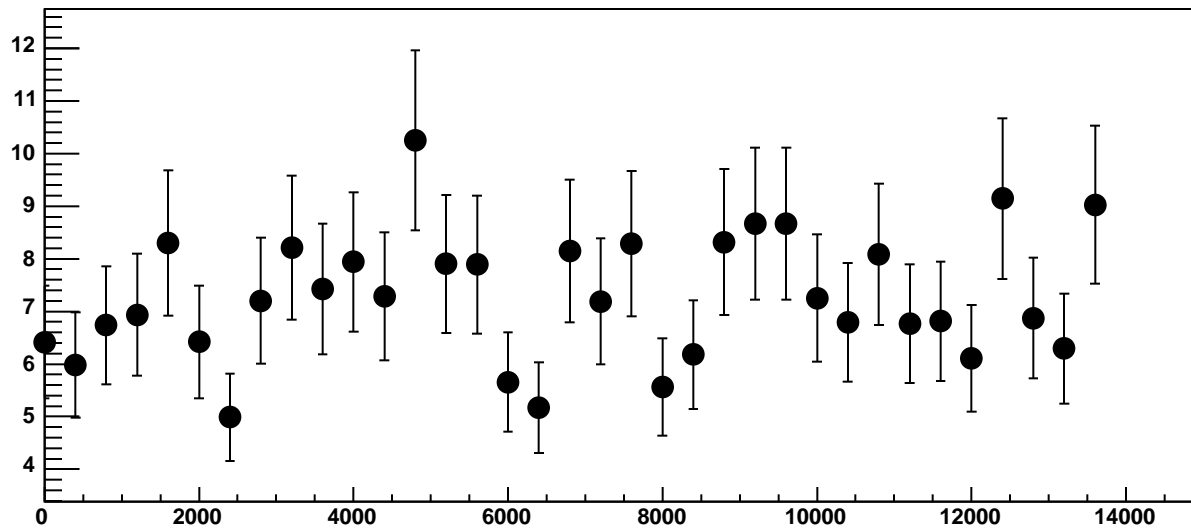


Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

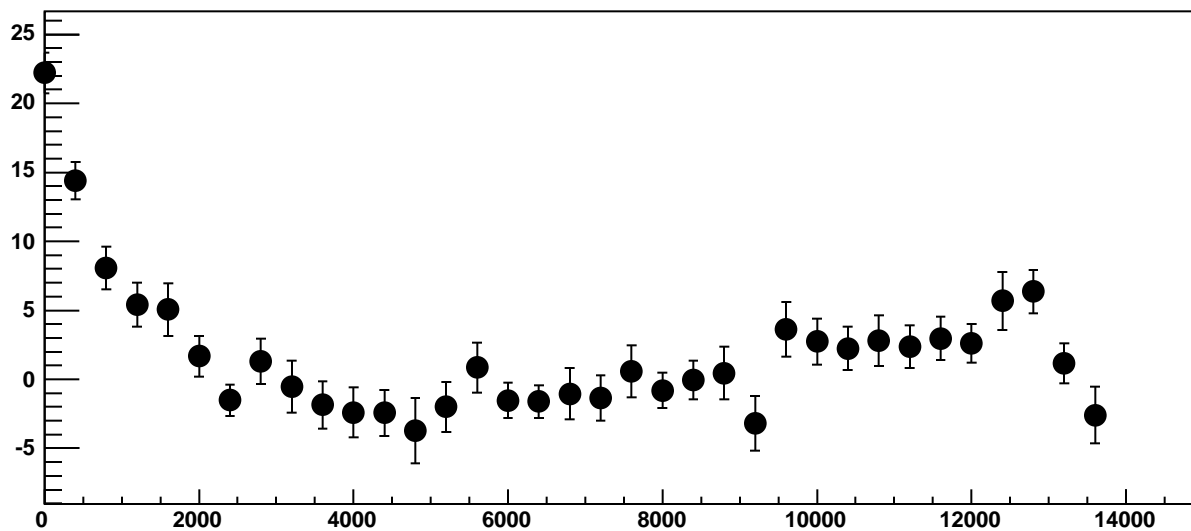


$\chi^2 / \text{ndf}$  49.04 / 23  
p0 169.6 ± 0.7412  
p1 0.02467 ± 0.0001137

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

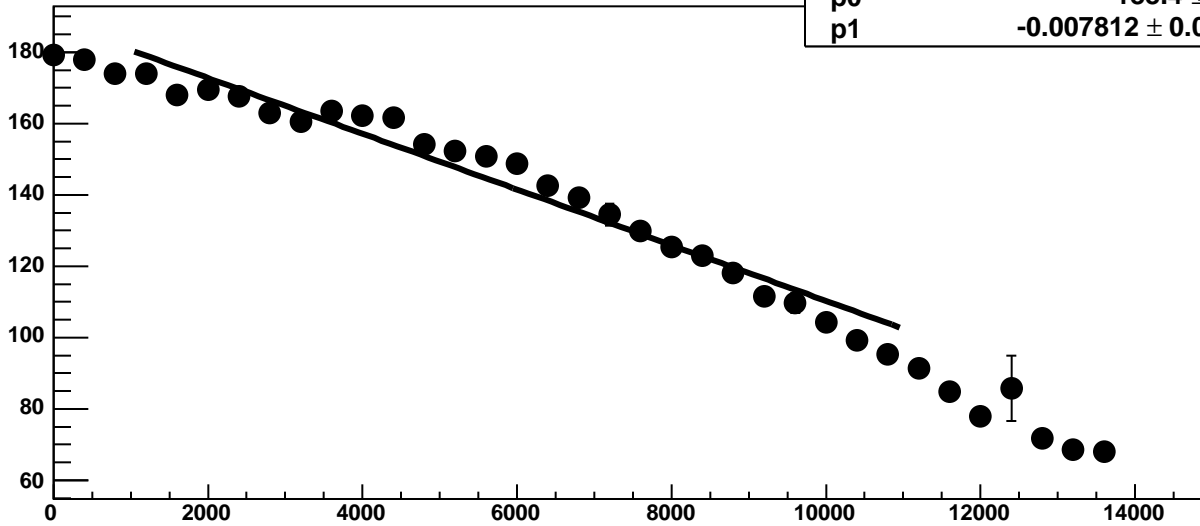


Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

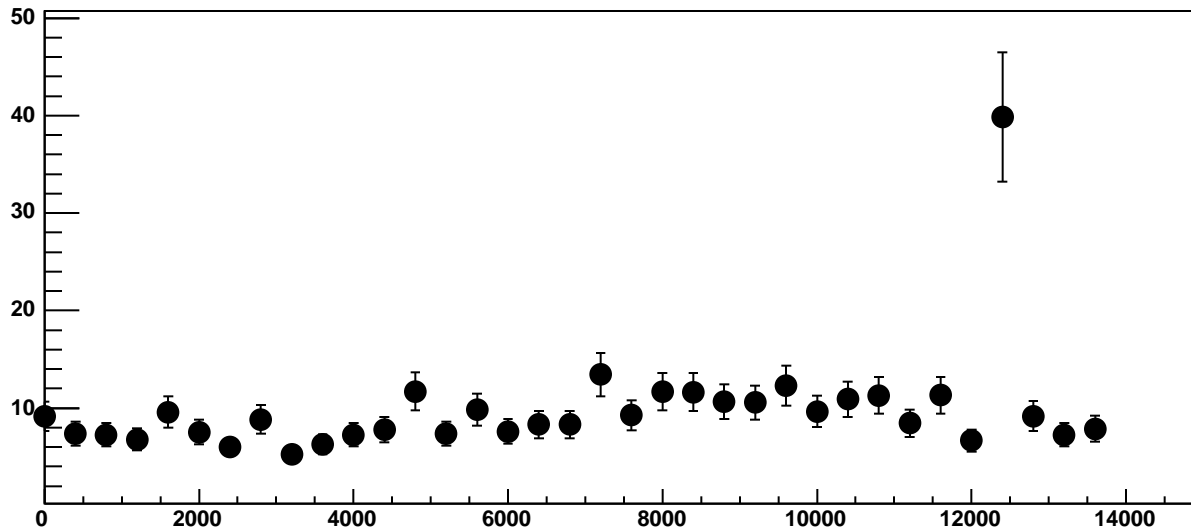


Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

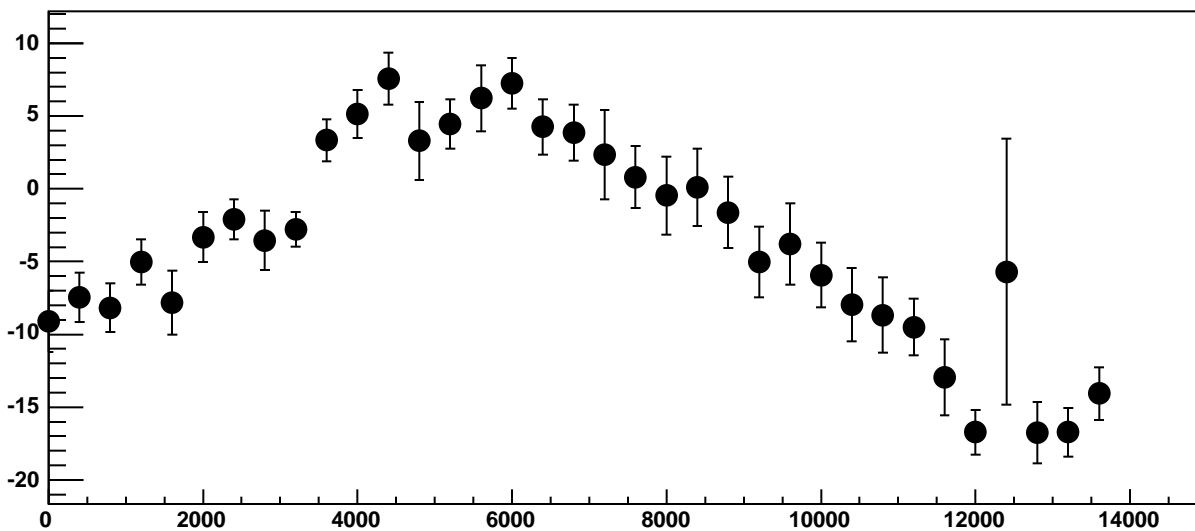
$\chi^2 / \text{ndf}$  149.1 / 23  
p0 188.4  $\pm$  0.8107  
p1 -0.007812  $\pm$  0.0001423



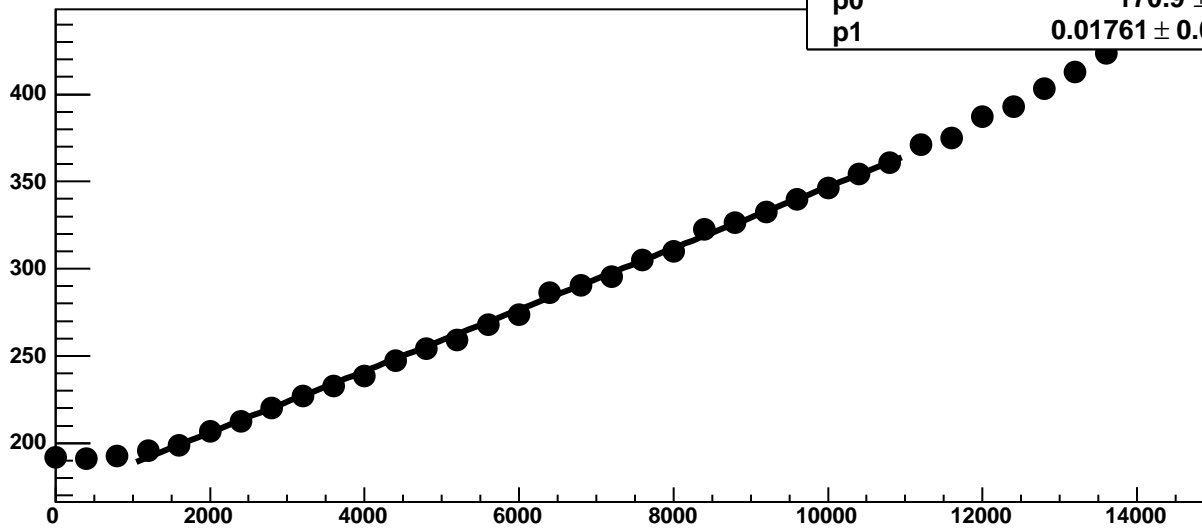
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

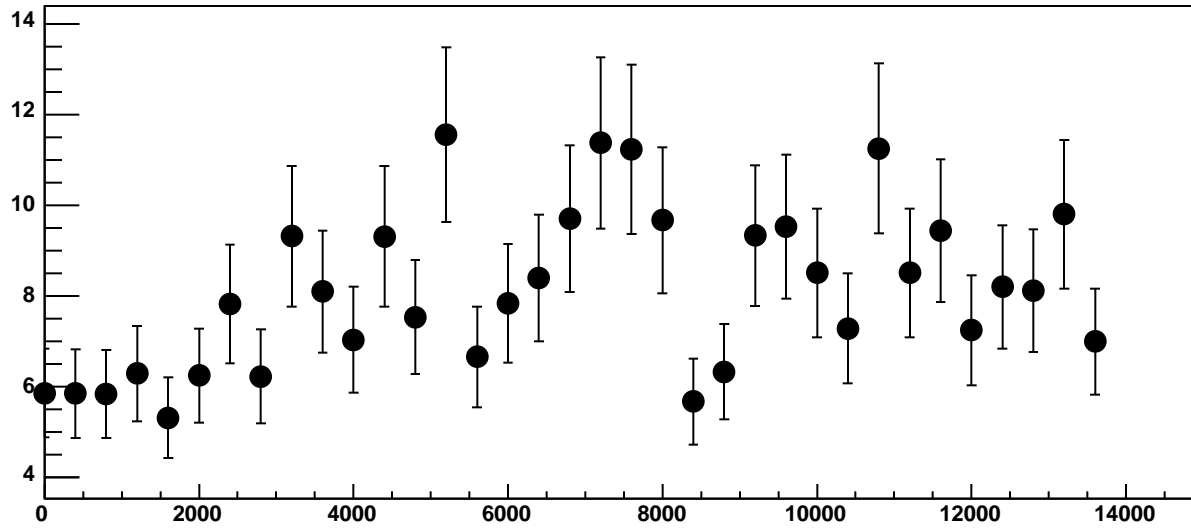


Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

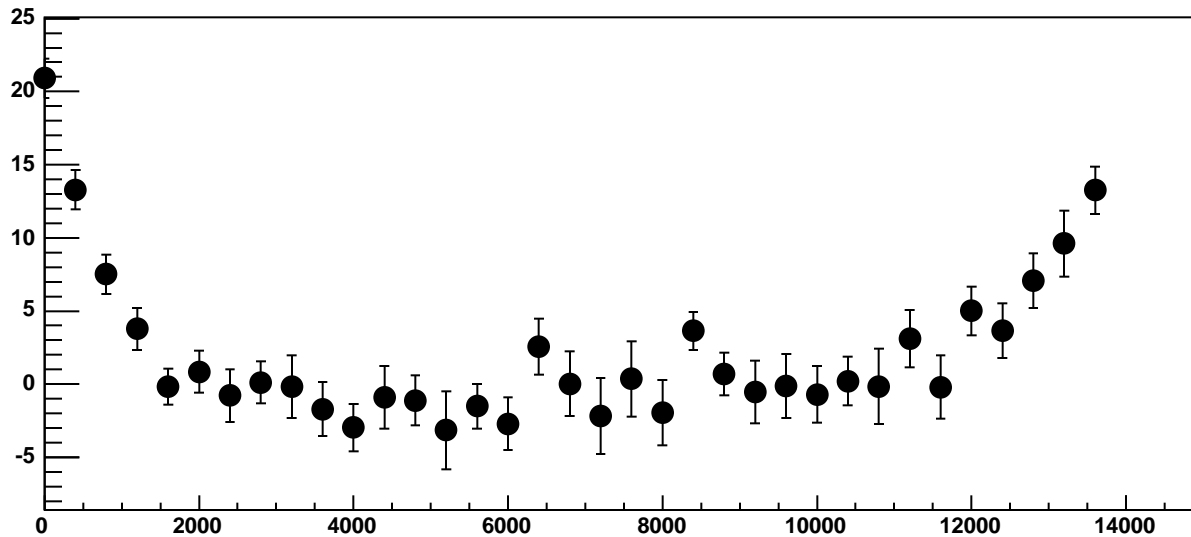


$\chi^2 / \text{ndf}$  28.45 / 23  
p0 170.9 ± 0.7295  
p1 0.01761 ± 0.0001171

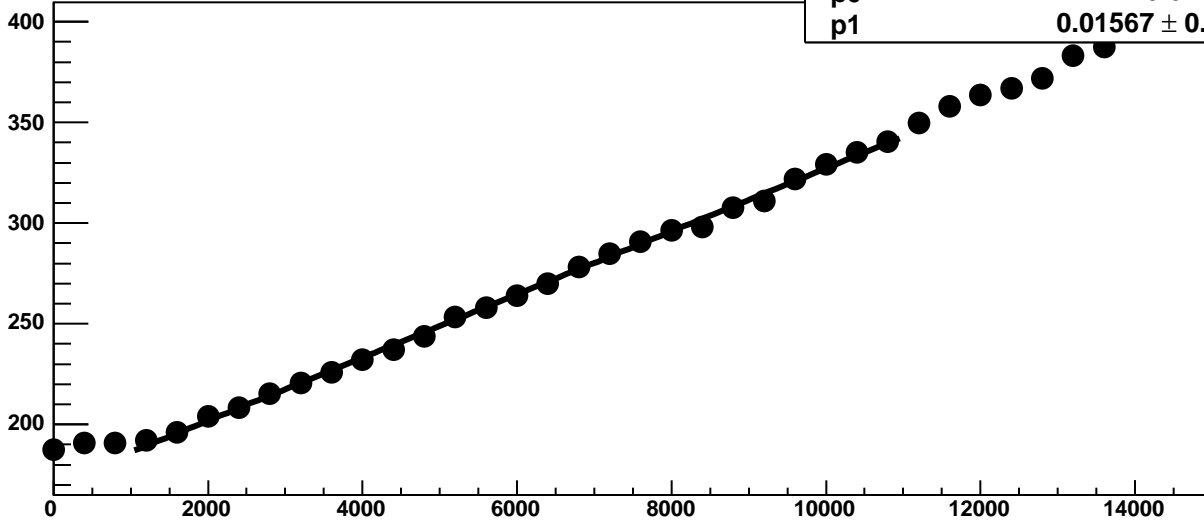
Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

22.19 / 23

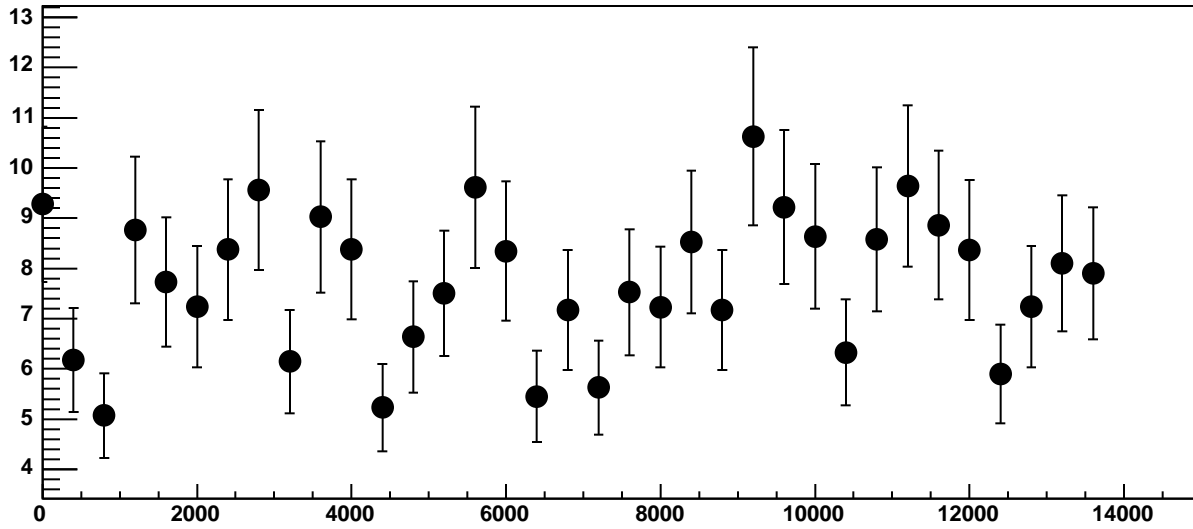
p0

$170.6 \pm 0.8193$

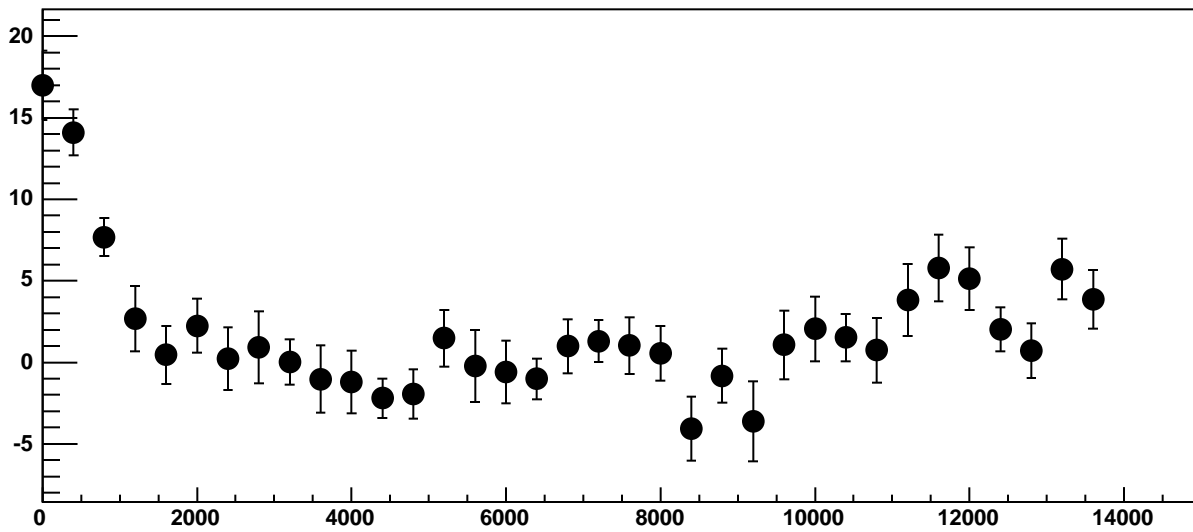
p1

$0.01567 \pm 0.000125$

Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

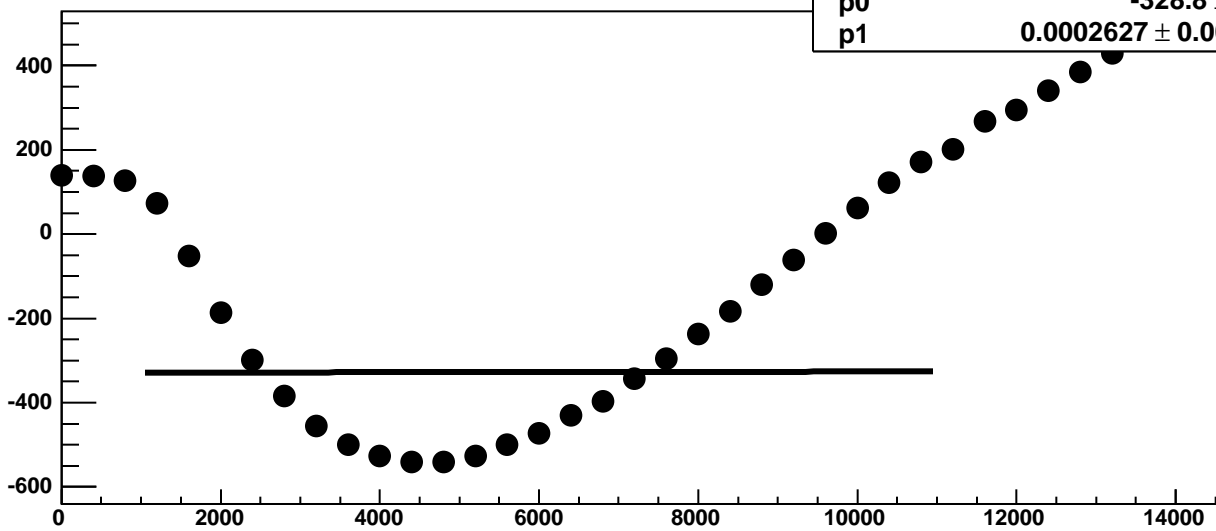


Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



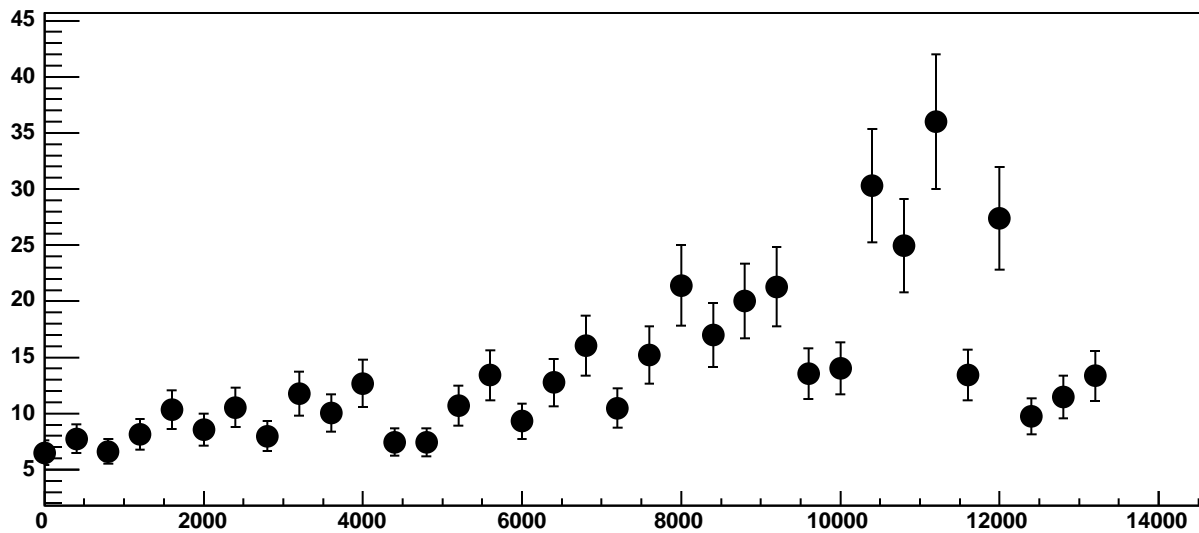


Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

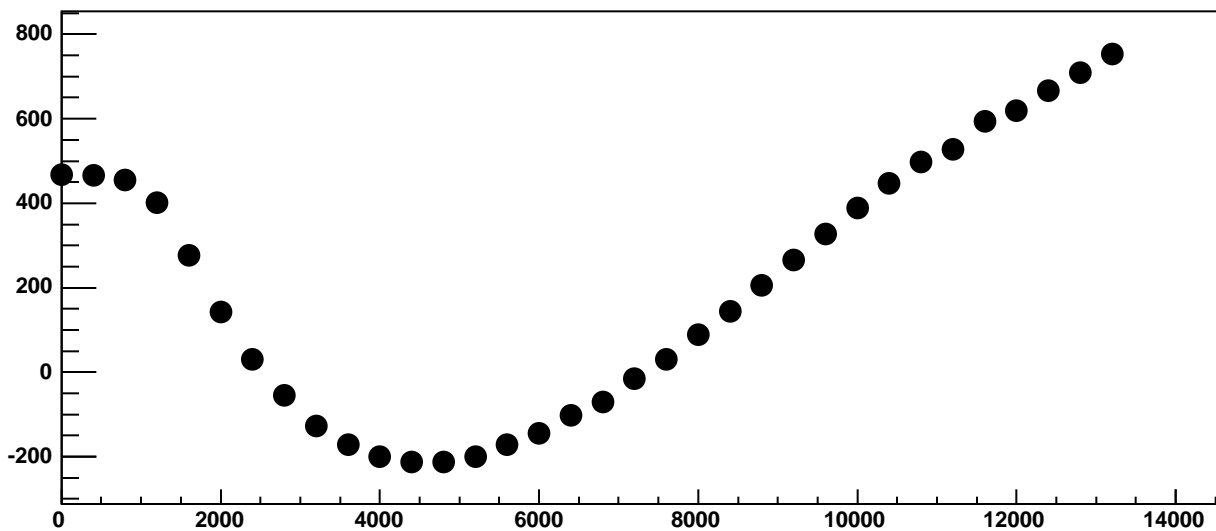


$\chi^2 / \text{ndf}$  1.696e+05 / 23  
p0  $-328.8 \pm 1.106$   
p1  $0.0002627 \pm 0.0002102$

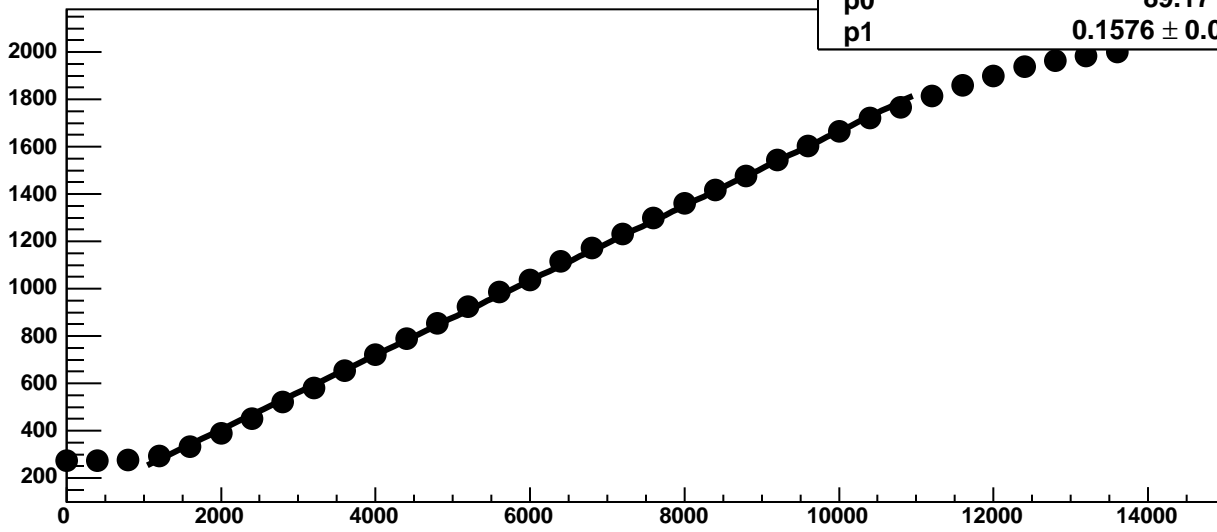
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

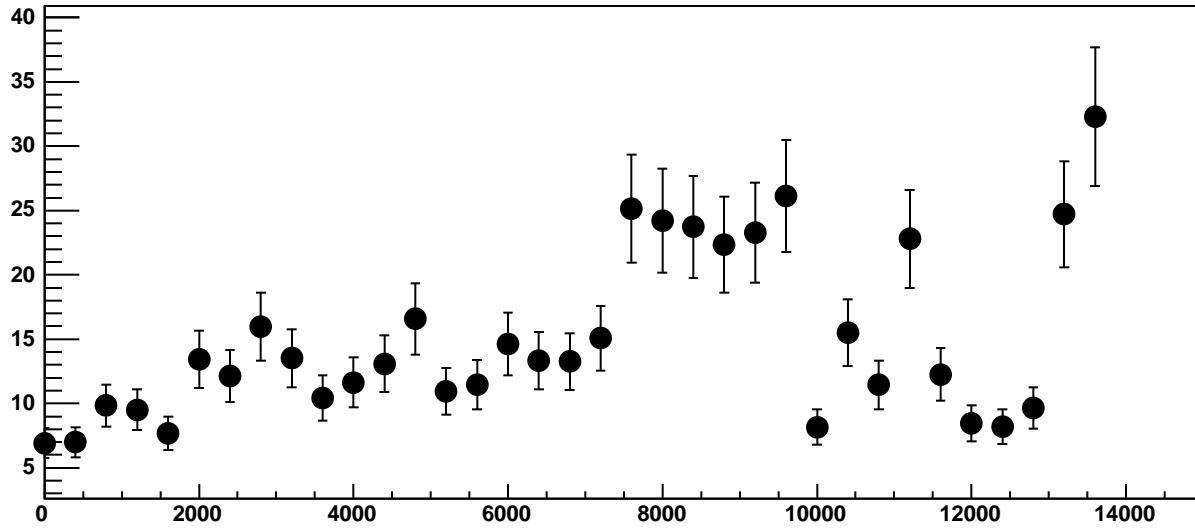


Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

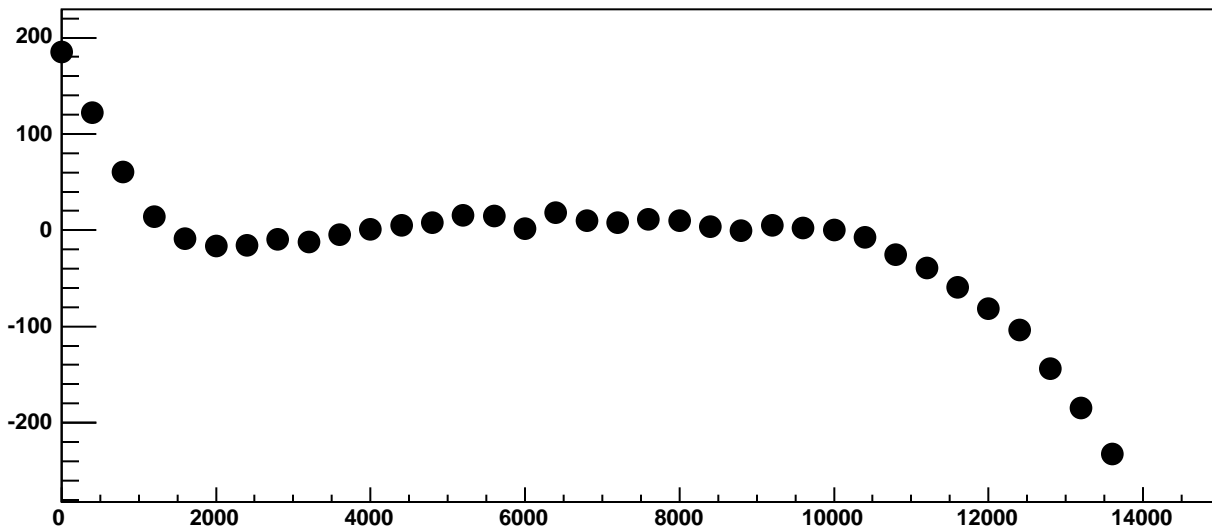


$\chi^2 / \text{ndf}$  381.5 / 23  
p0 89.17 ± 1.165  
p1 0.1576 ± 0.0001901

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

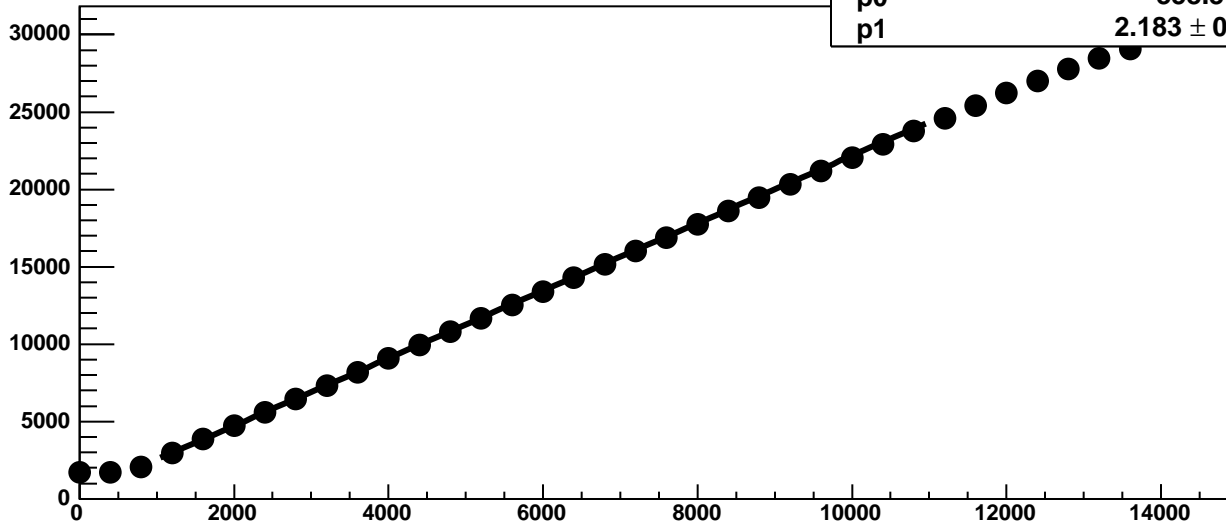


Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

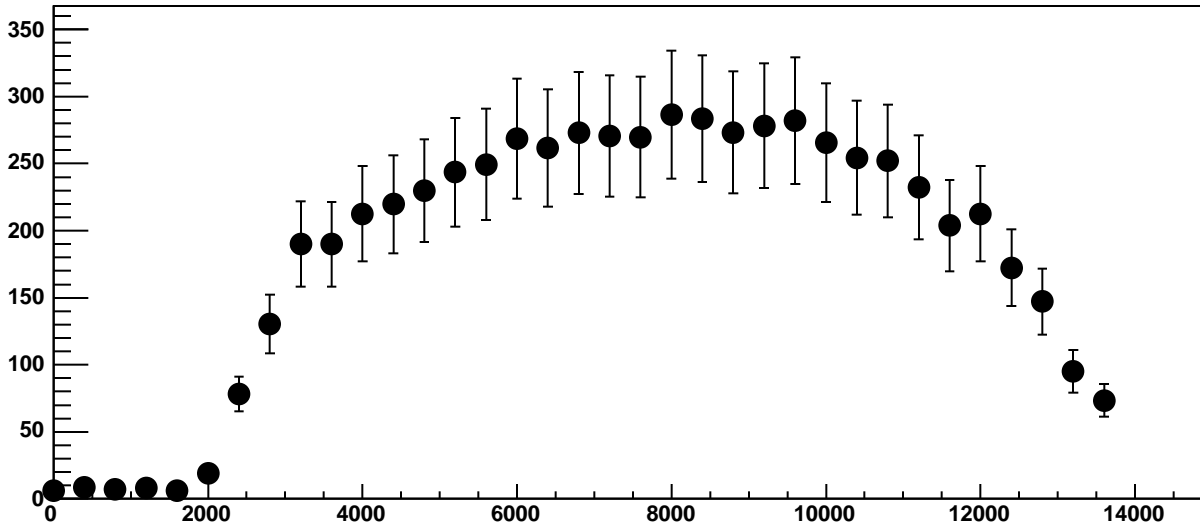


Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

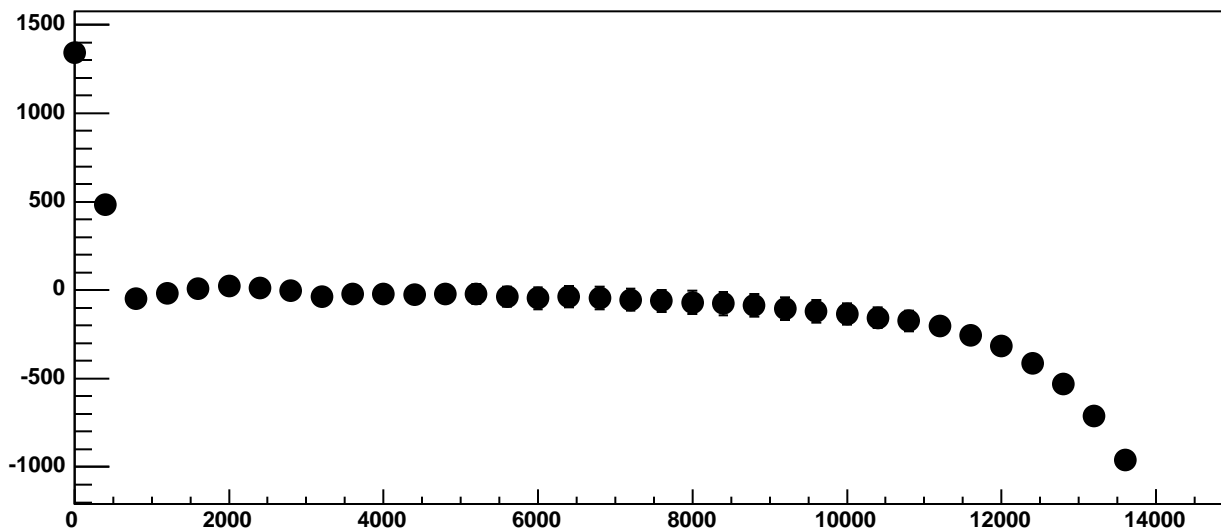
$\chi^2 / \text{ndf}$  185.6 / 23  
p0  $358.3 \pm 3.284$   
p1  $2.183 \pm 0.002031$



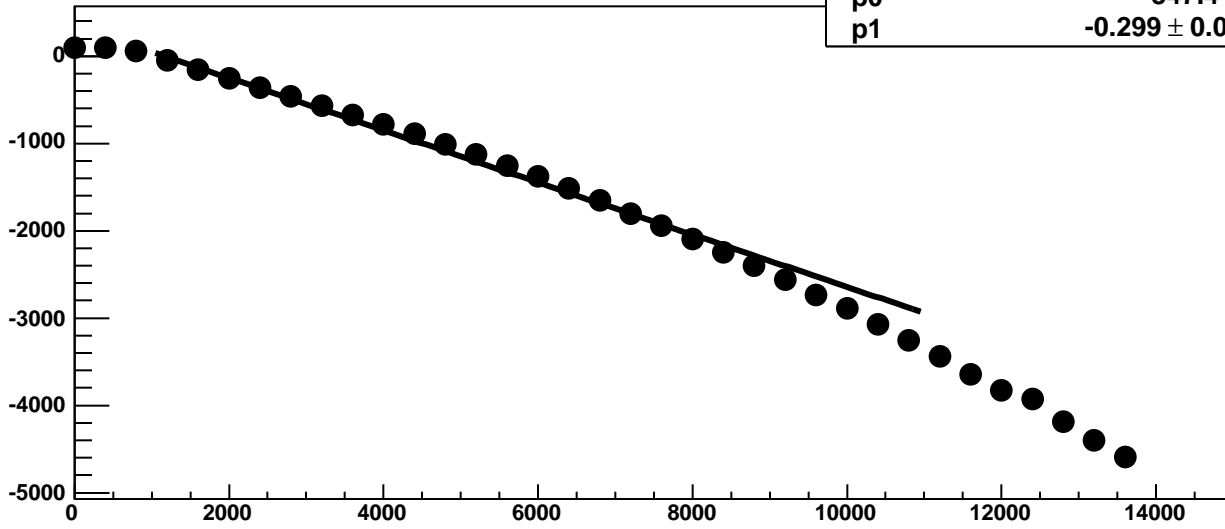
Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

3515 / 23

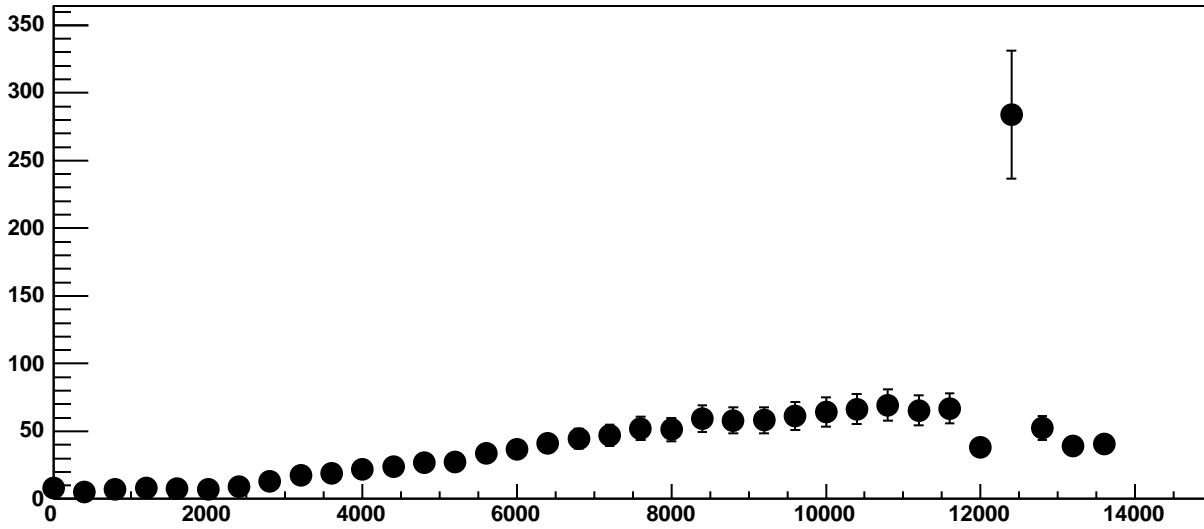
p0

$347.4 \pm 1.435$

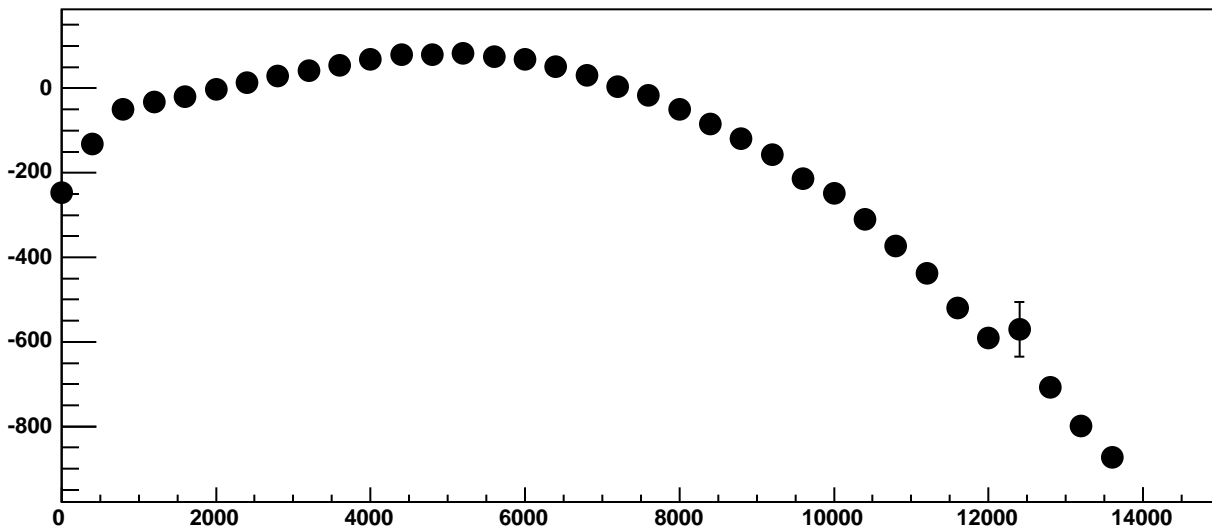
p1

$-0.299 \pm 0.0004735$

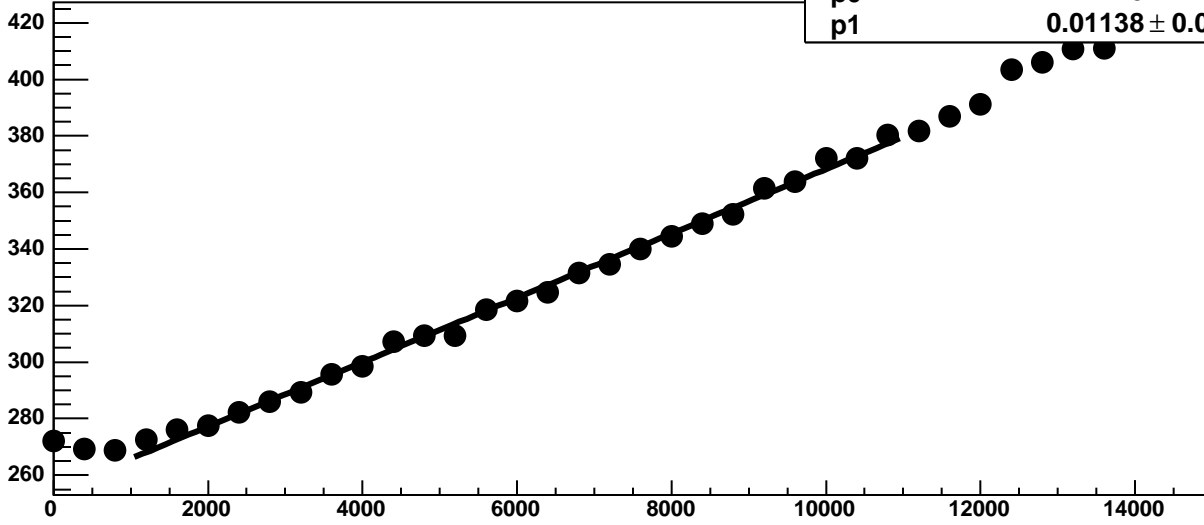
Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

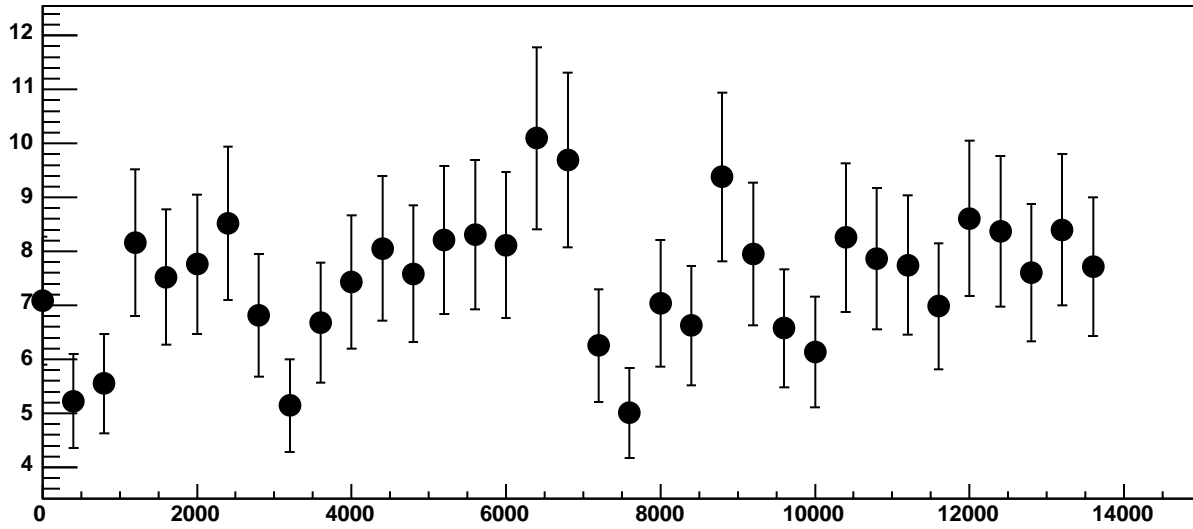


Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

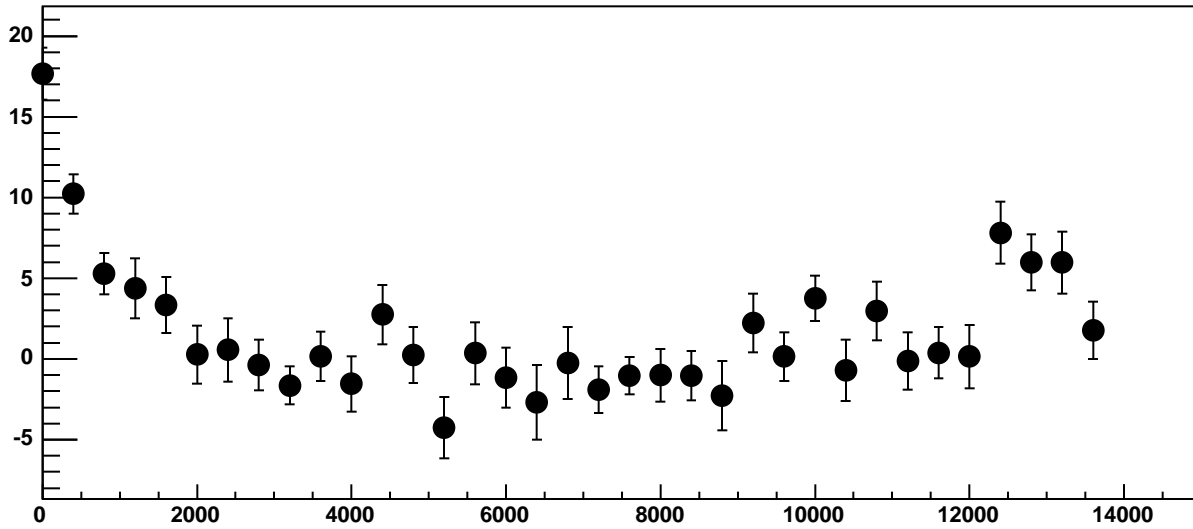


$\chi^2 / \text{ndf}$  37.27 / 23  
p0  $254.4 \pm 0.7759$   
p1  $0.01138 \pm 0.0001159$

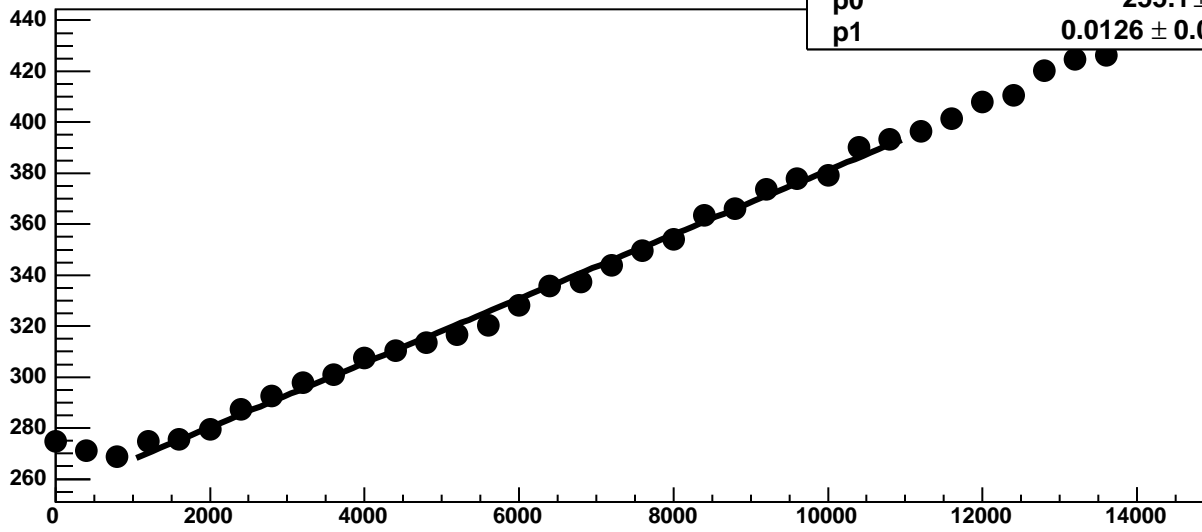
Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

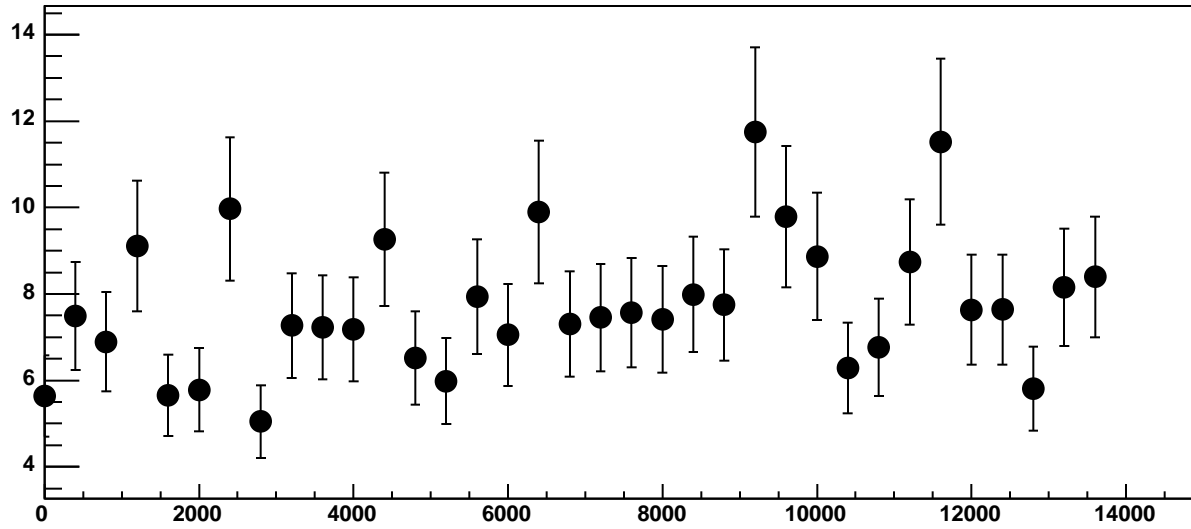


Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

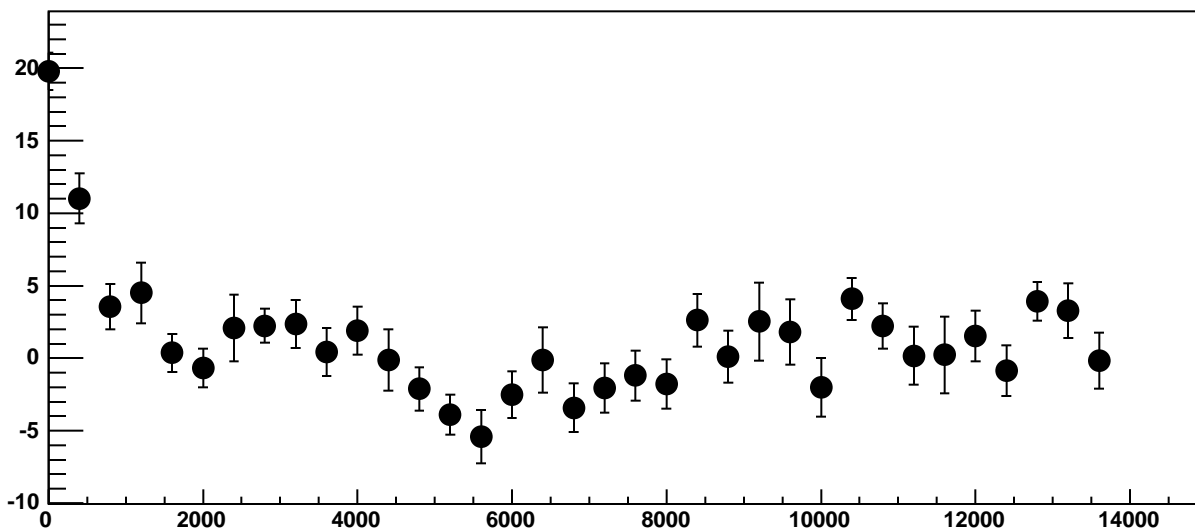


$\chi^2 / \text{ndf}$  56.14 / 23  
p0  $255.1 \pm 0.7248$   
p1  $0.0126 \pm 0.0001142$

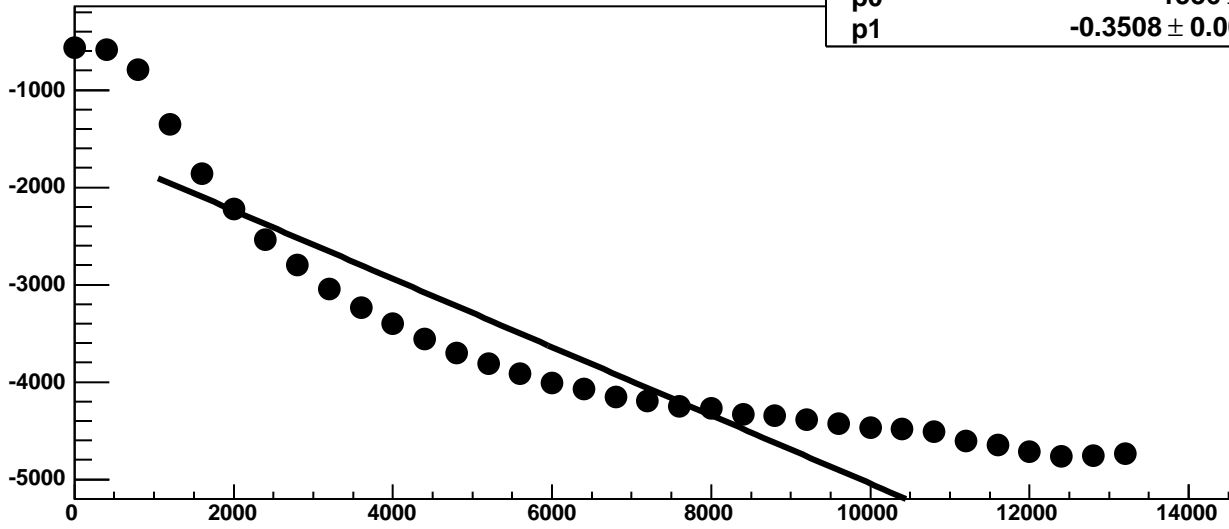
Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

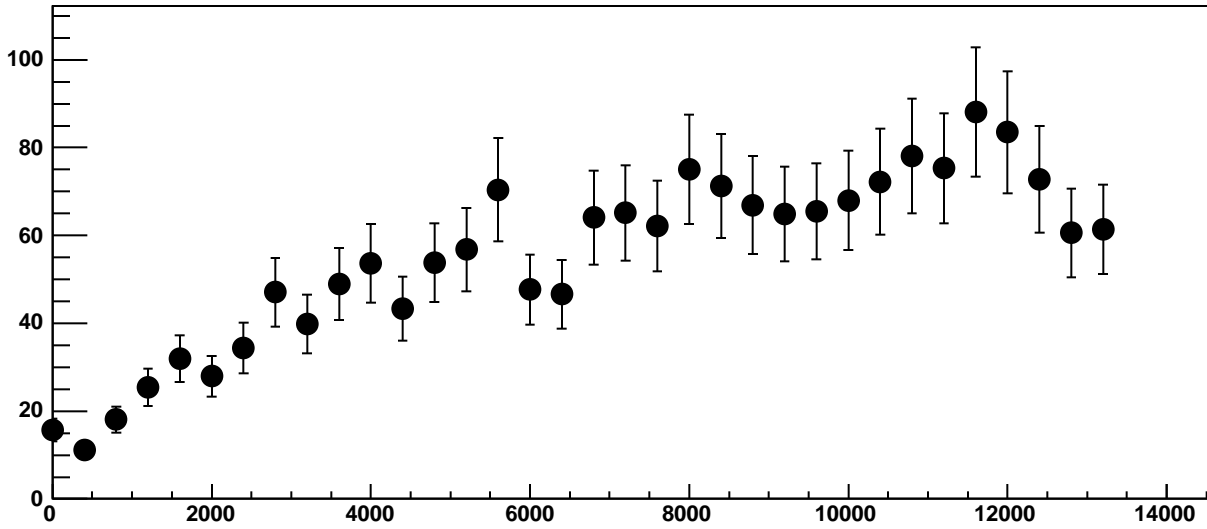


Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

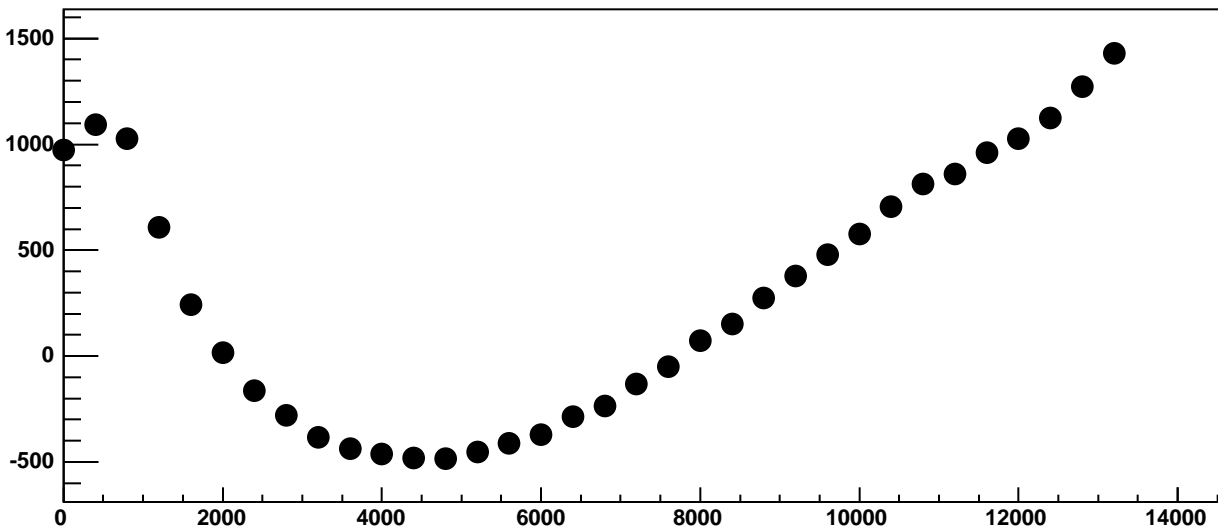


$\chi^2 / \text{ndf}$  3.3e+04 / 23  
p0 -1536 ± 3.932  
p1 -0.3508 ± 0.0007748

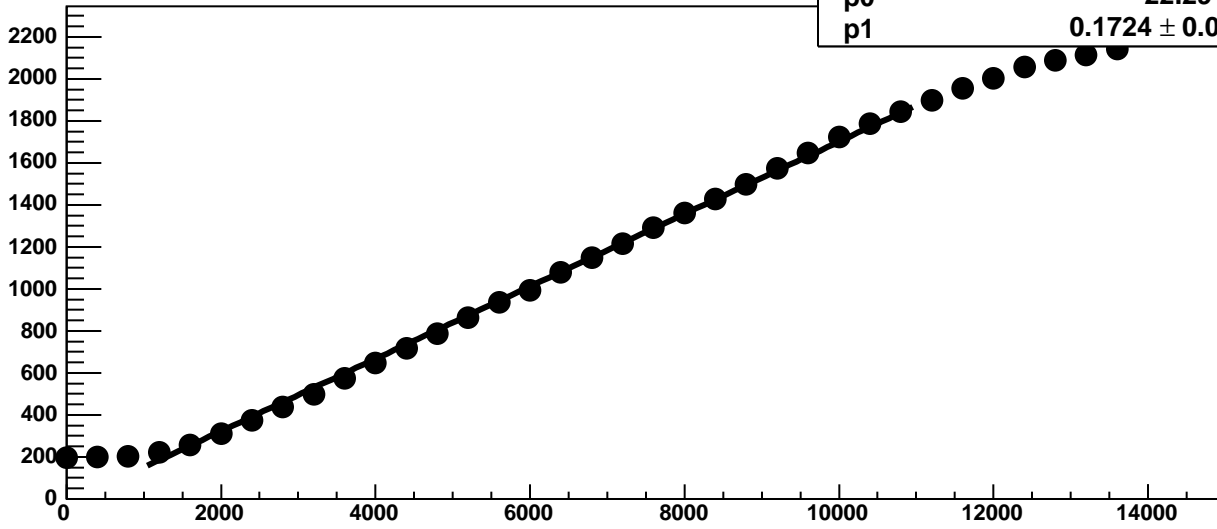
Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

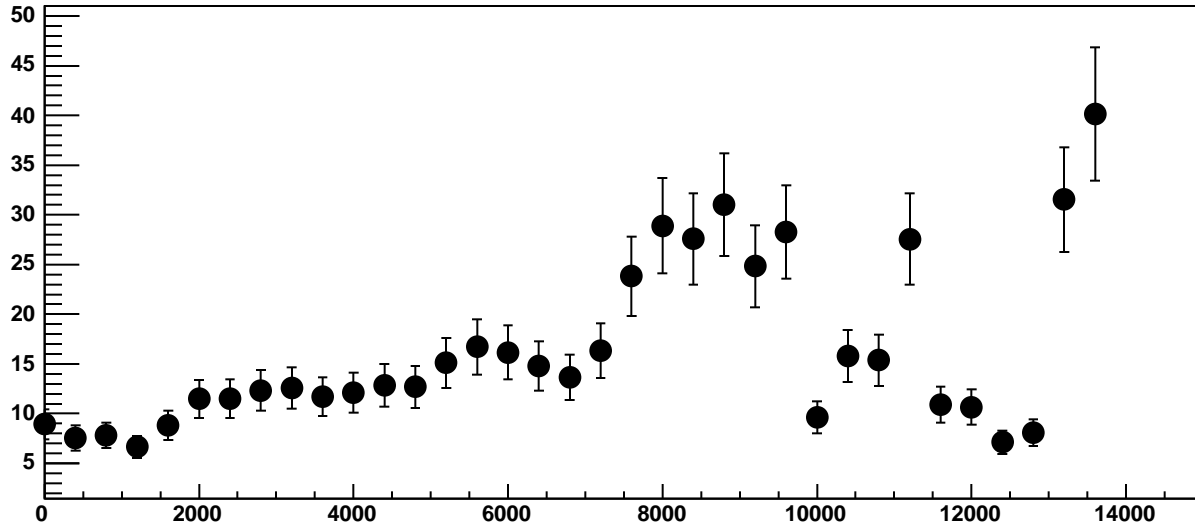


Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

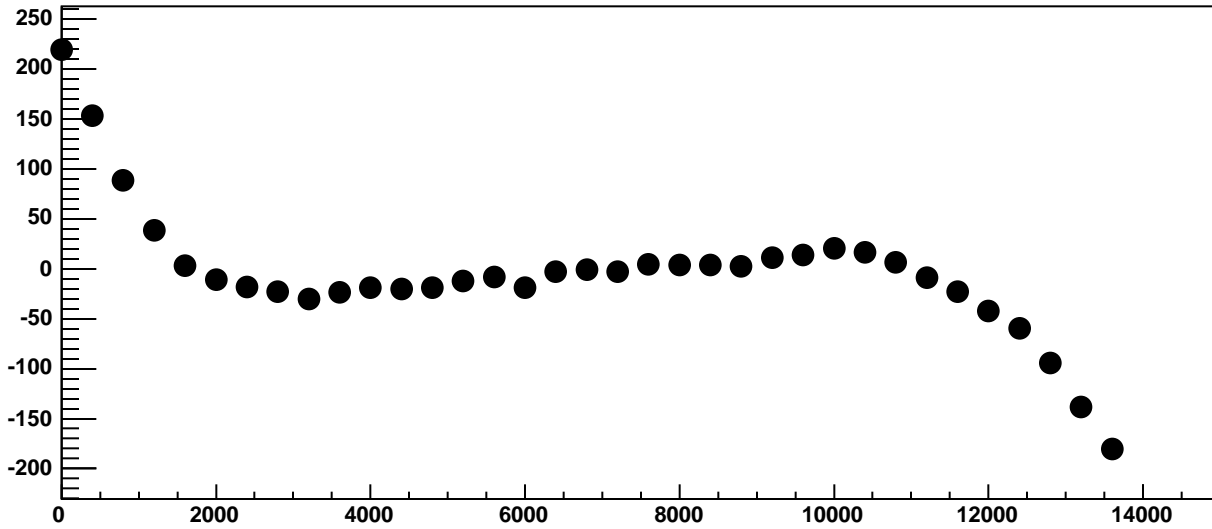


$\chi^2 / \text{ndf}$  1248 / 23  
p0  $-22.29 \pm 1.095$   
p1  $0.1724 \pm 0.0001986$

Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

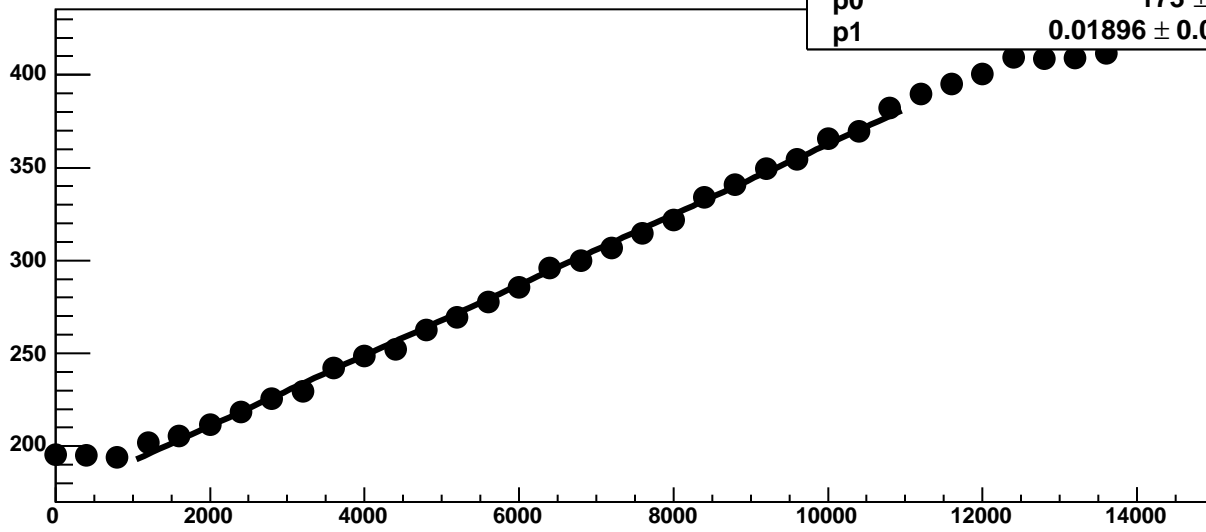


Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

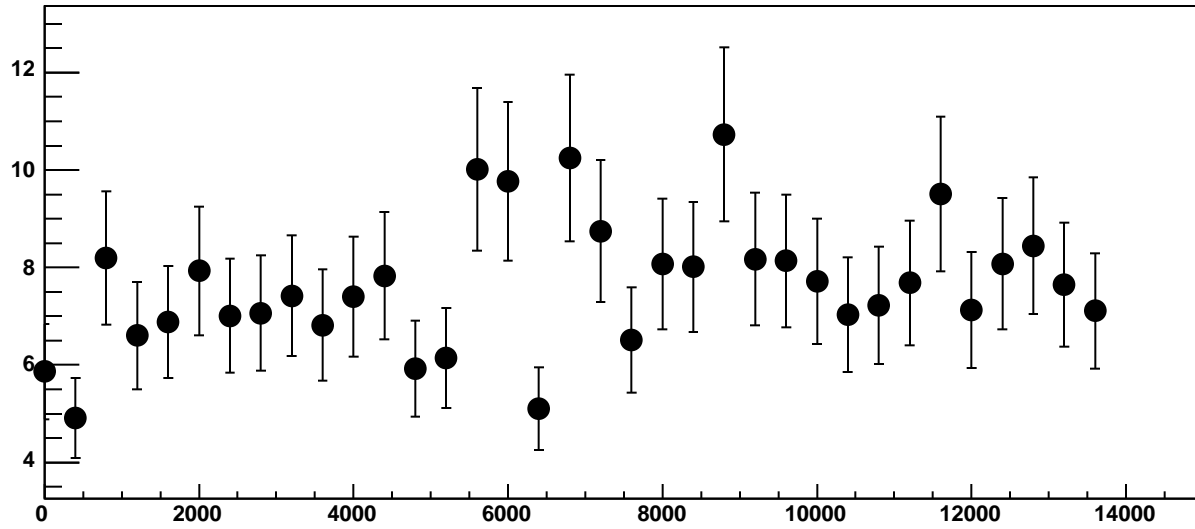




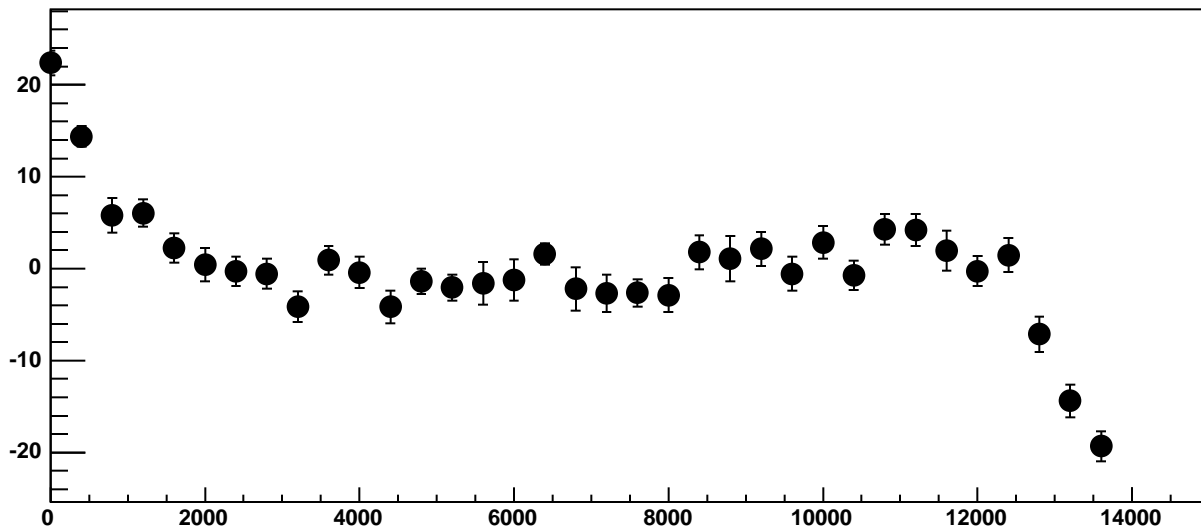
Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



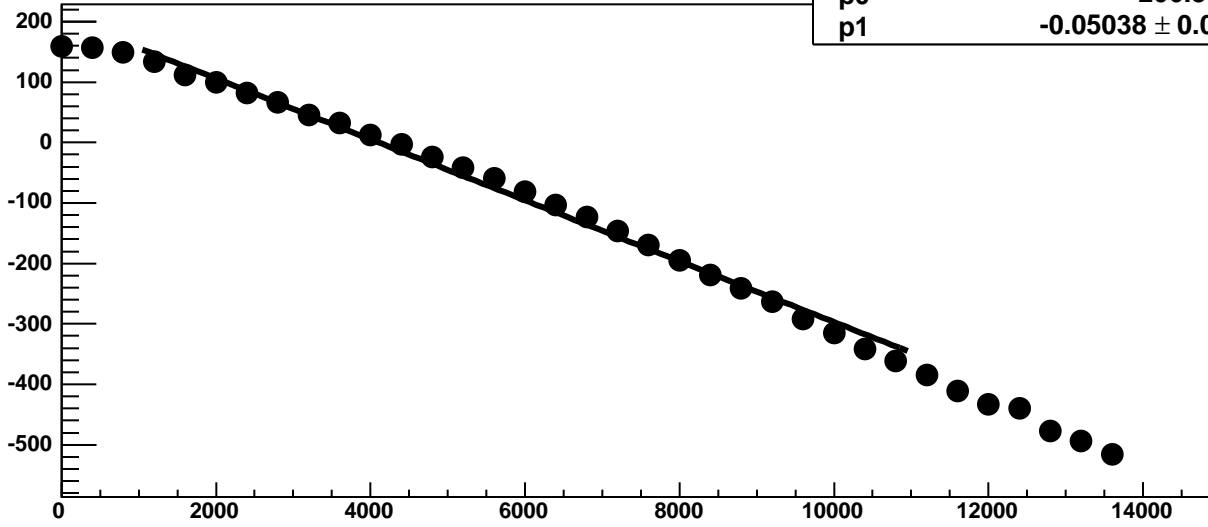
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

766.1 / 23

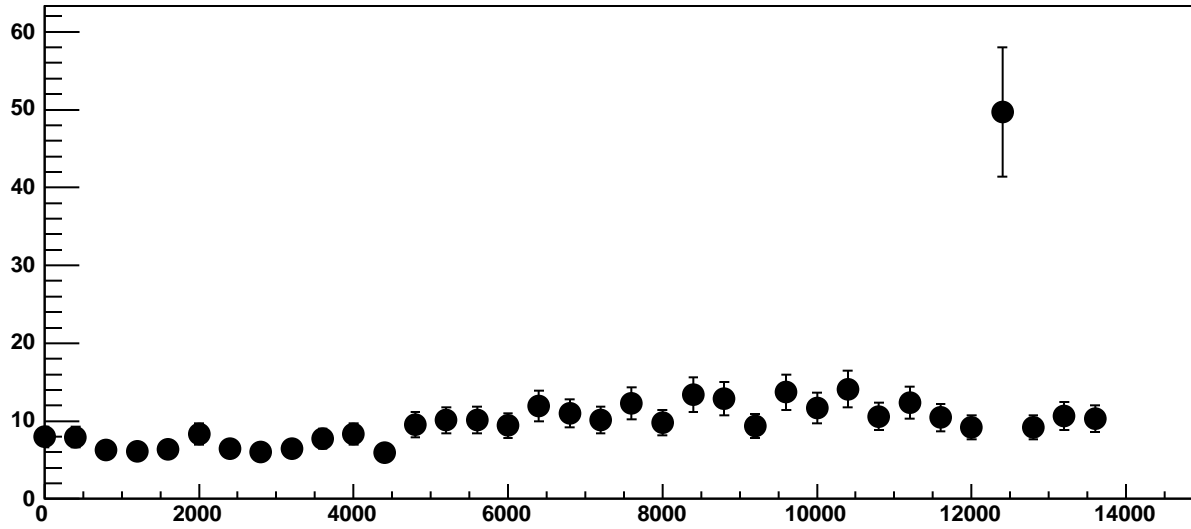
p0

$206.8 \pm 0.783$

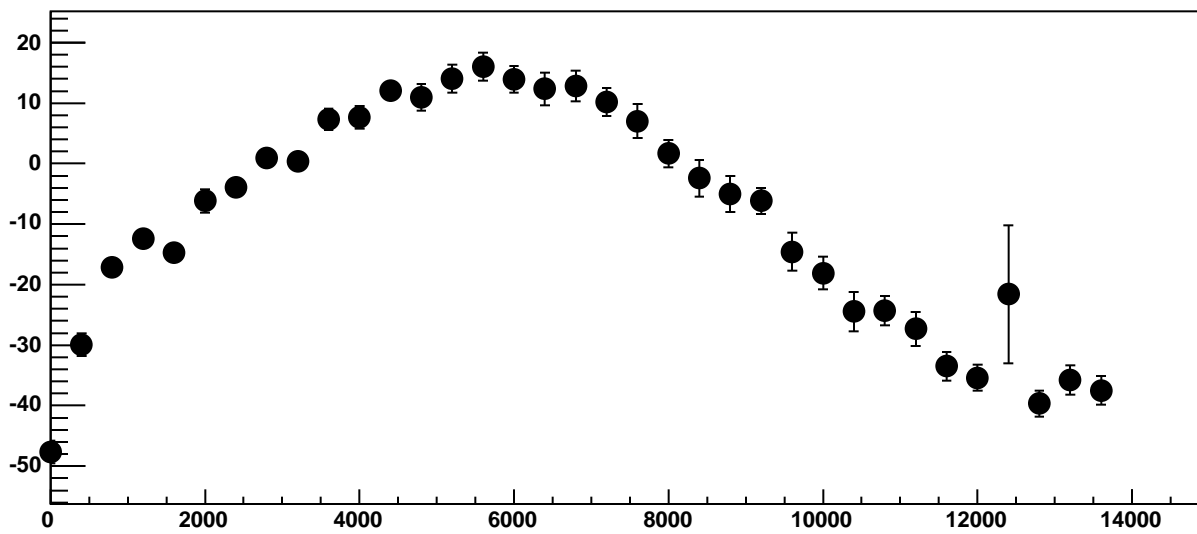
p1

$-0.05038 \pm 0.0001443$

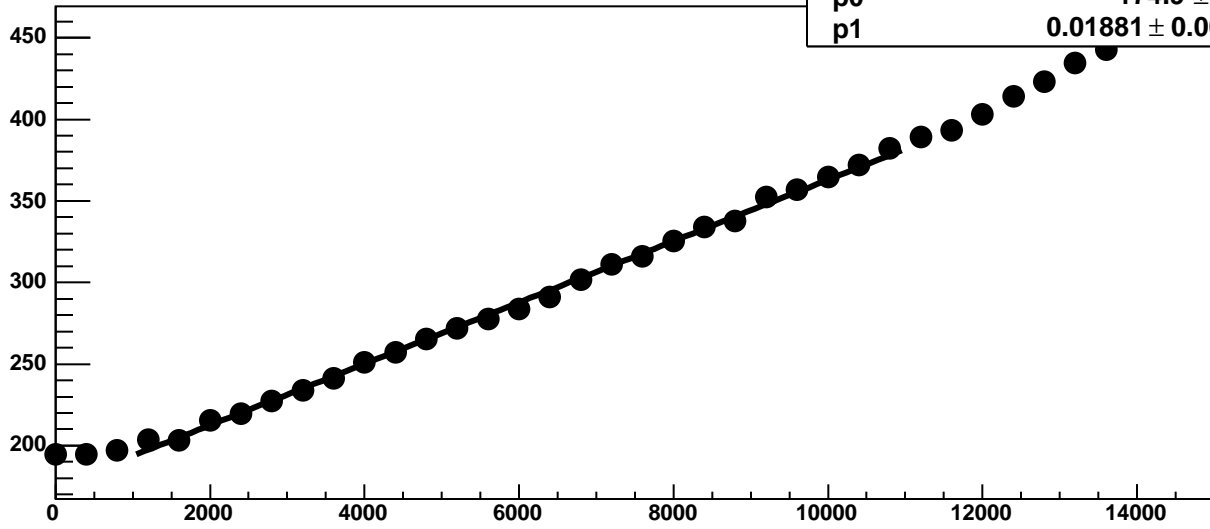
Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

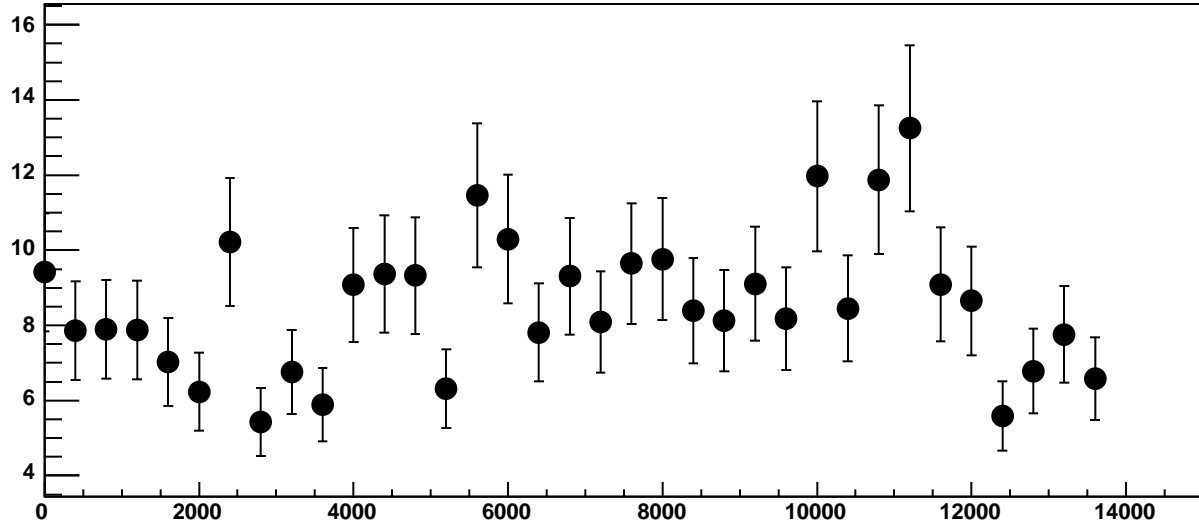


Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

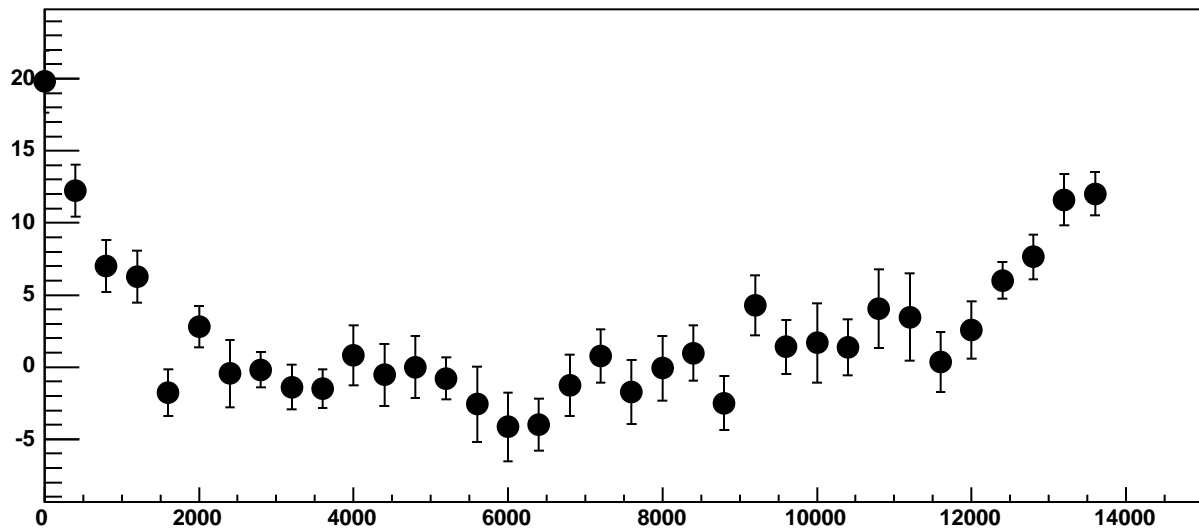


$\chi^2 / \text{ndf}$  39.8 / 23  
p0  $174.9 \pm 0.7878$   
p1  $0.01881 \pm 0.0001305$

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

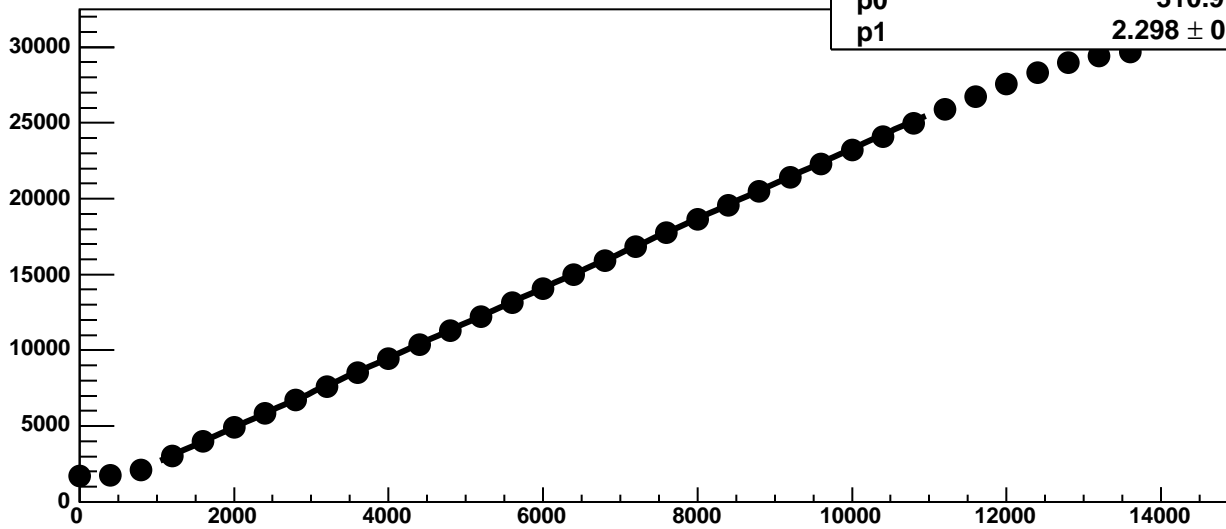


Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

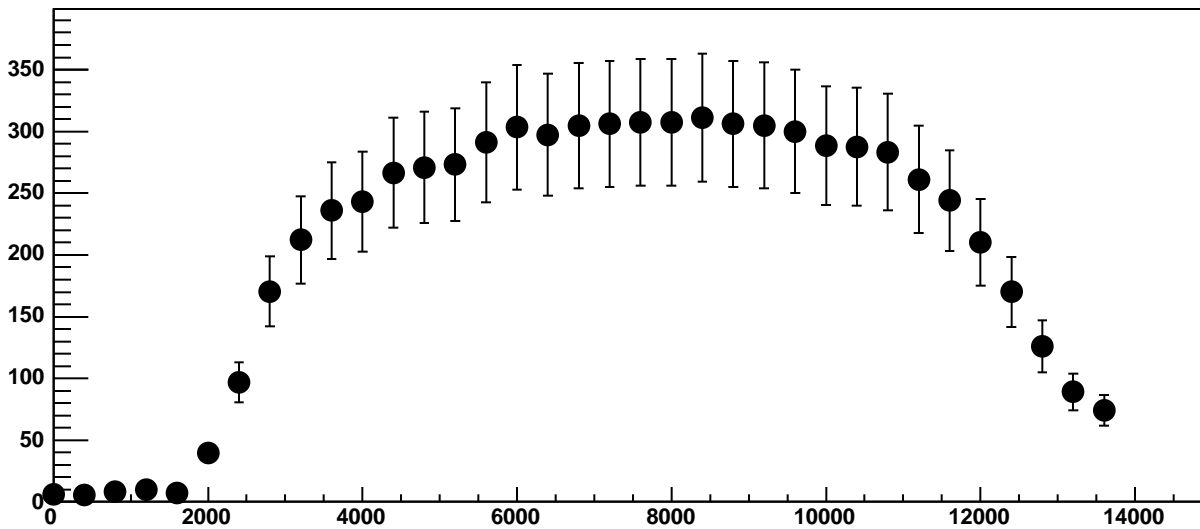


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

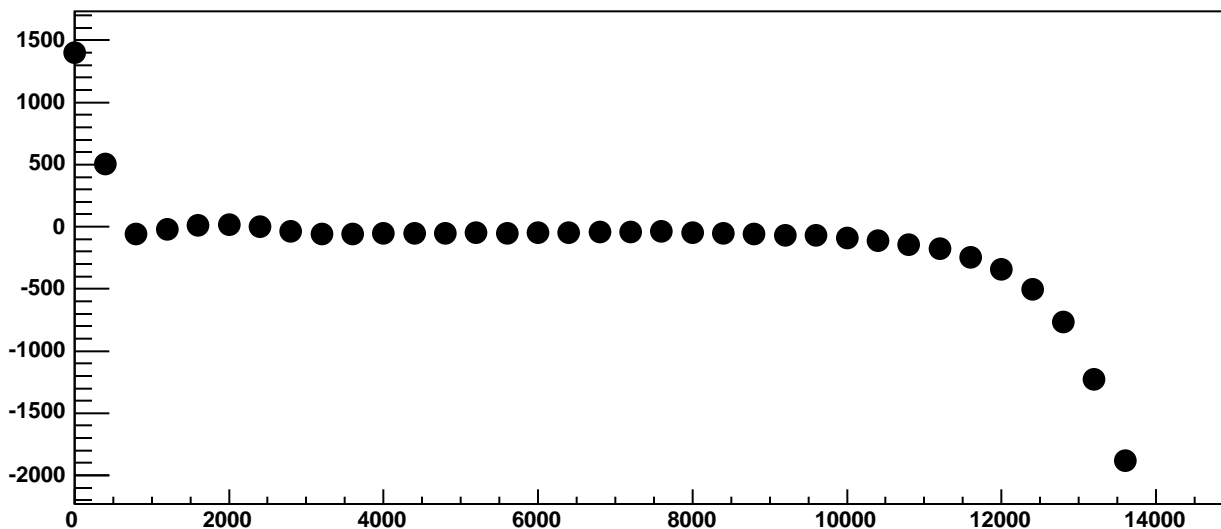
$\chi^2 / \text{ndf}$  160.7 / 23  
p0  $310.9 \pm 3.812$   
p1  $2.298 \pm 0.002345$



Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

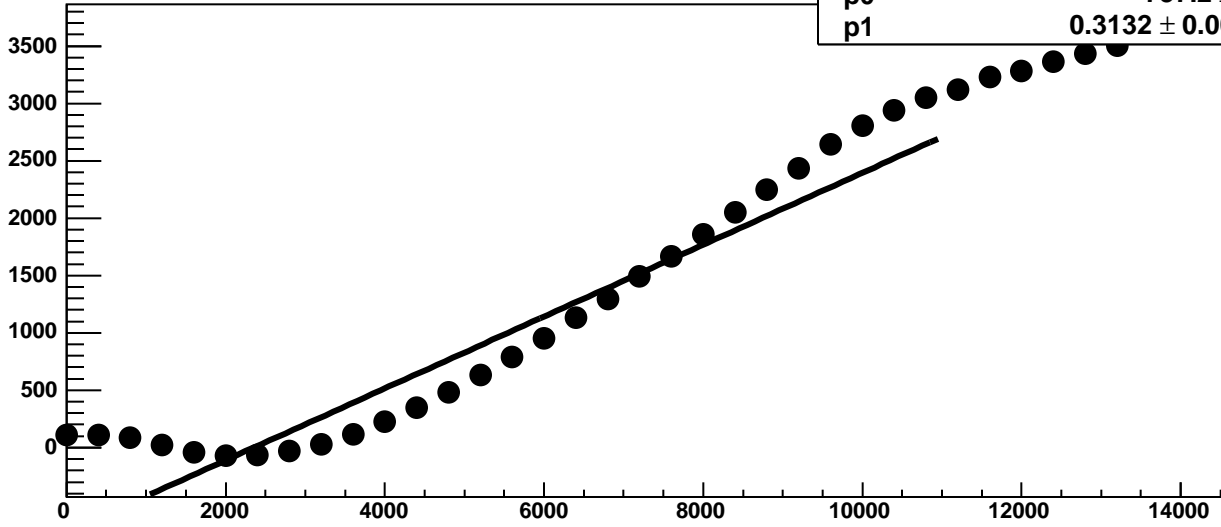


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

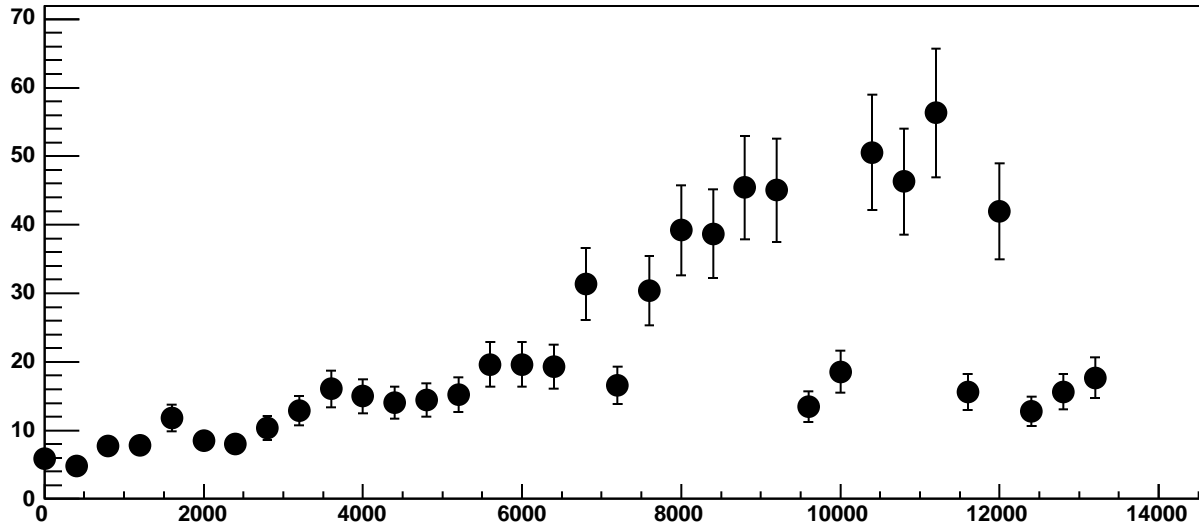


Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

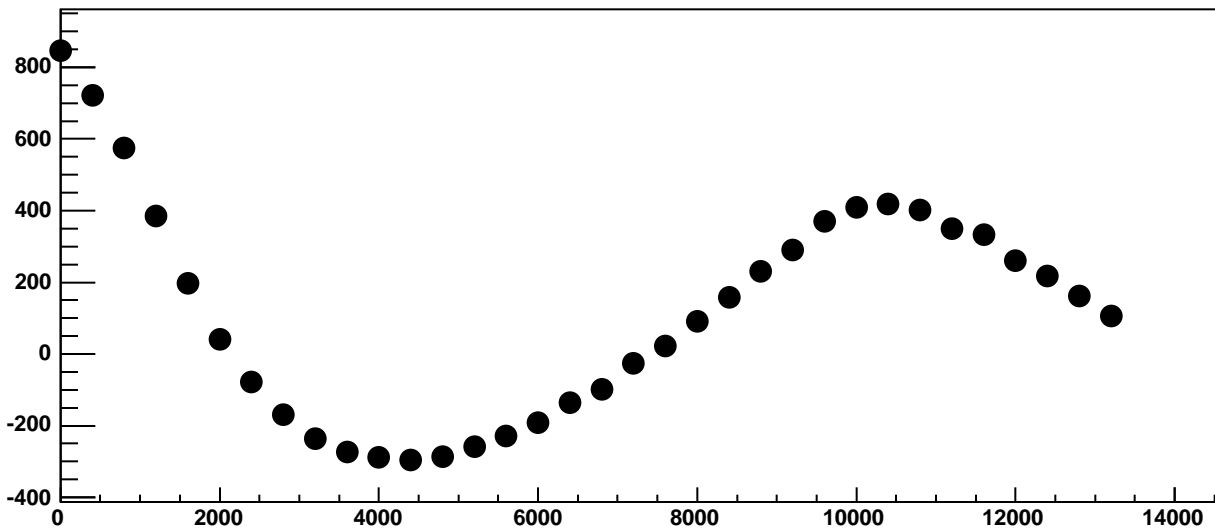
$\chi^2 / \text{ndf}$  1.32e+05 / 23  
p0 -737.2 ± 1.217  
p1 0.3132 ± 0.0002665



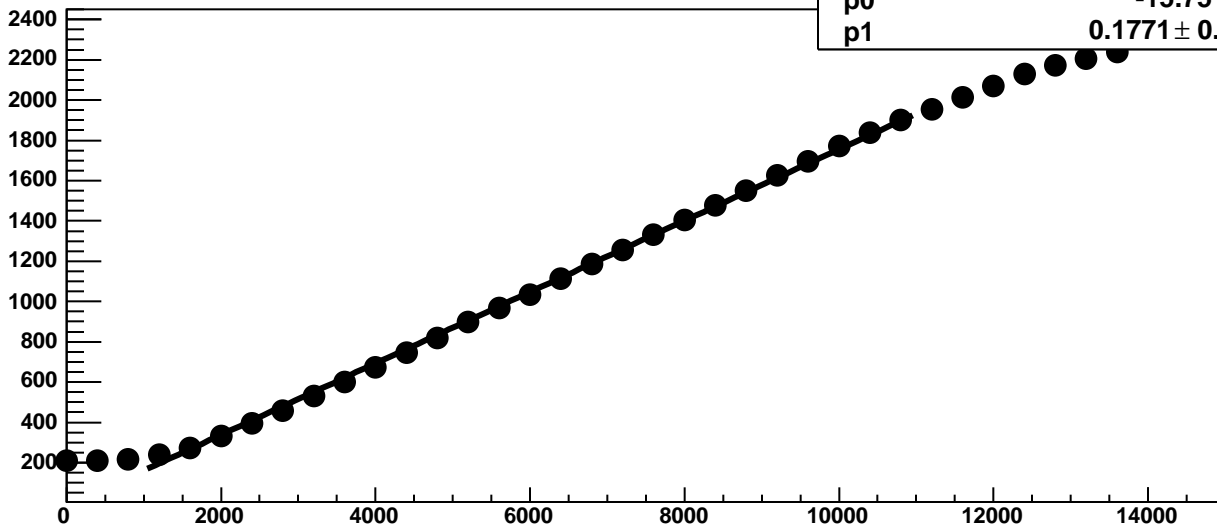
Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



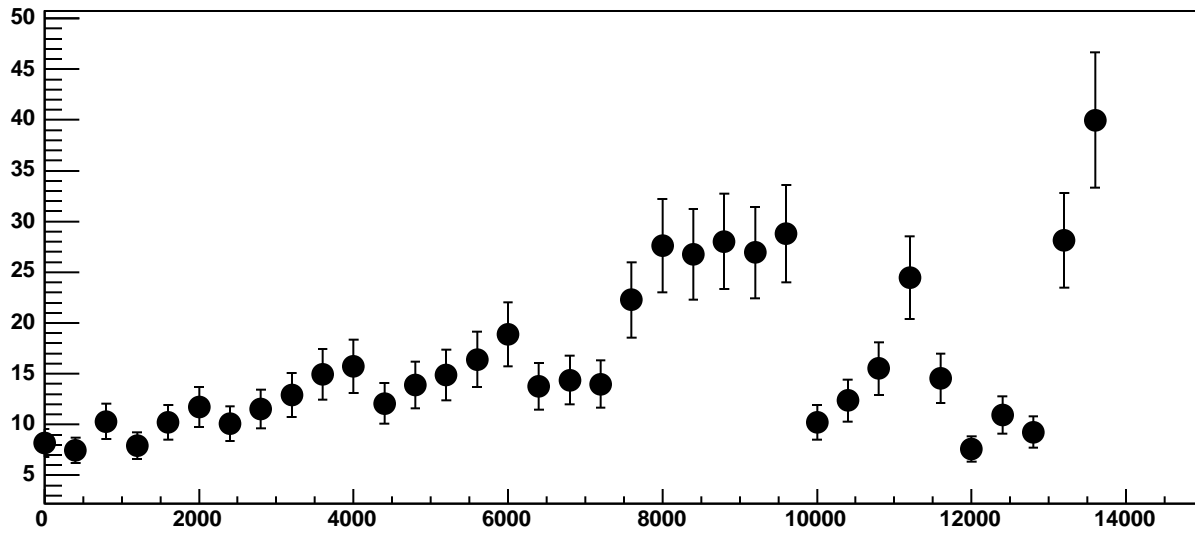
Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



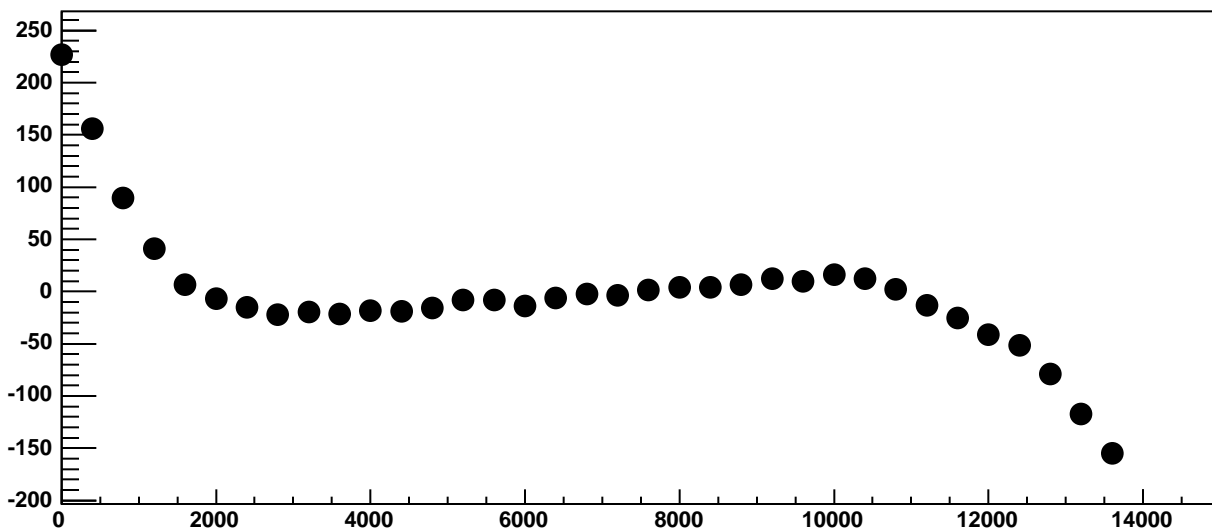
Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

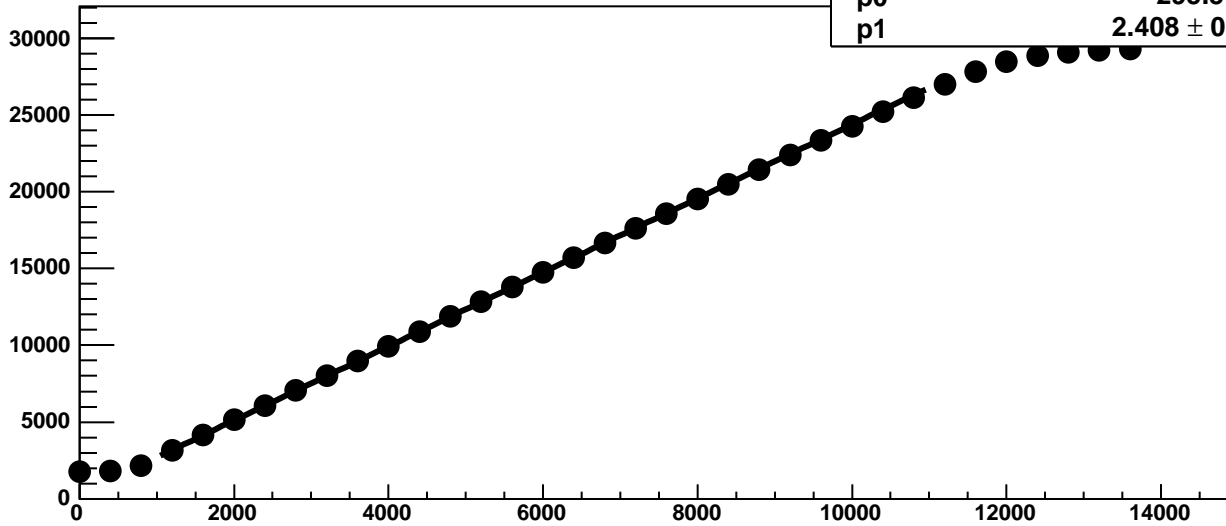


Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

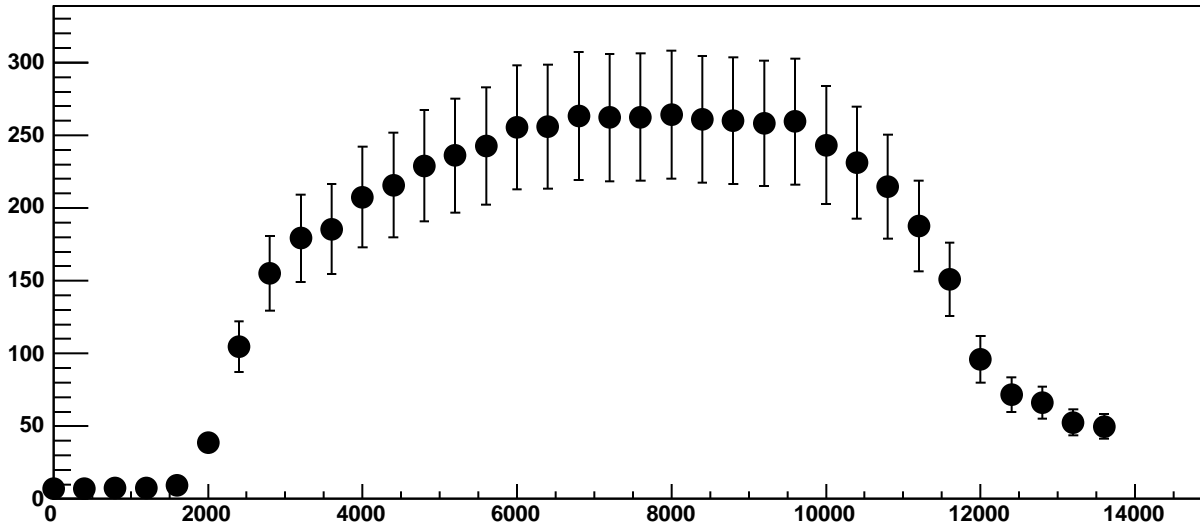


Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

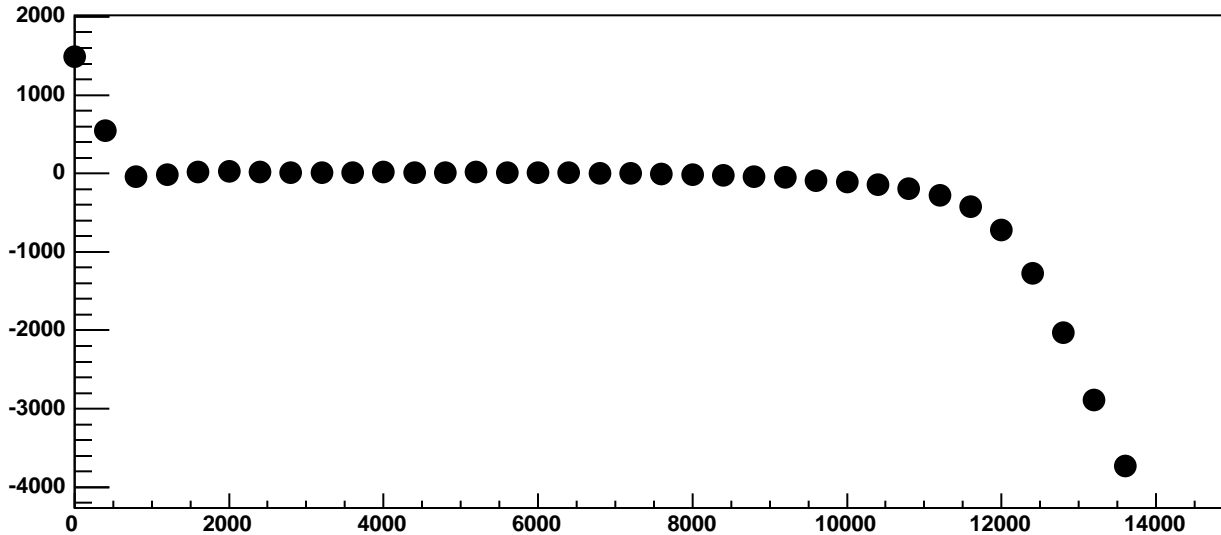
$\chi^2 / \text{ndf}$  162.3 / 23  
p0  $295.5 \pm 3.107$   
p1  $2.408 \pm 0.001952$



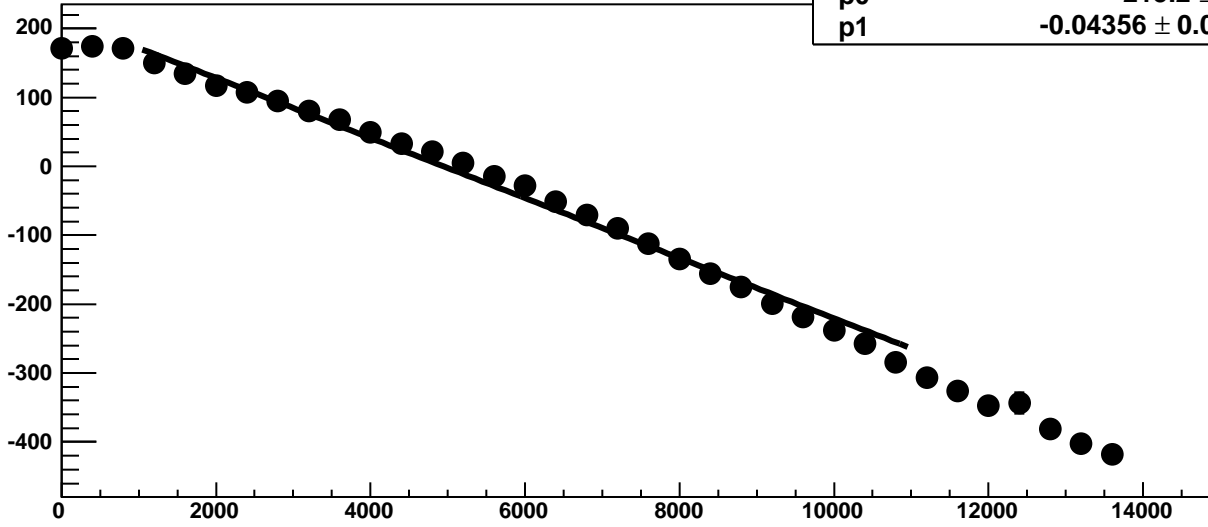
Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

625.5 / 23

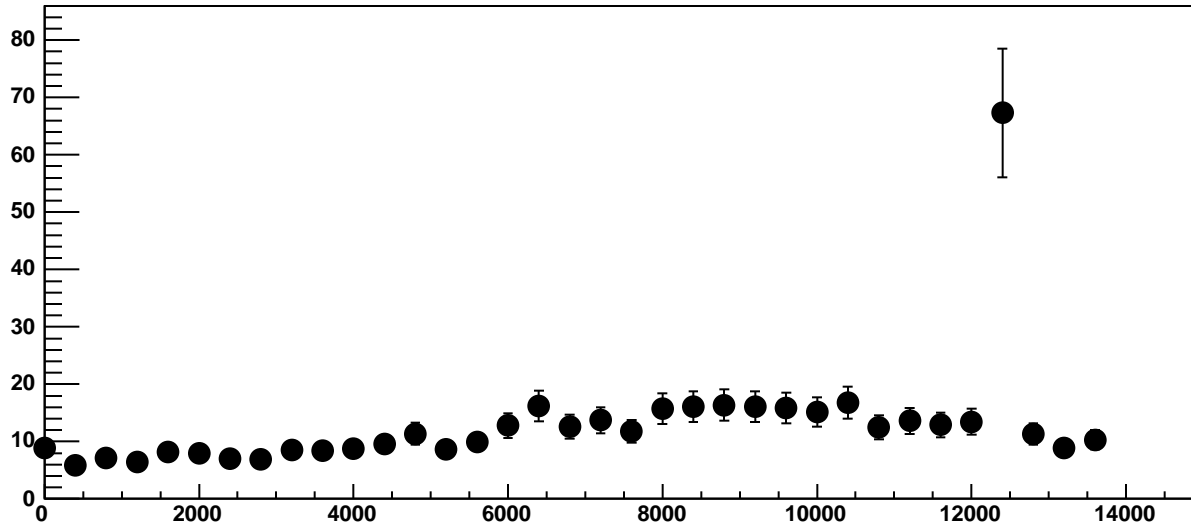
p0

$215.2 \pm 0.8986$

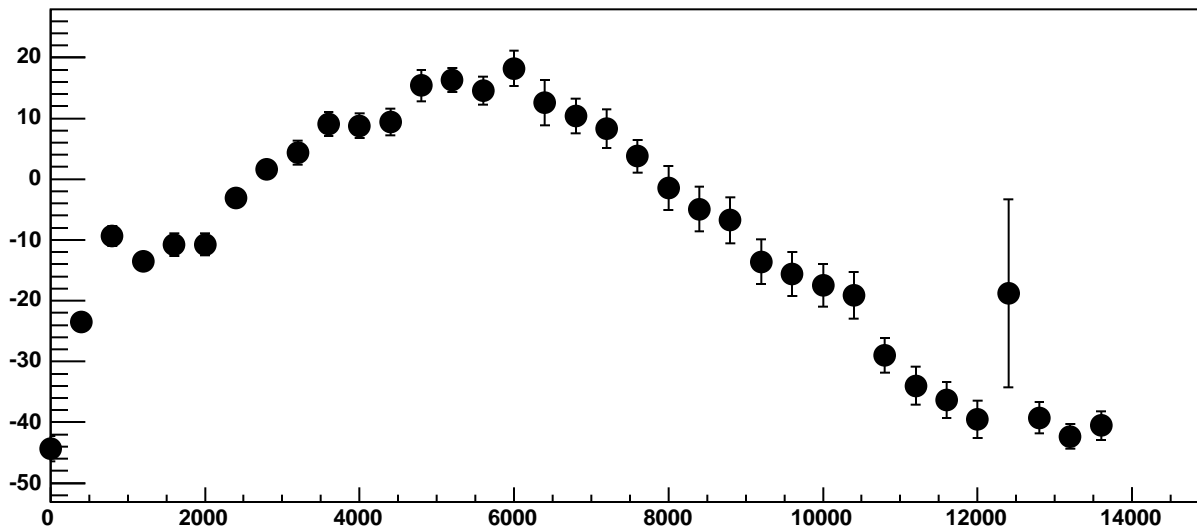
p1

$-0.04356 \pm 0.0001739$

Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

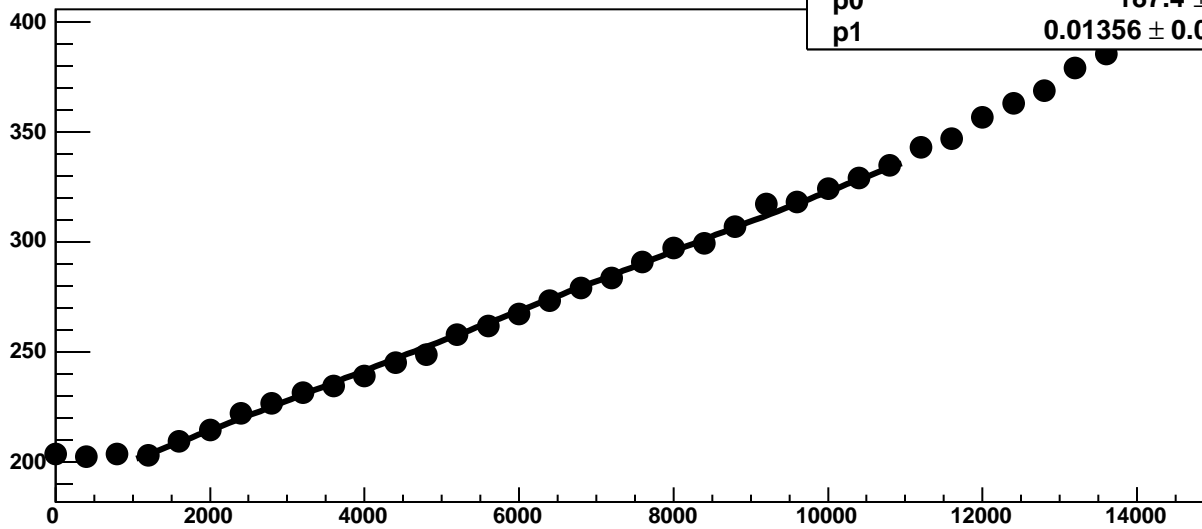


Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



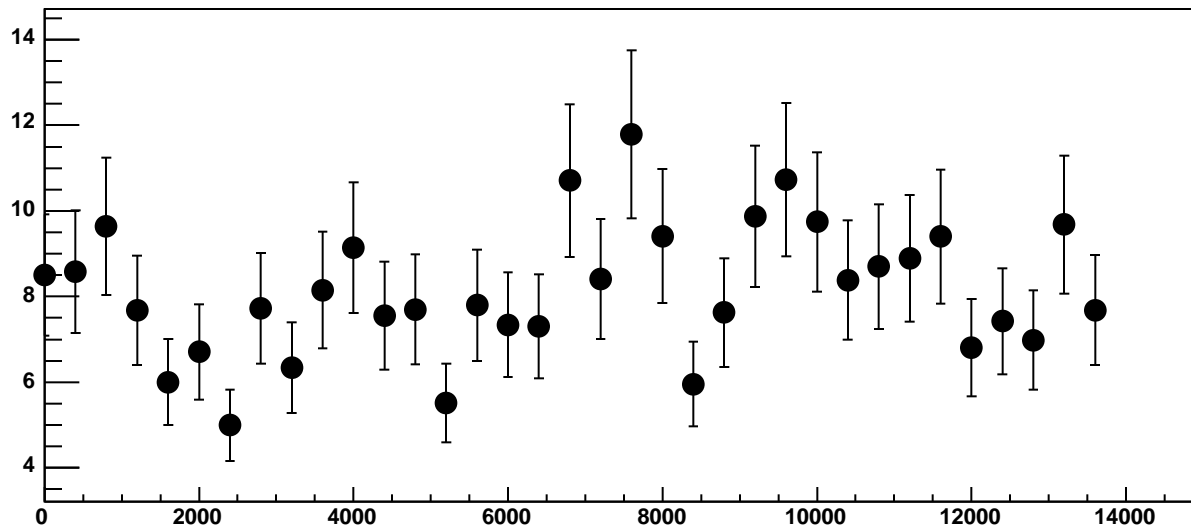


Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

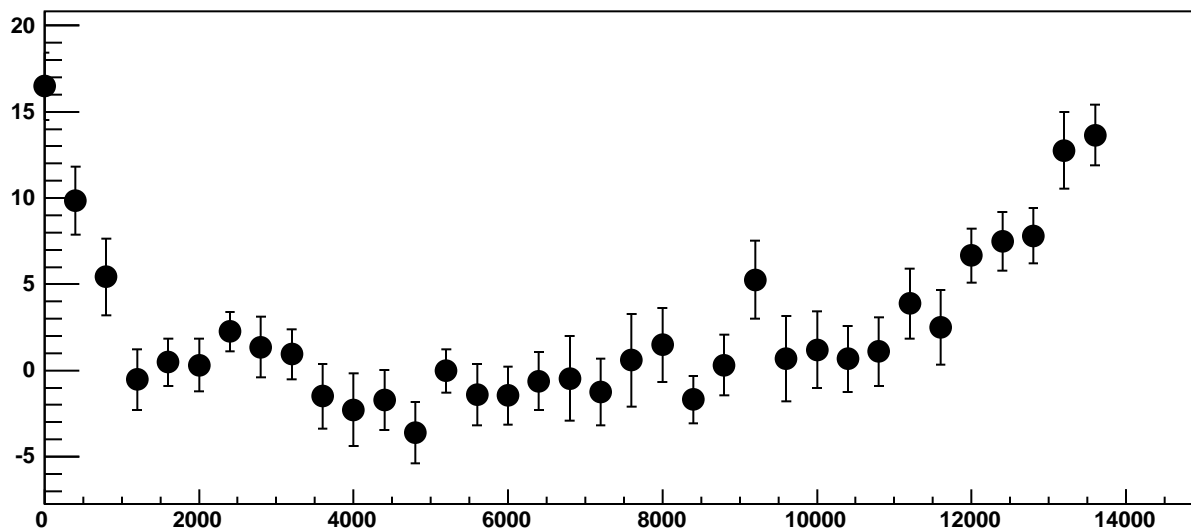


$\chi^2 / \text{ndf}$  22.27 / 23  
p0 187.4 ± 0.7358  
p1 0.01356 ± 0.0001215

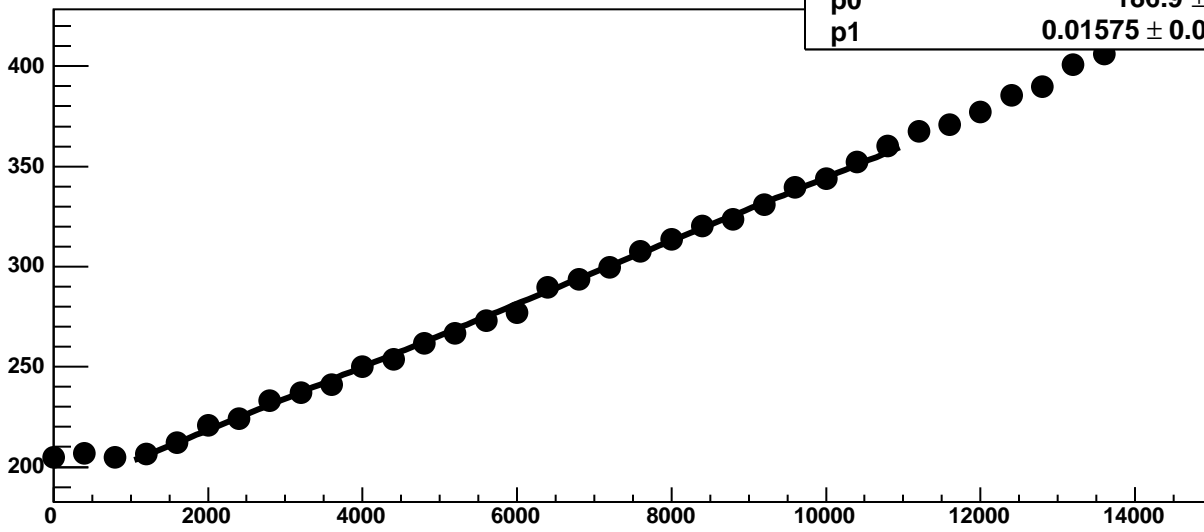
Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

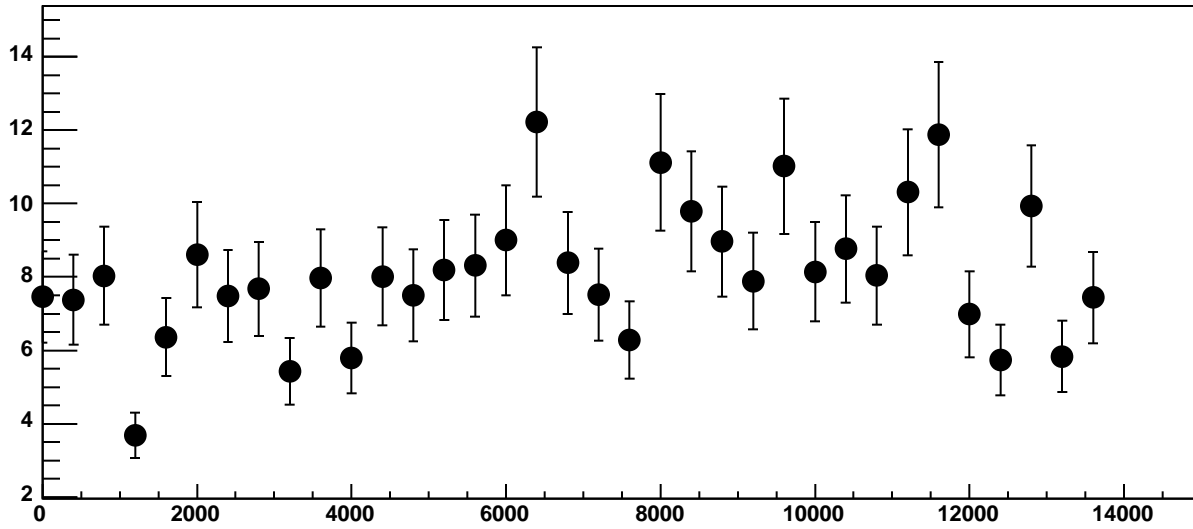


Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

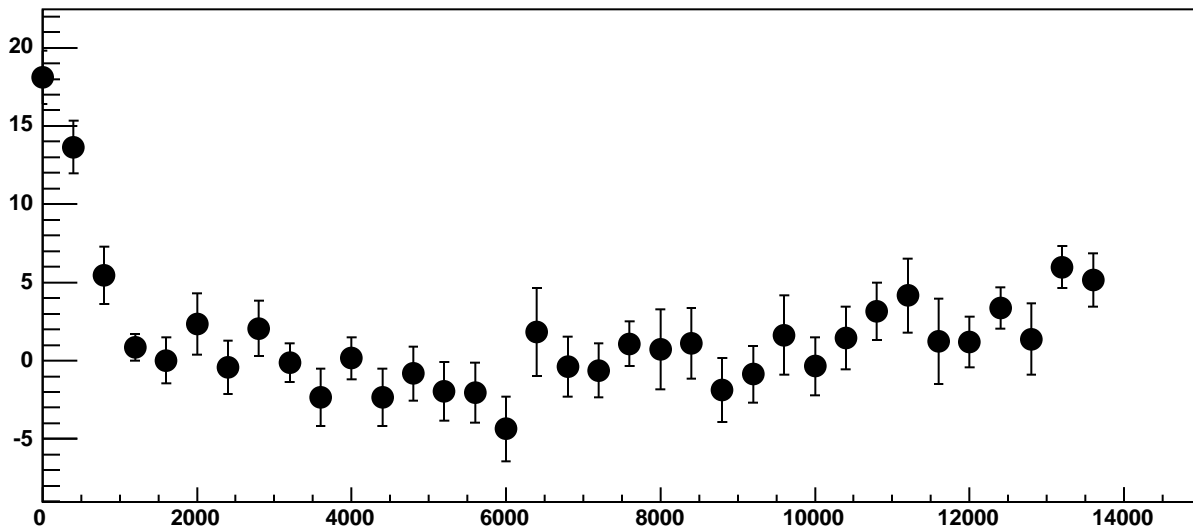


$\chi^2 / \text{ndf}$  20.44 / 23  
p0  $186.9 \pm 0.6475$   
p1  $0.01575 \pm 0.0001107$

Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

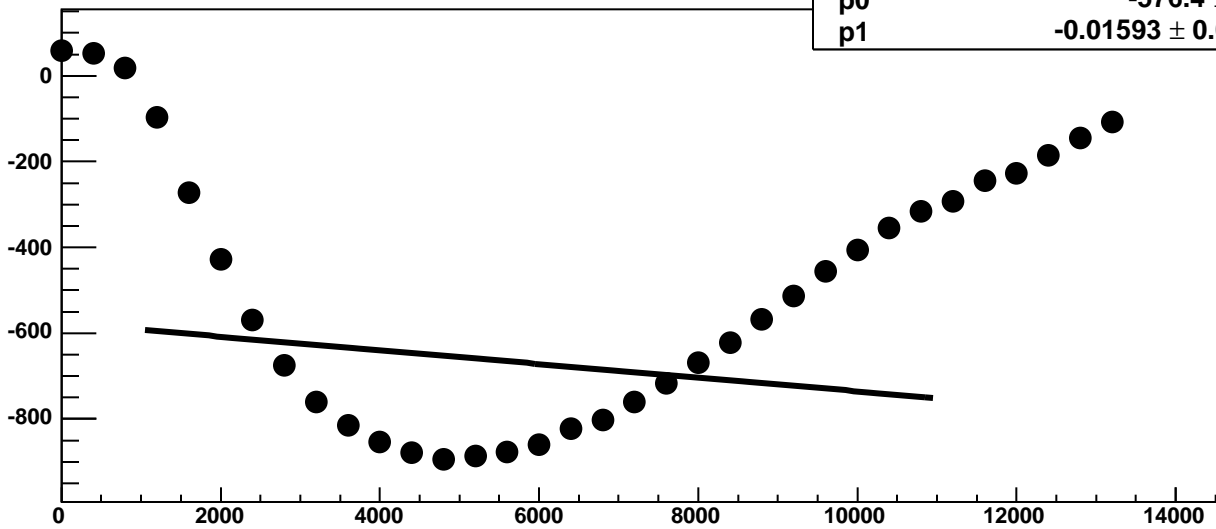


Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

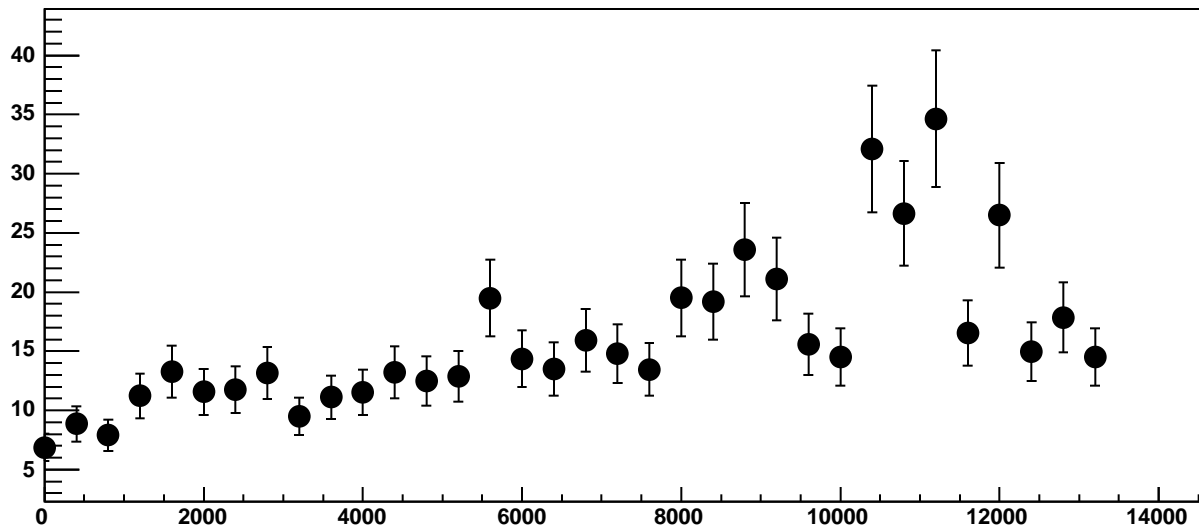


Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

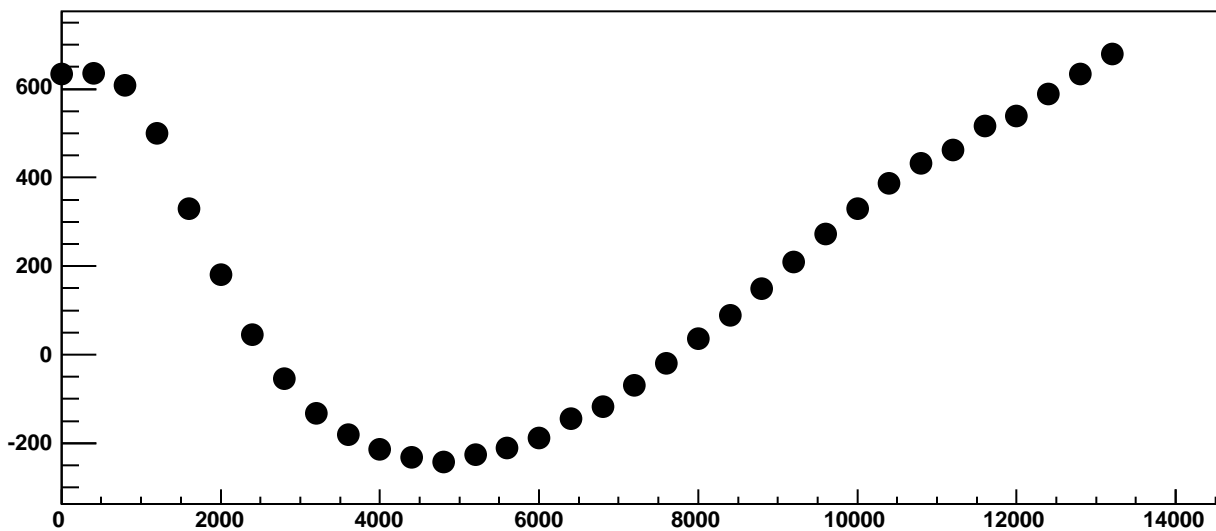
$\chi^2 / \text{ndf}$  1.244e+05 / 23  
p0 -576.4 ± 1.366  
p1 -0.01593 ± 0.000245



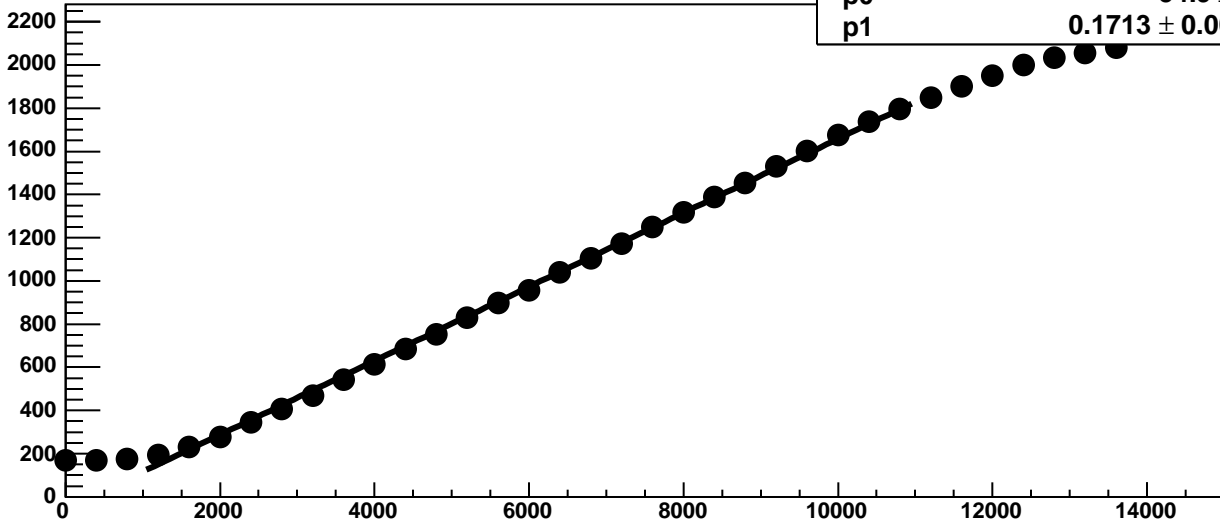
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

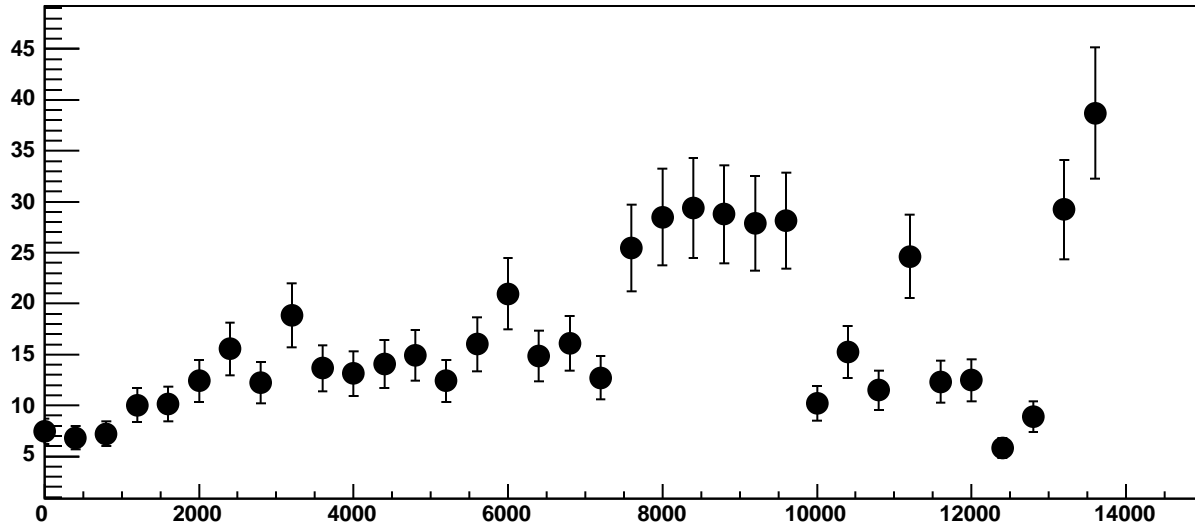


Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

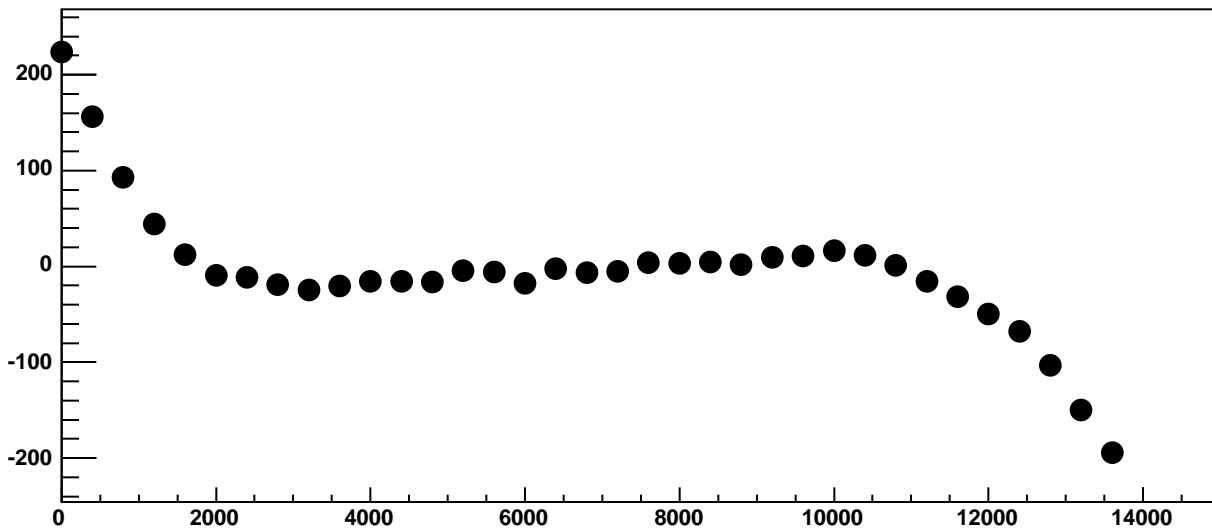


$\chi^2 / \text{ndf}$  703 / 23  
p0  $-54.9 \pm 1.313$   
p1  $0.1713 \pm 0.0002122$

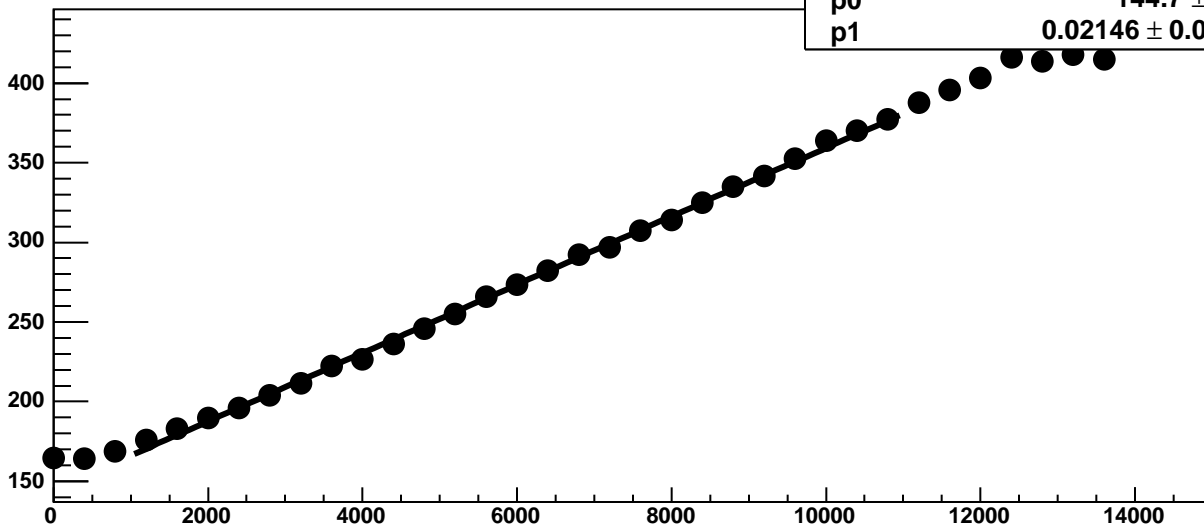
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

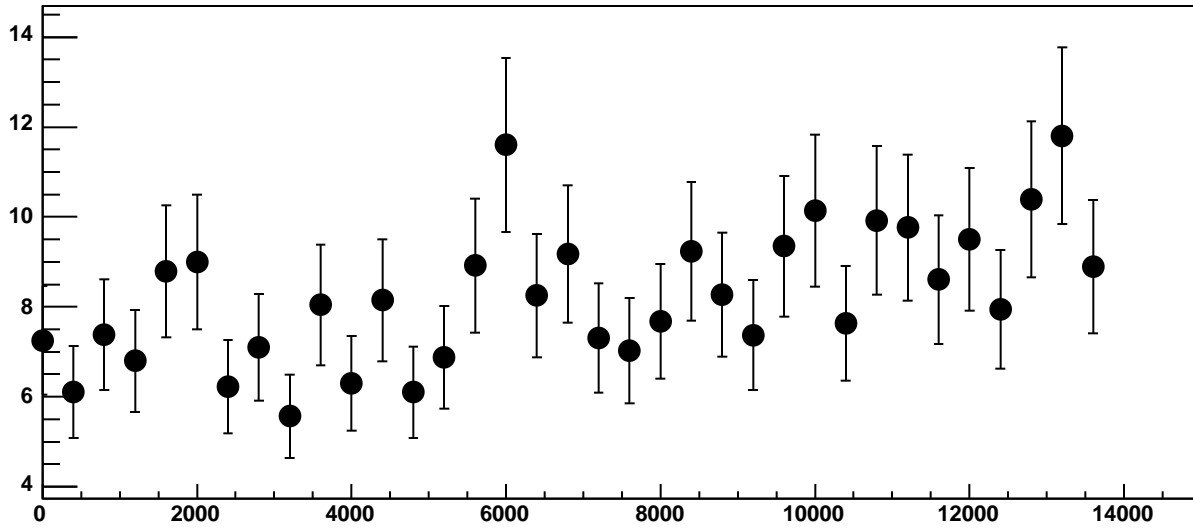


Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

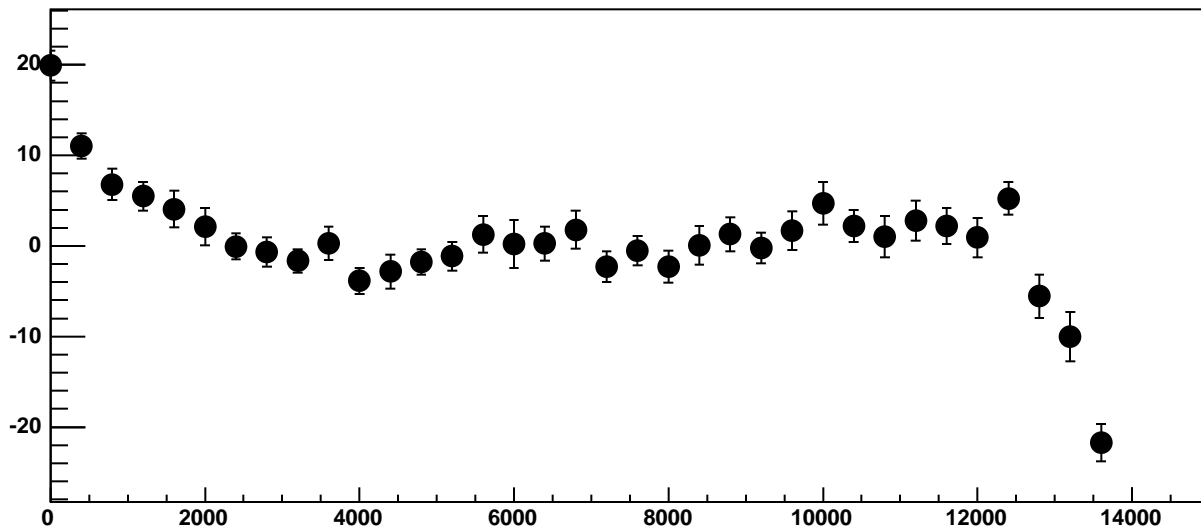


$\chi^2 / \text{ndf}$  42.61 / 23  
p0 144.7 ± 0.7824  
p1 0.02146 ± 0.0001256

Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

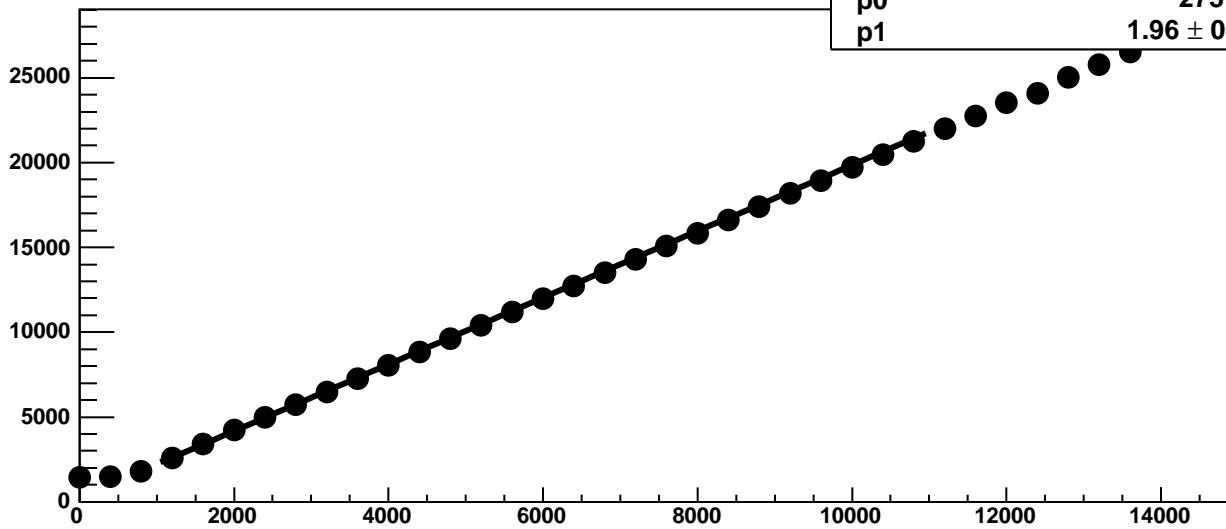


Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

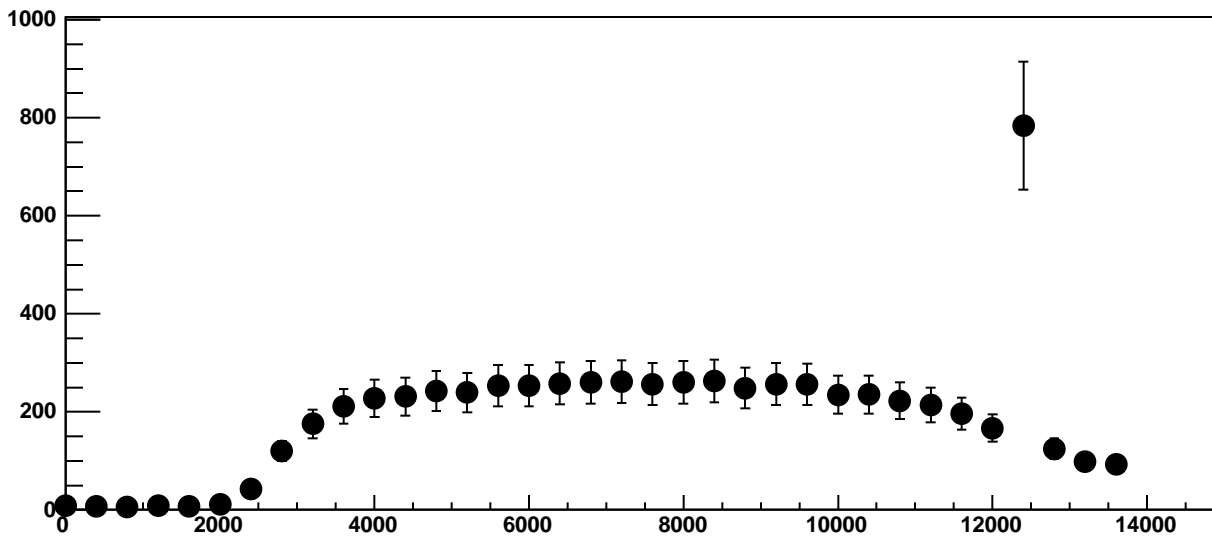


Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

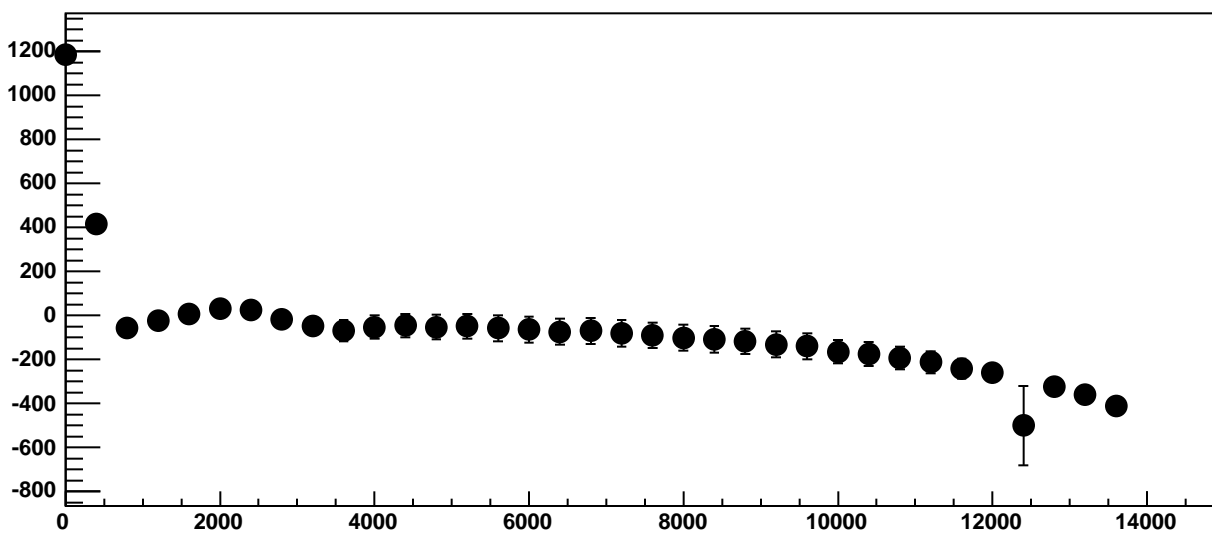
$\chi^2 / \text{ndf}$  332.1 / 23  
p0  $273 \pm 3.218$   
p1  $1.96 \pm 0.001876$



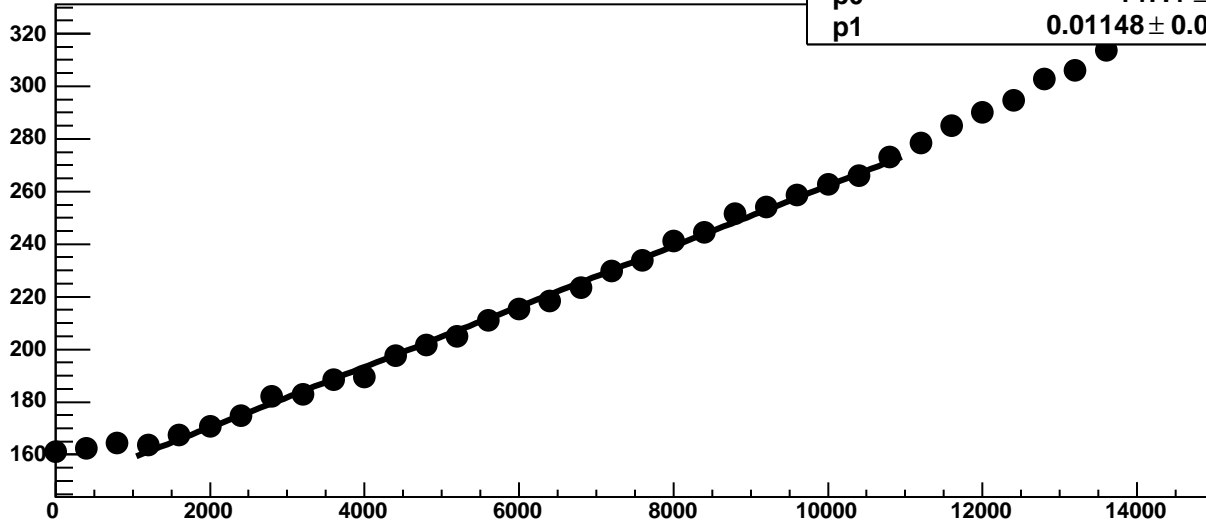
Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

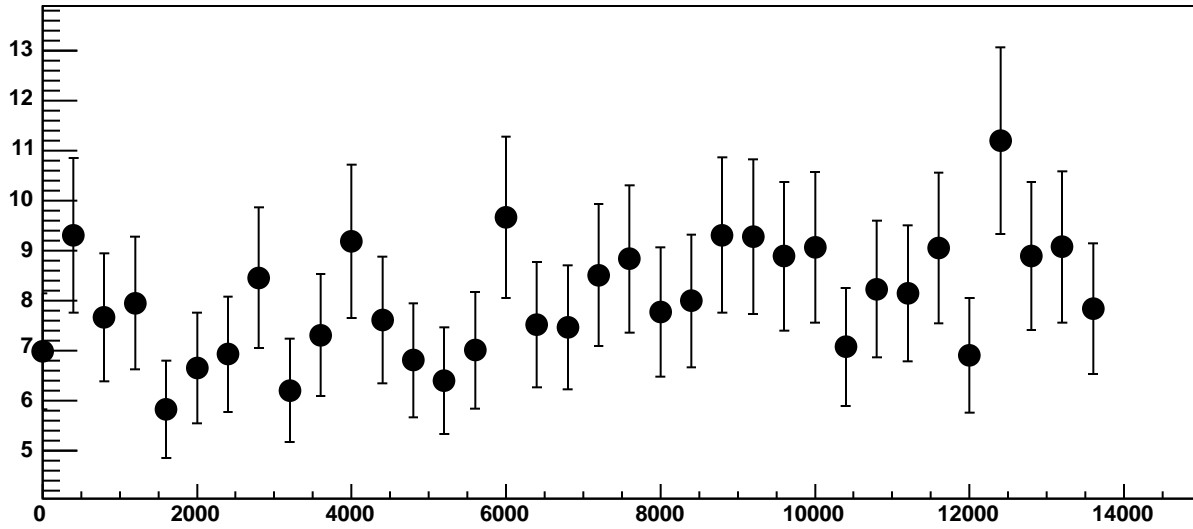


Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

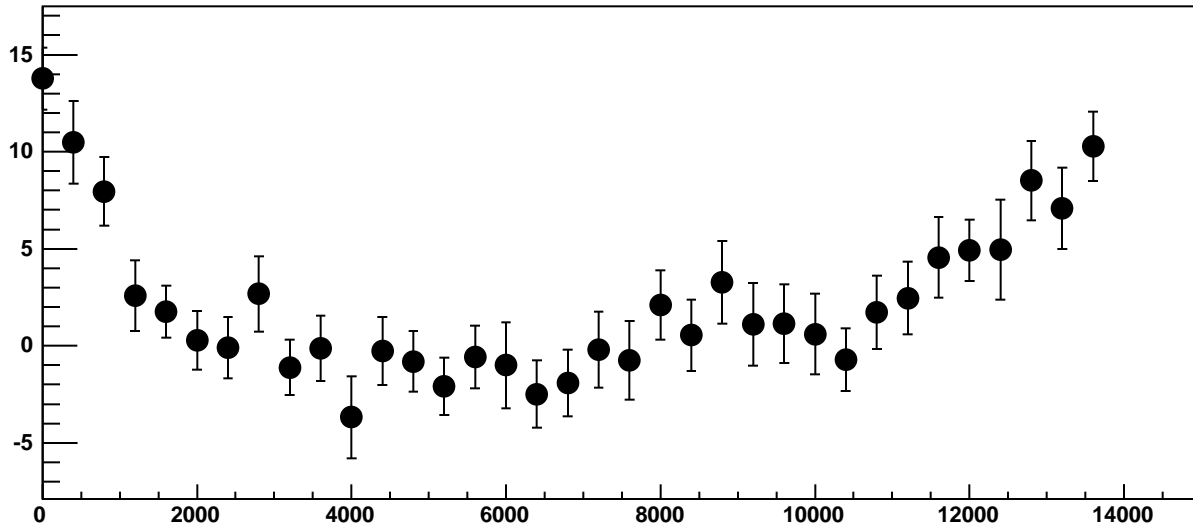


$\chi^2 / \text{ndf}$  21.01 / 23  
p0 147.4 ± 0.7584  
p1 0.01148 ± 0.0001209

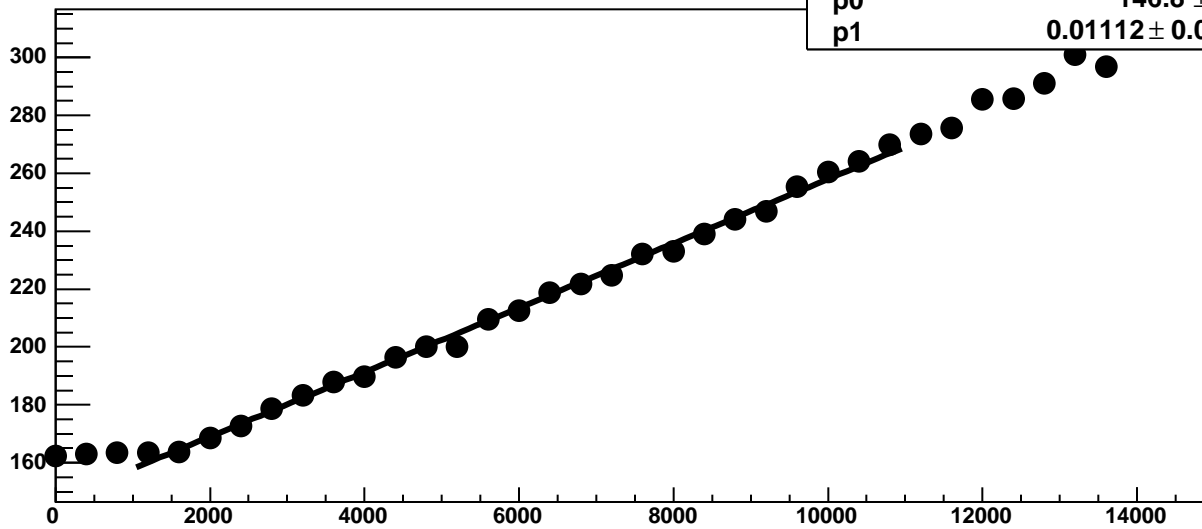
Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

25.2 / 23

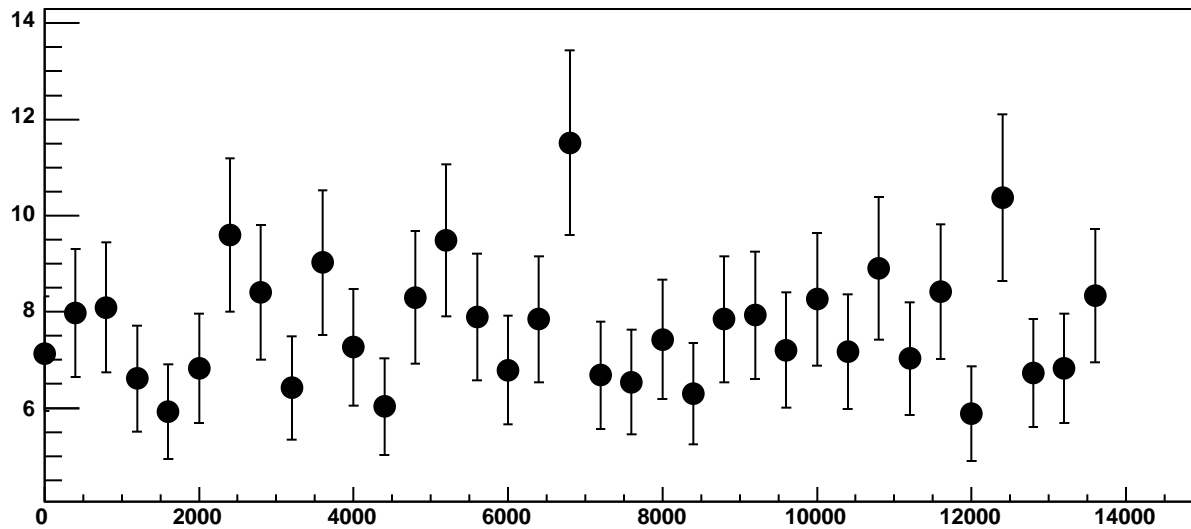
p0

$146.8 \pm 0.7593$

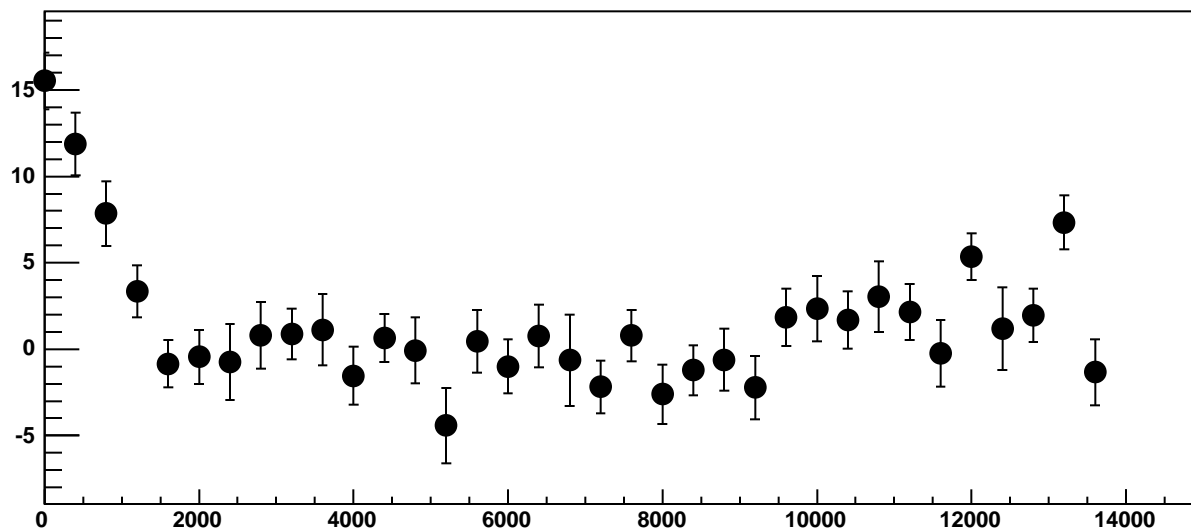
p1

$0.01112 \pm 0.0001163$

Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

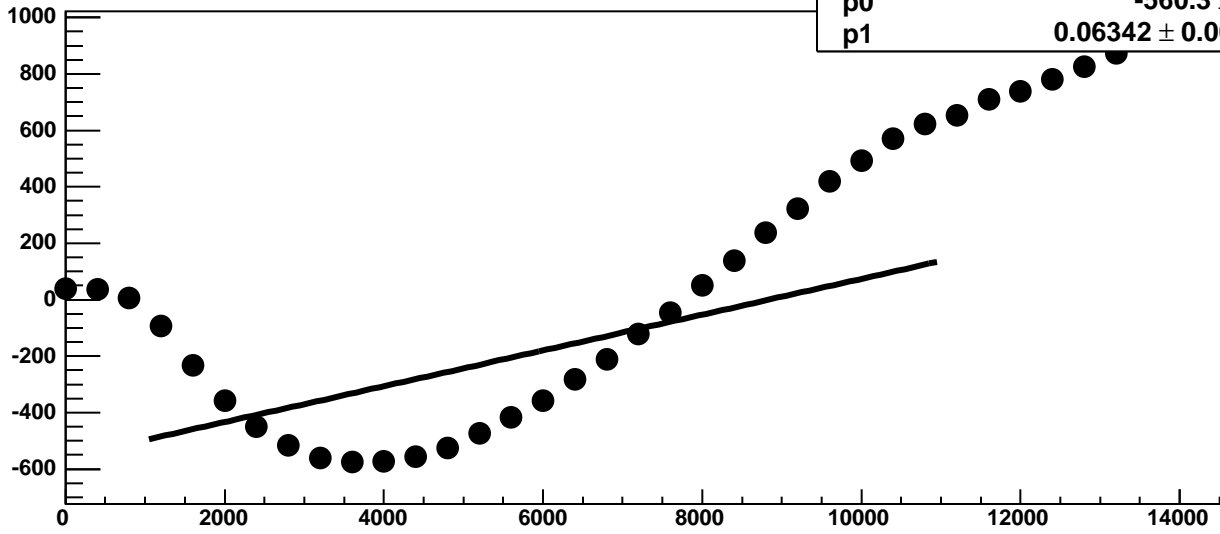


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



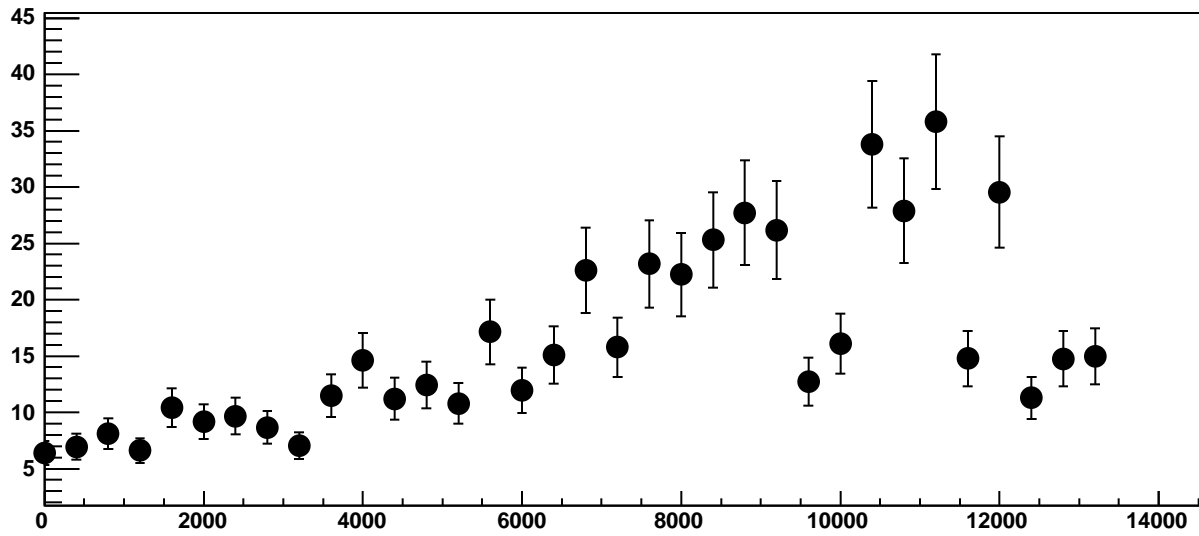


Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

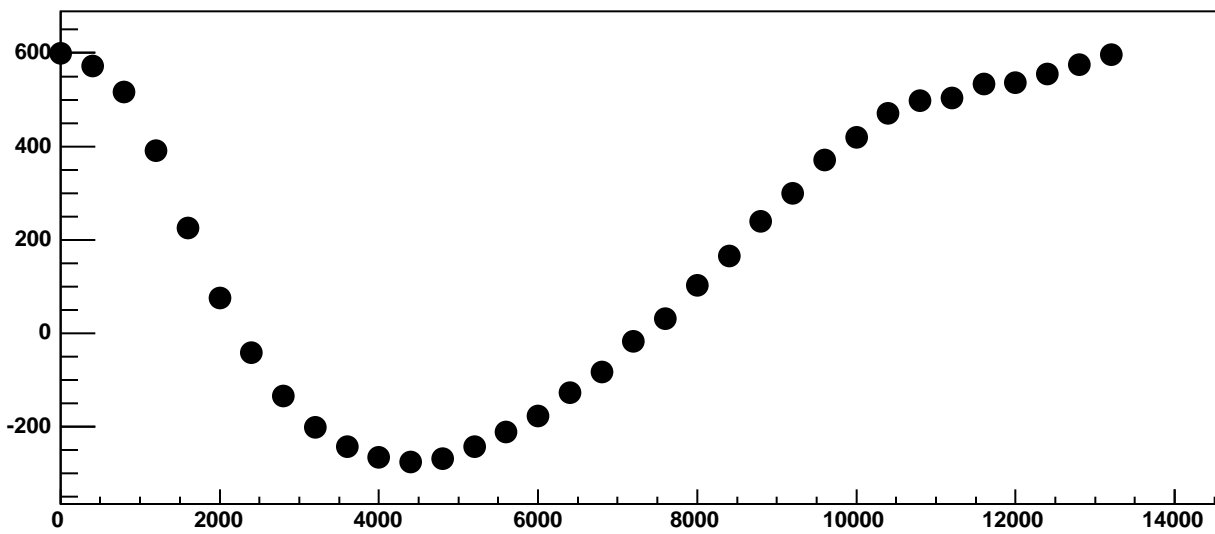


$\chi^2 / \text{ndf}$  1.95e+05 / 23  
p0 -560.3 ± 1.069  
p1 0.06342 ± 0.0002228

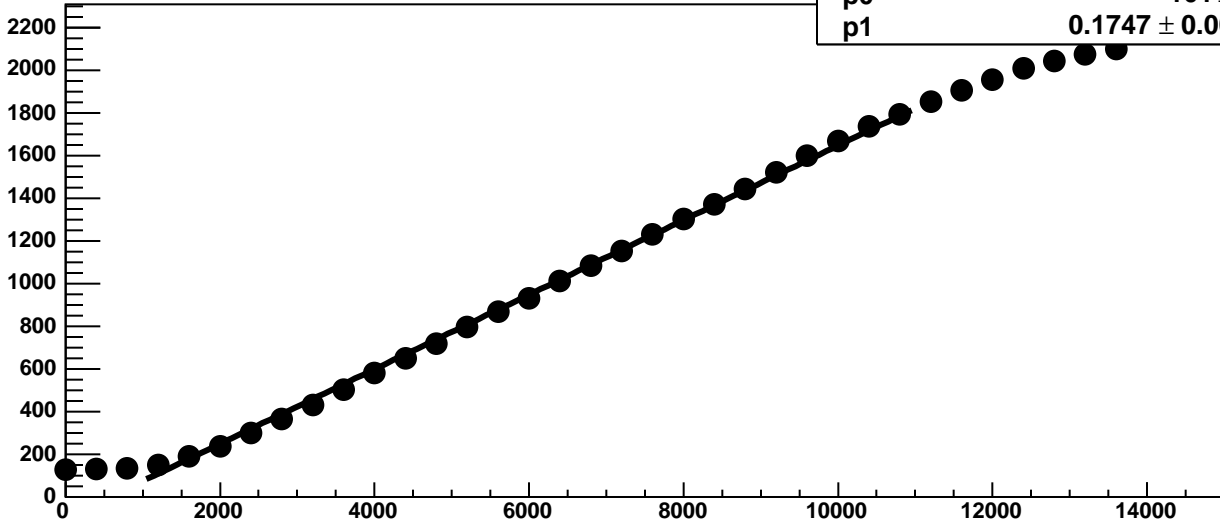
Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



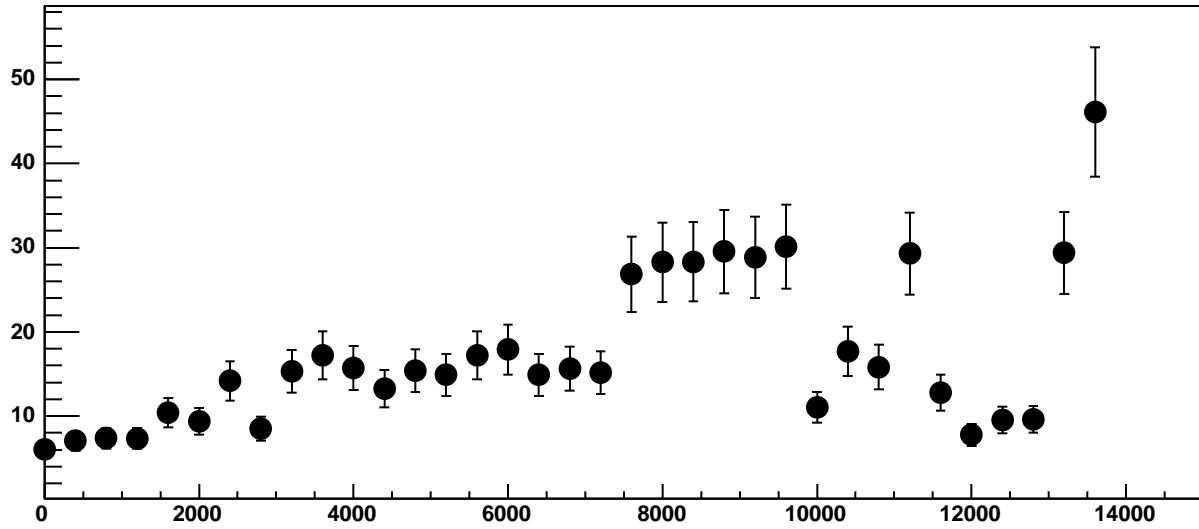
Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



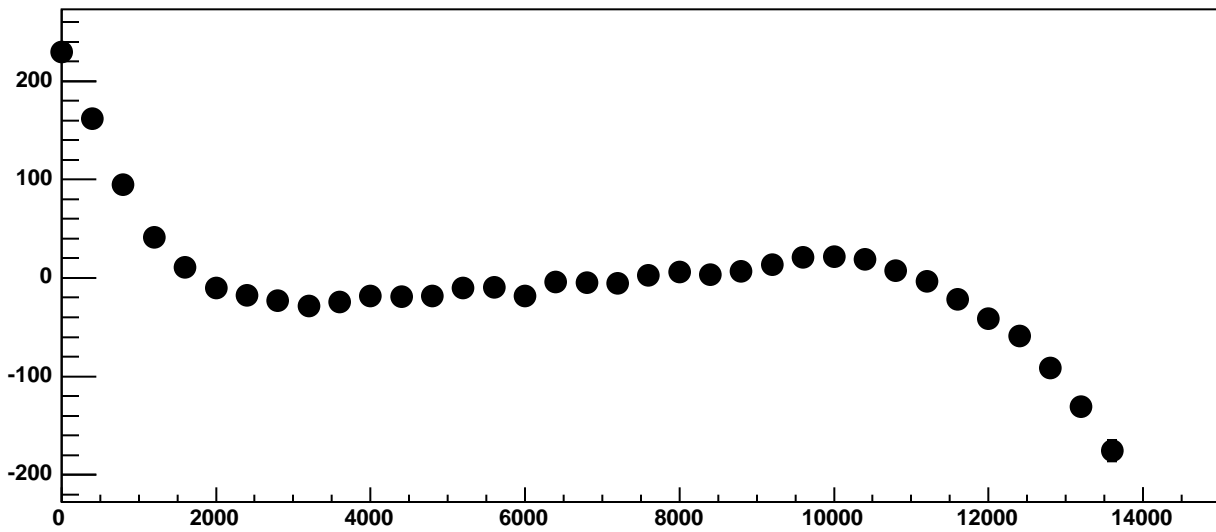
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



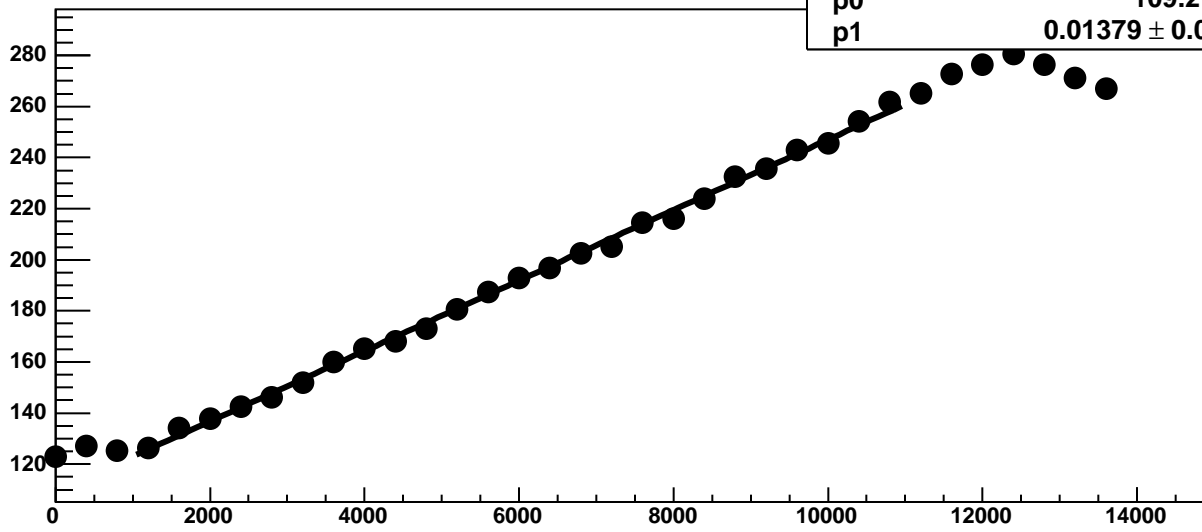
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

27.98 / 23

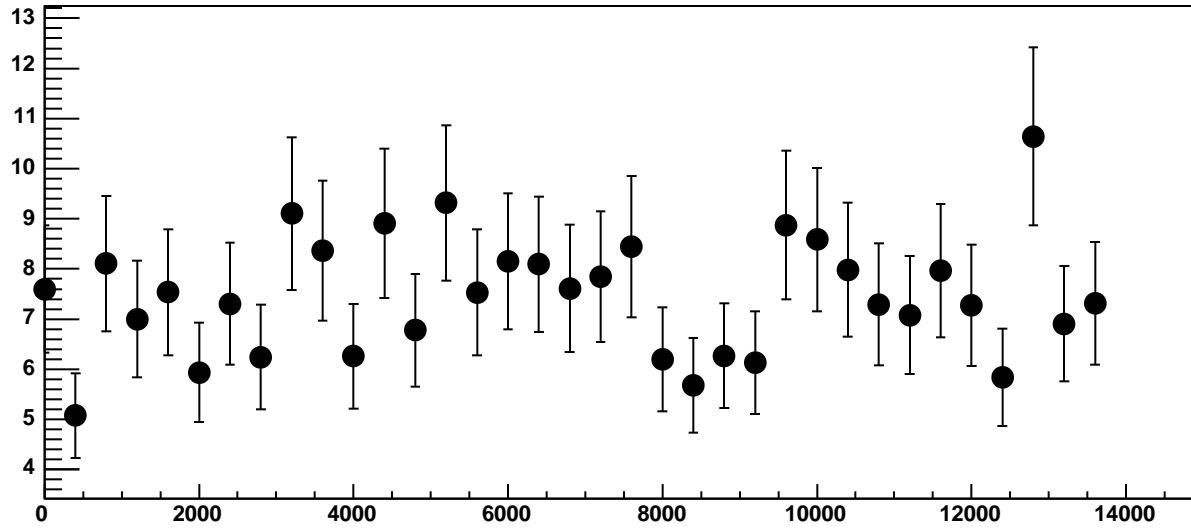
p0

$109.2 \pm 0.756$

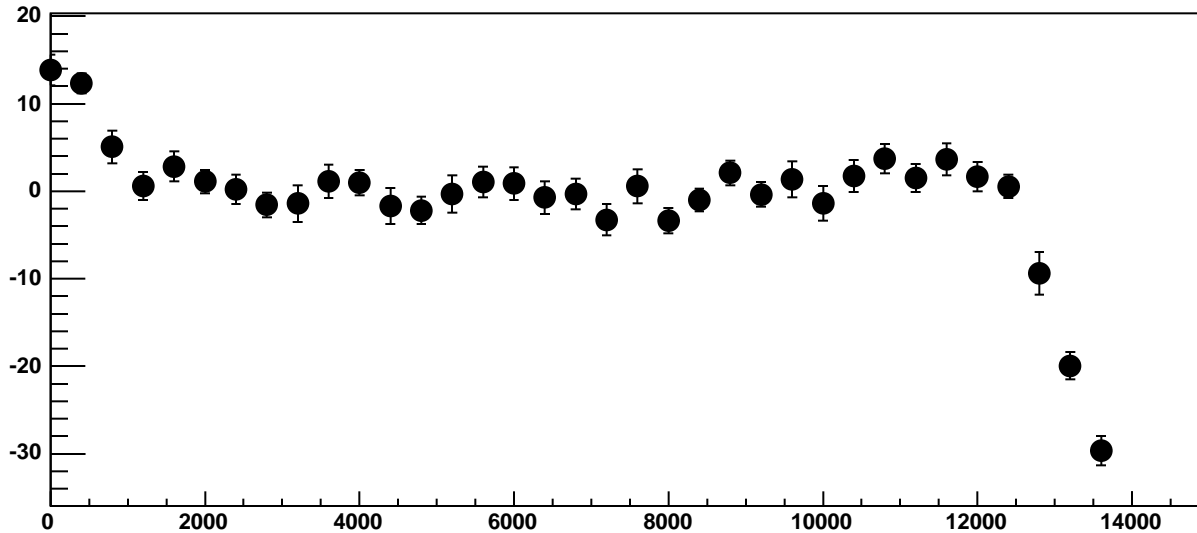
p1

$0.01379 \pm 0.0001137$

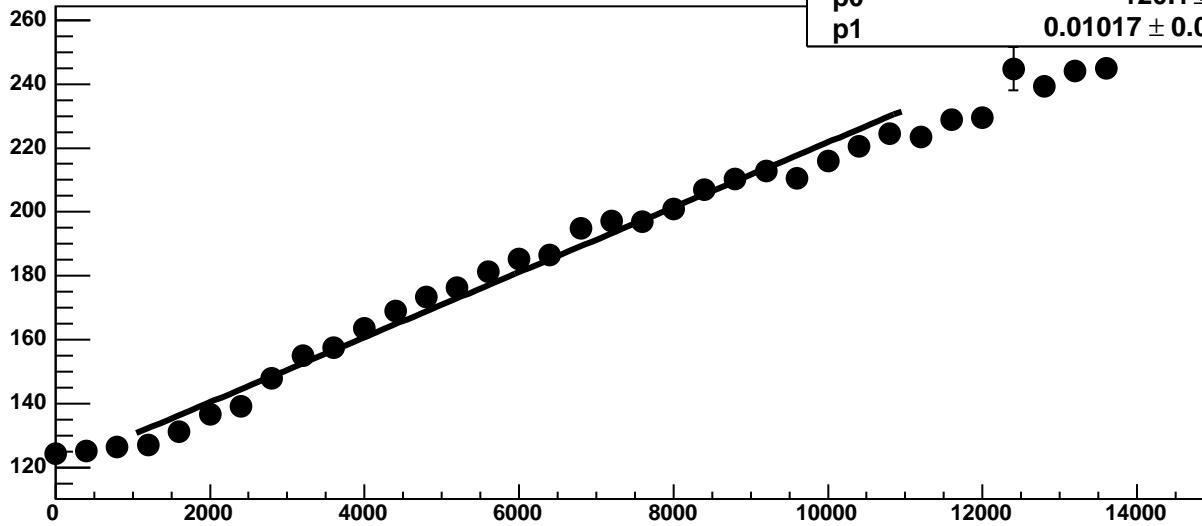
Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



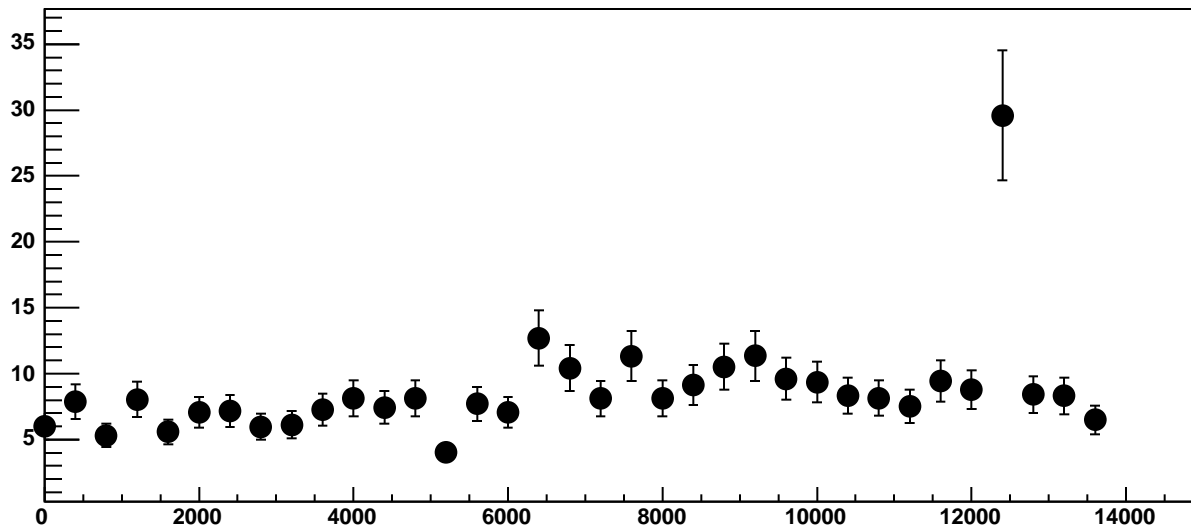
Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



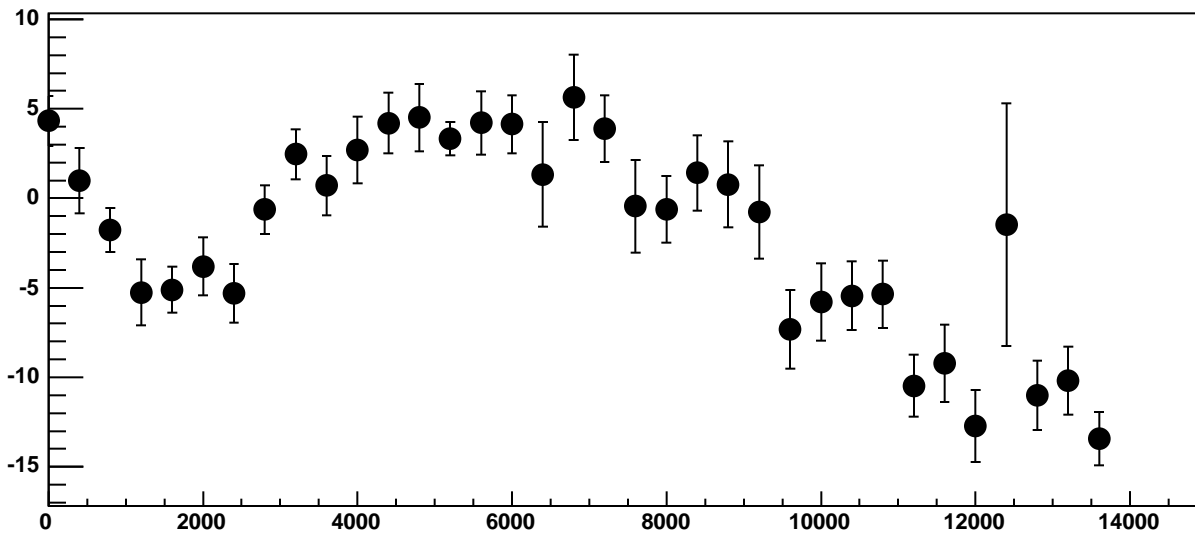
Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



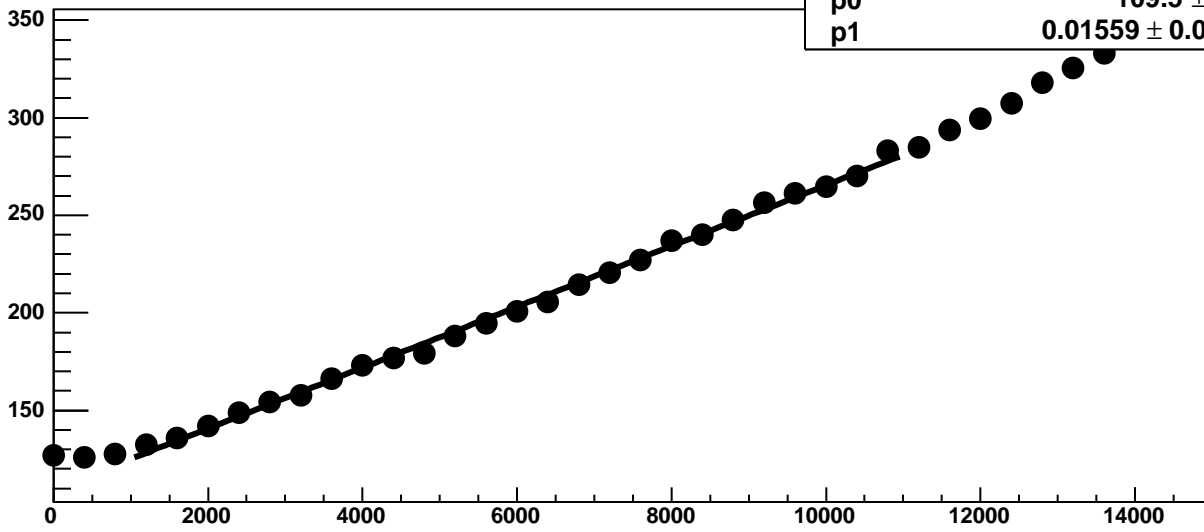
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

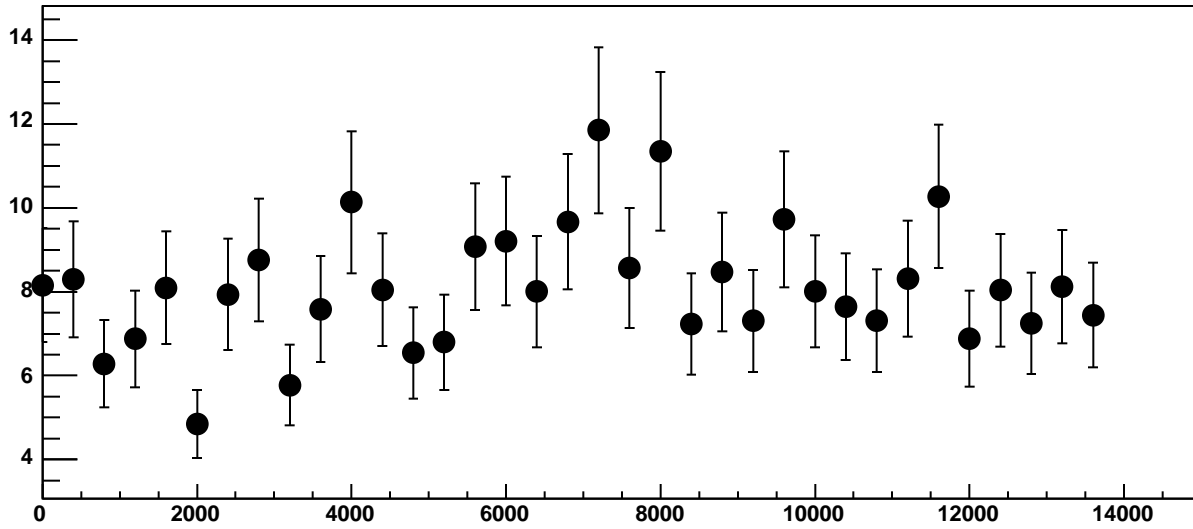


Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

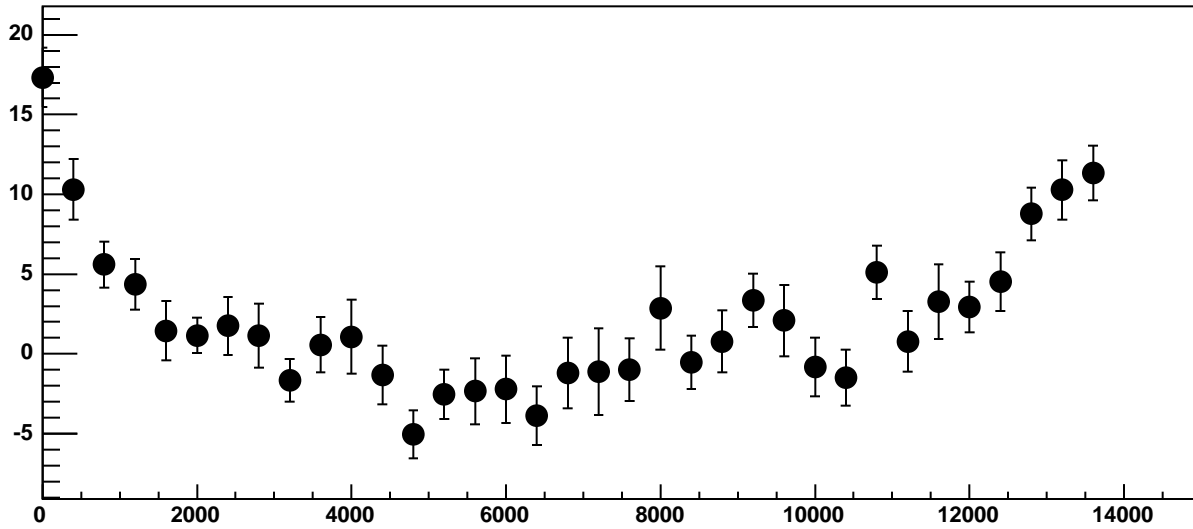


$\chi^2 / \text{ndf}$  51 / 23  
p0  $109.5 \pm 0.7414$   
p1  $0.01559 \pm 0.0001173$

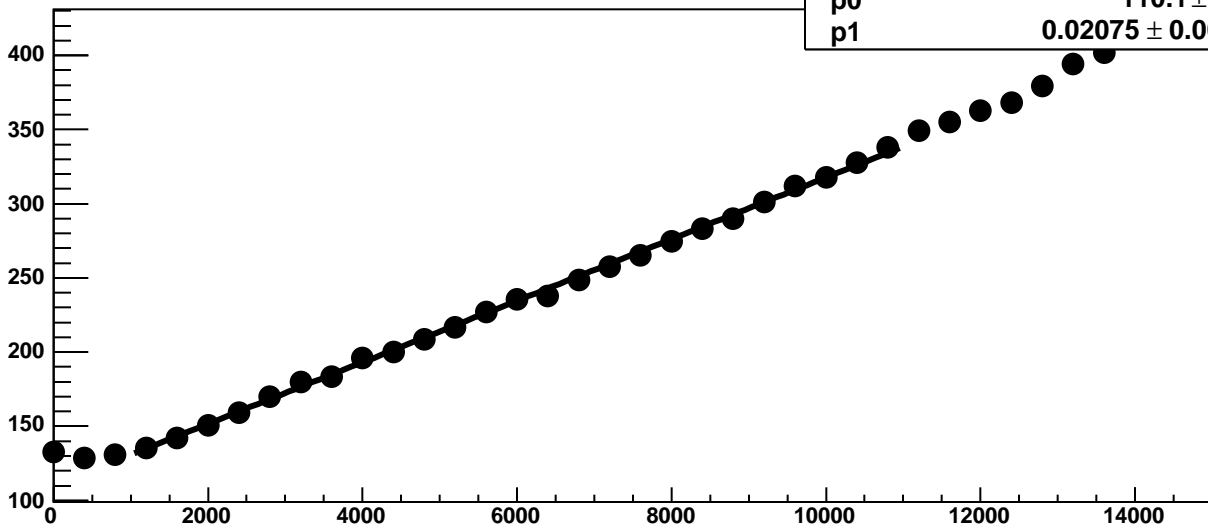
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

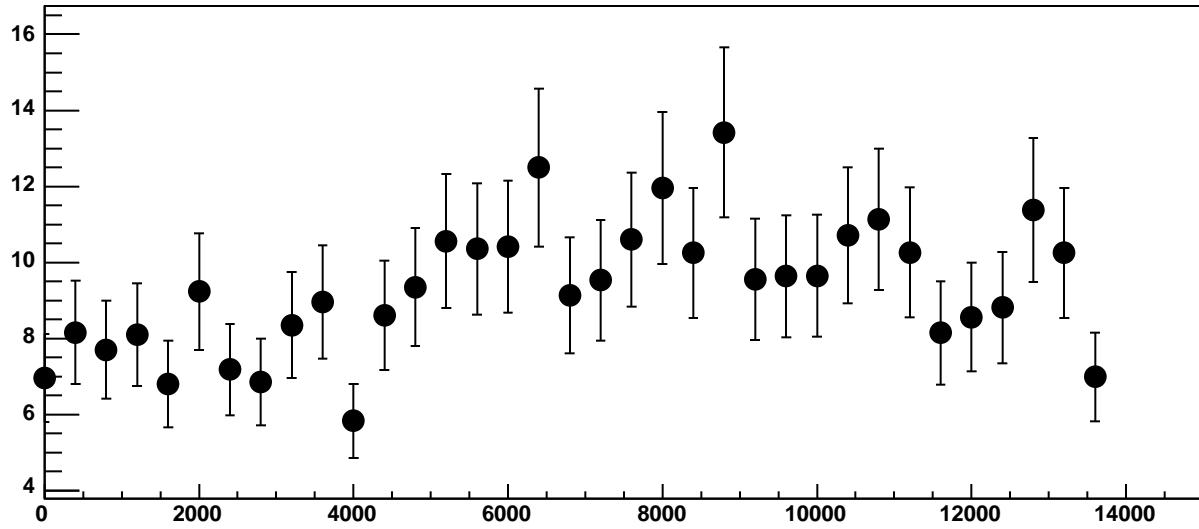


Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

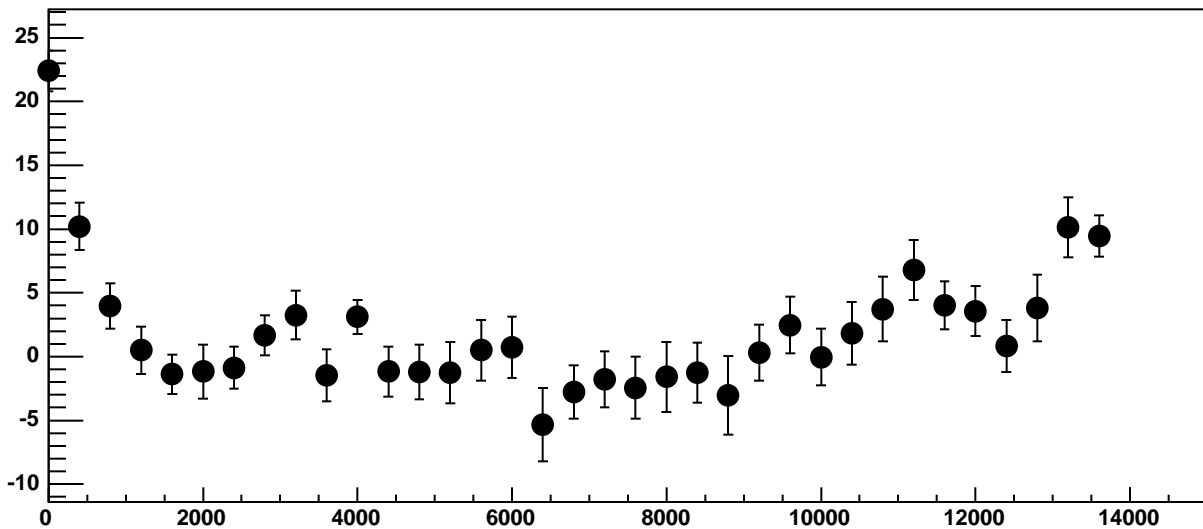


$\chi^2 / \text{ndf}$  24.87 / 23  
p0 110.1 ± 0.8559  
p1 0.02075 ± 0.0001438

Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

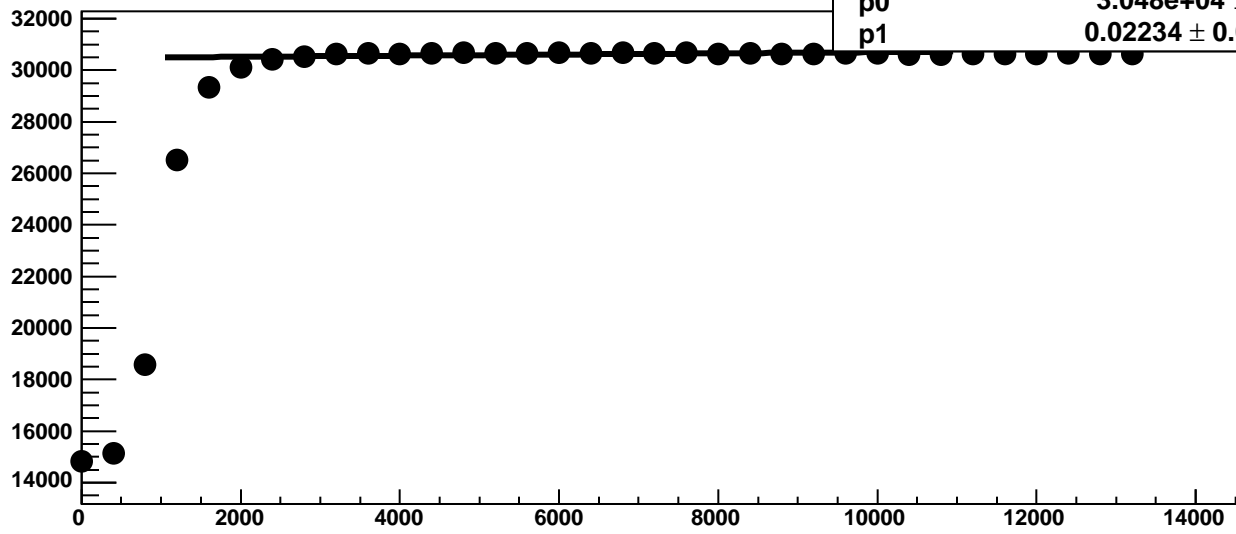
5432 / 23

p0

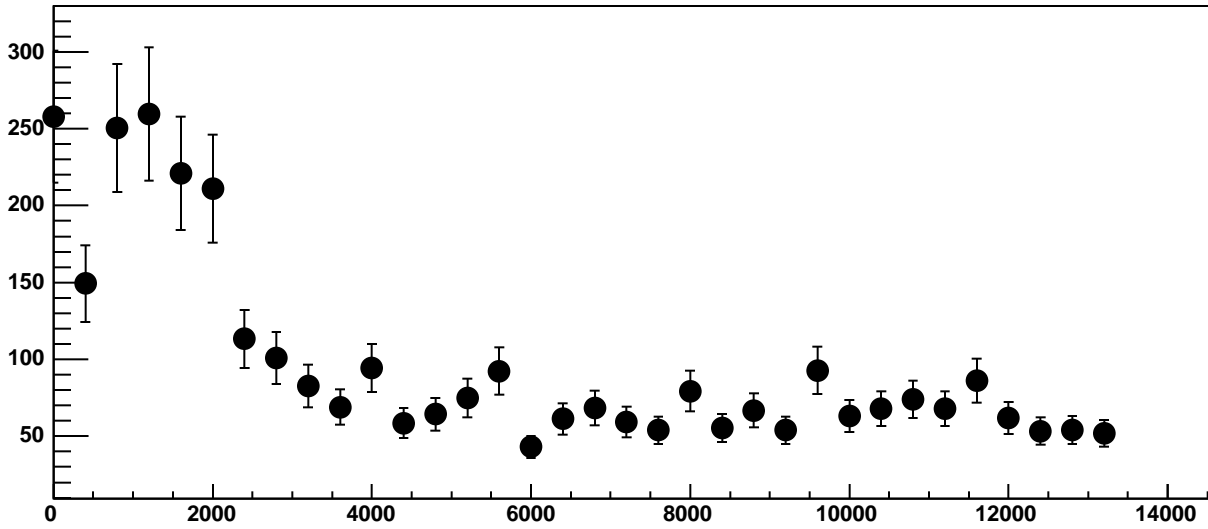
$3.048\text{e}+04 \pm 10.18$

p1

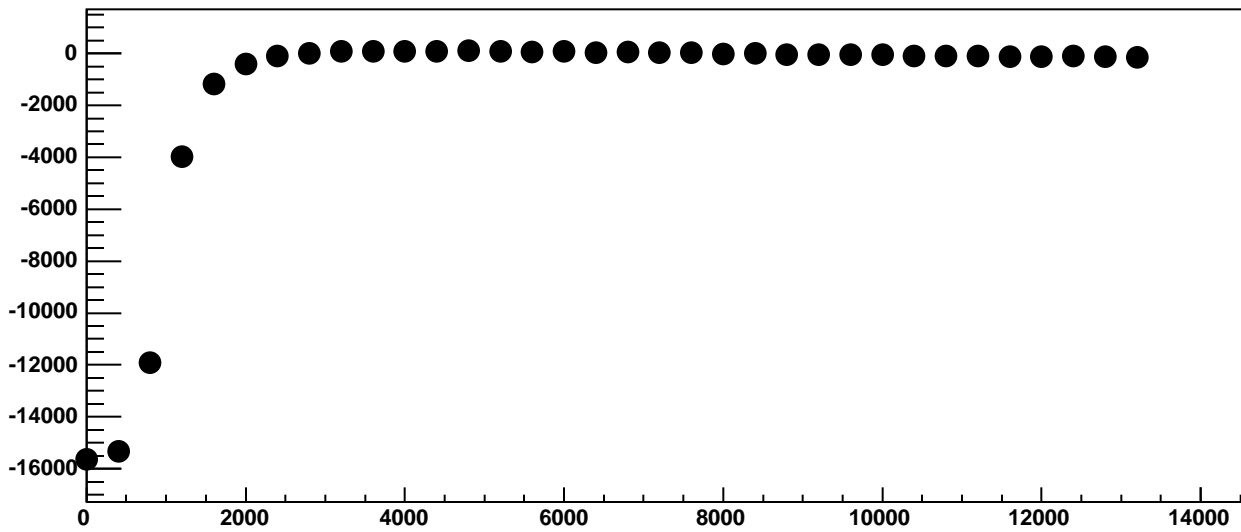
$0.02234 \pm 0.001406$



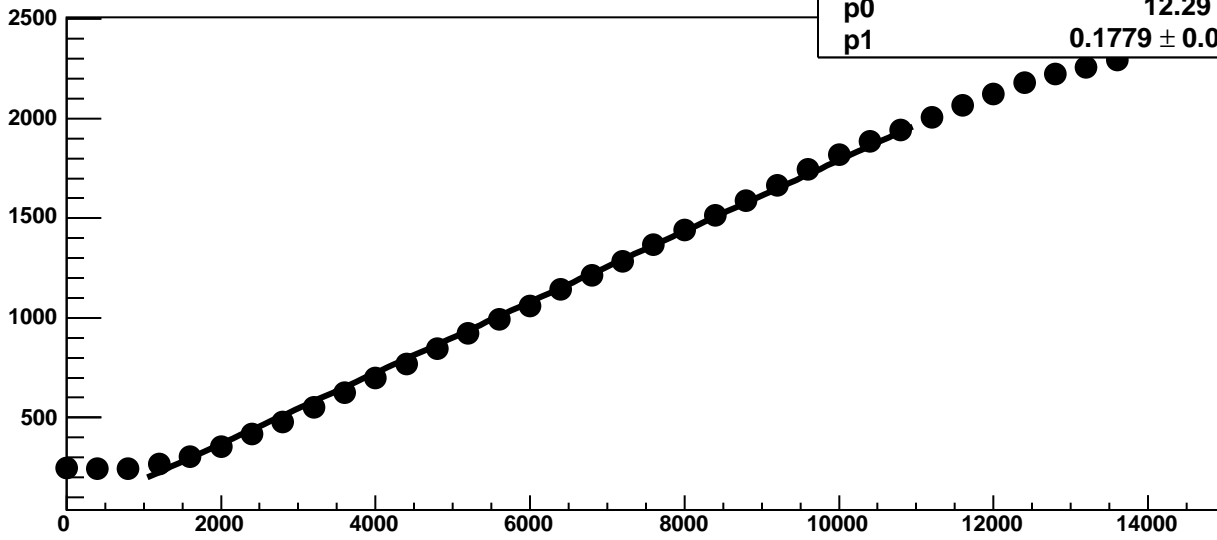
Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

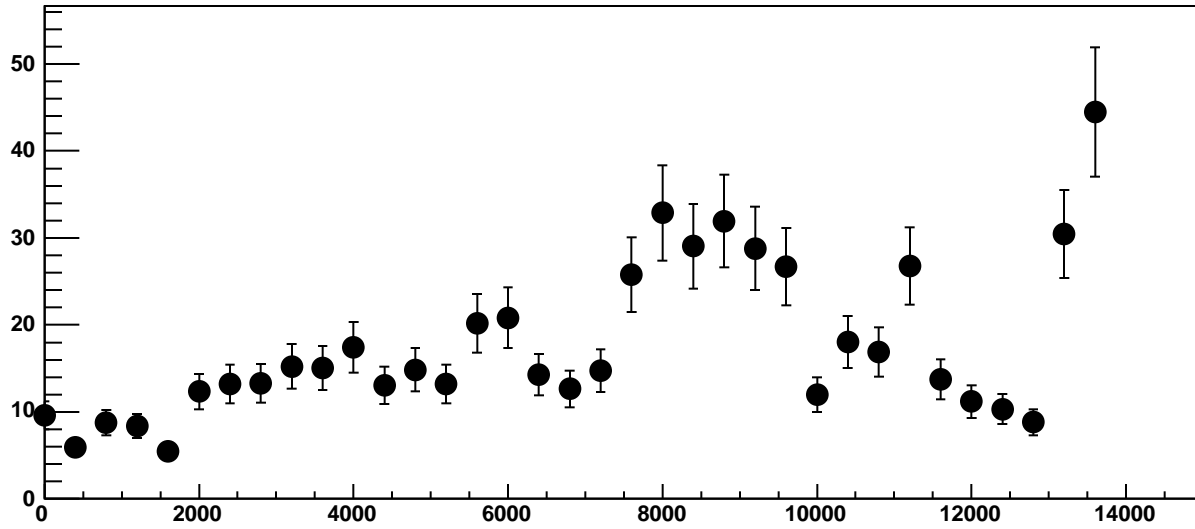


Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

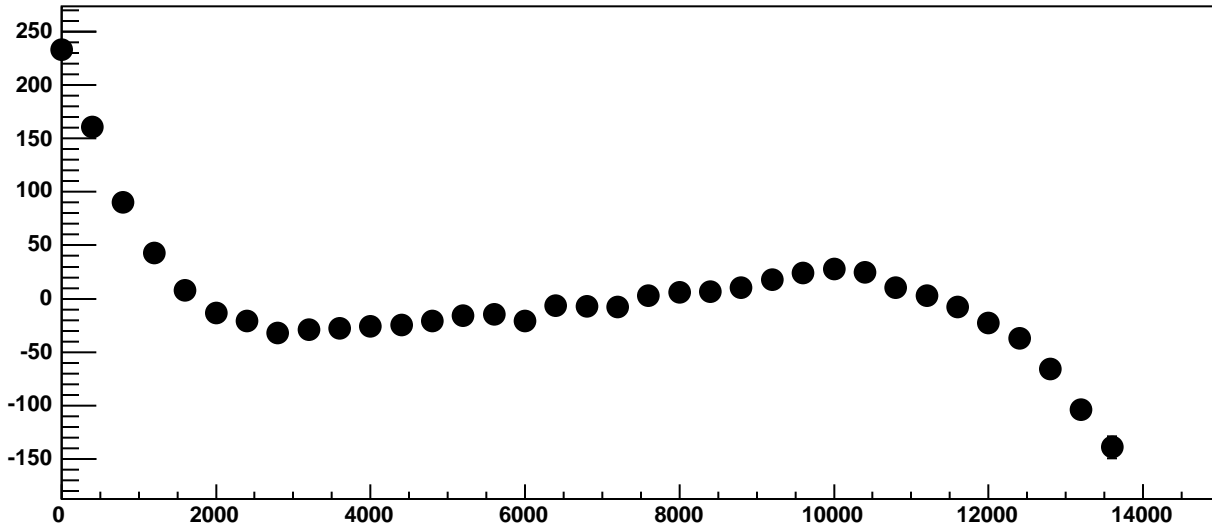


$\chi^2 / \text{ndf}$  1242 / 23  
p0  $12.29 \pm 1.083$   
p1  $0.1779 \pm 0.0002097$

Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

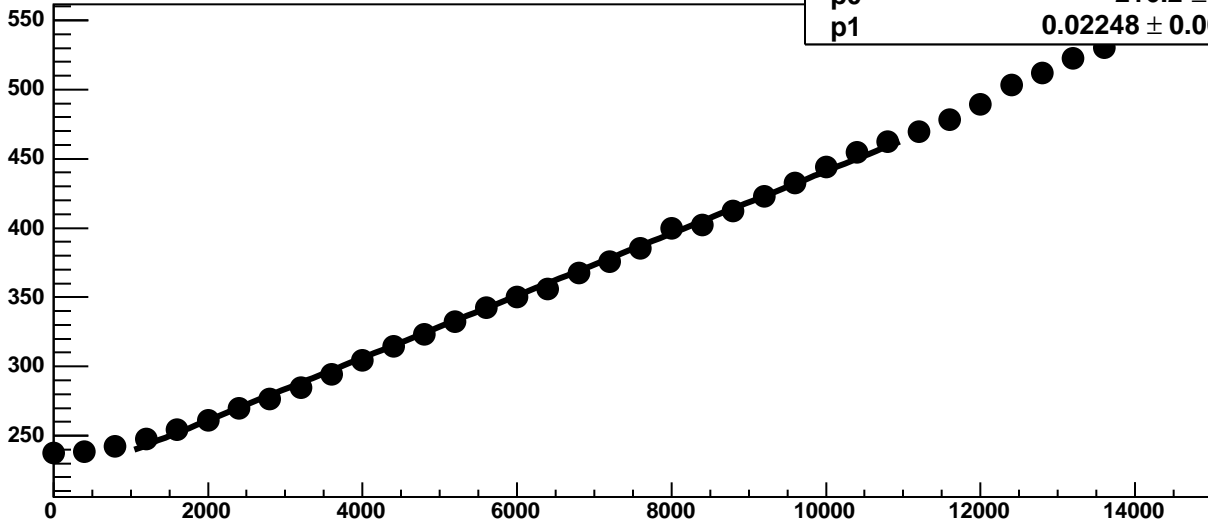


Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



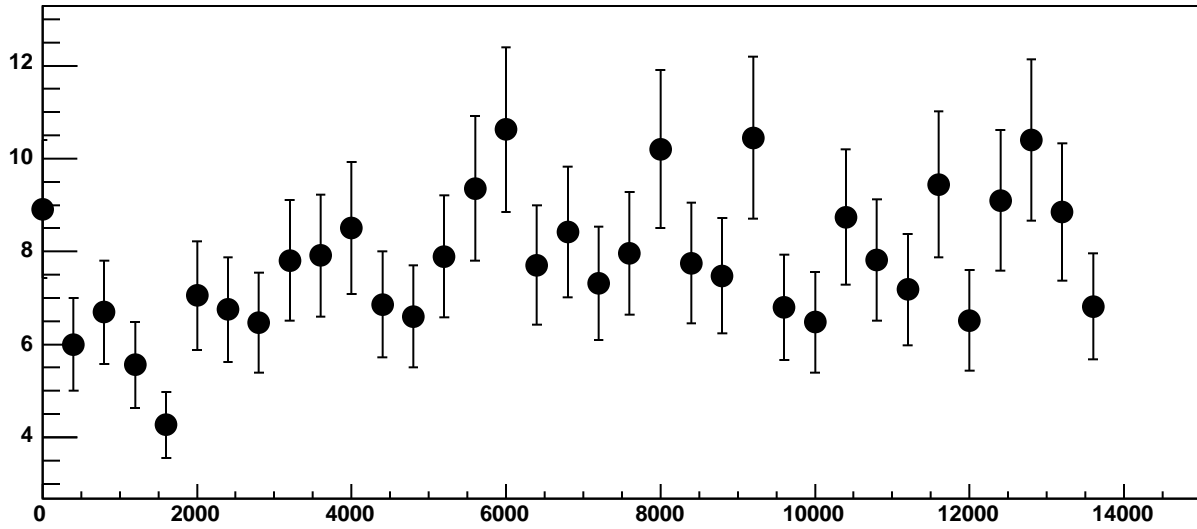


Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

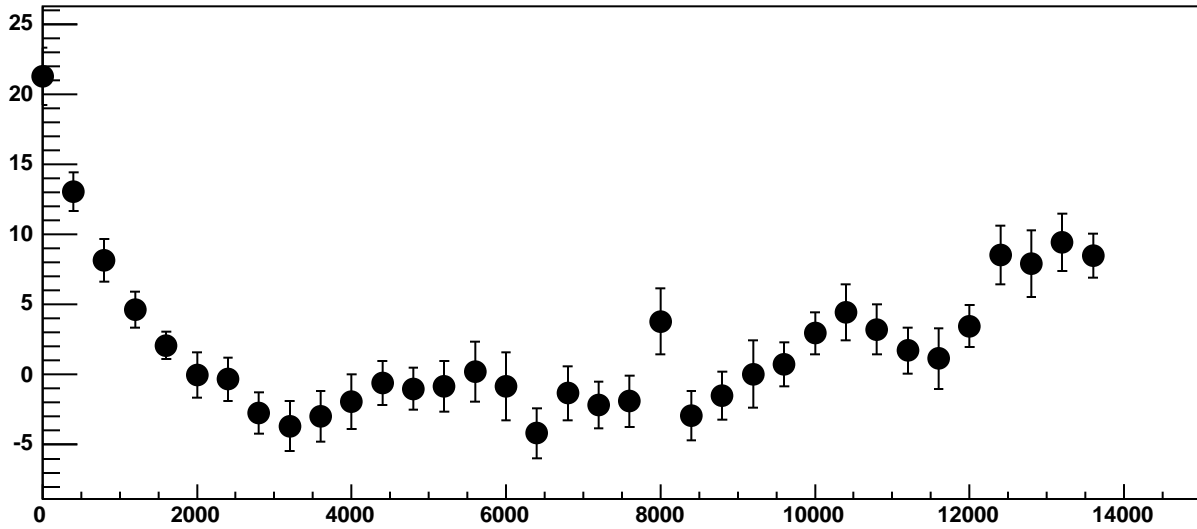


$\chi^2 / \text{ndf}$  57.26 / 23  
p0  $216.2 \pm 0.6633$   
p1  $0.02248 \pm 0.0001073$

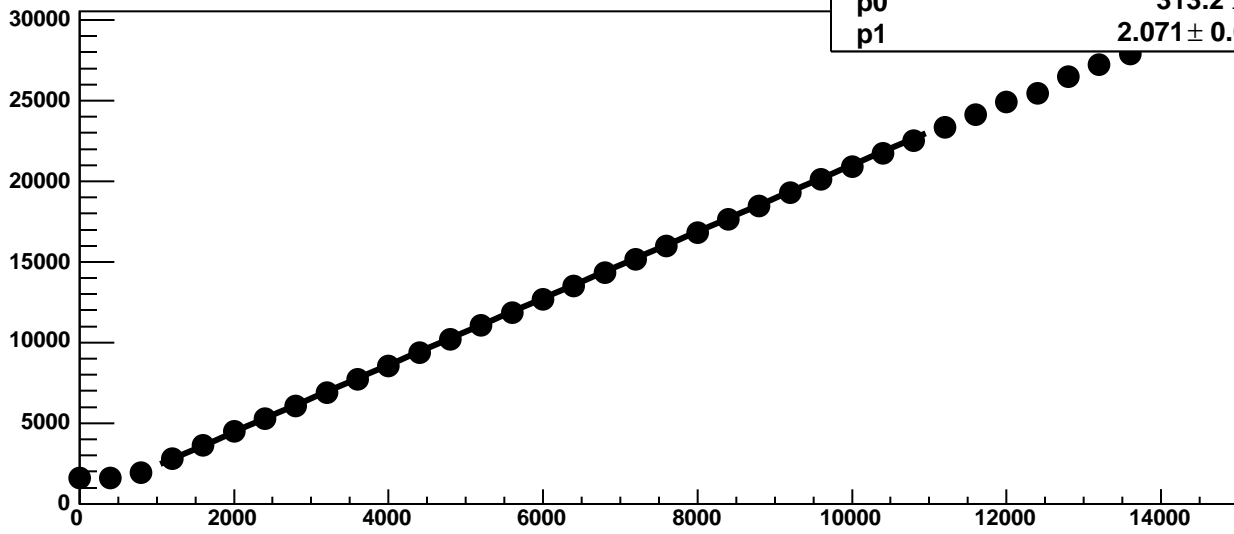
Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

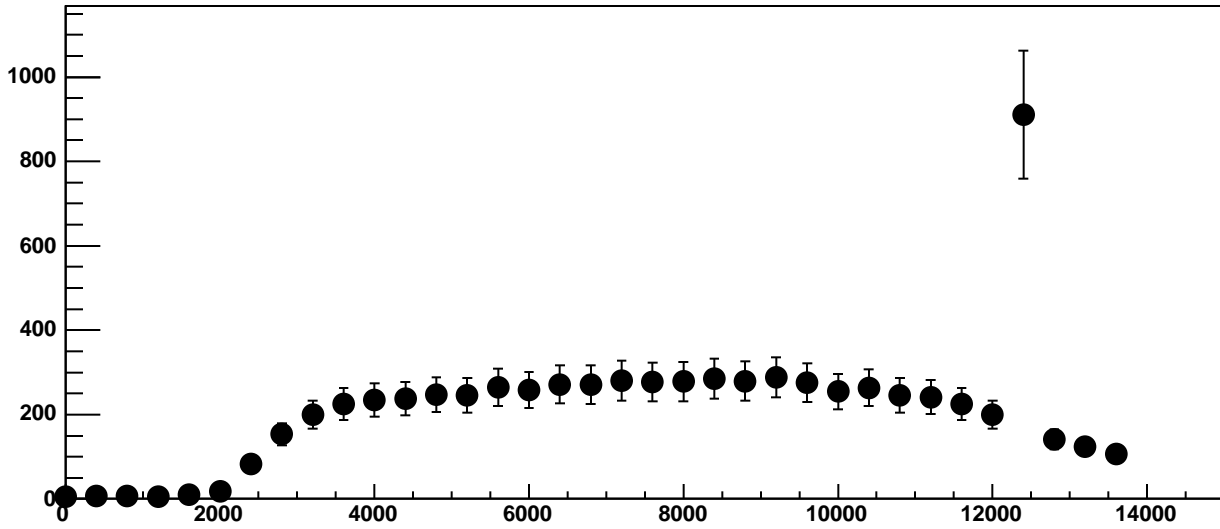


Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

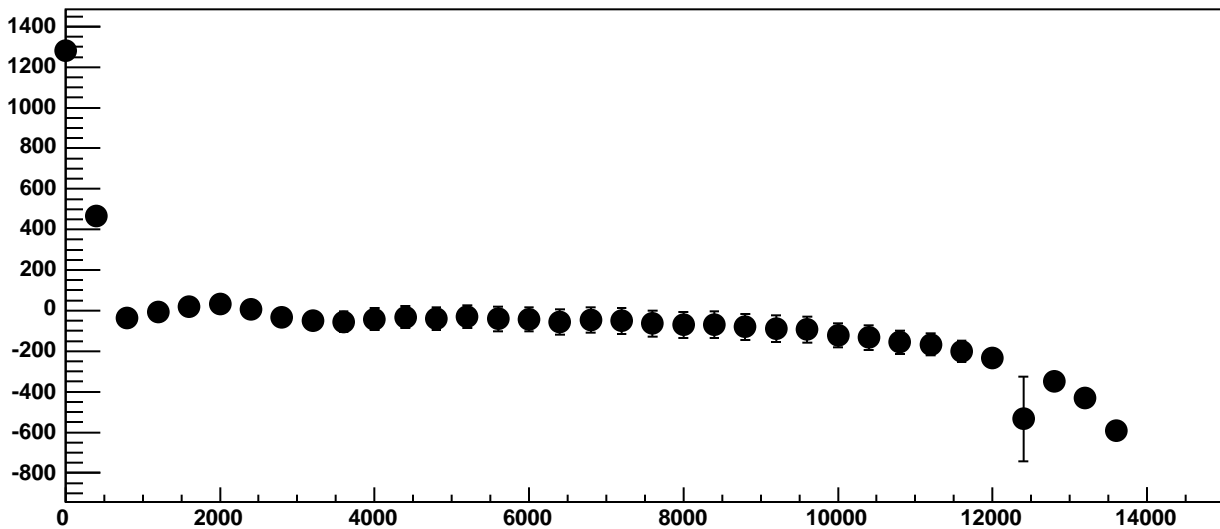


$\chi^2 / \text{ndf}$  176.3 / 23  
p0  $313.2 \pm 3.027$   
p1  $2.071 \pm 0.002006$

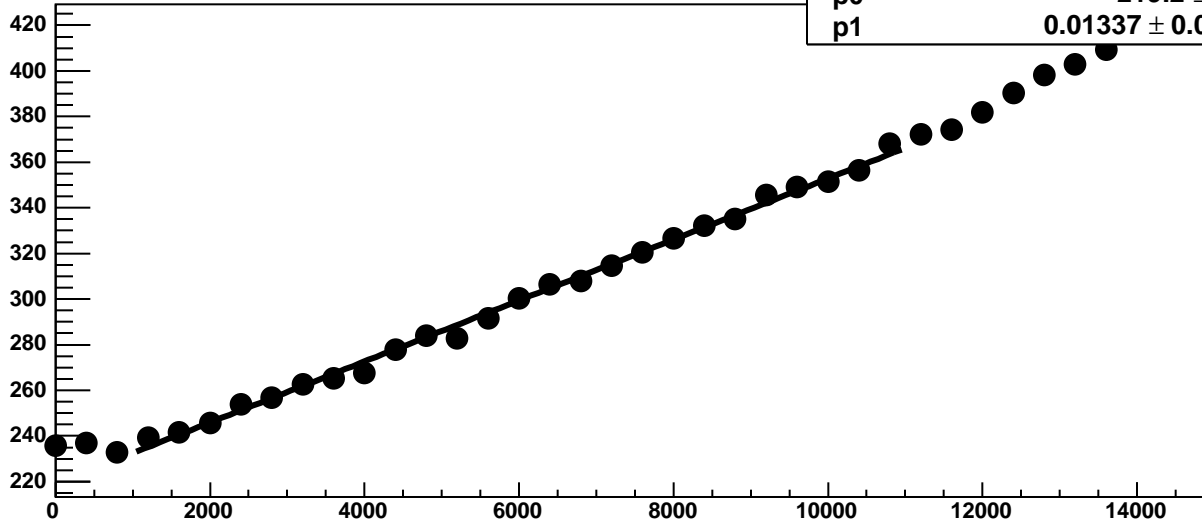
Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

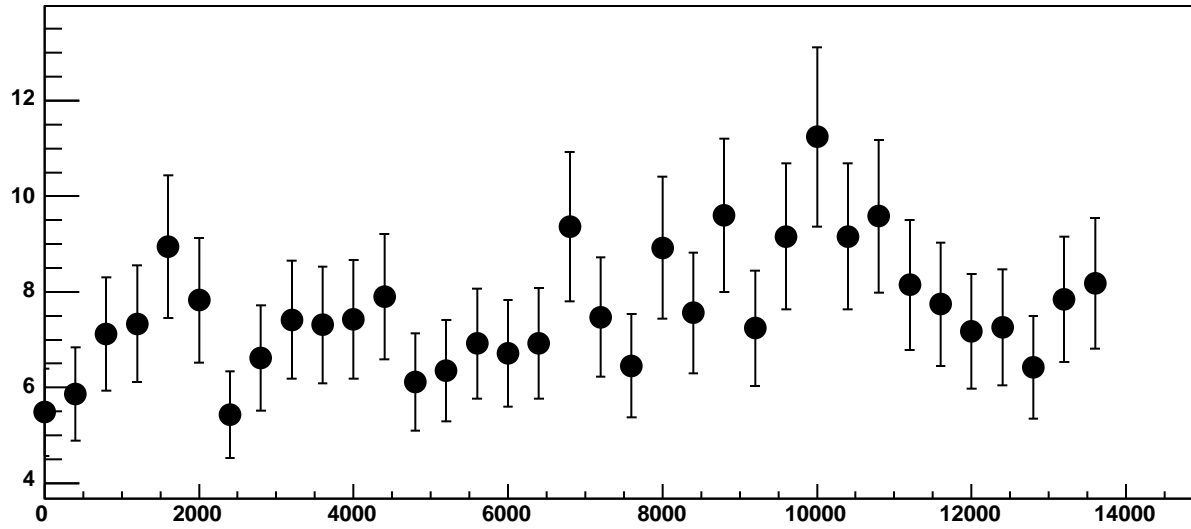


Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

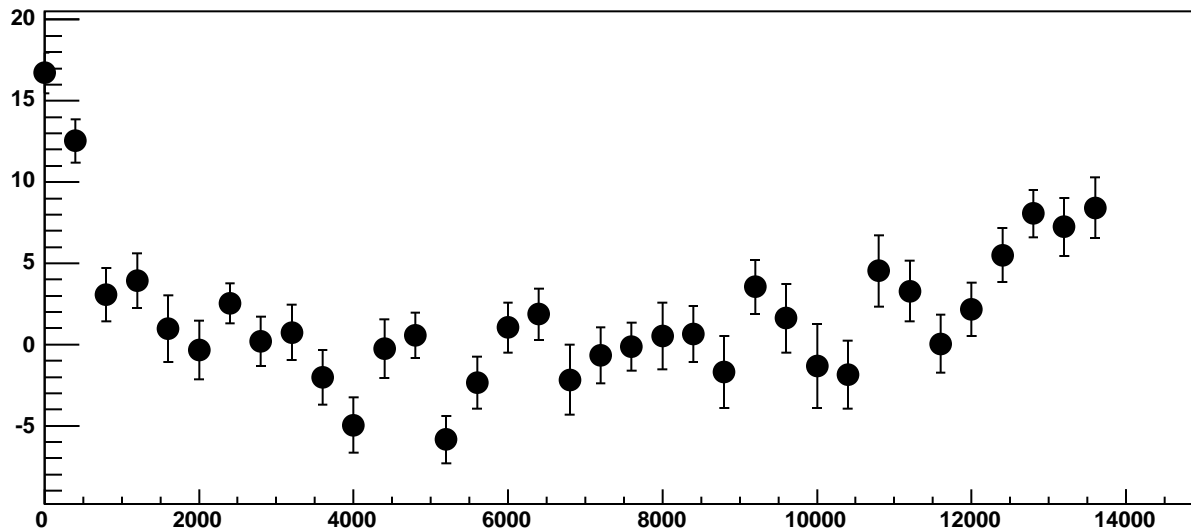


$\chi^2 / \text{ndf}$  52.64 / 23  
p0 219.2 ± 0.7864  
p1 0.01337 ± 0.0001278

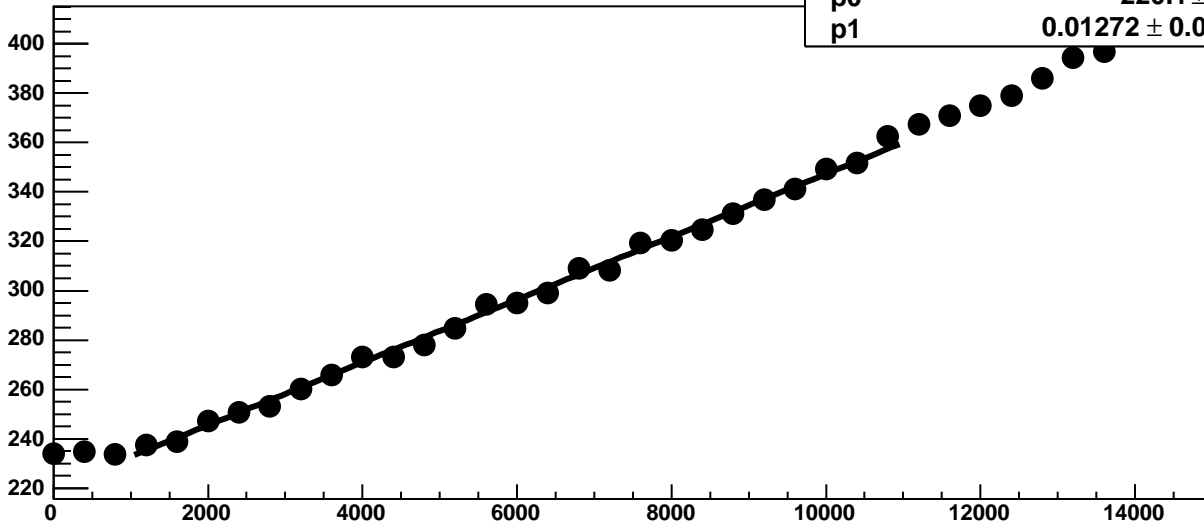
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

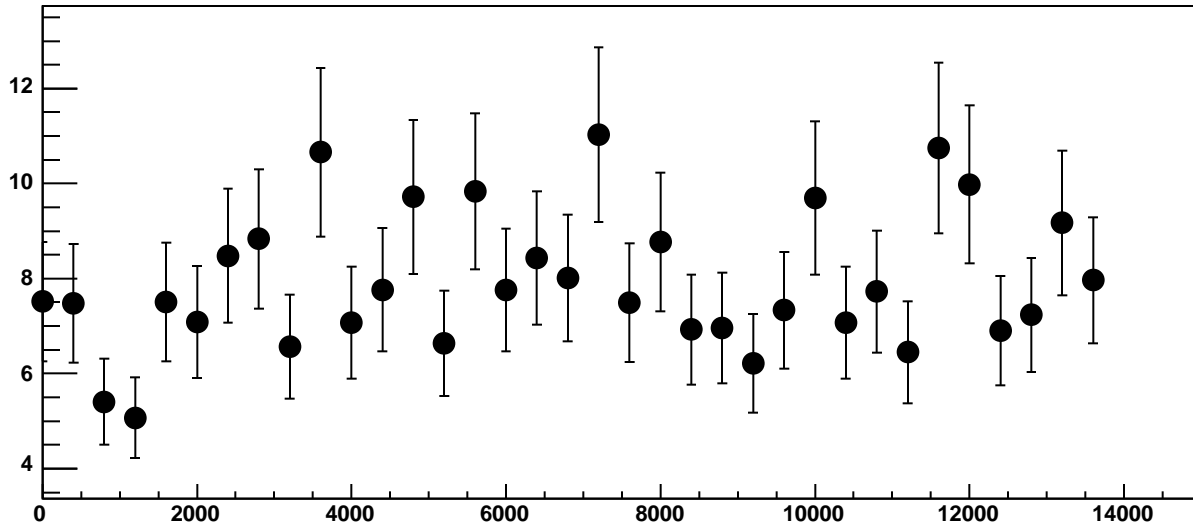


Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

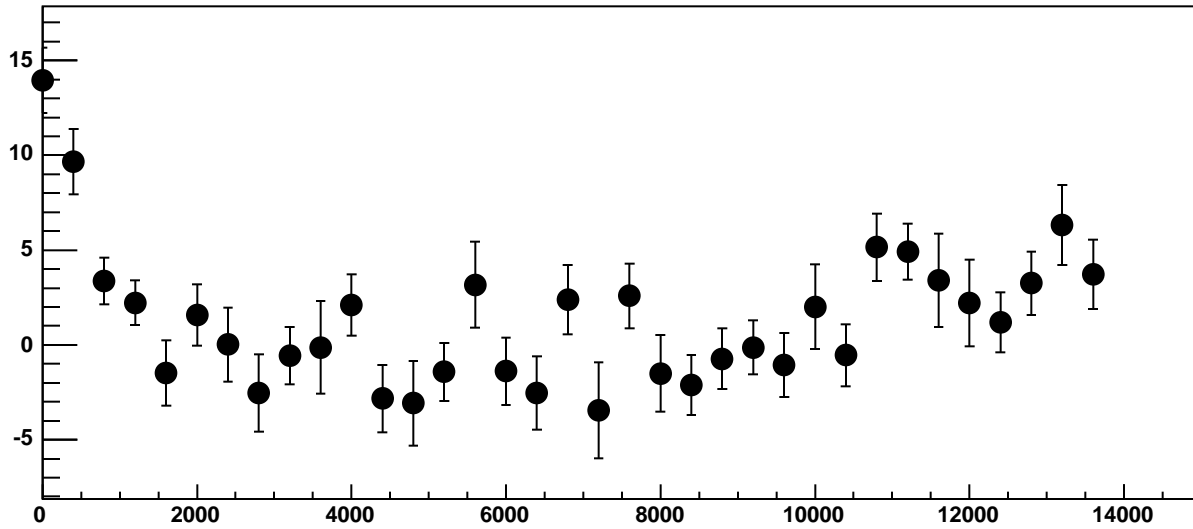


$\chi^2 / \text{ndf}$  36.47 / 23  
p0  $220.1 \pm 0.7472$   
p1  $0.01272 \pm 0.0001133$

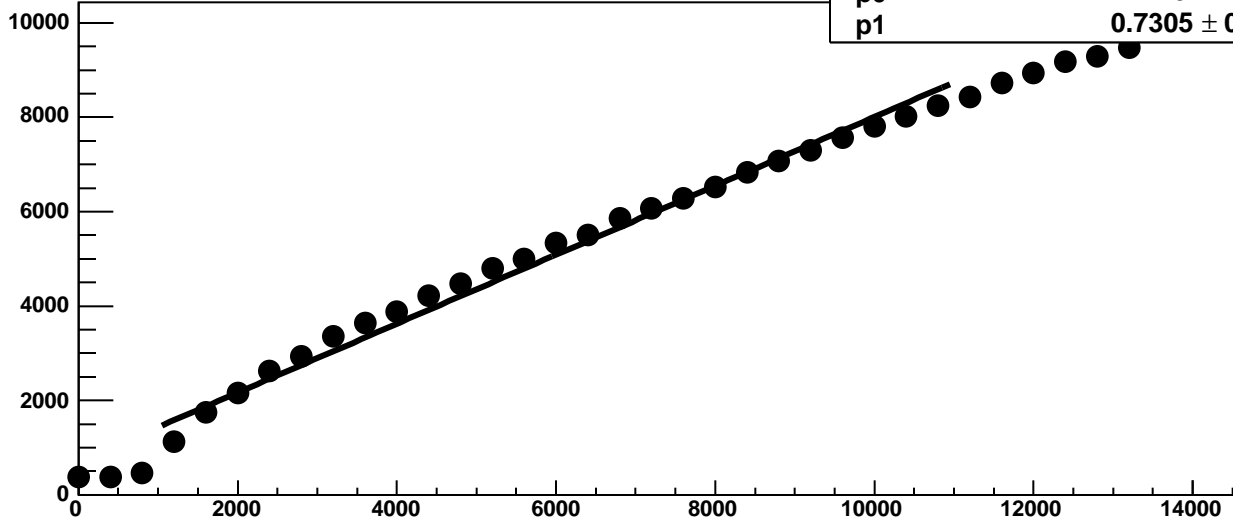
Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



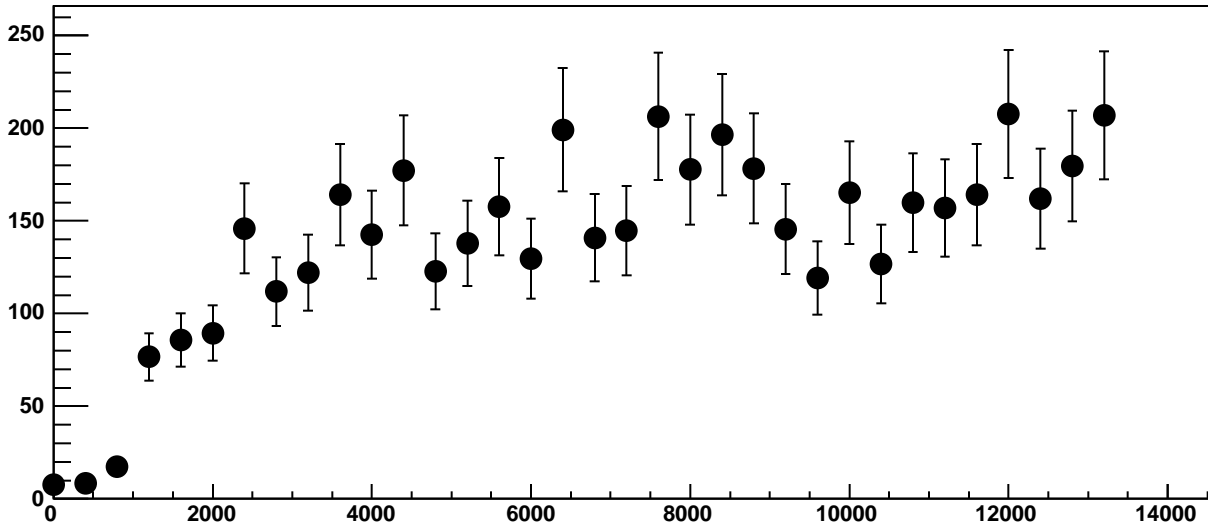
Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



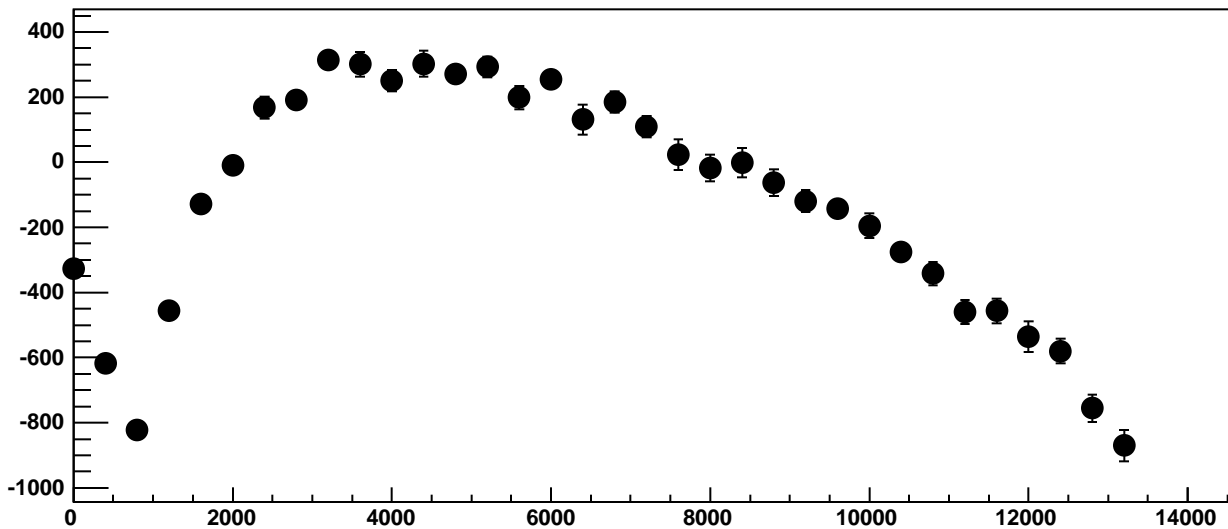
Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC



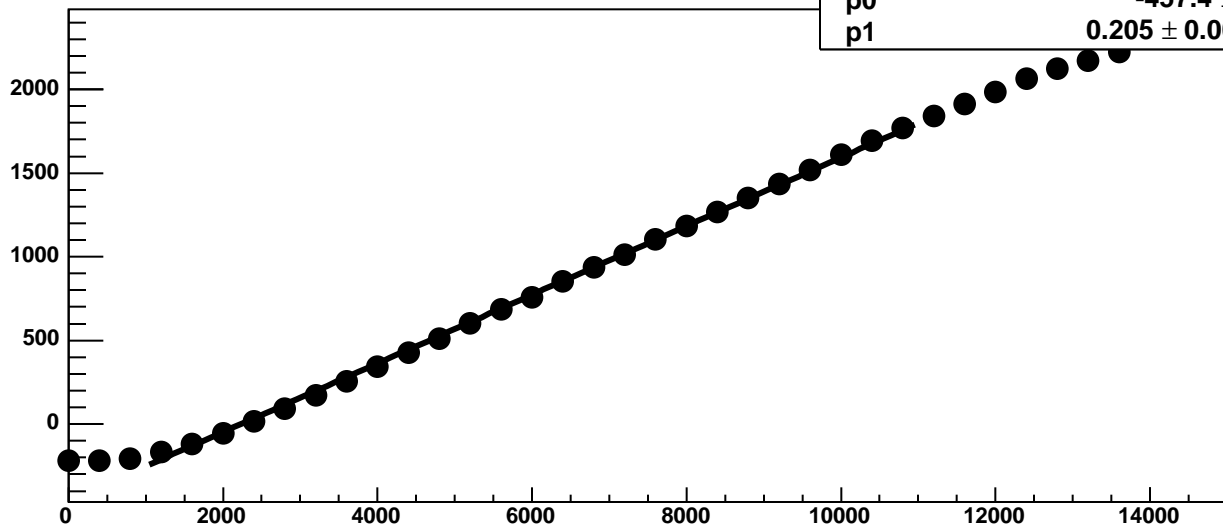
Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



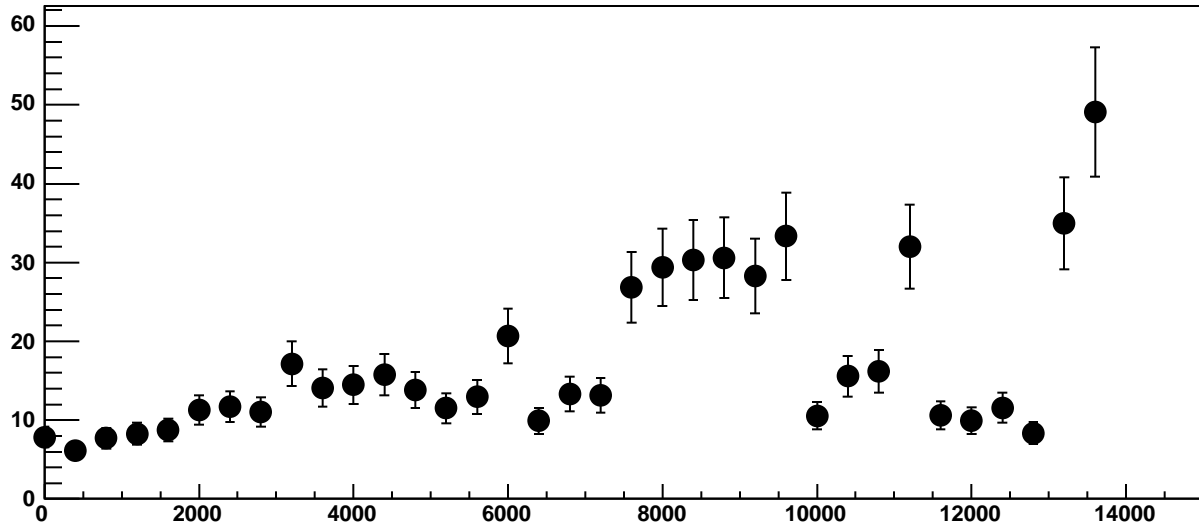
Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC



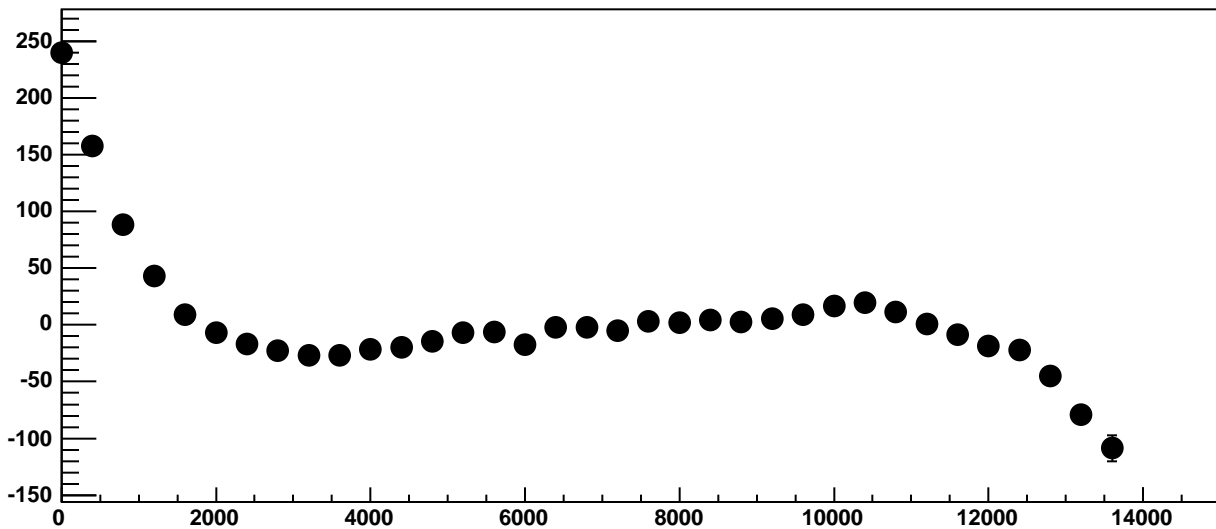
Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



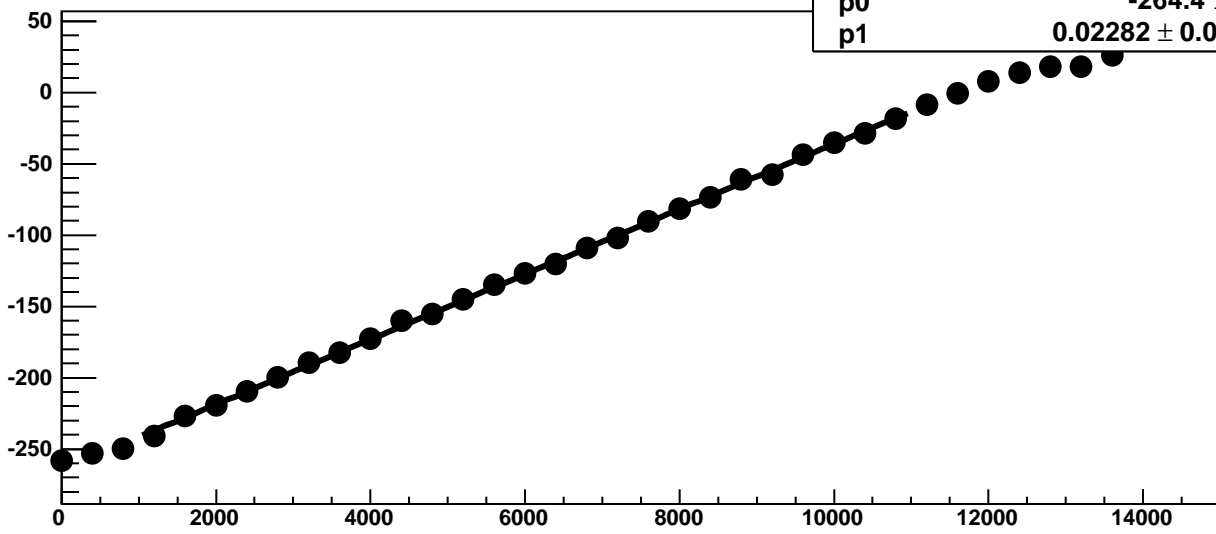
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



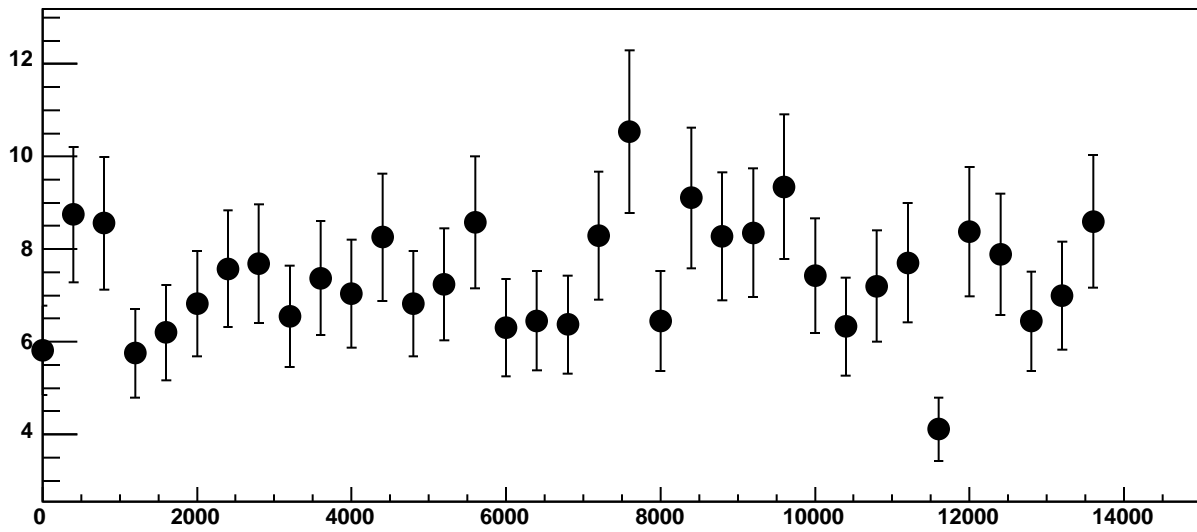
Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC



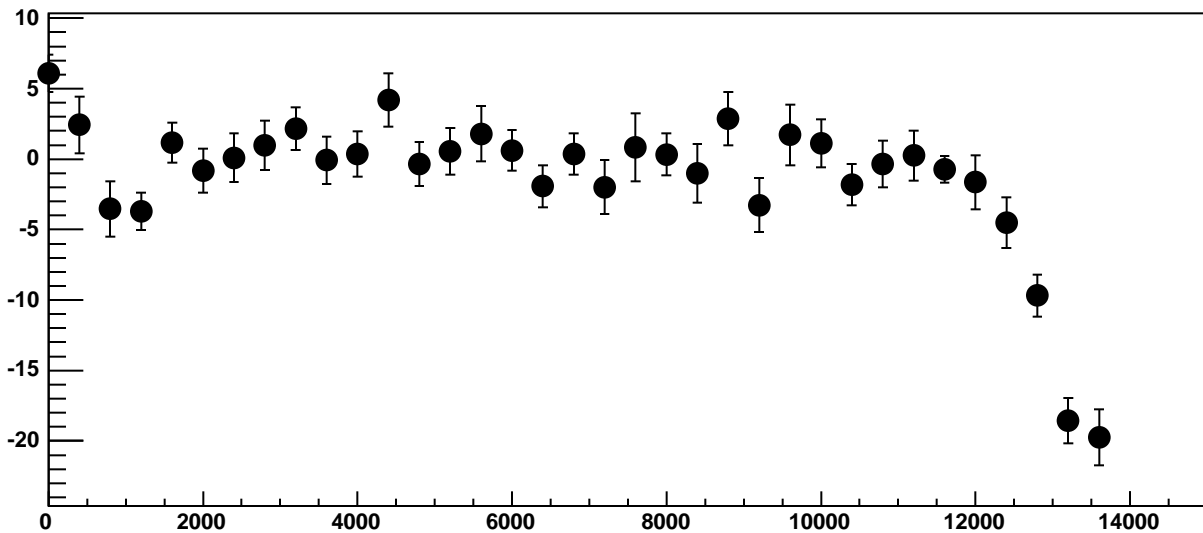
Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



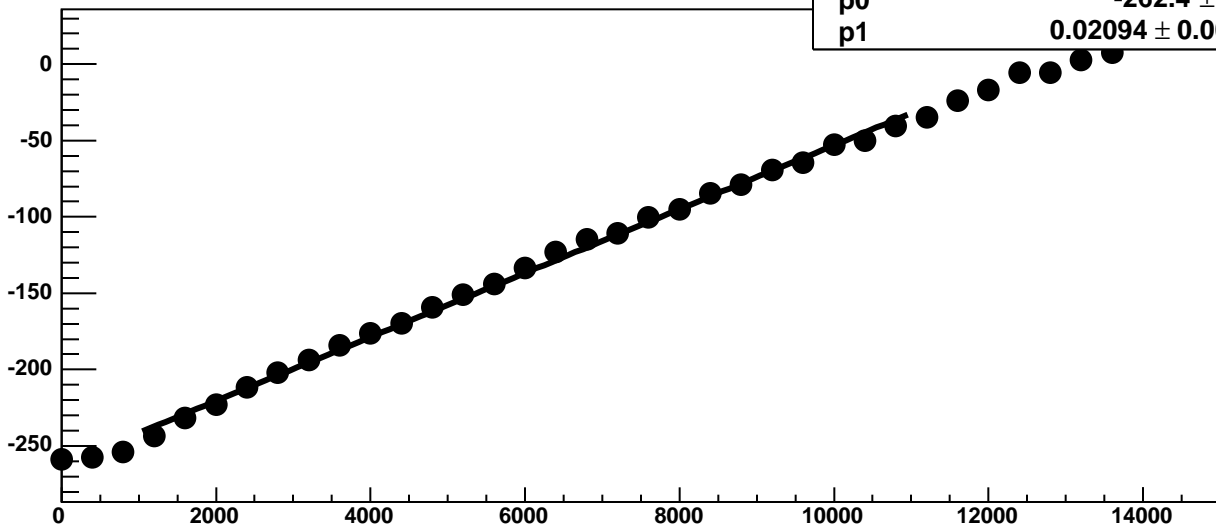
Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



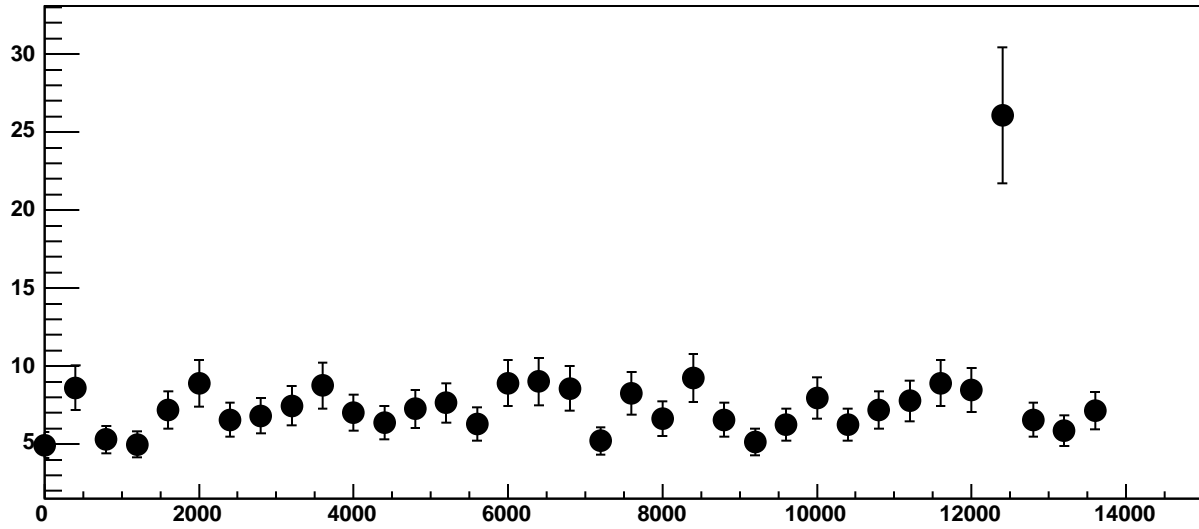
Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



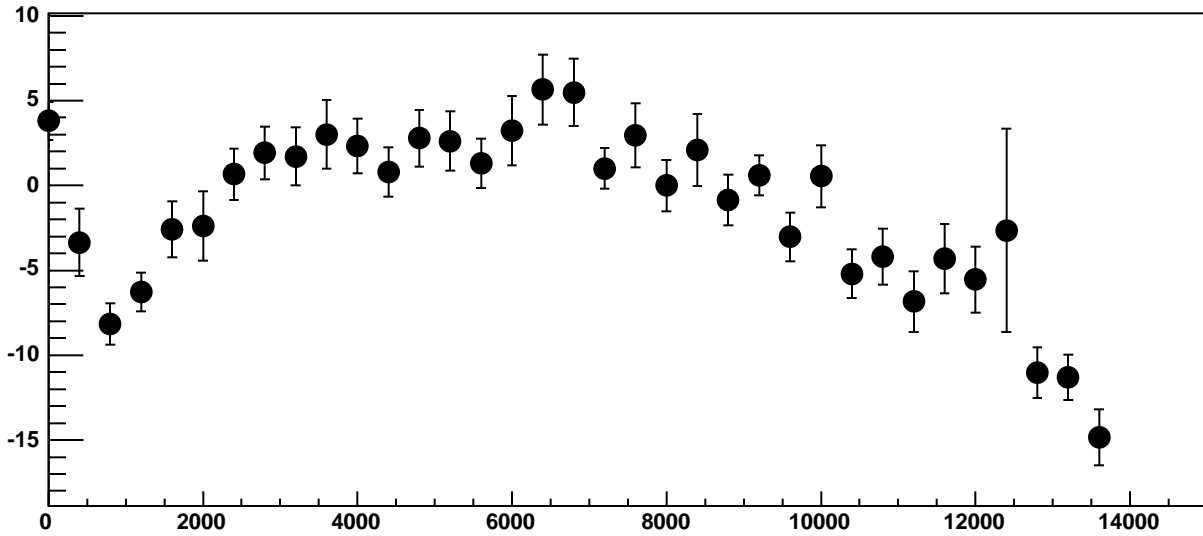
Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

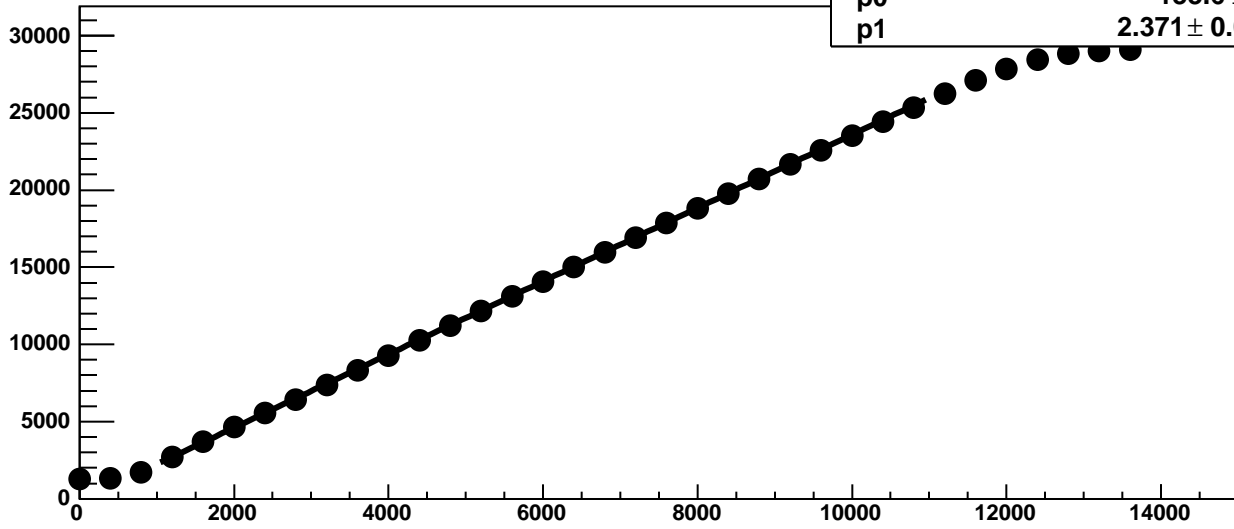


Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

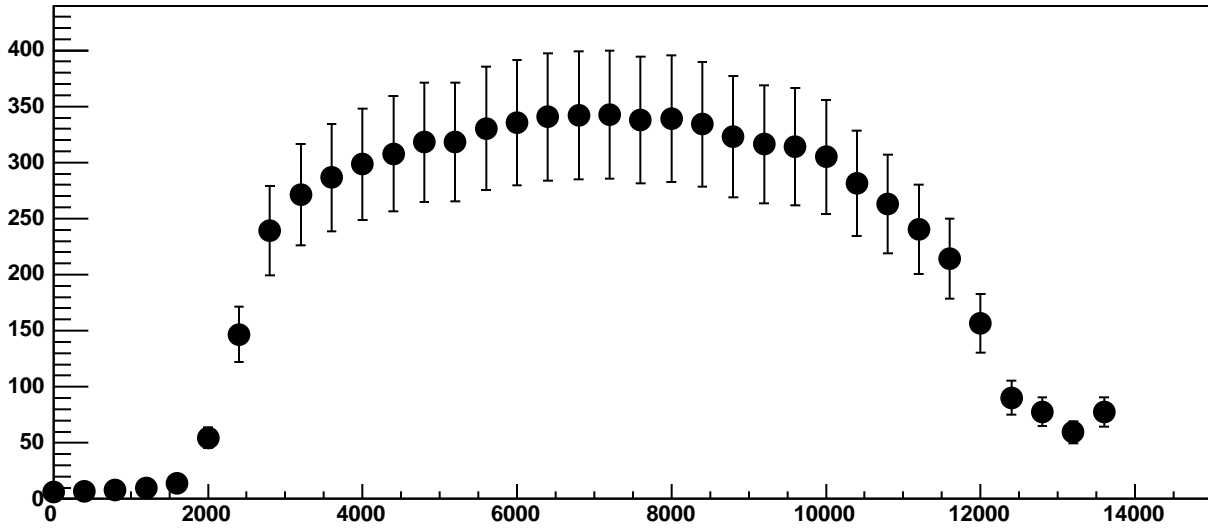




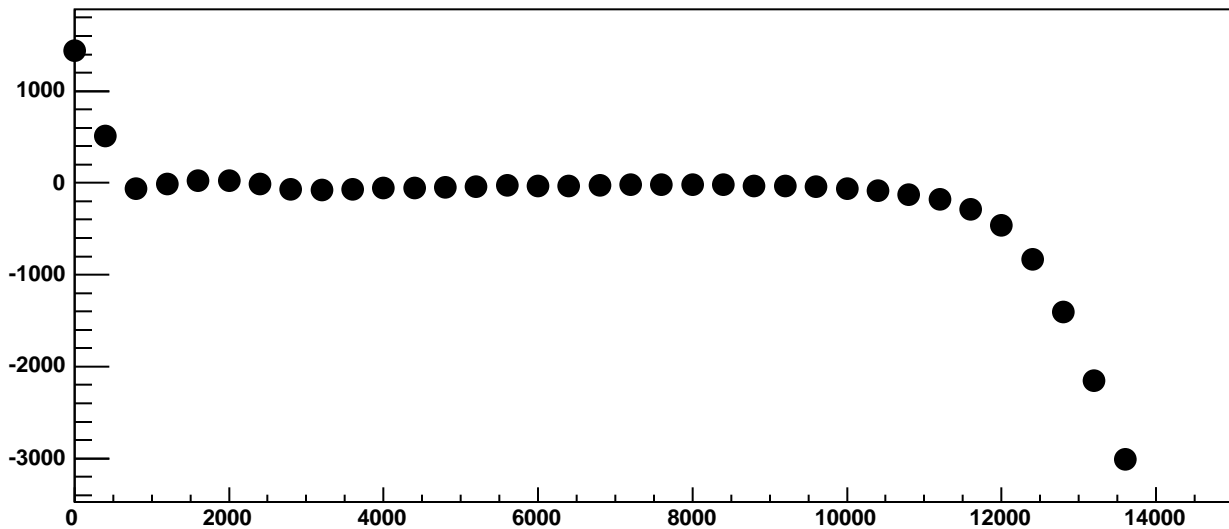
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



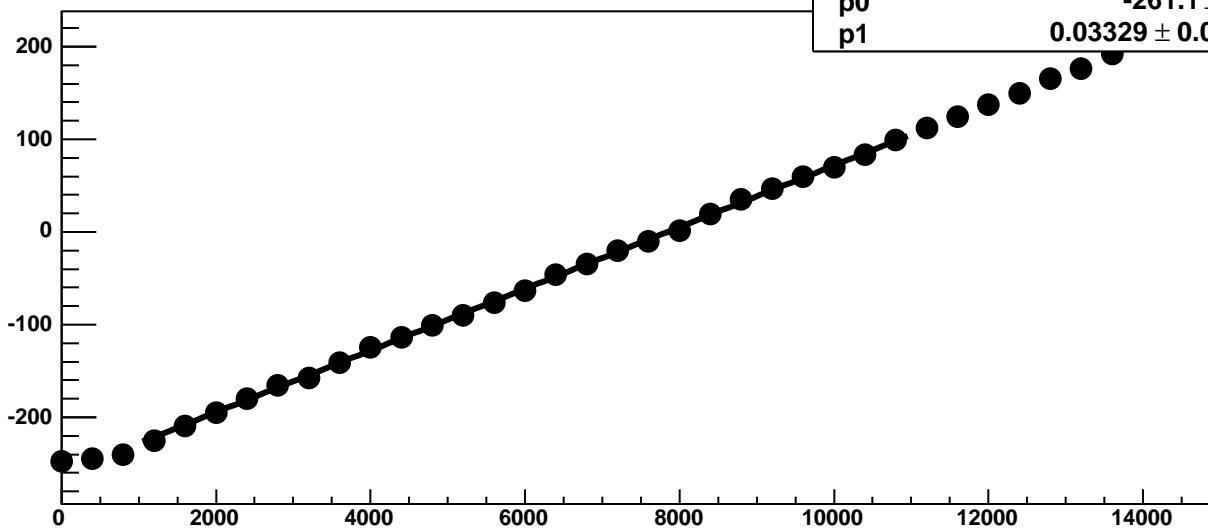
Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

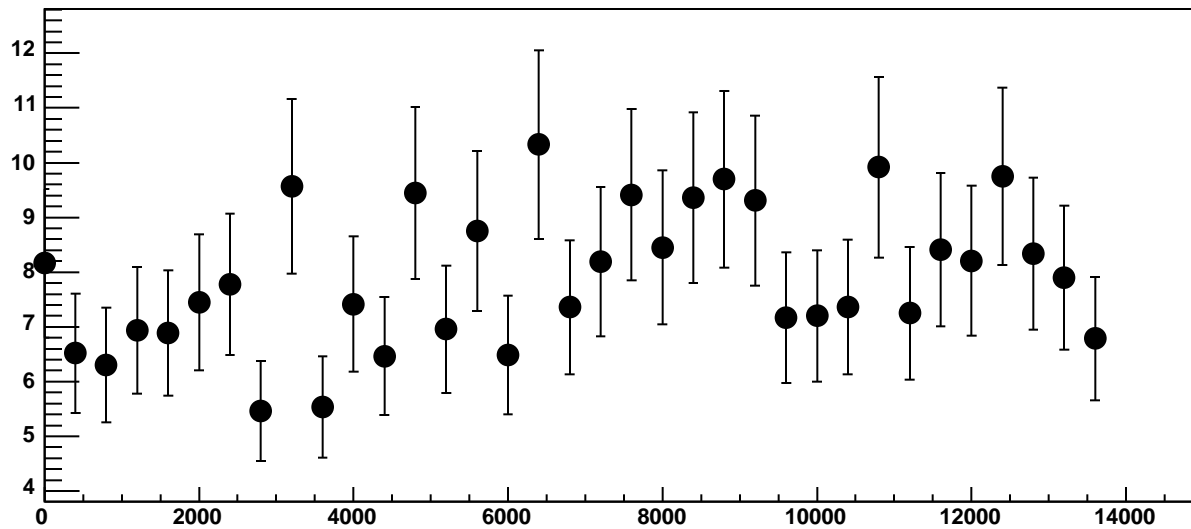


Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

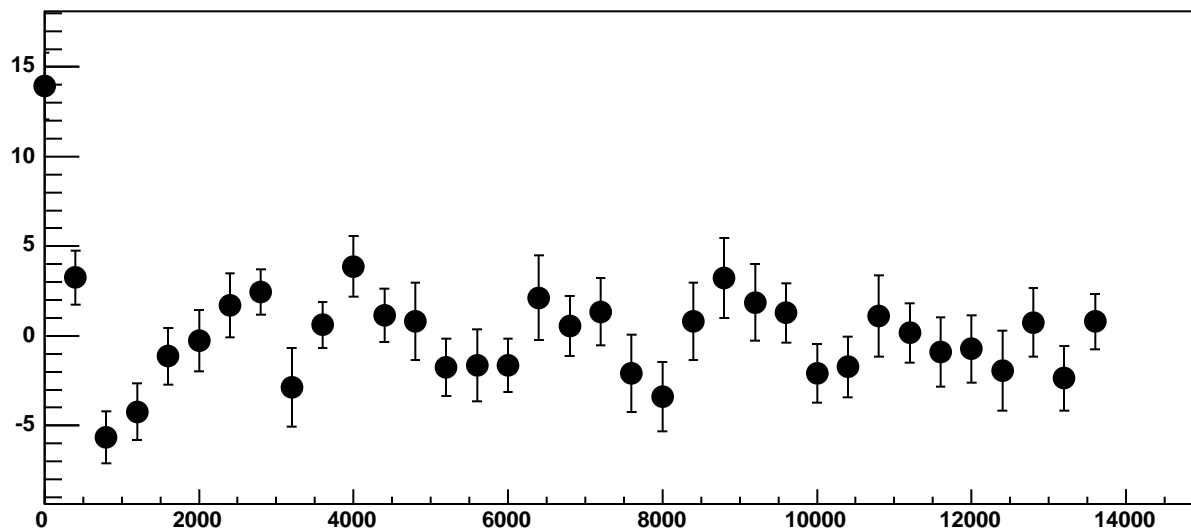


$\chi^2 / \text{ndf}$  35.25 / 23  
p0 -261.1 ± 0.7541  
p1 0.03329 ± 0.0001206

Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

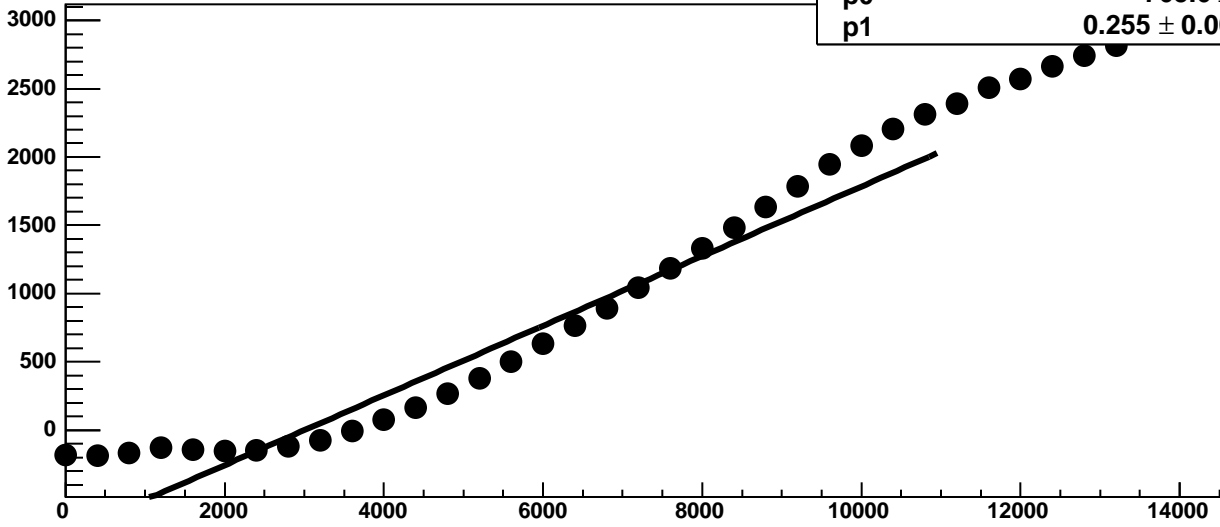


Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

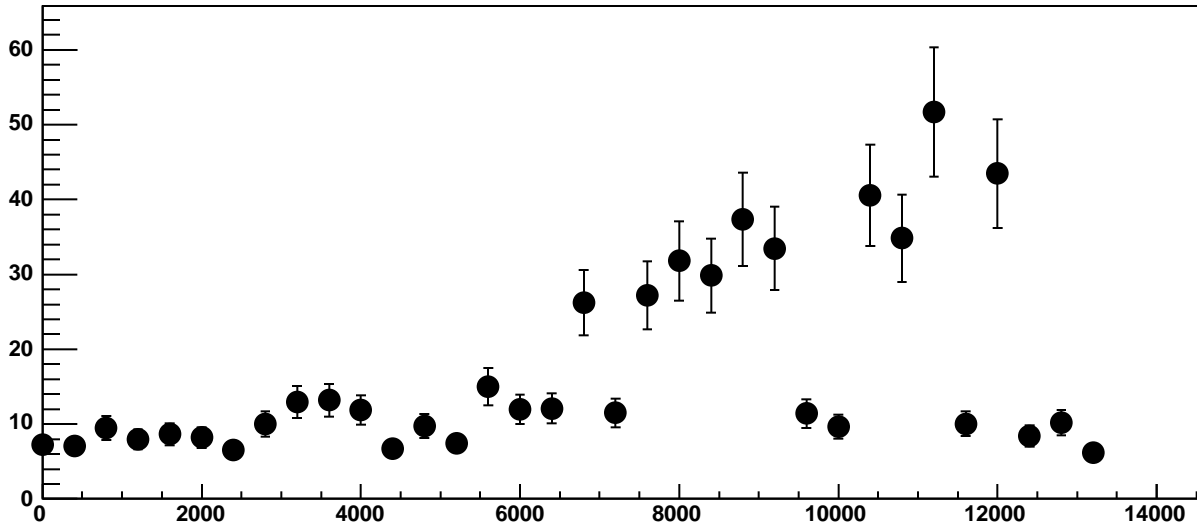


Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

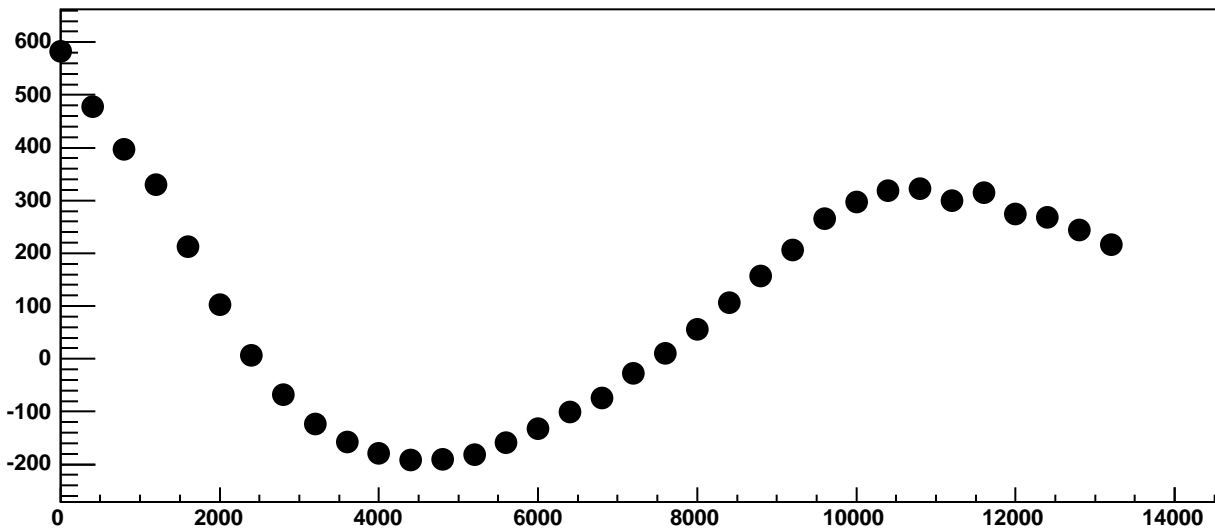
$\chi^2 / \text{ndf}$  1.291e+05 / 23  
p0 -765.6 ± 1.027  
p1 0.255 ± 0.0002018



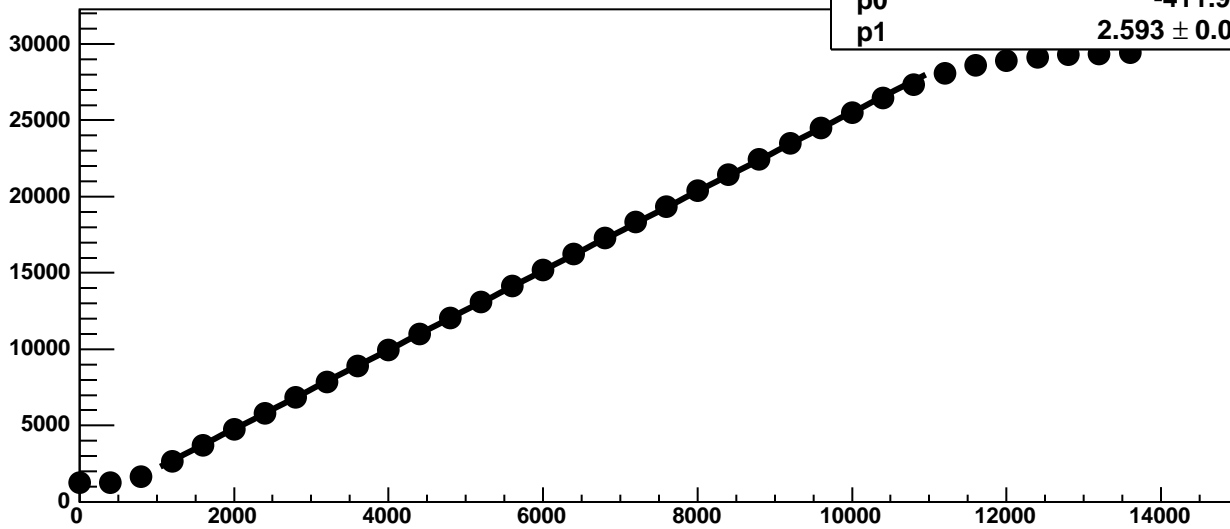
Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



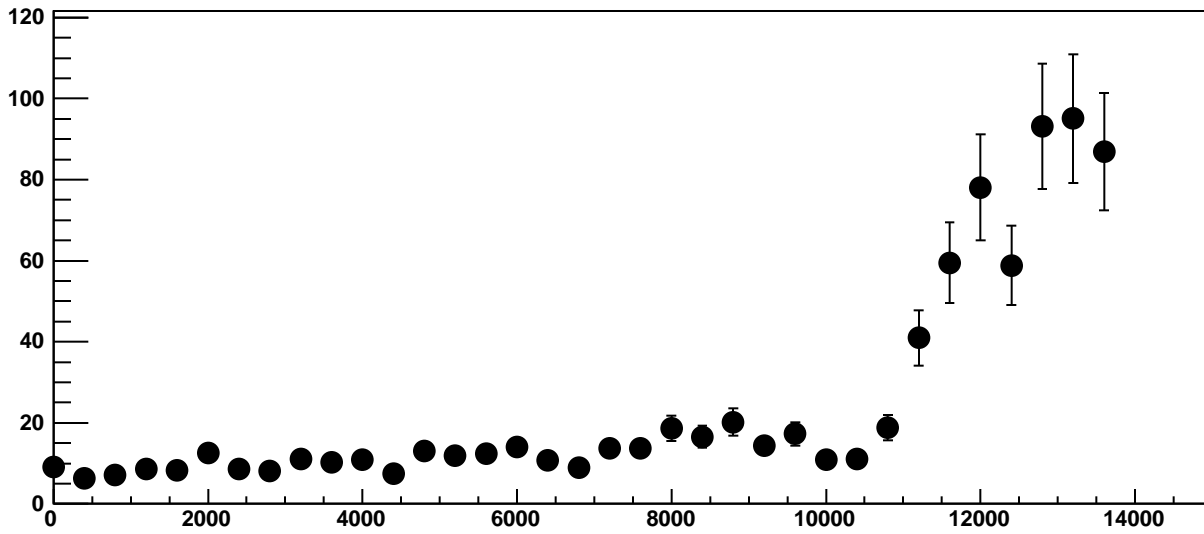
Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



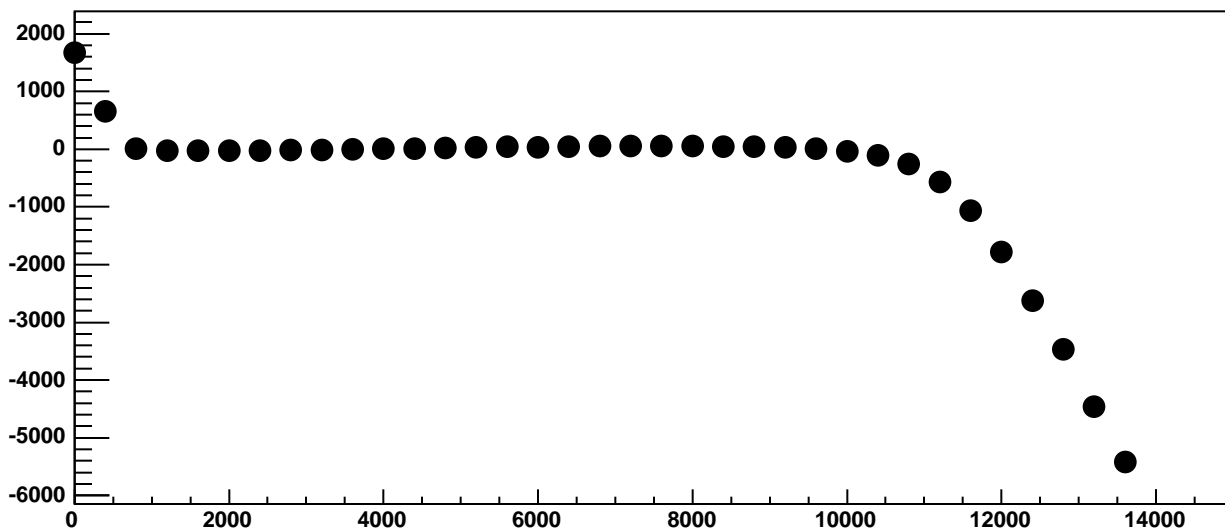
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



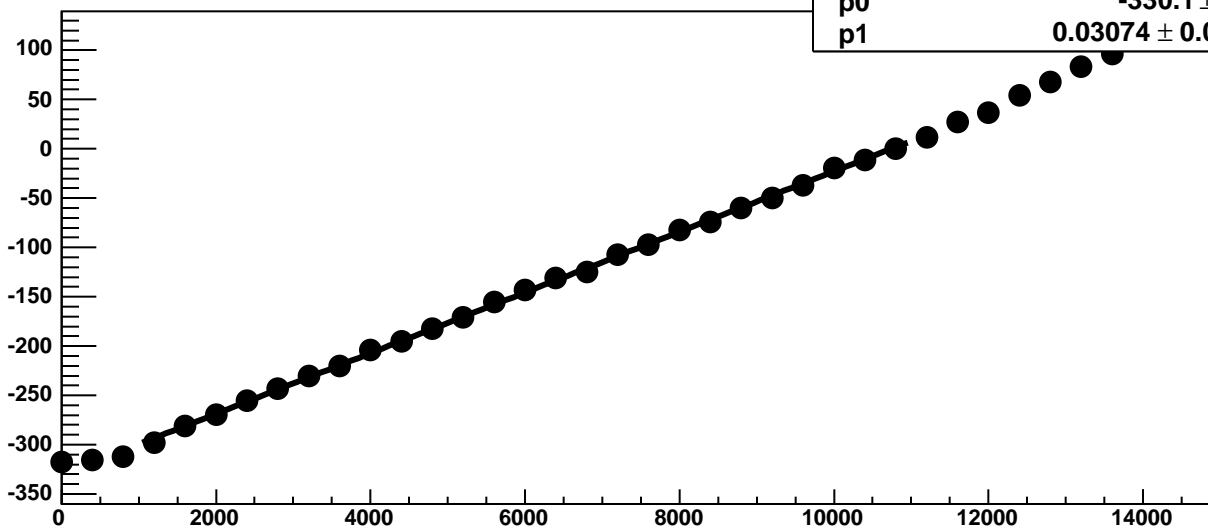
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

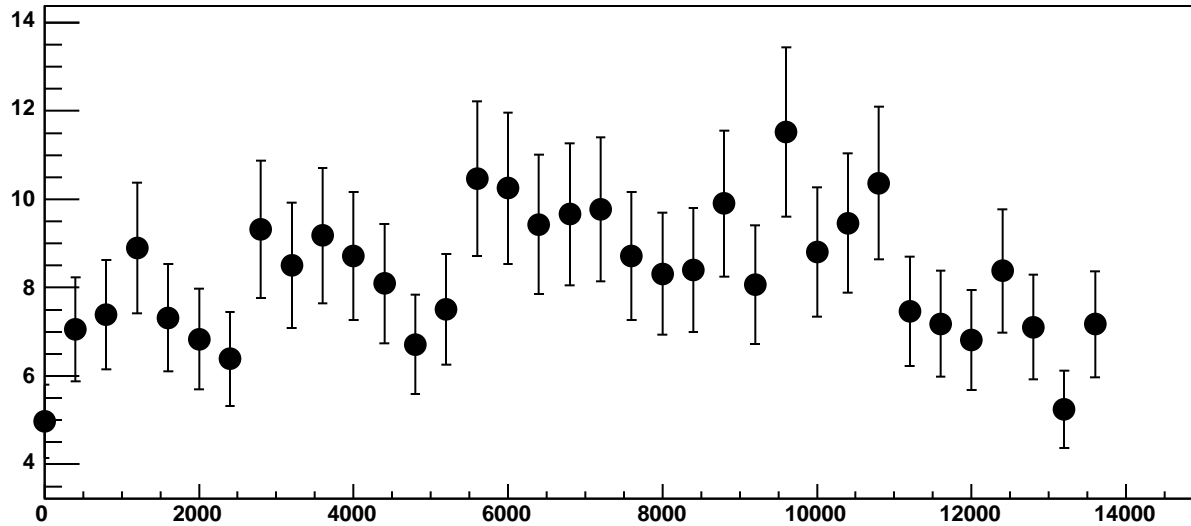


Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

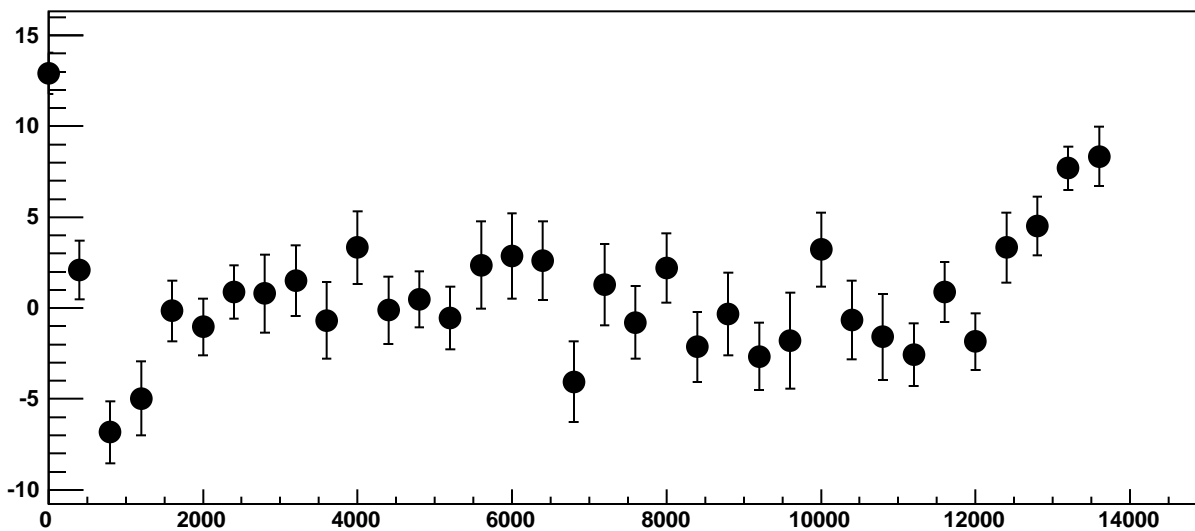


$\chi^2 / \text{ndf}$  26.48 / 23  
p0  $-330.1 \pm 0.8504$   
p1  $0.03074 \pm 0.0001361$

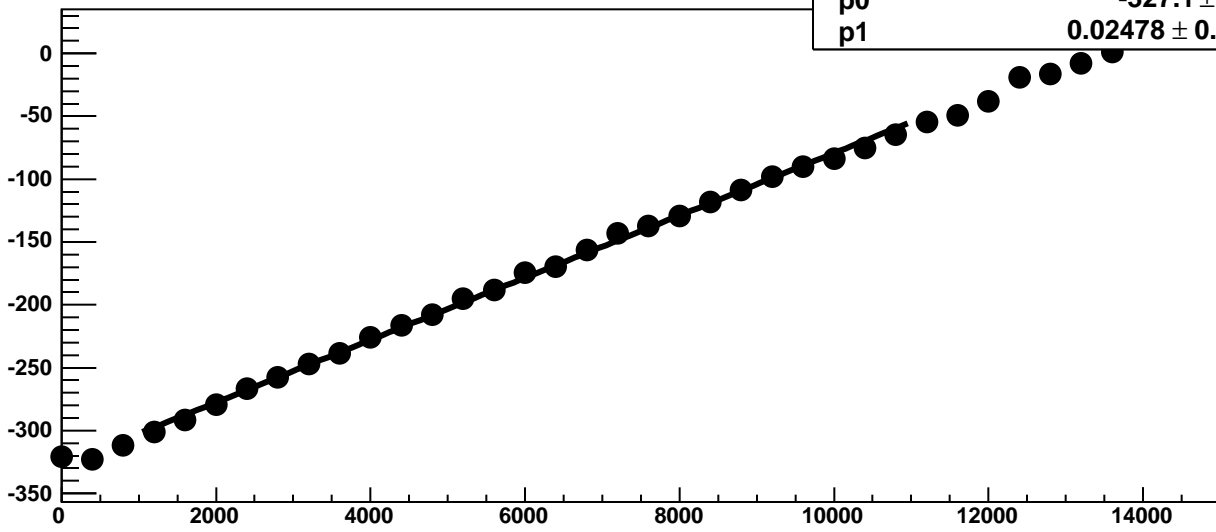
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

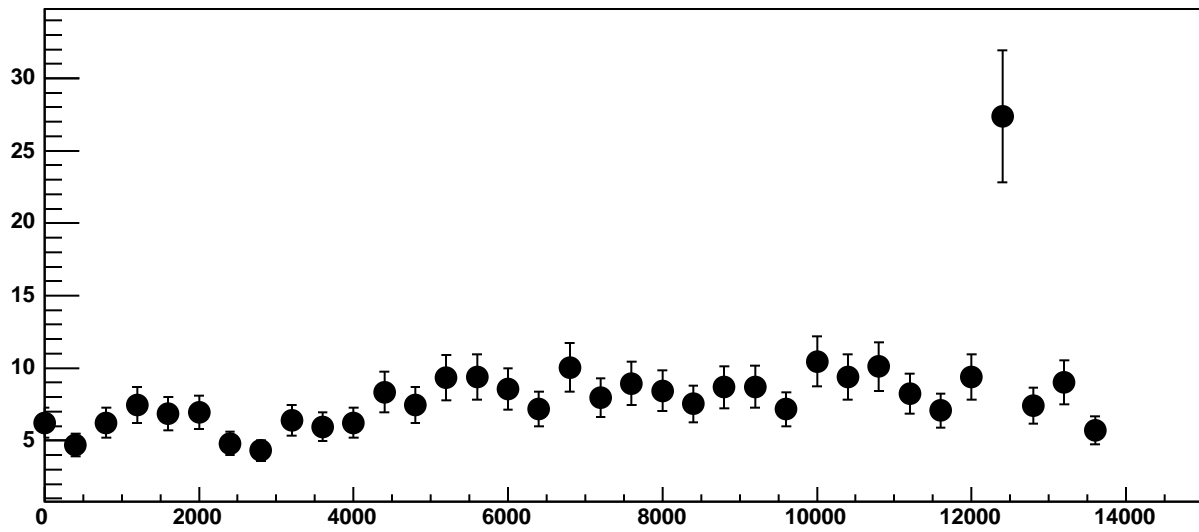


Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

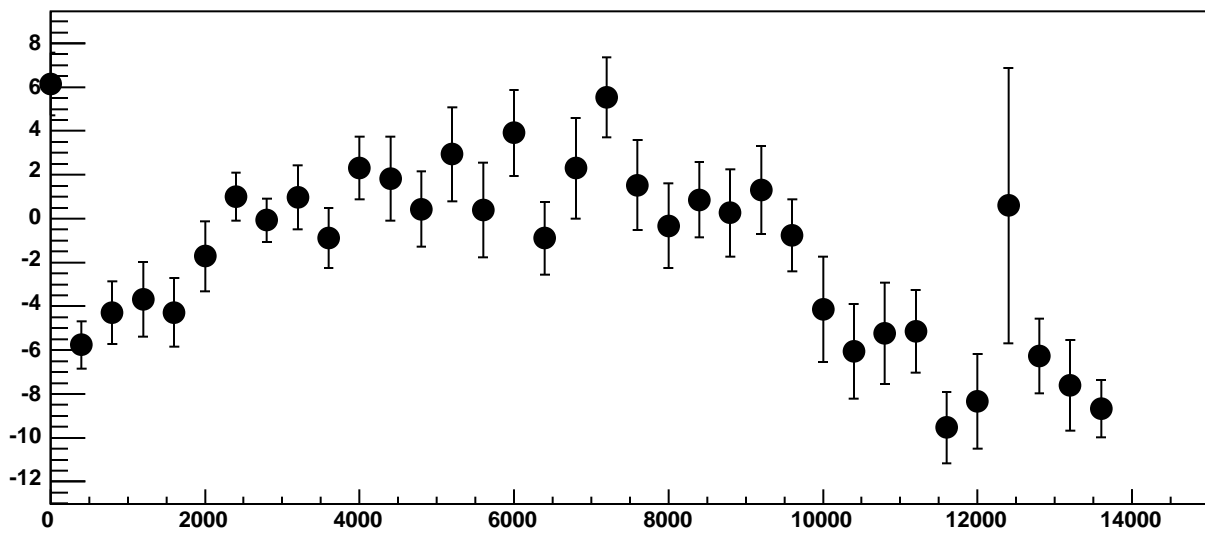


$\chi^2 / \text{ndf}$  52.38 / 23  
p0  $-327.1 \pm 0.6918$   
p1  $0.02478 \pm 0.000119$

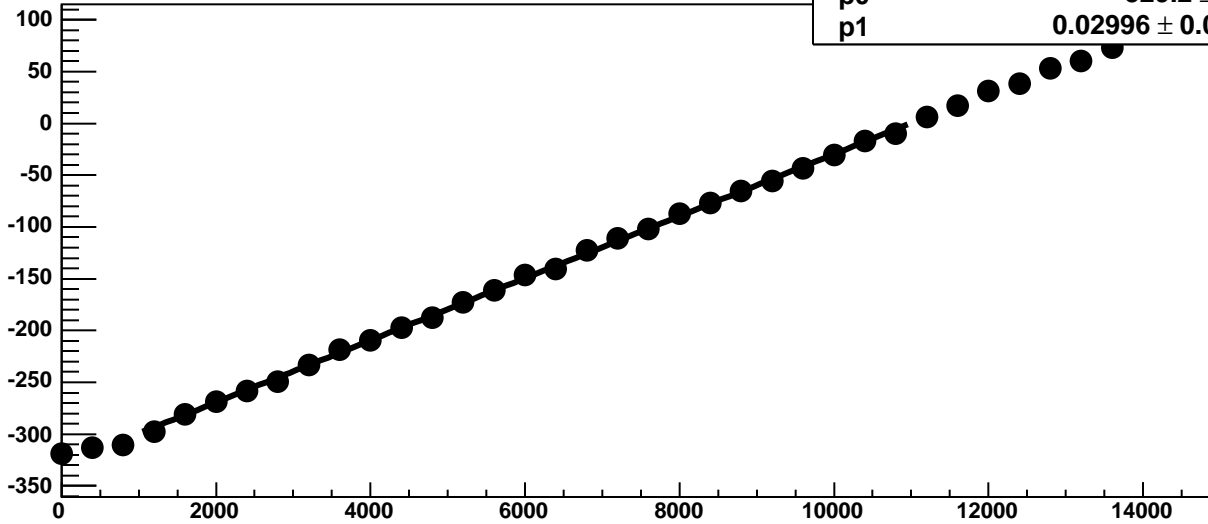
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

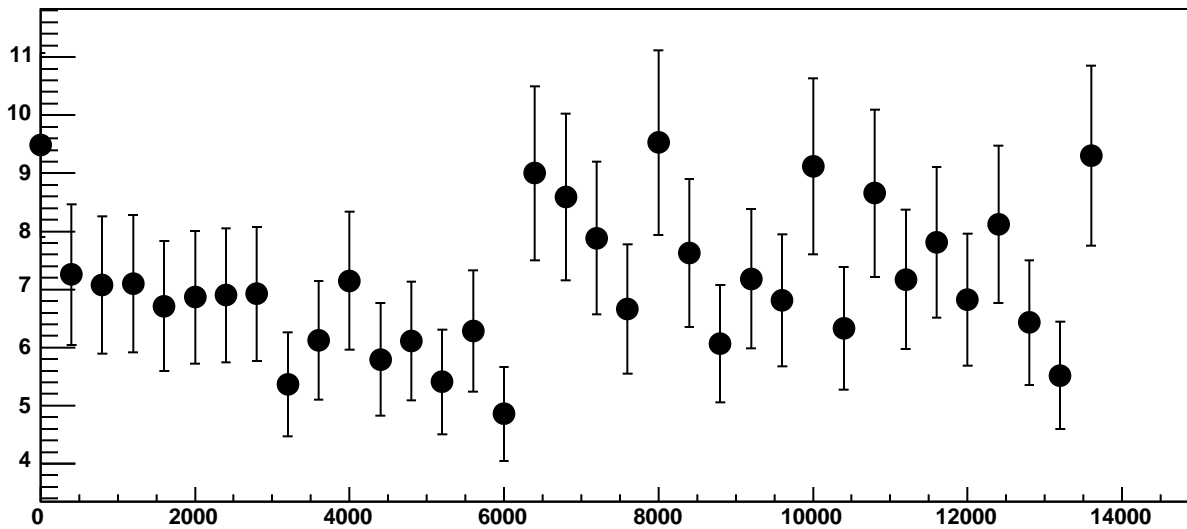


Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

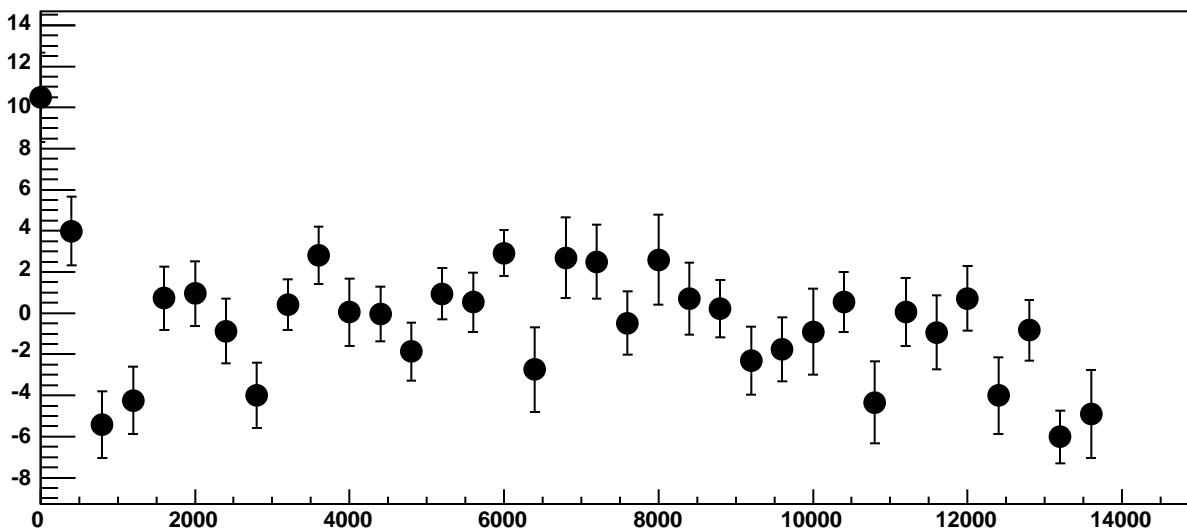


$\chi^2 / \text{ndf}$  43.11 / 23  
p0  $-329.2 \pm 0.7075$   
p1  $0.02996 \pm 0.0001117$

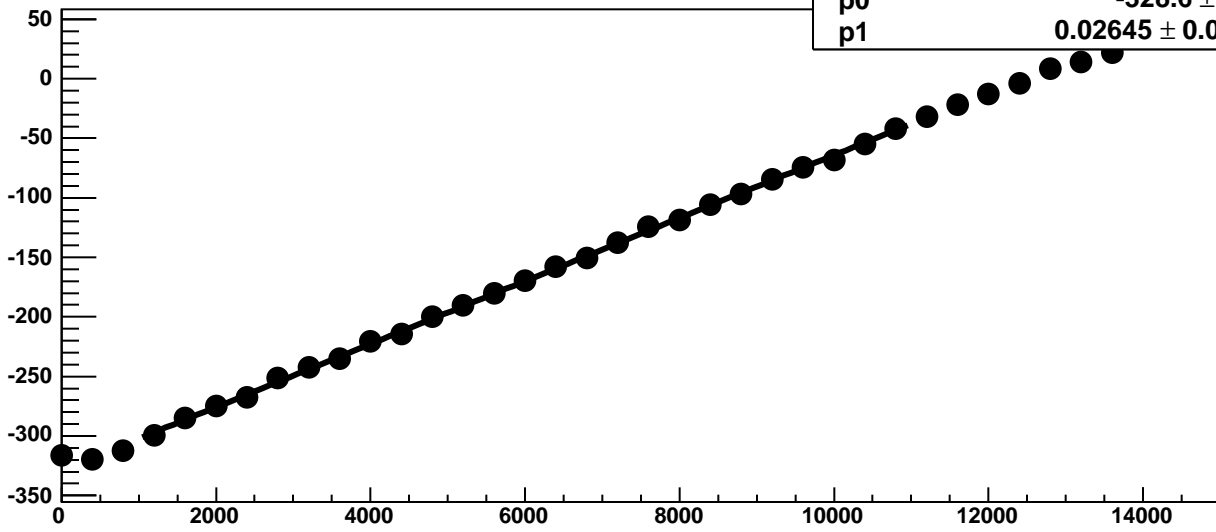
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



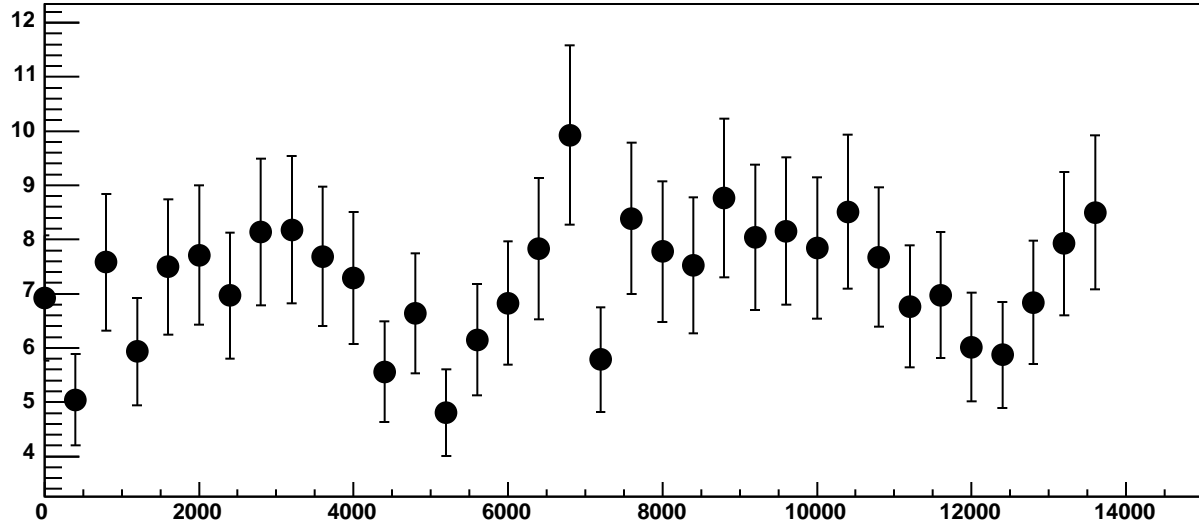
Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



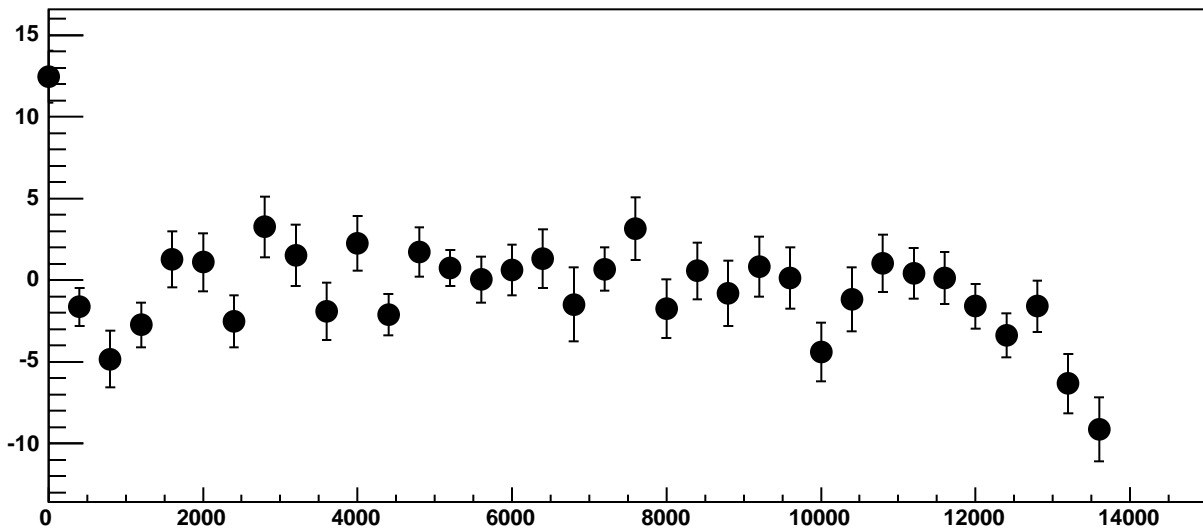
Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

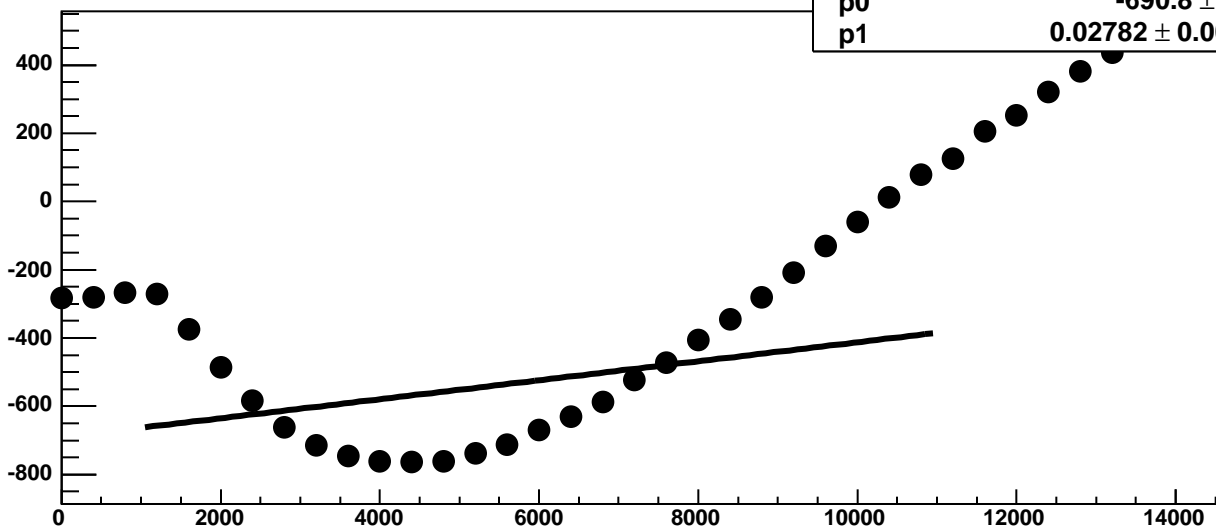


Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

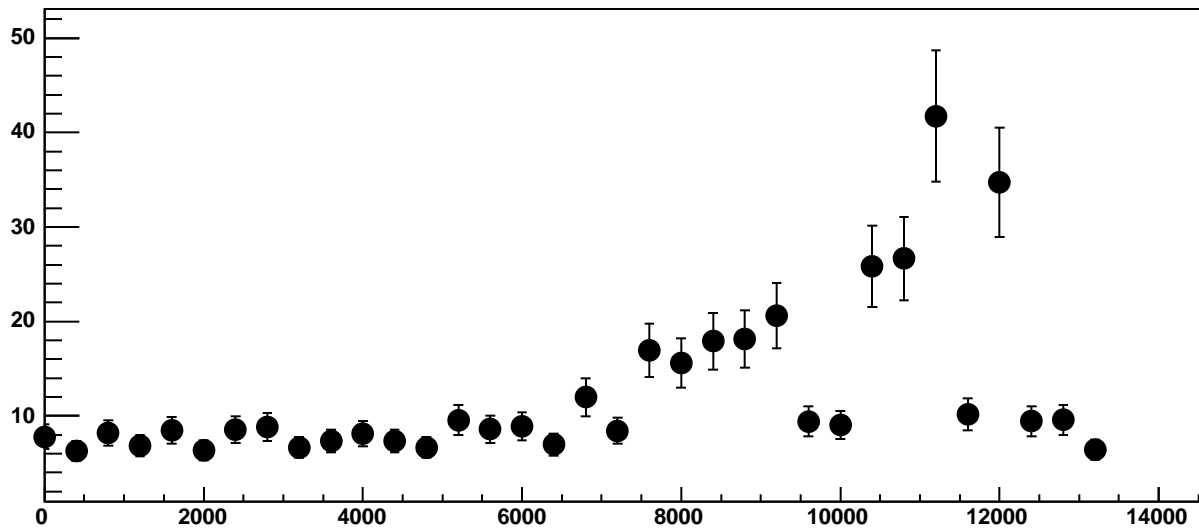




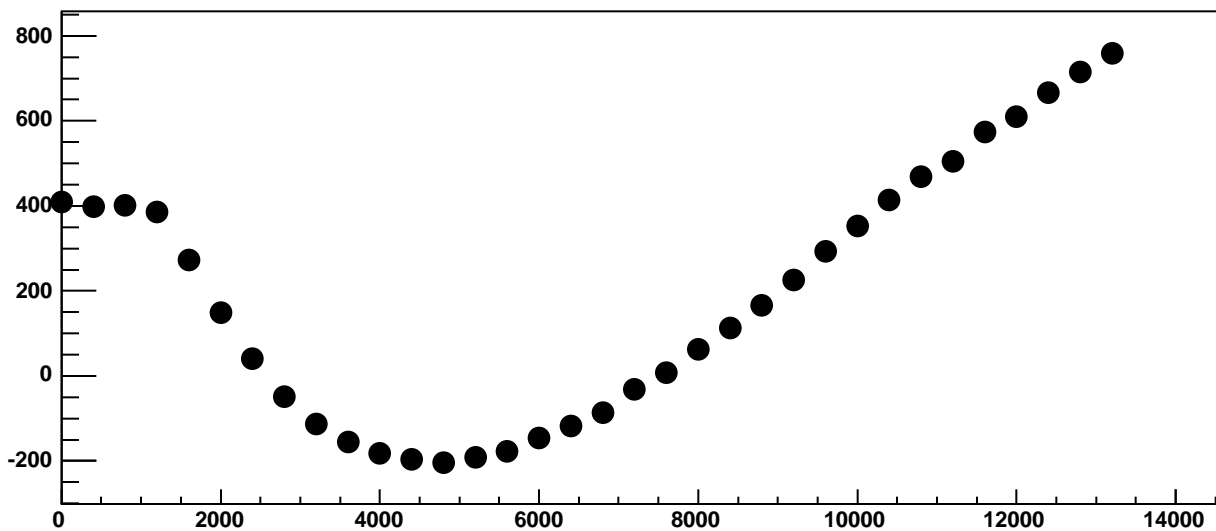
Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



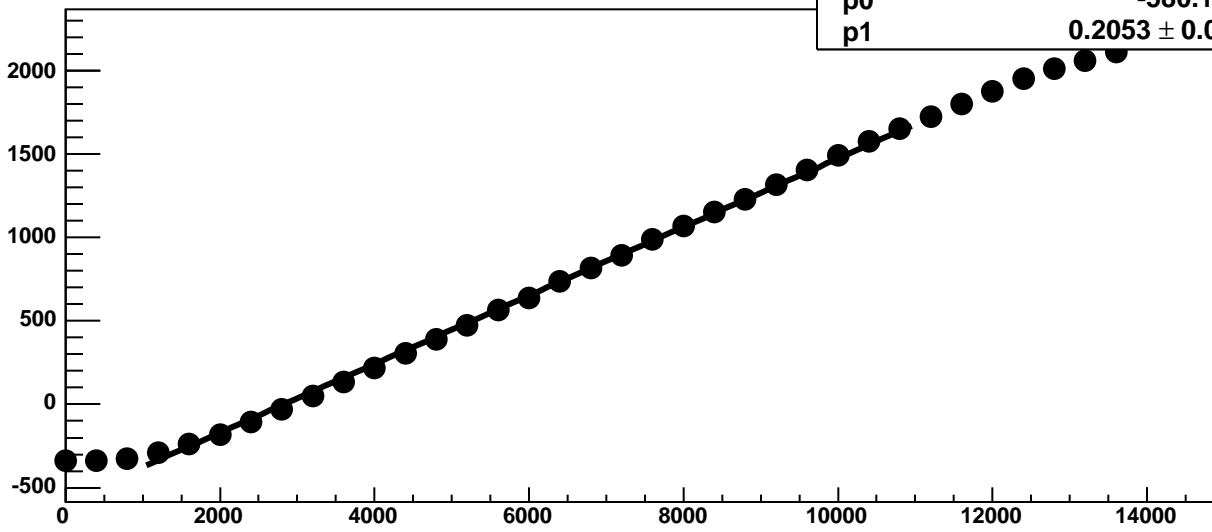
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



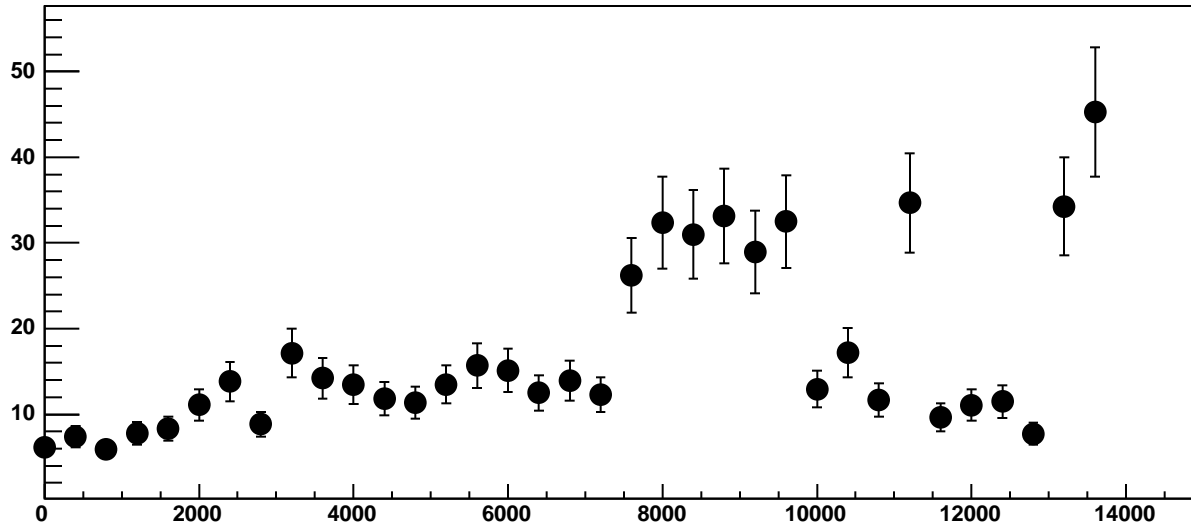
Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



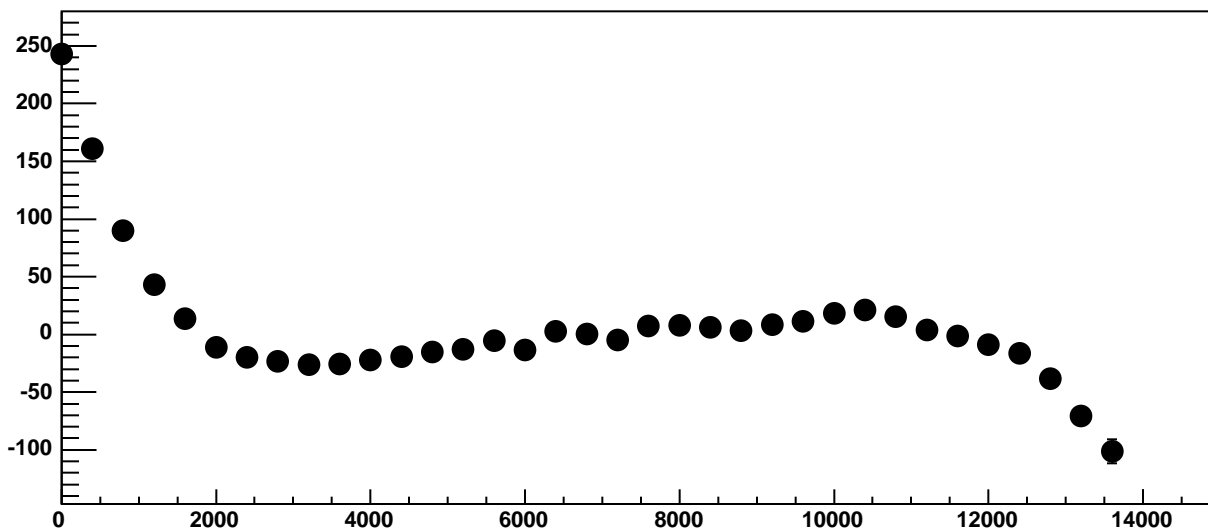
Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



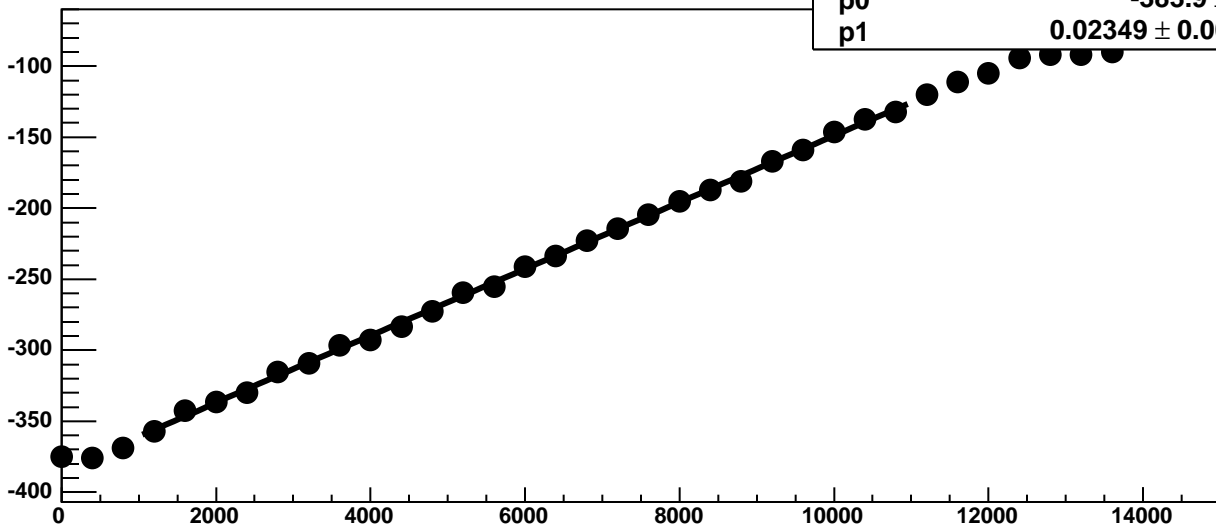
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

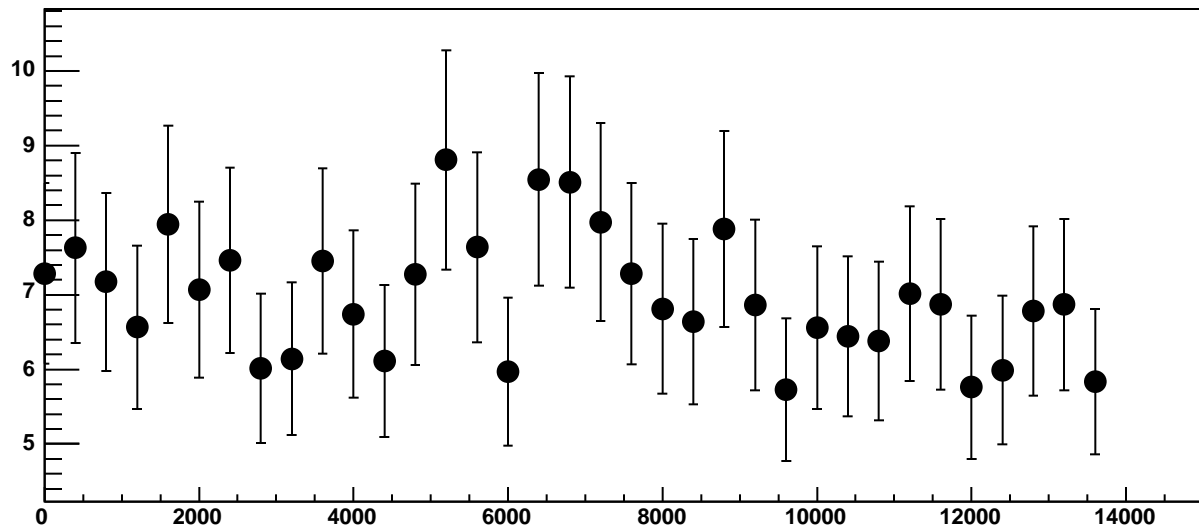


Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

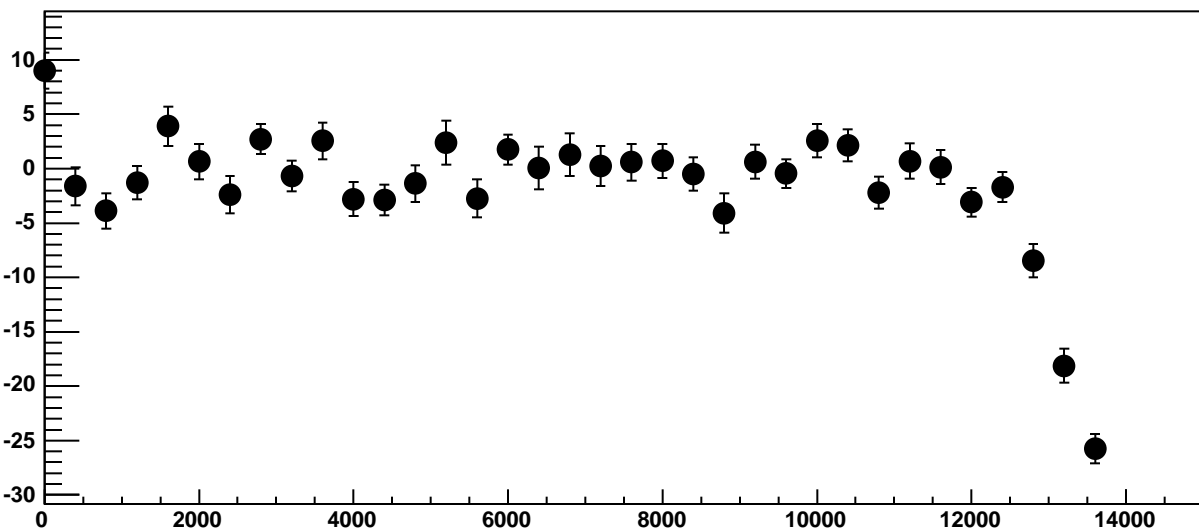


$\chi^2 / \text{ndf}$  40.87 / 23  
p0  $-383.9 \pm 0.723$   
p1  $0.02349 \pm 0.0001067$

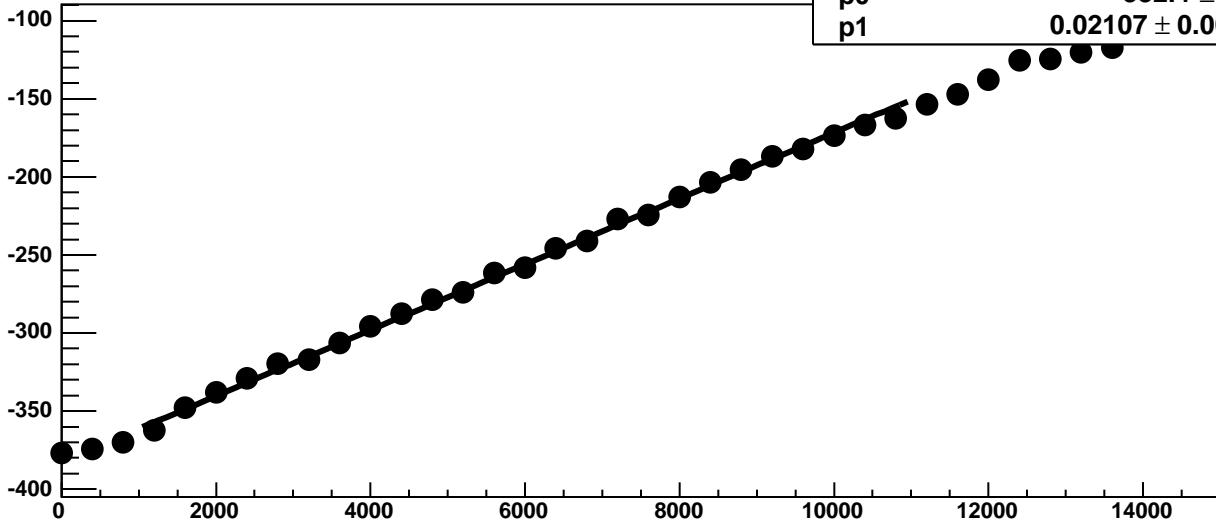
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



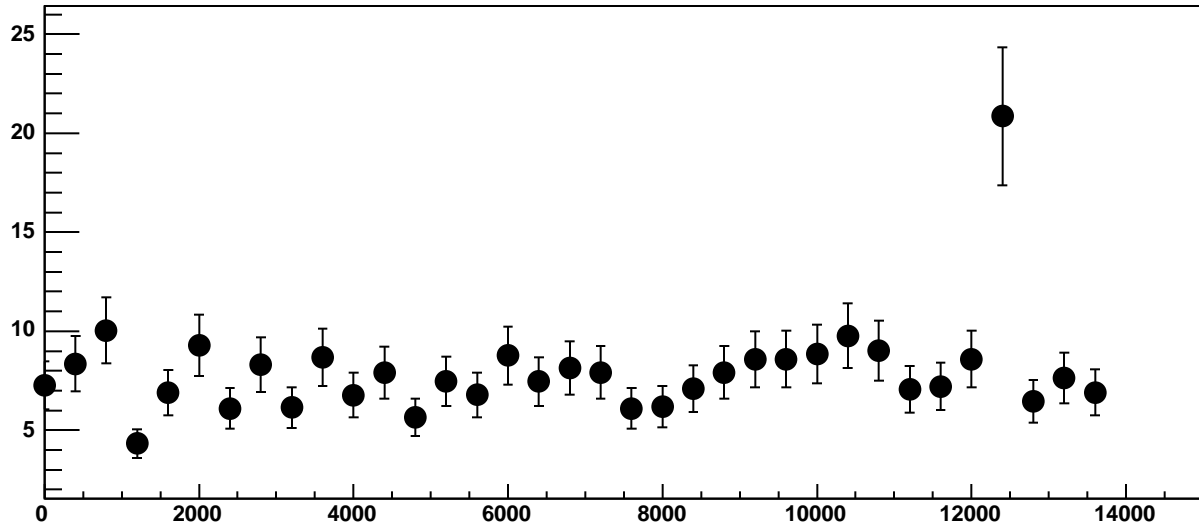
Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



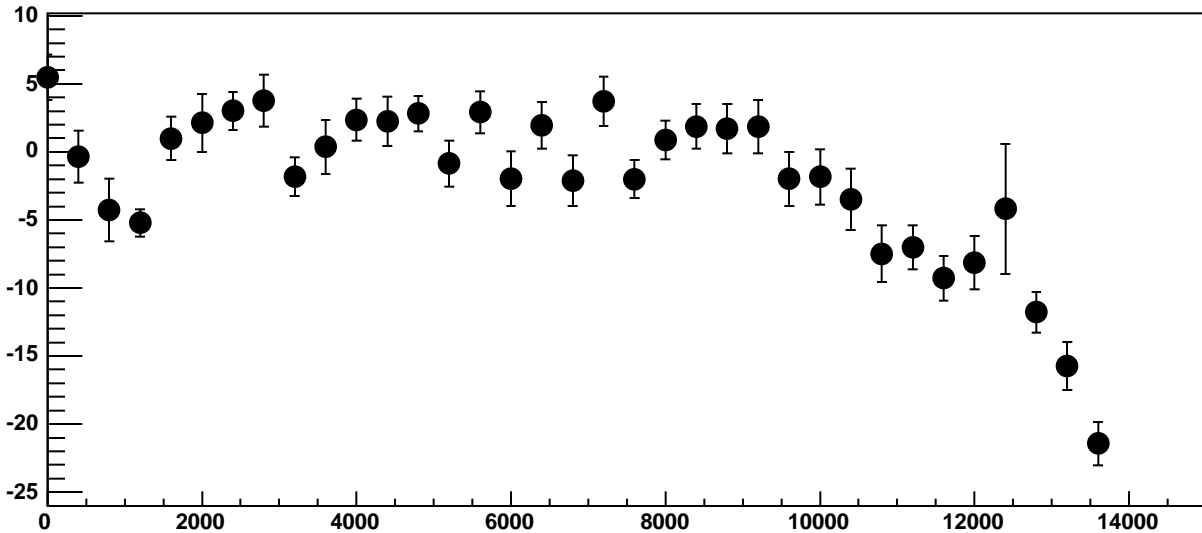
Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

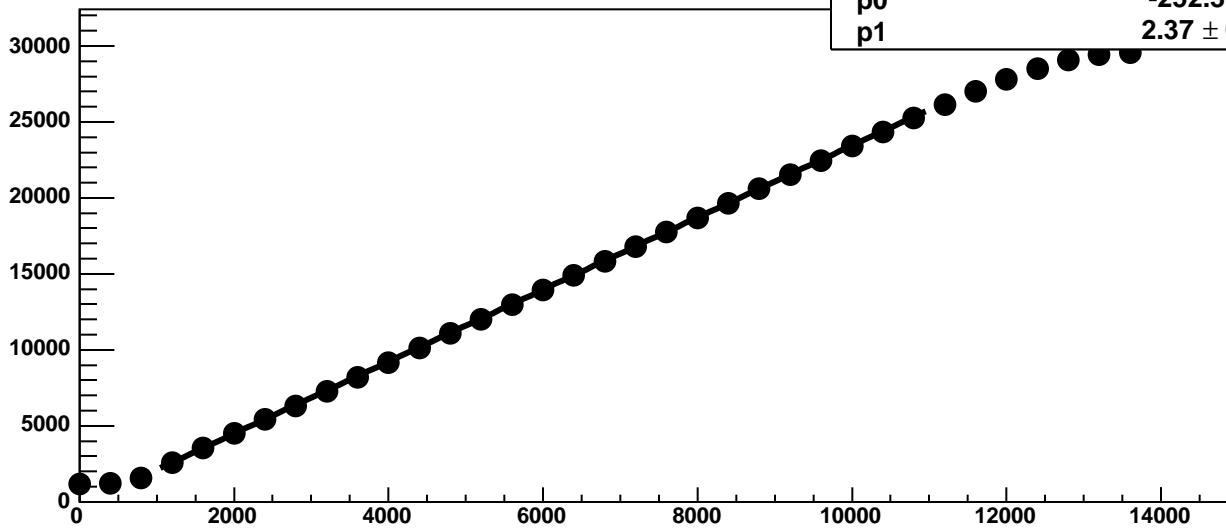


Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

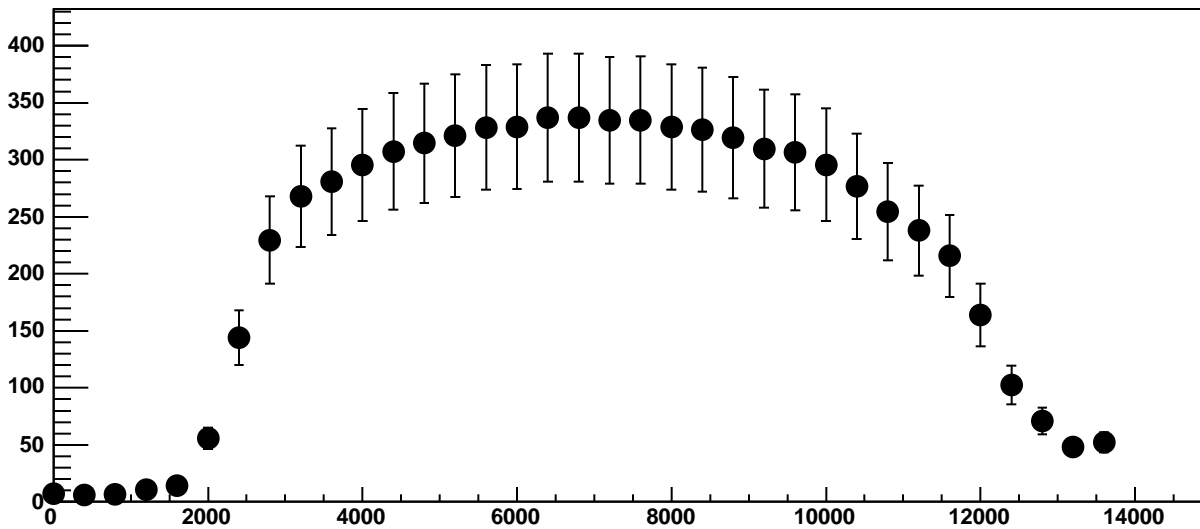


Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

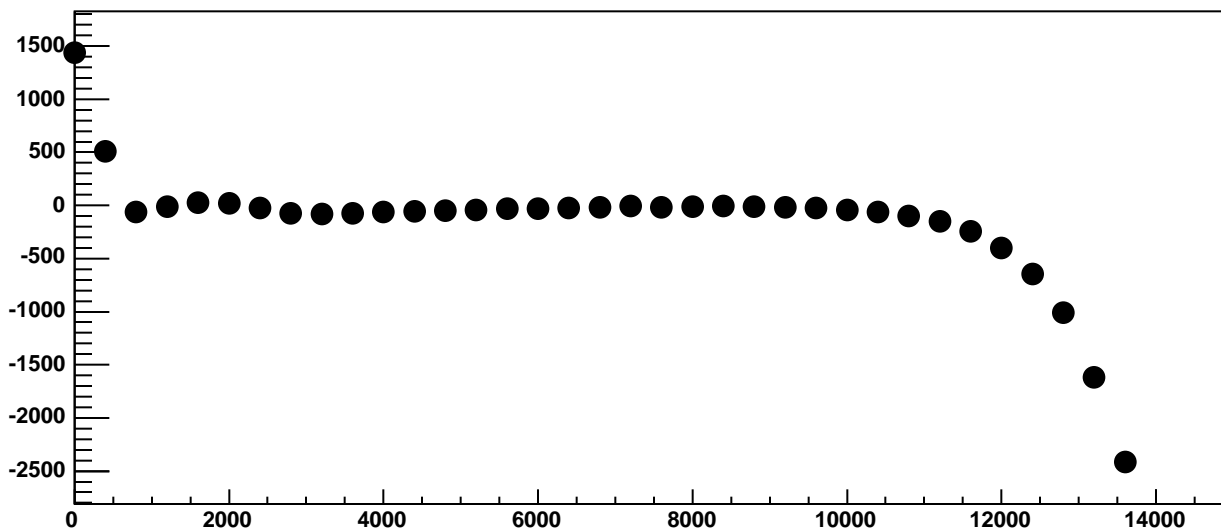
$\chi^2 / \text{ndf}$  99.42 / 23  
p0  $-252.3 \pm 4.015$   
p1  $2.37 \pm 0.00245$



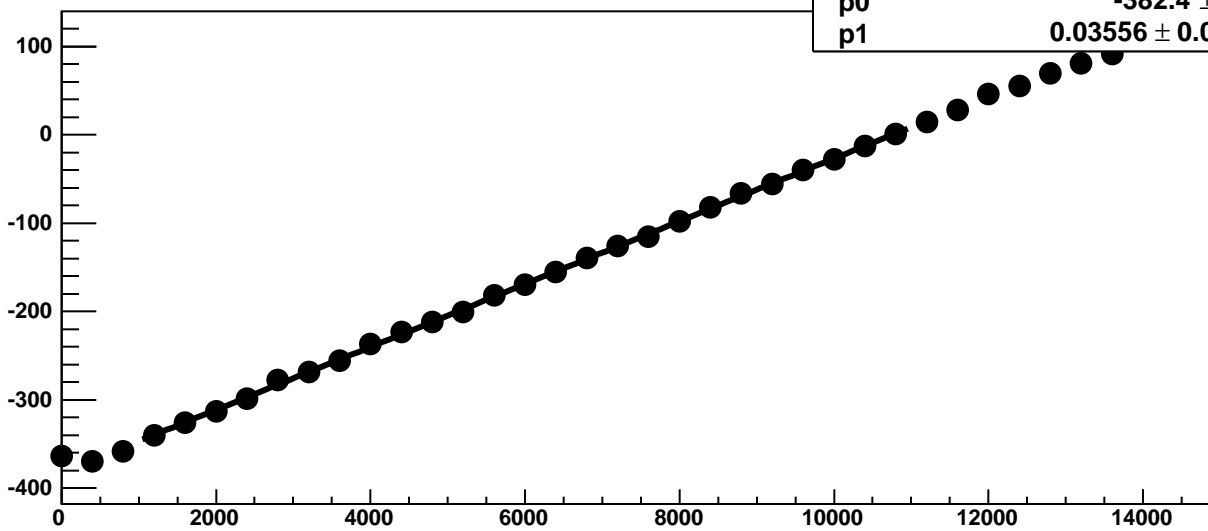
Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

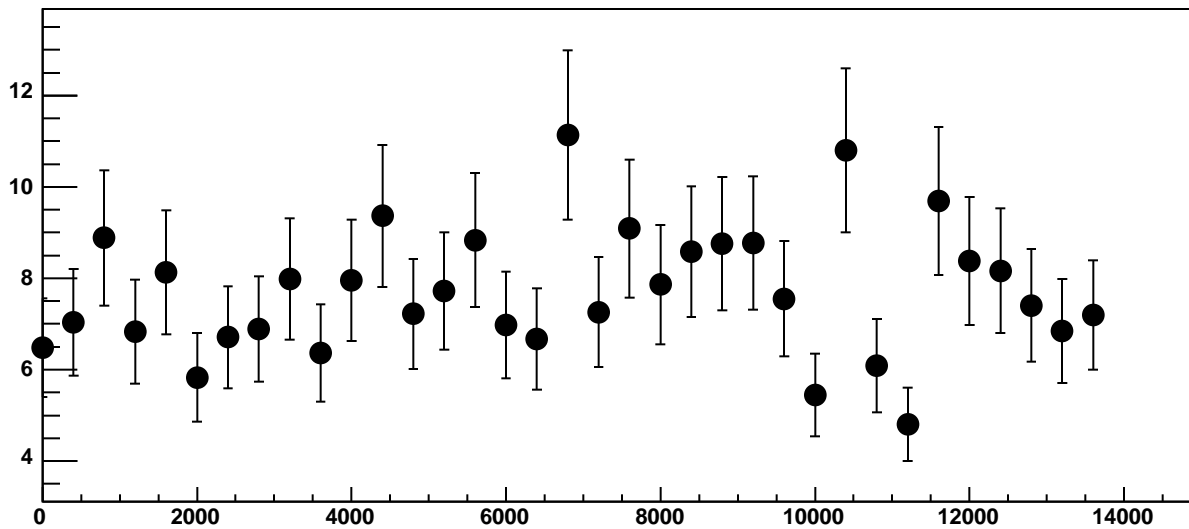


Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

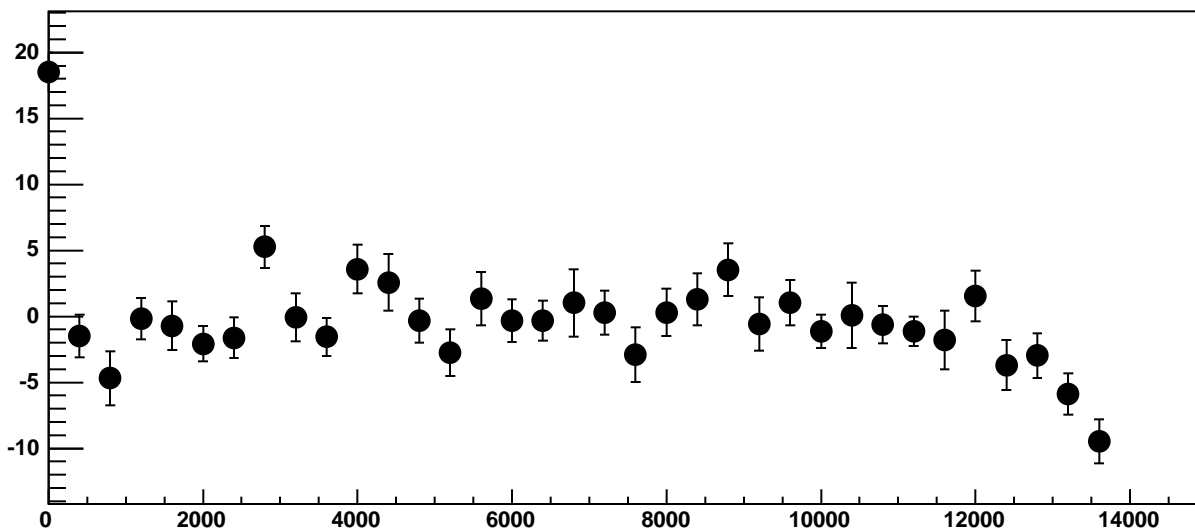


$\chi^2 / \text{ndf}$  31.33 / 23  
p0  $-382.4 \pm 0.7432$   
p1  $0.03556 \pm 0.0001126$

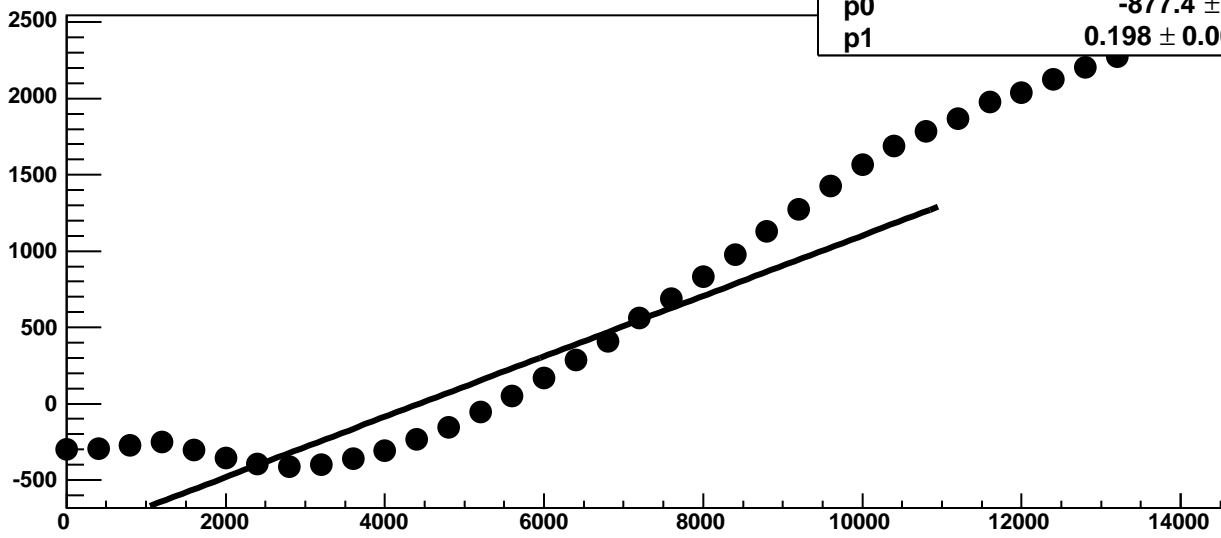
Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

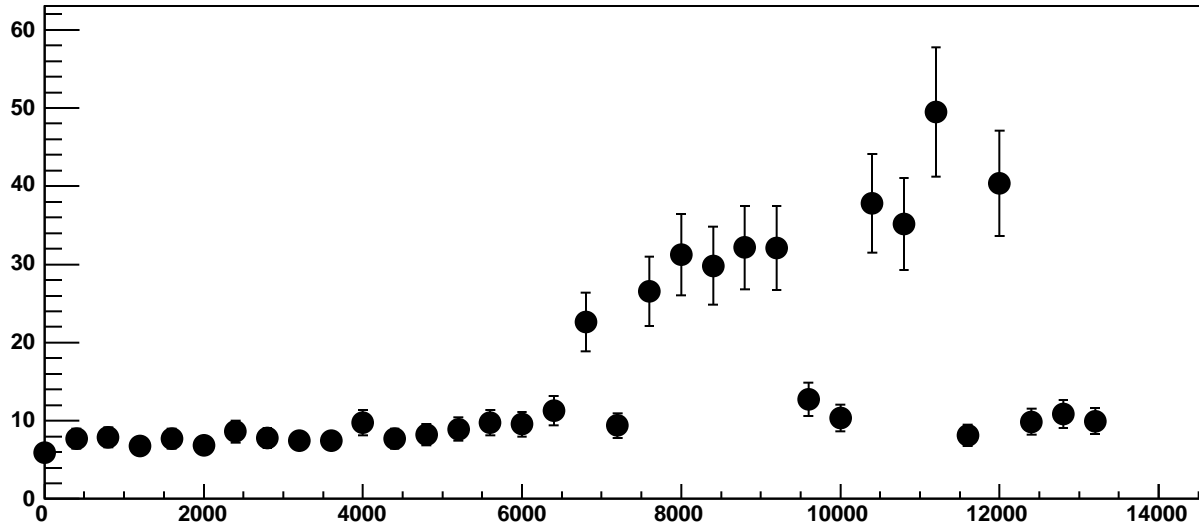


Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

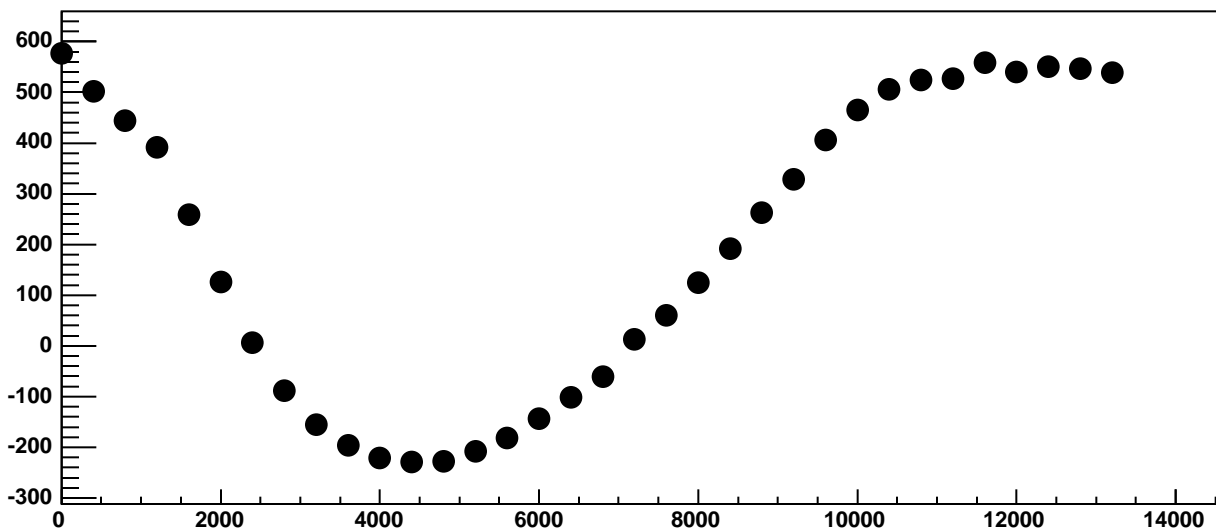


$\chi^2 / \text{ndf}$  2.486e+05 / 23  
p0 -877.4 ± 0.9396  
p1 0.198 ± 0.0001945

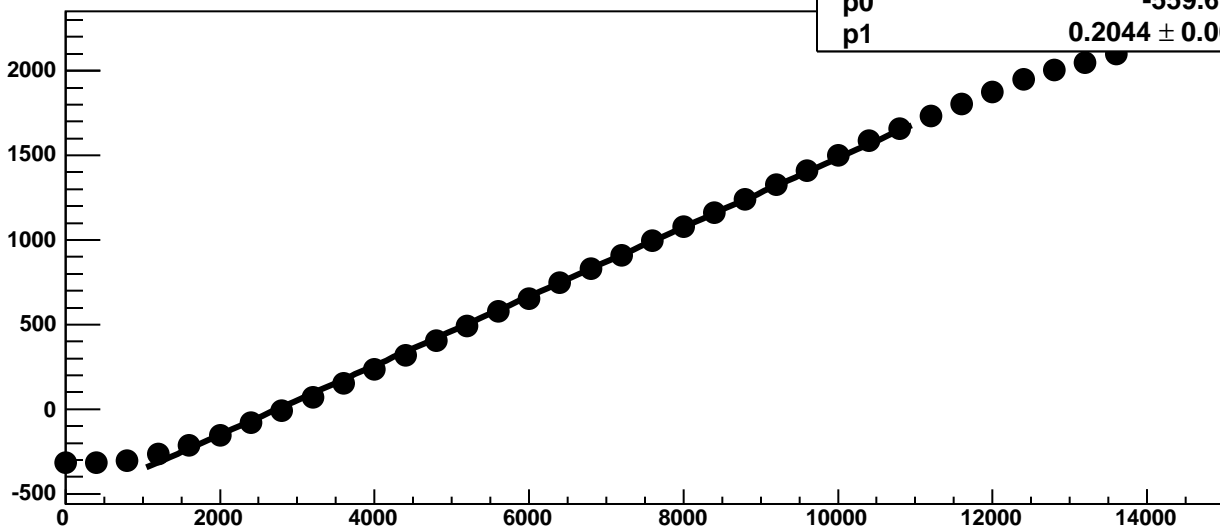
Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



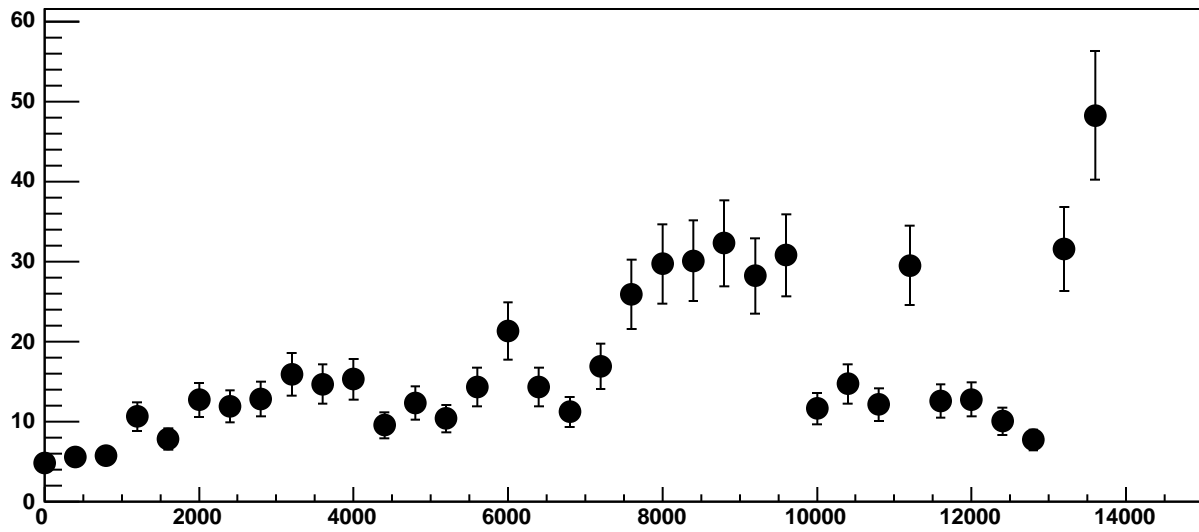
Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



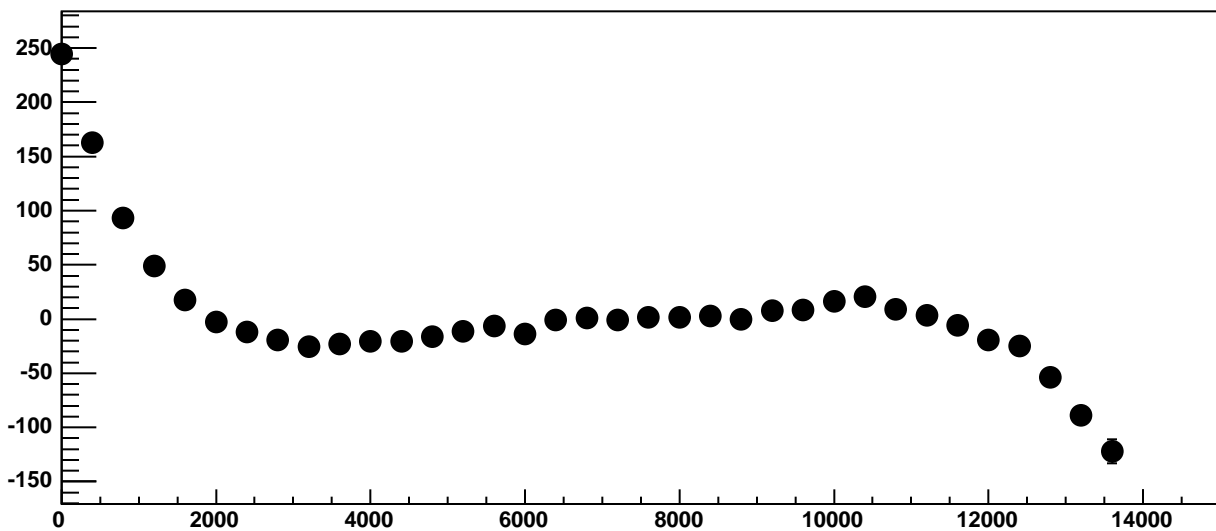
Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

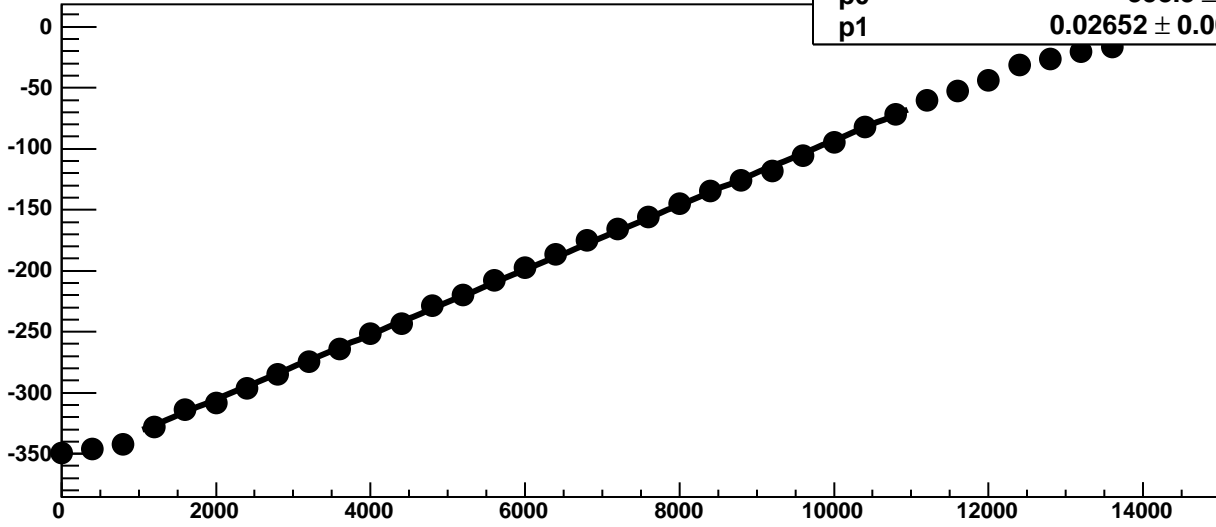


Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

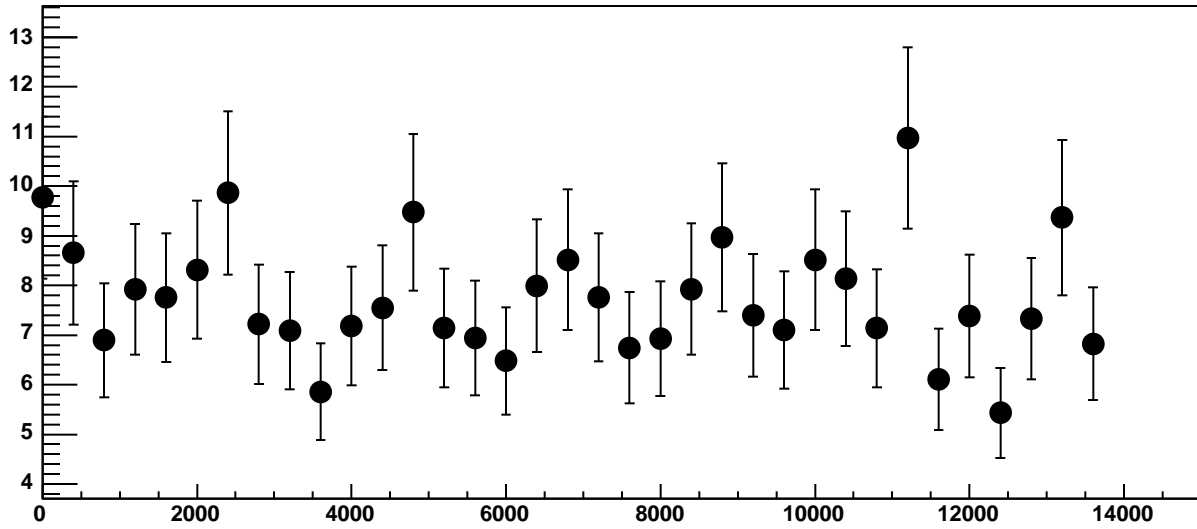




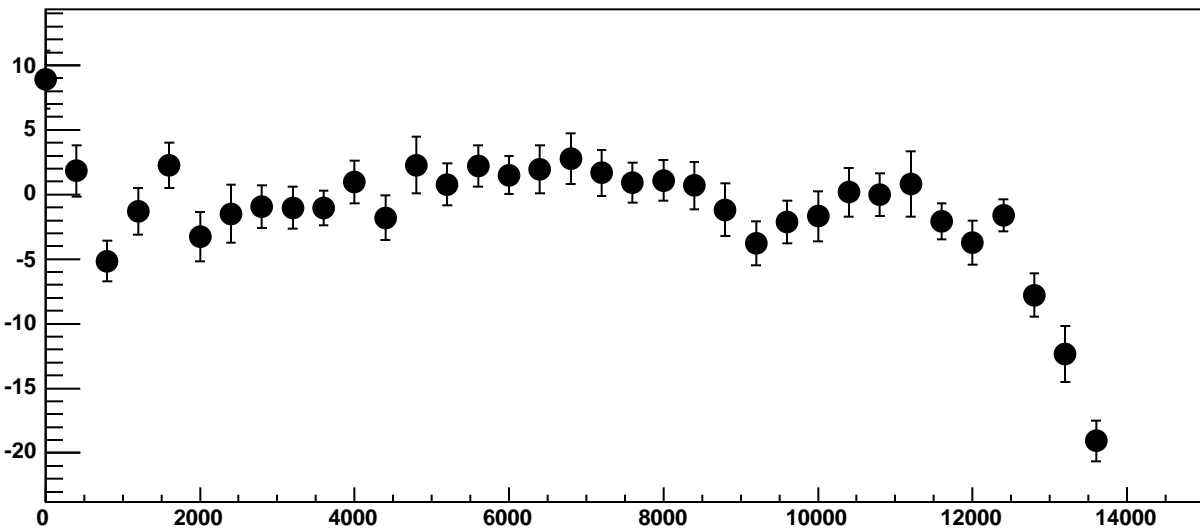
Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



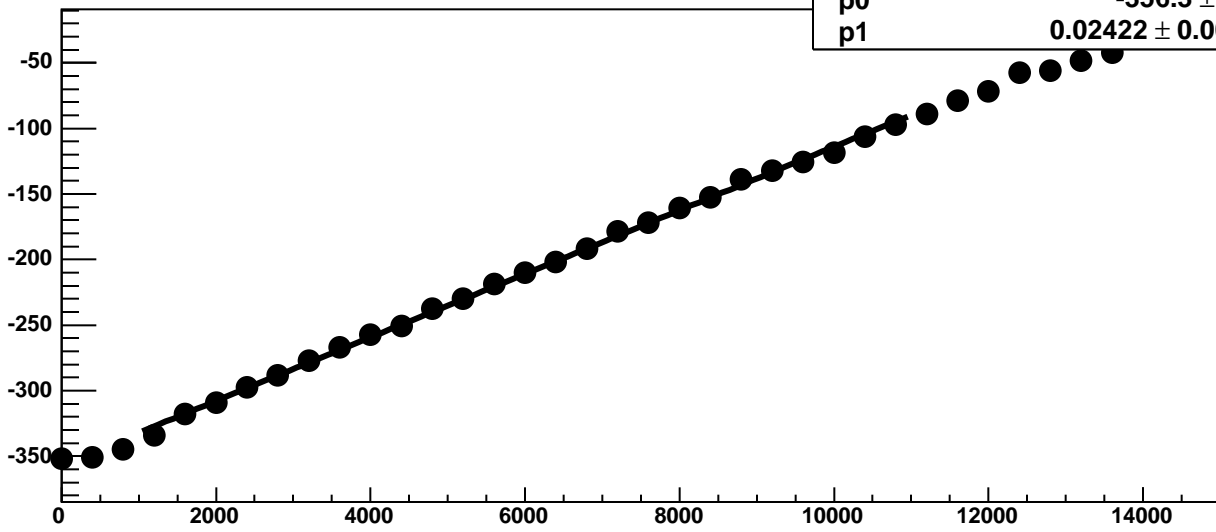
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



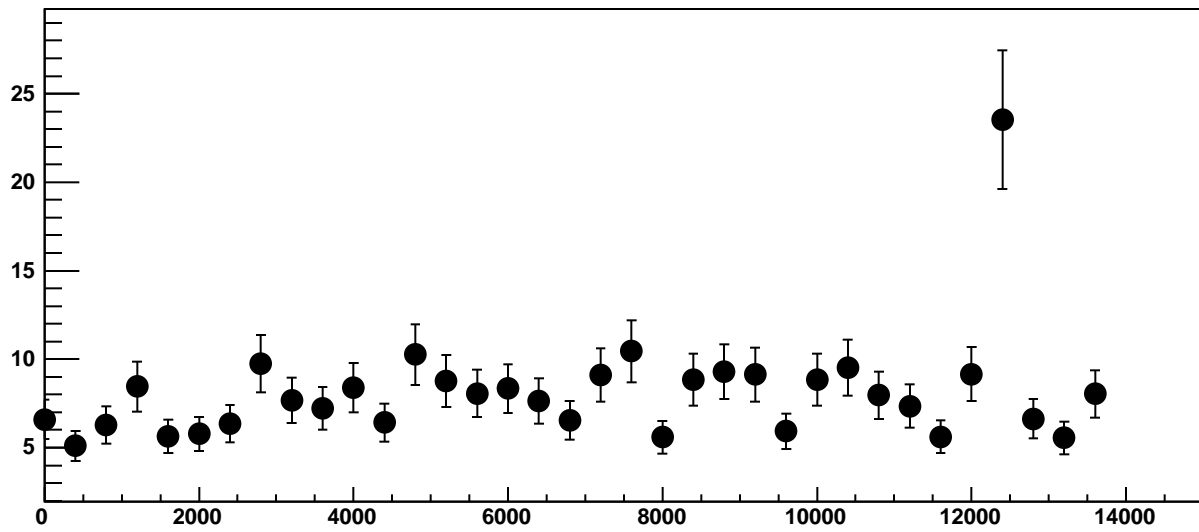
Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



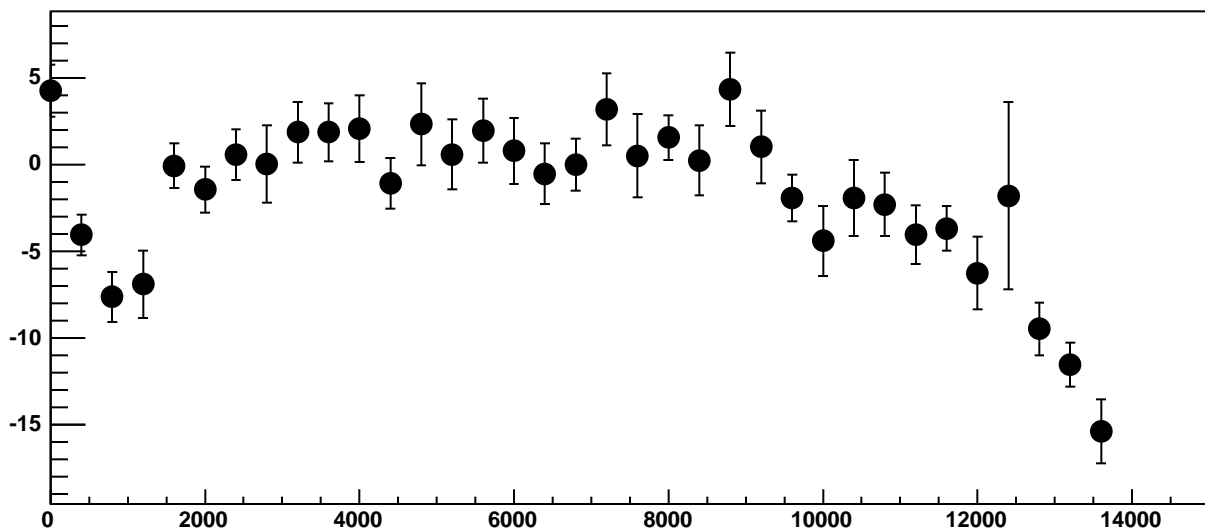
Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



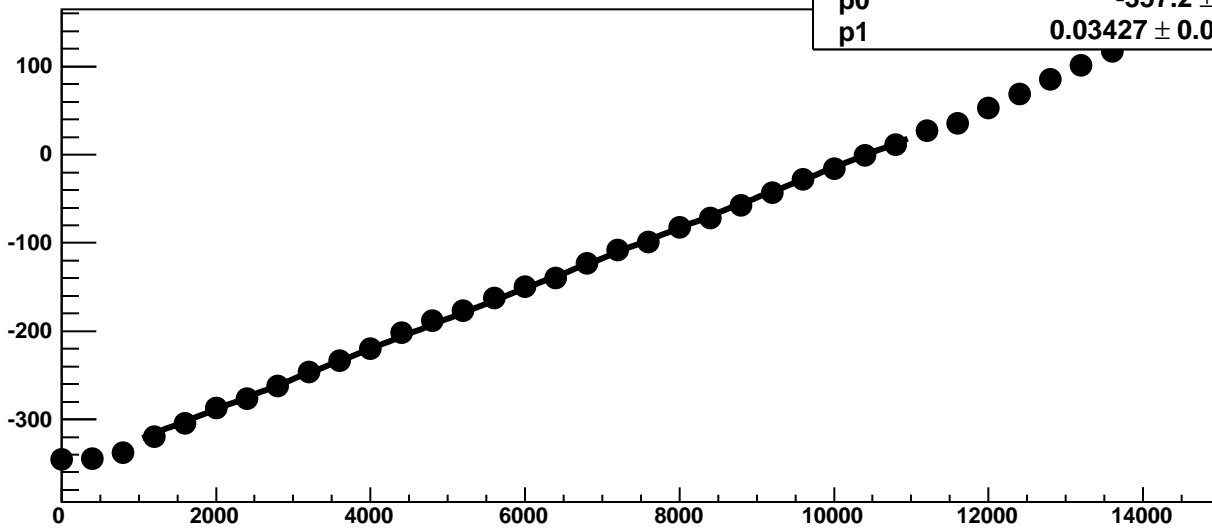
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

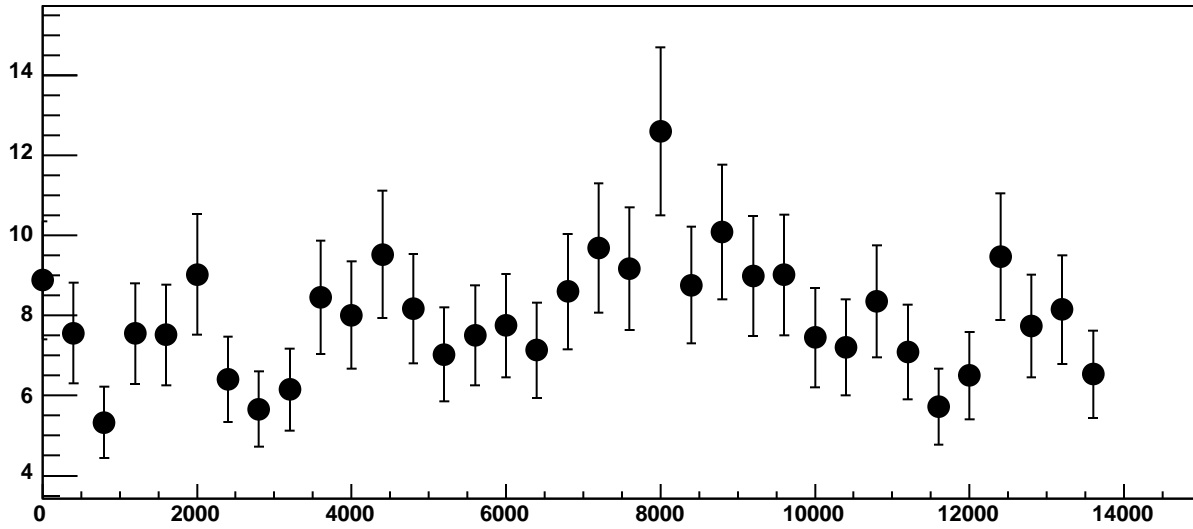


Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

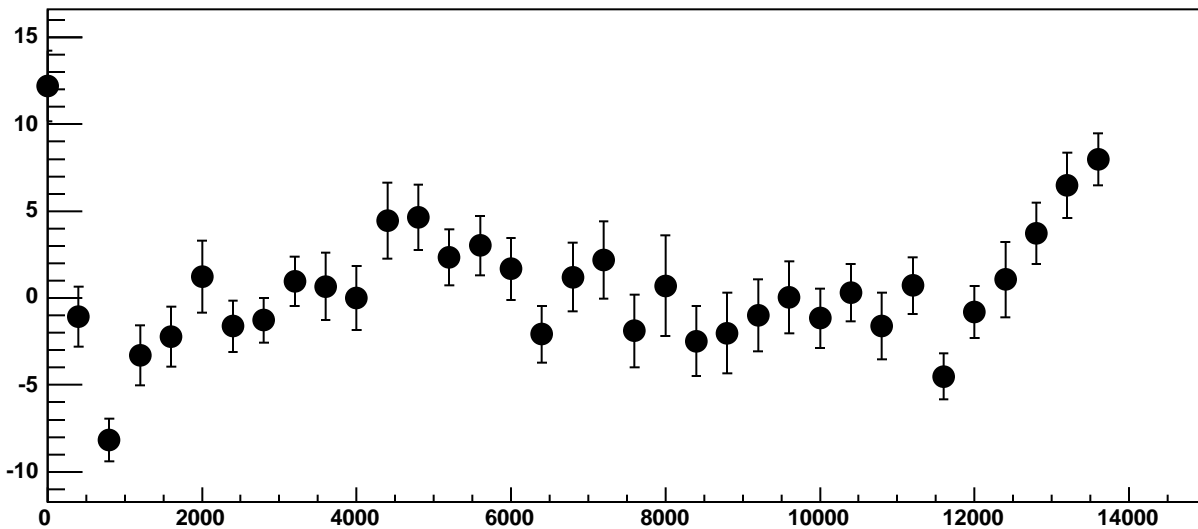


$\chi^2 / \text{ndf}$  32.34 / 23  
p0  $-357.2 \pm 0.7805$   
p1  $0.03427 \pm 0.0001237$

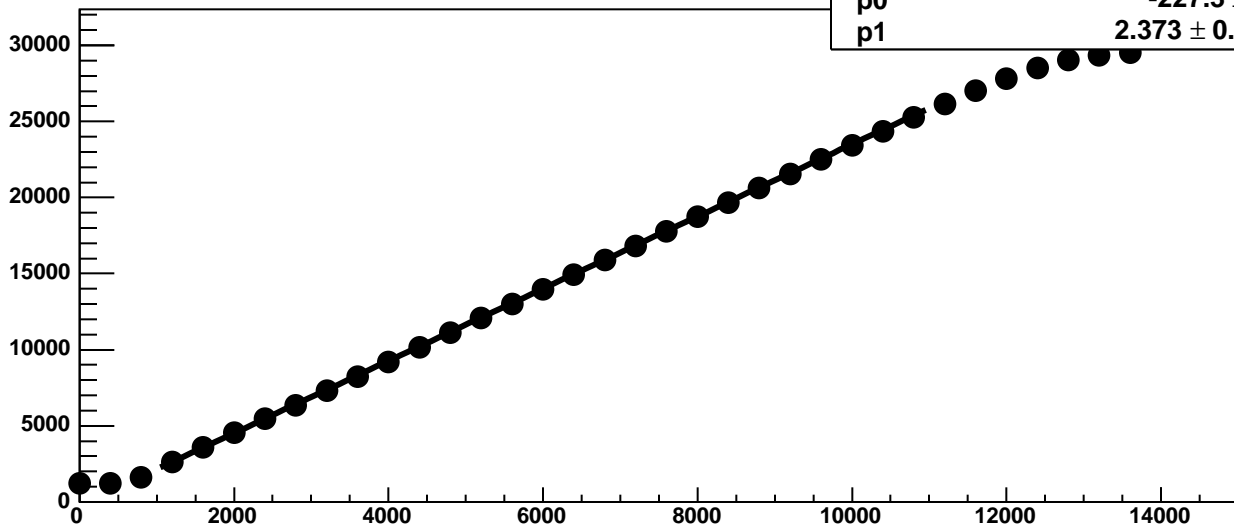
Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



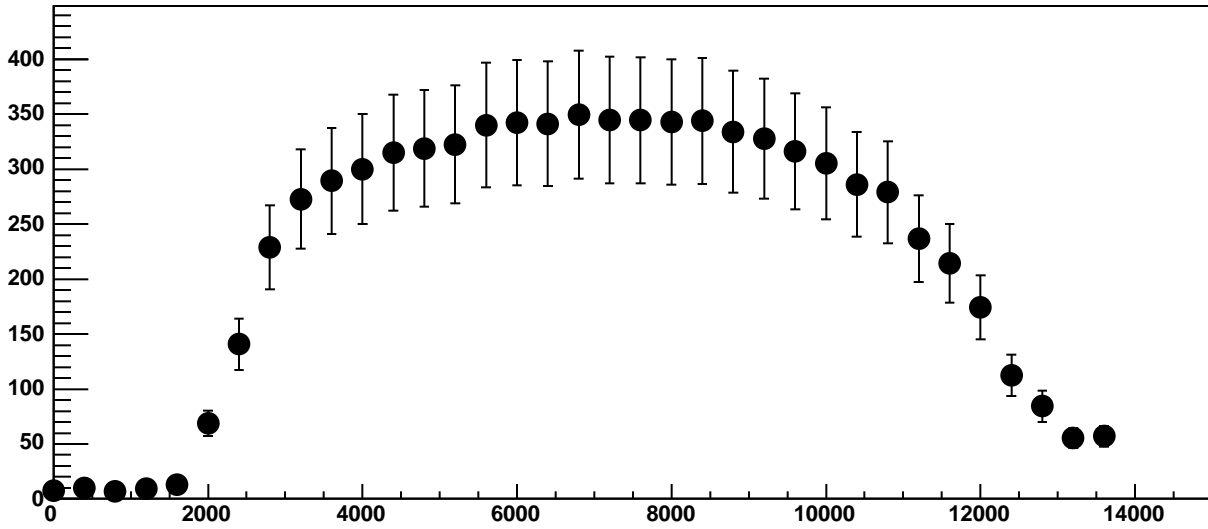
Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



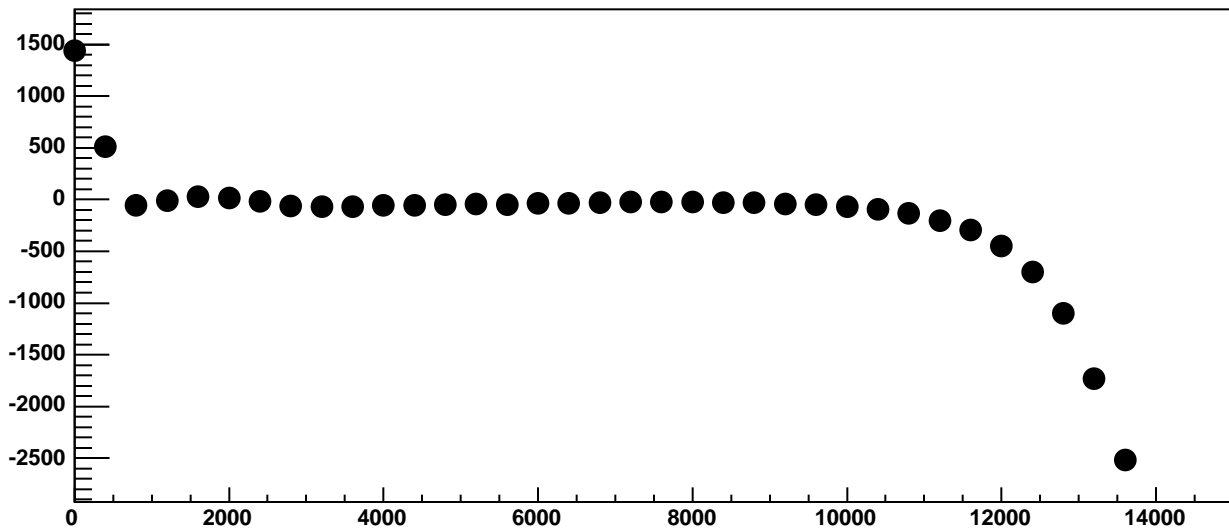
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



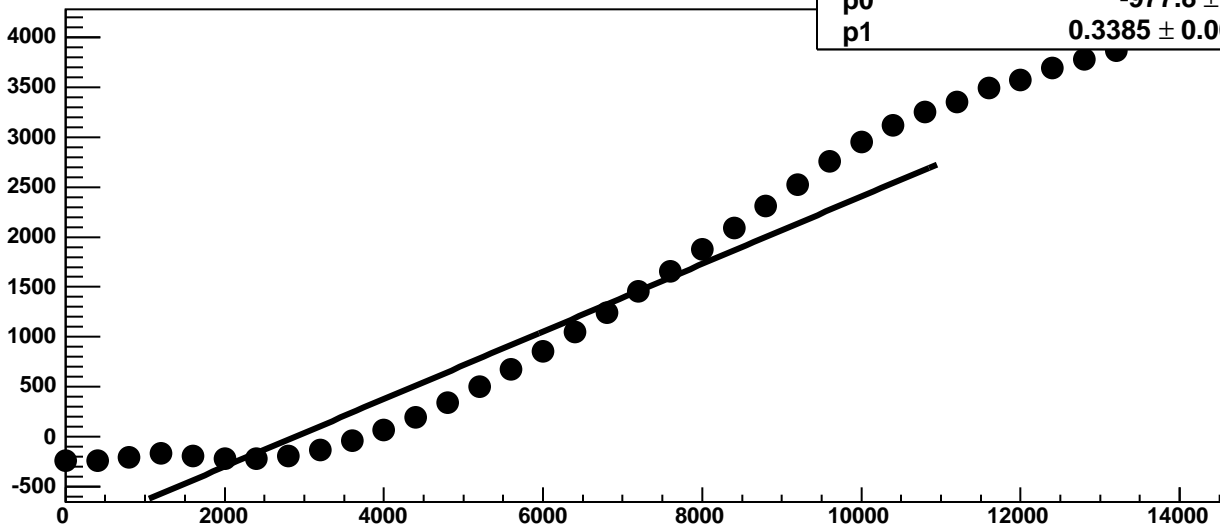
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



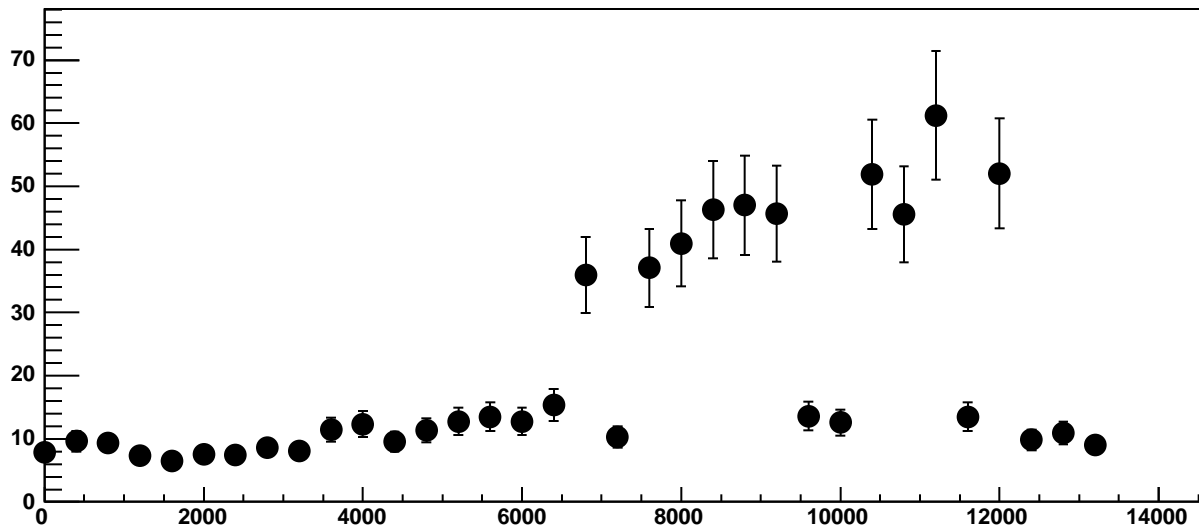
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



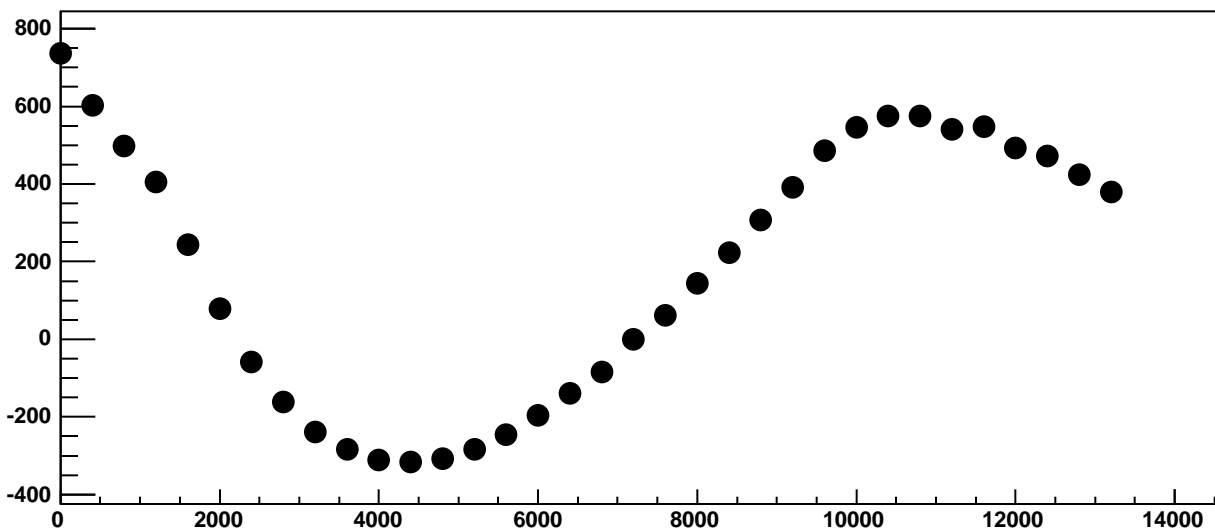
Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



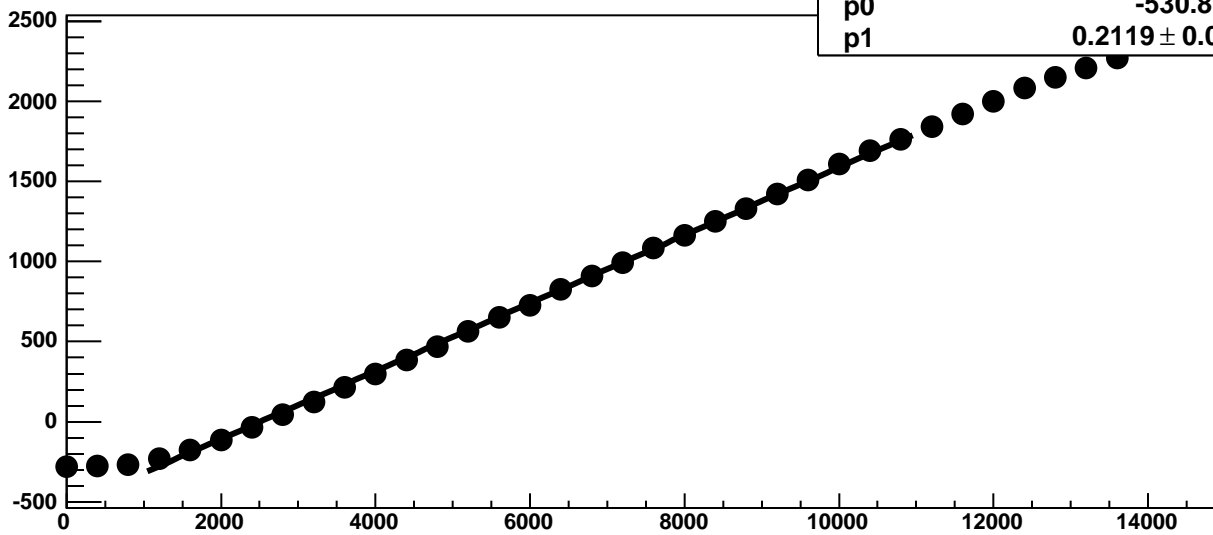
Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



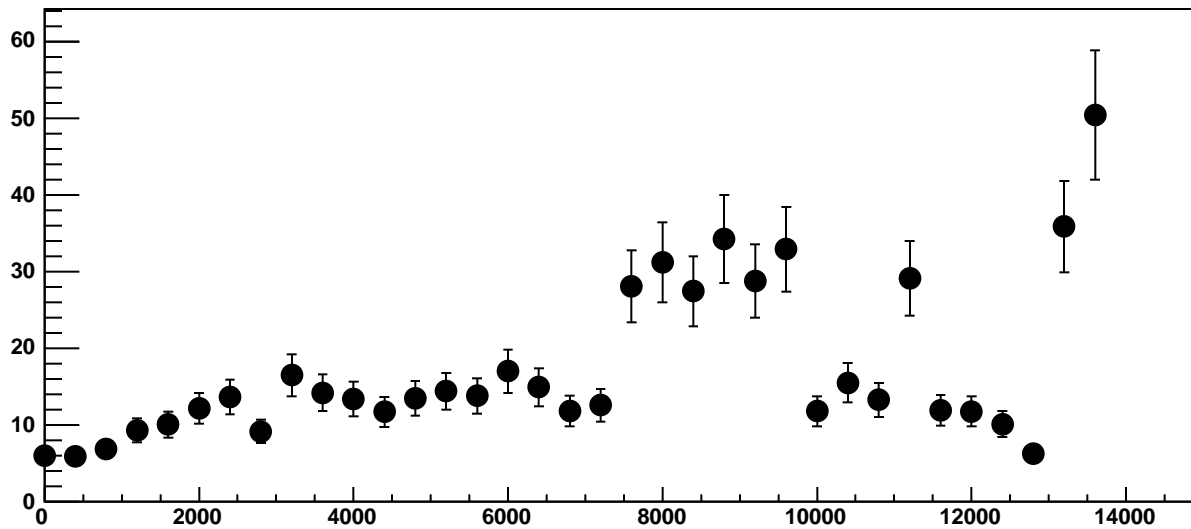
Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



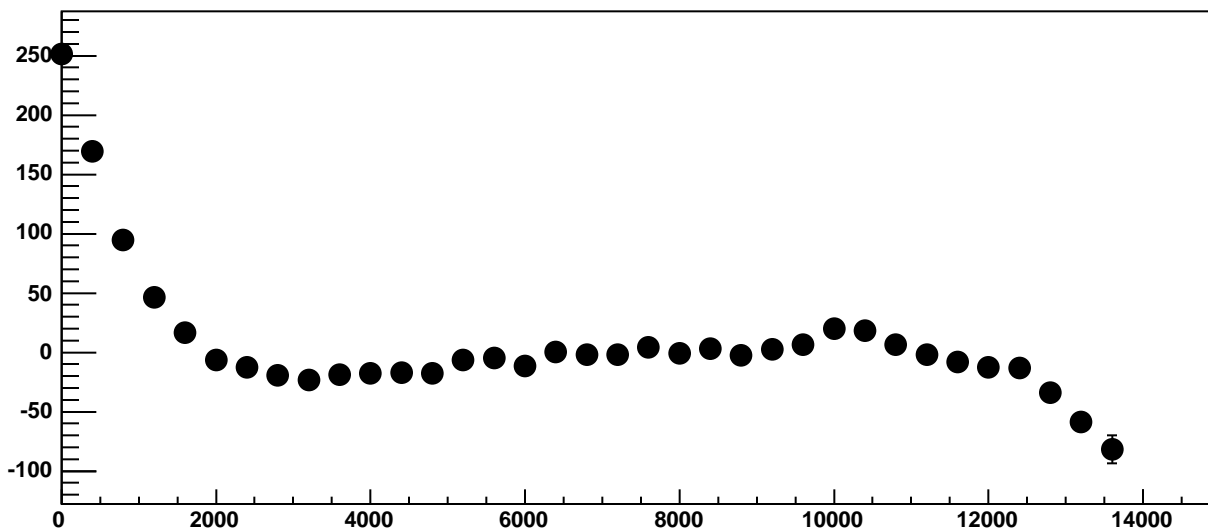
Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



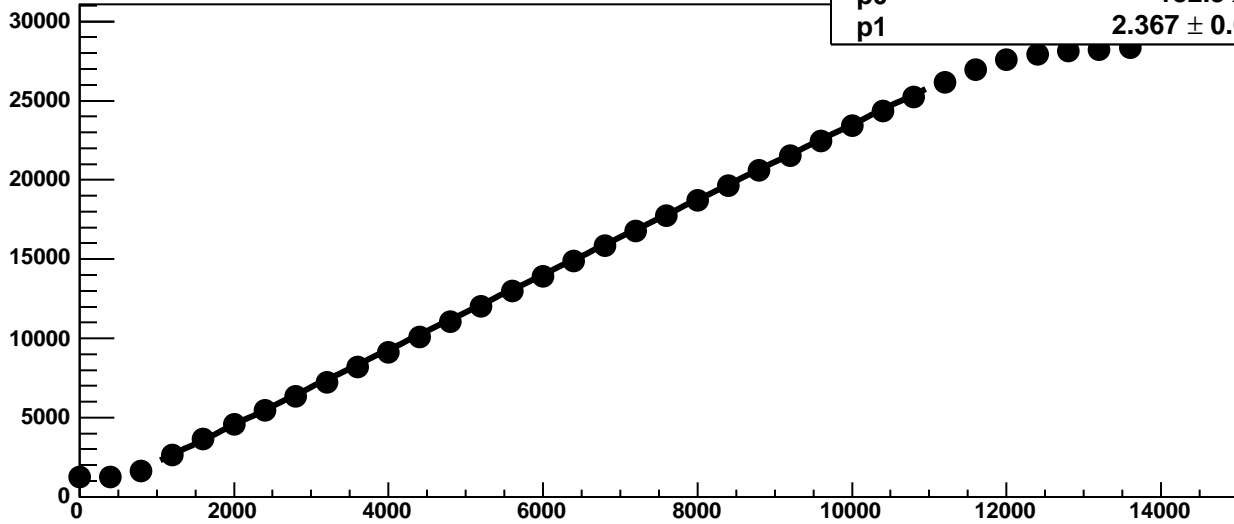
Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



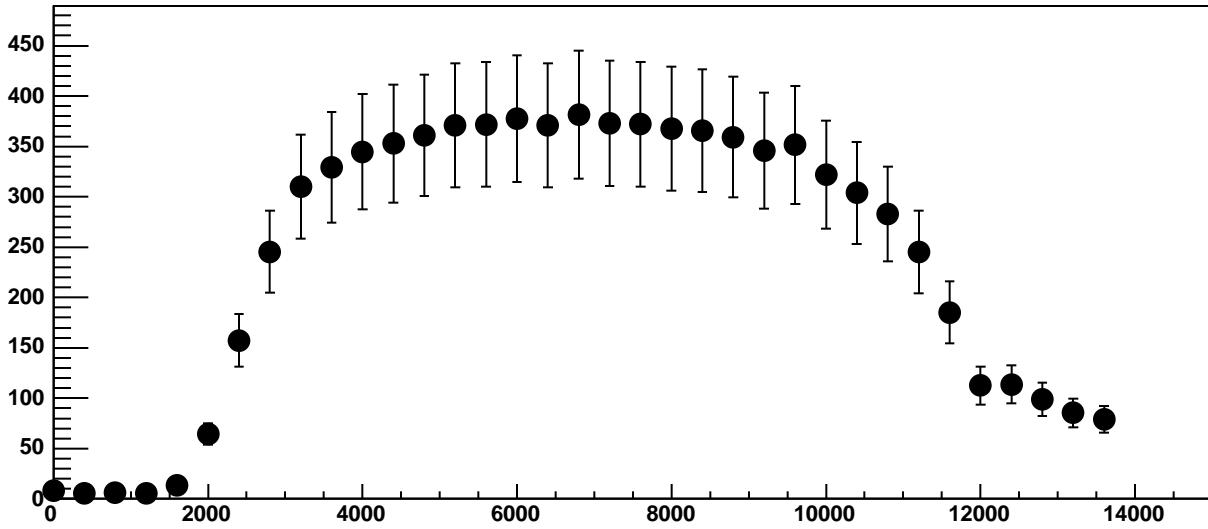
Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



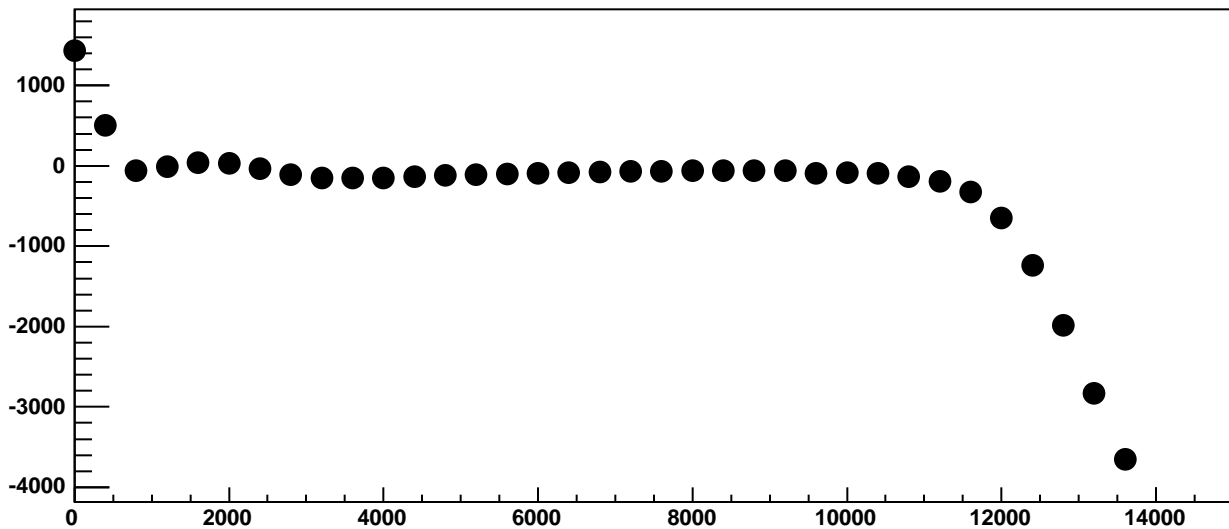
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC



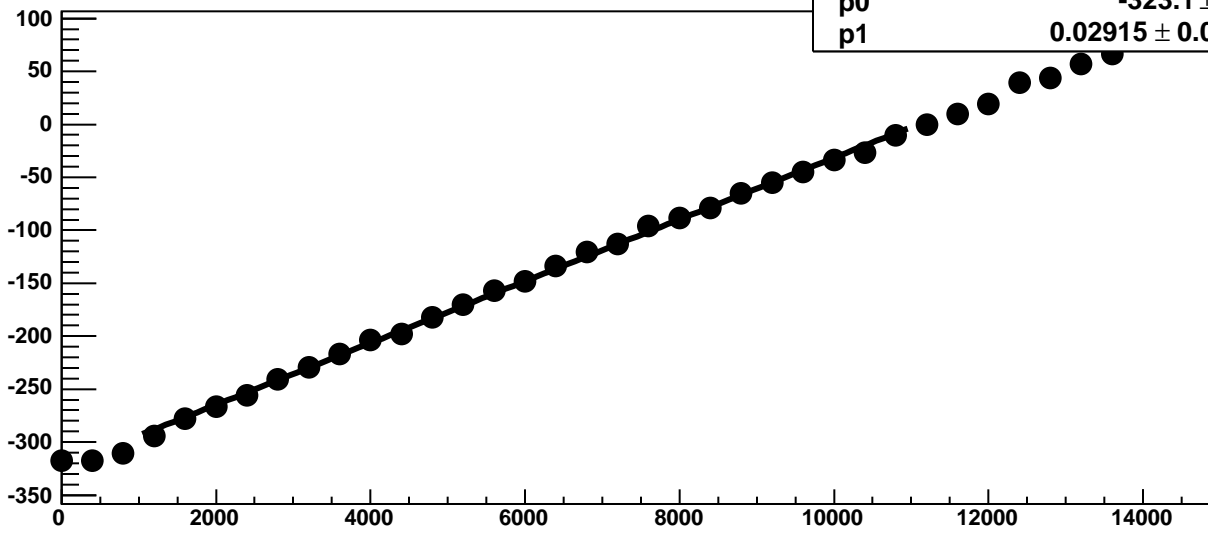
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



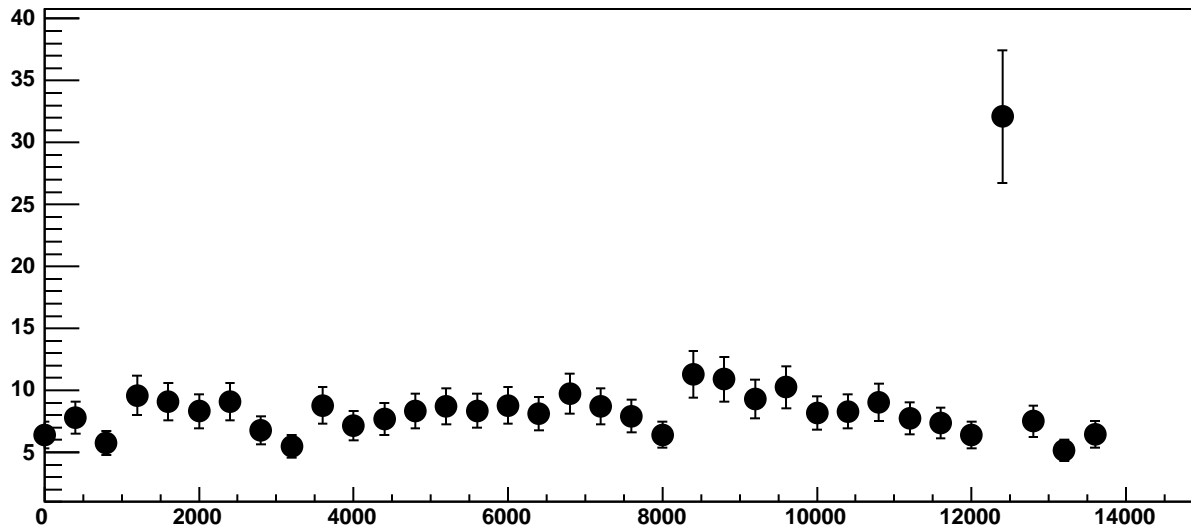
Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



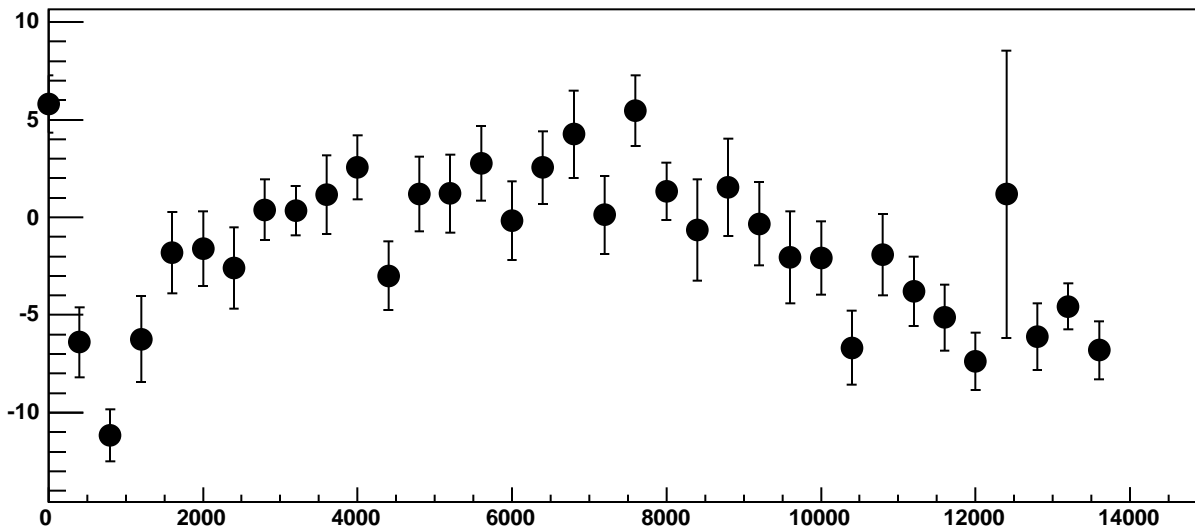
Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

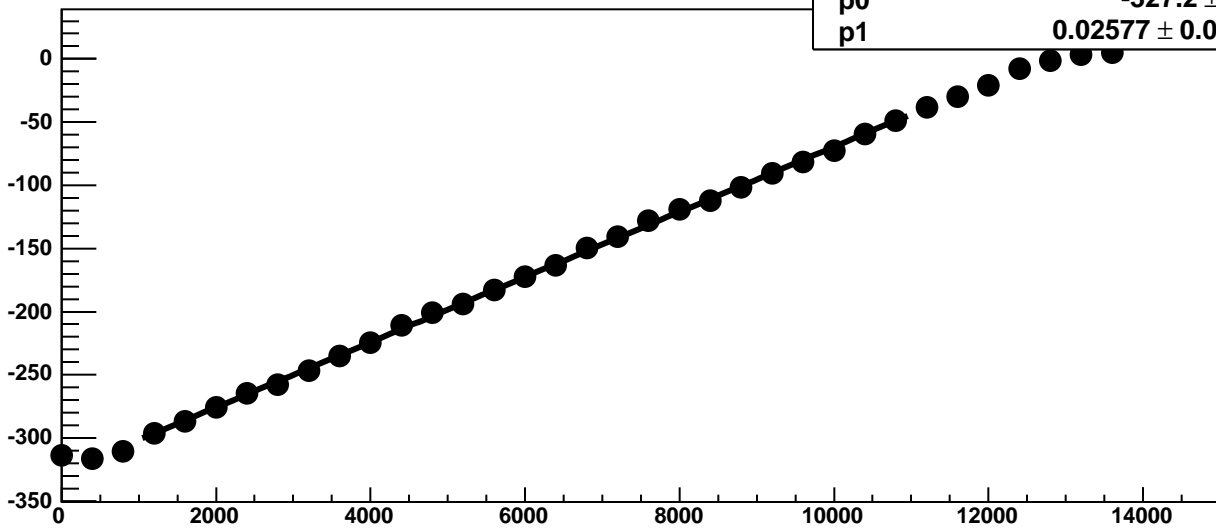


Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

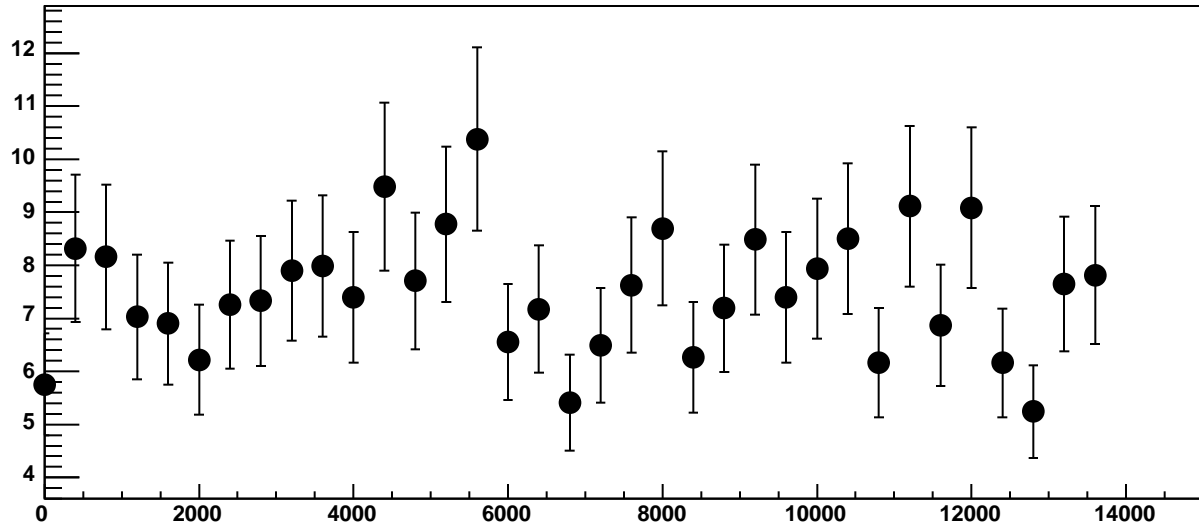




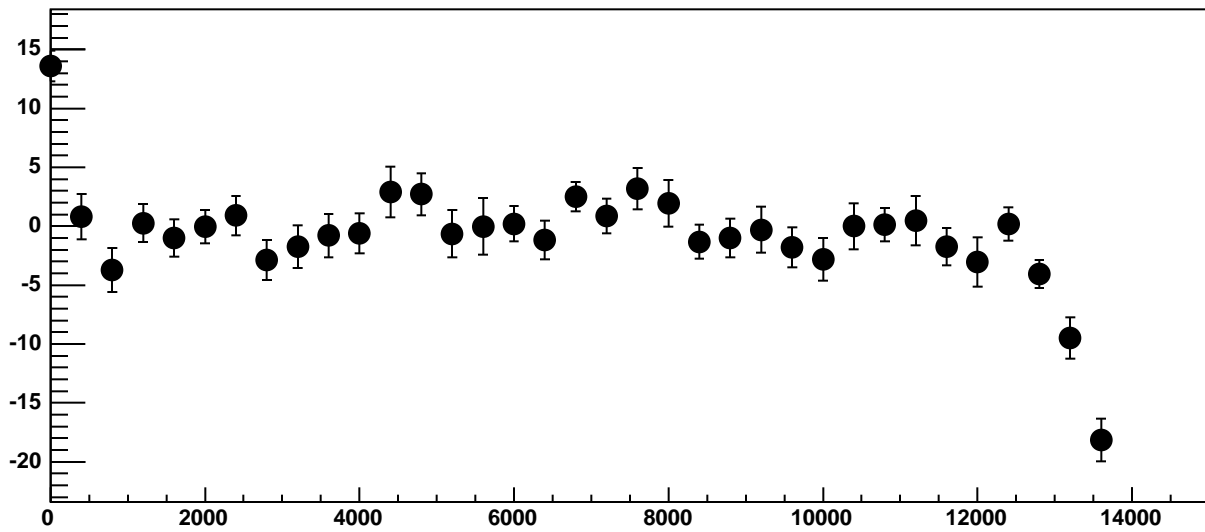
Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



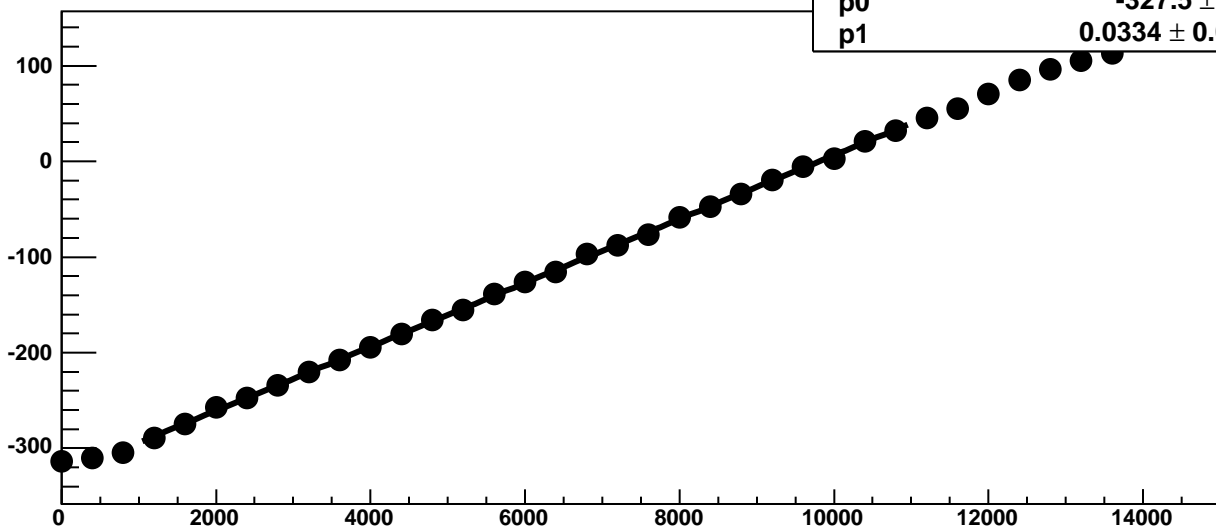
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

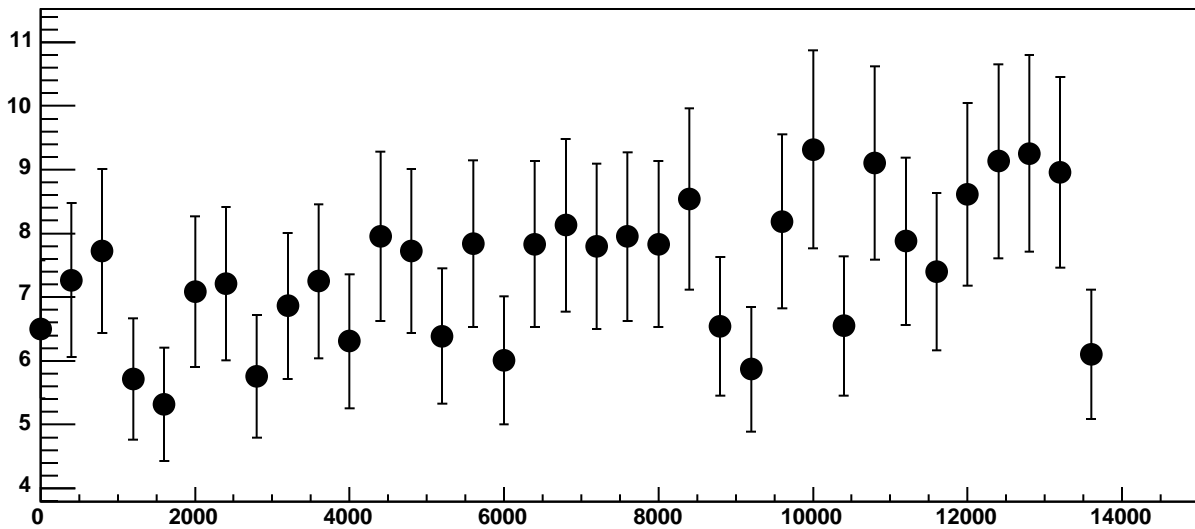


Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

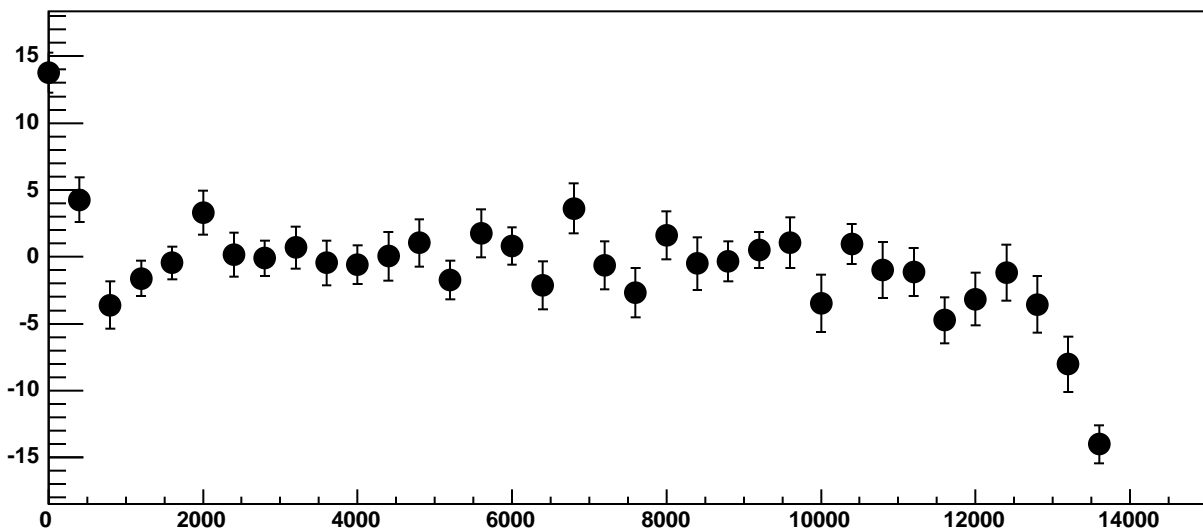


$\chi^2 / \text{ndf}$  21.33 / 23  
p0  $-327.5 \pm 0.6853$   
p1  $0.0334 \pm 0.000109$

Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

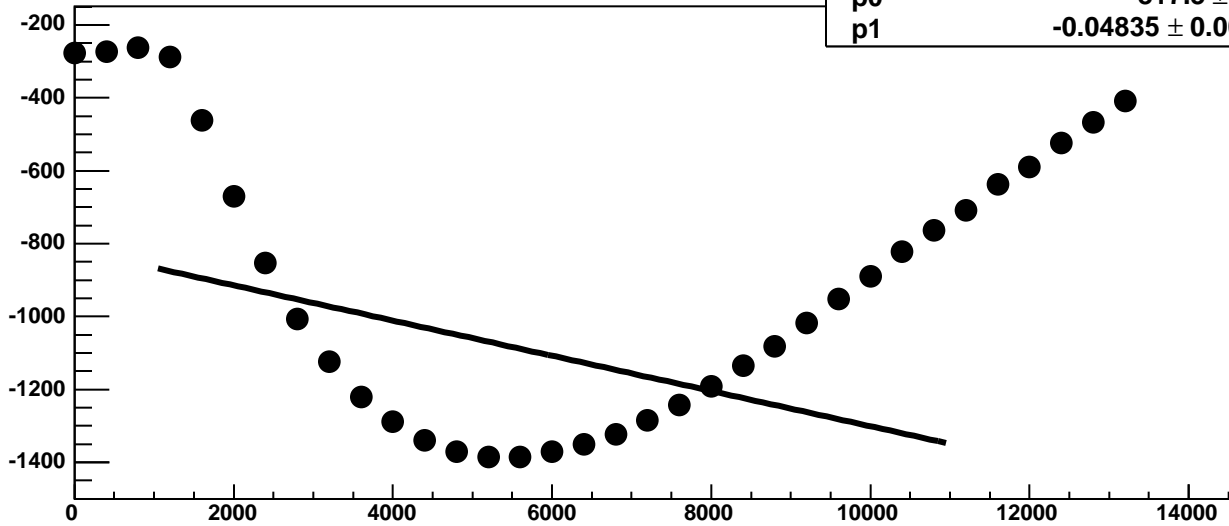


Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

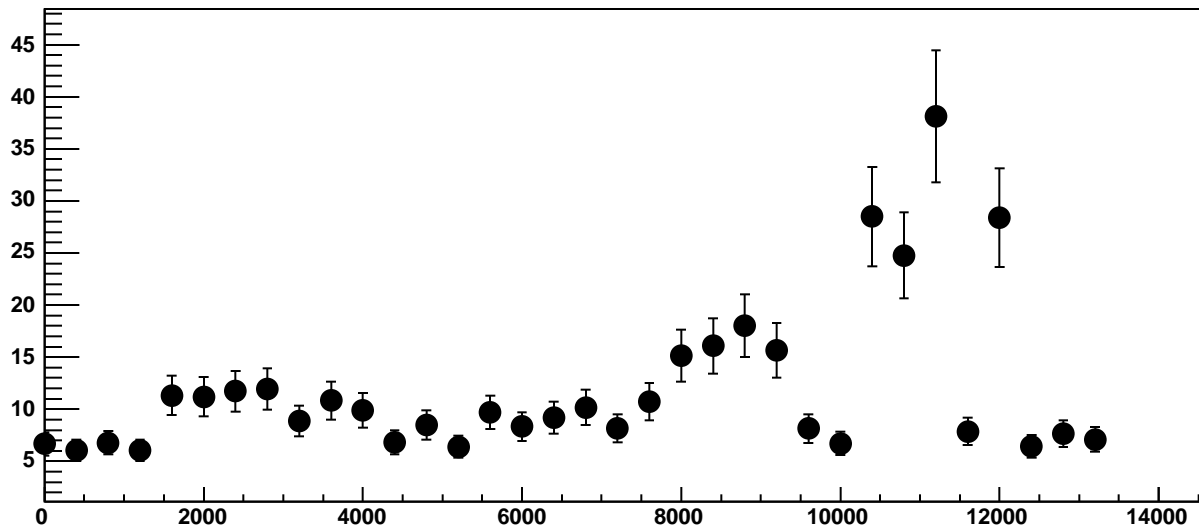


Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

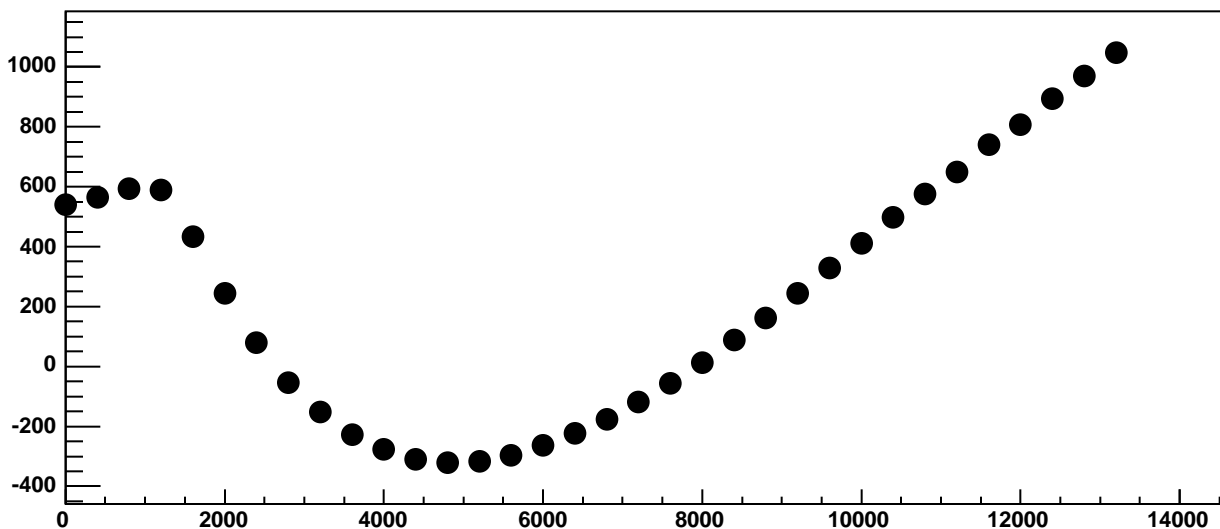
$\chi^2 / \text{ndf}$  5.447e+05 / 23  
p0 -817.3 ± 0.9756  
p1 -0.04835 ± 0.0001607



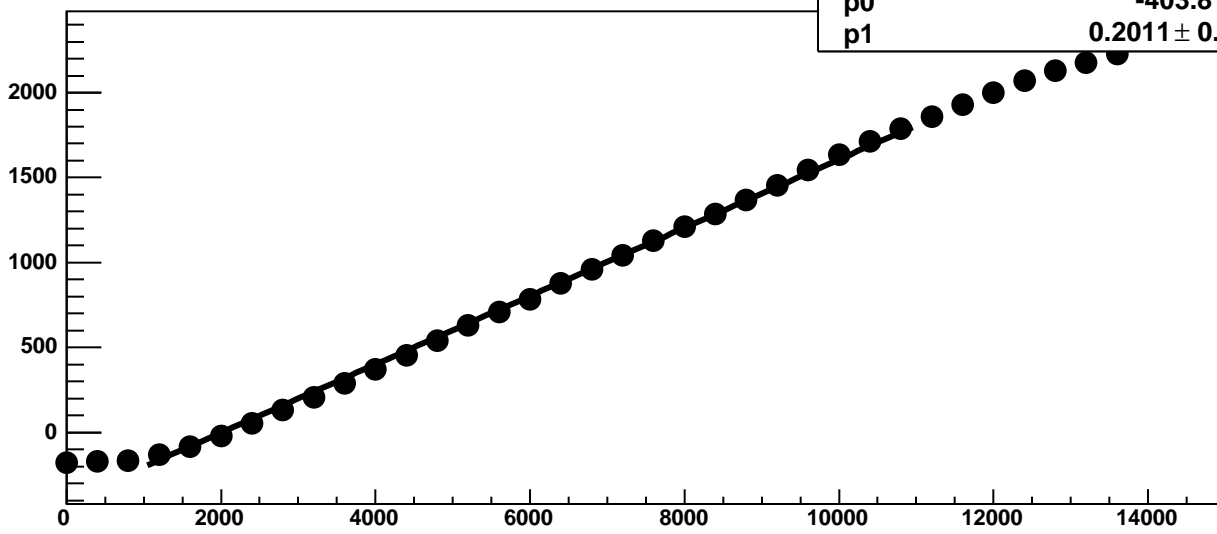
Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



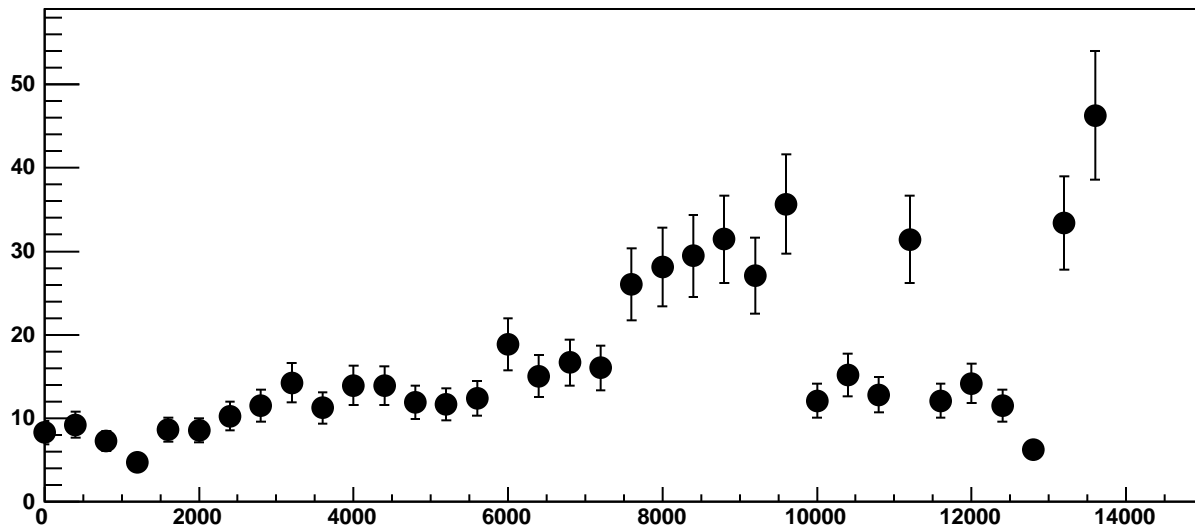
Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



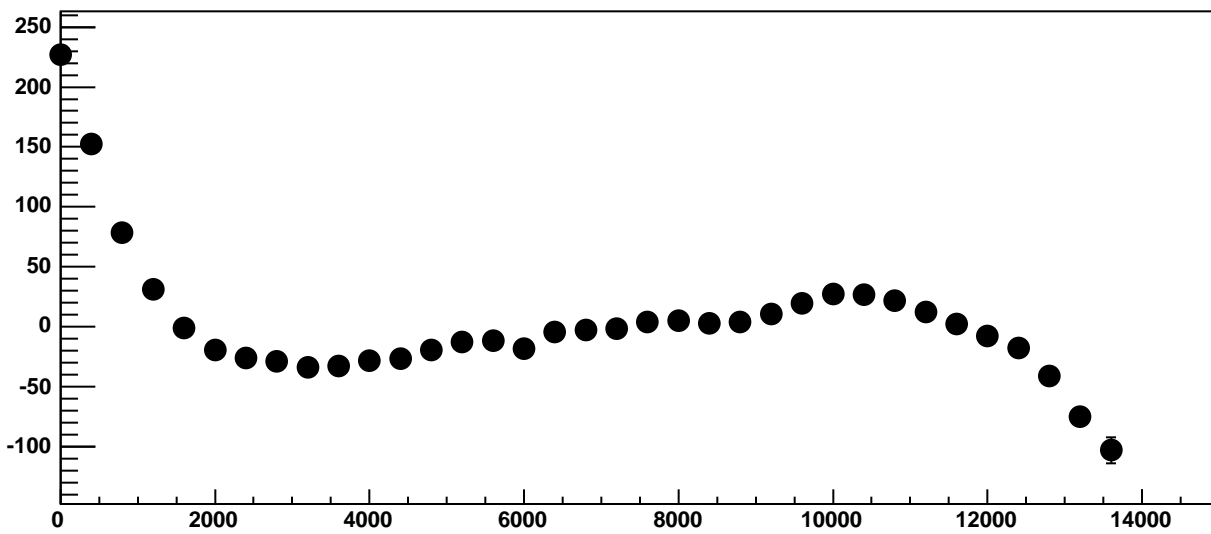
Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



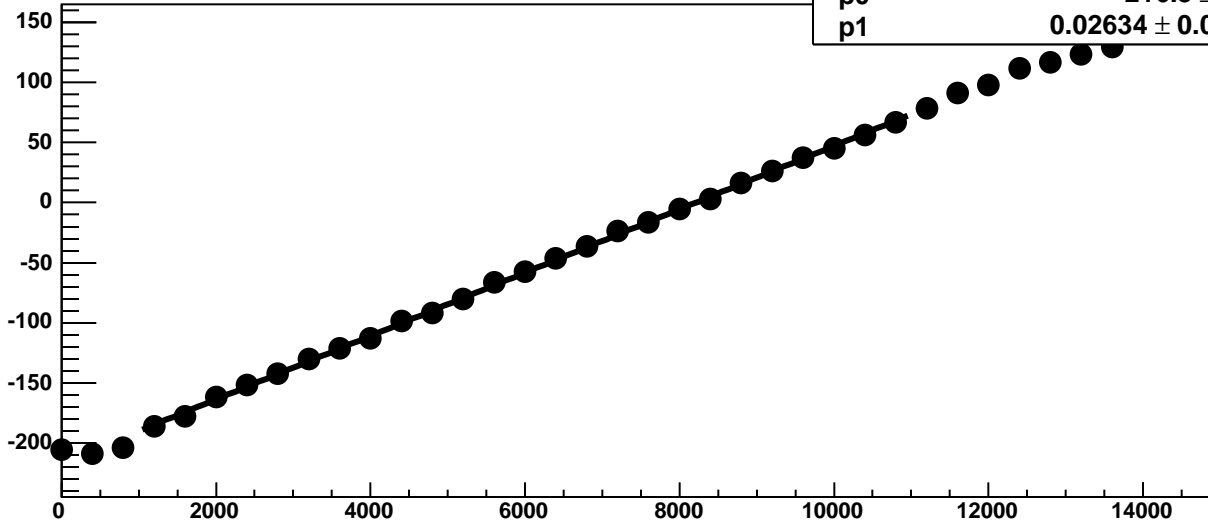
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

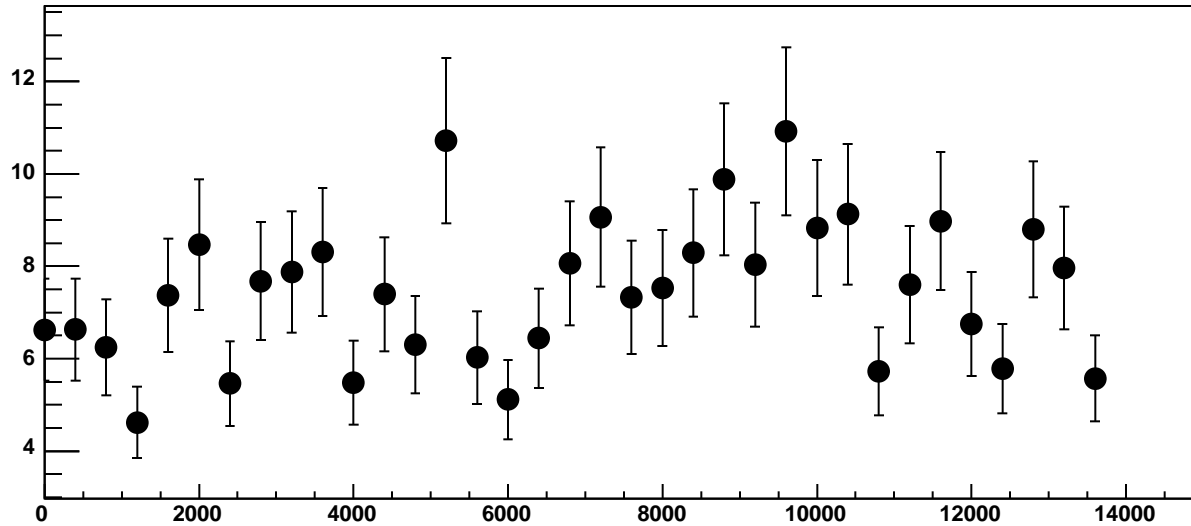


Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

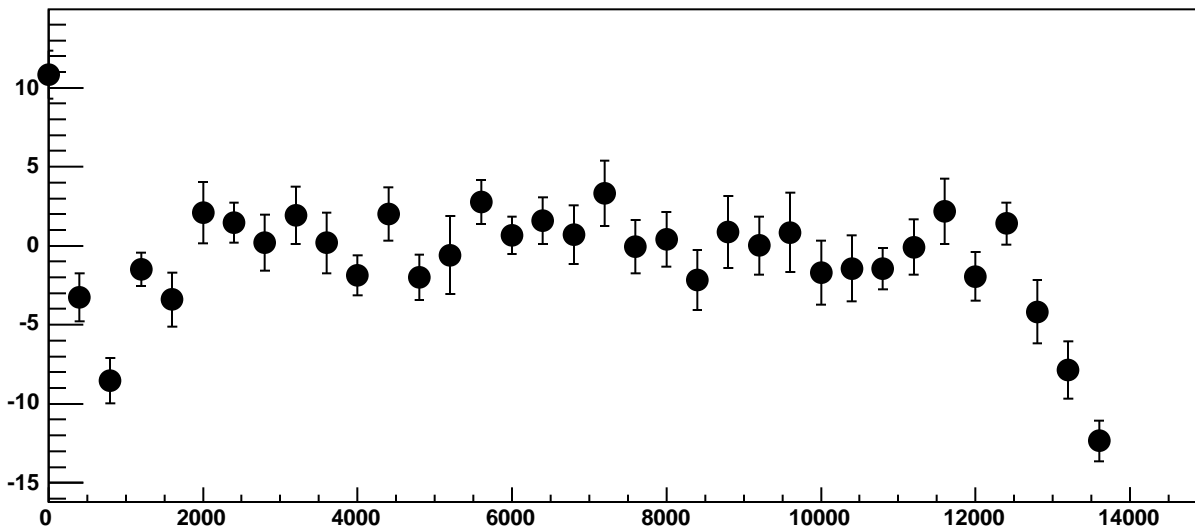


$\chi^2 / \text{ndf}$  27.58 / 23  
p0  $-216.3 \pm 0.6875$   
p1  $0.02634 \pm 0.0001108$

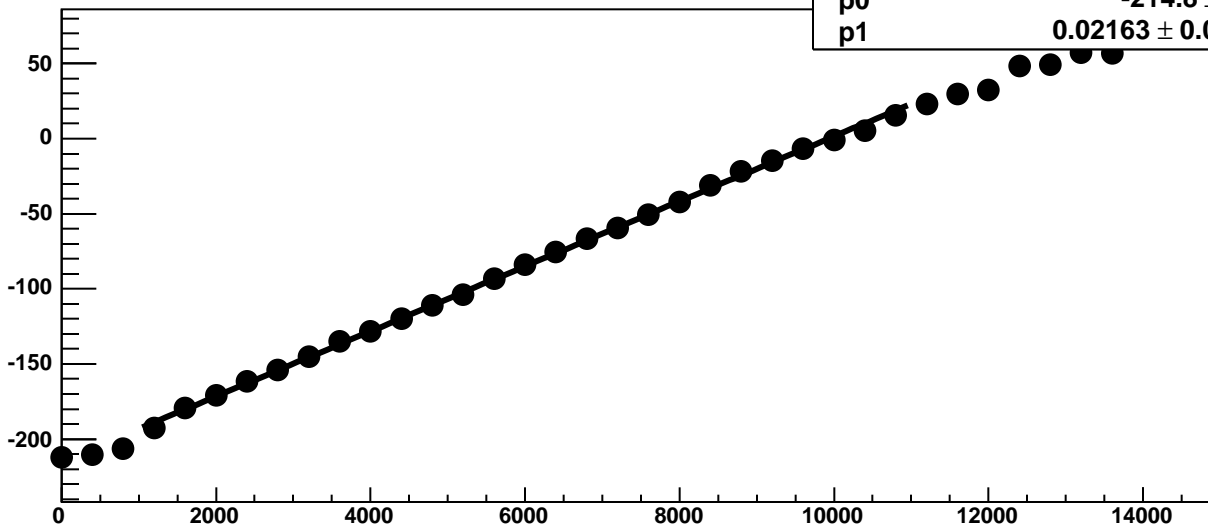
Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



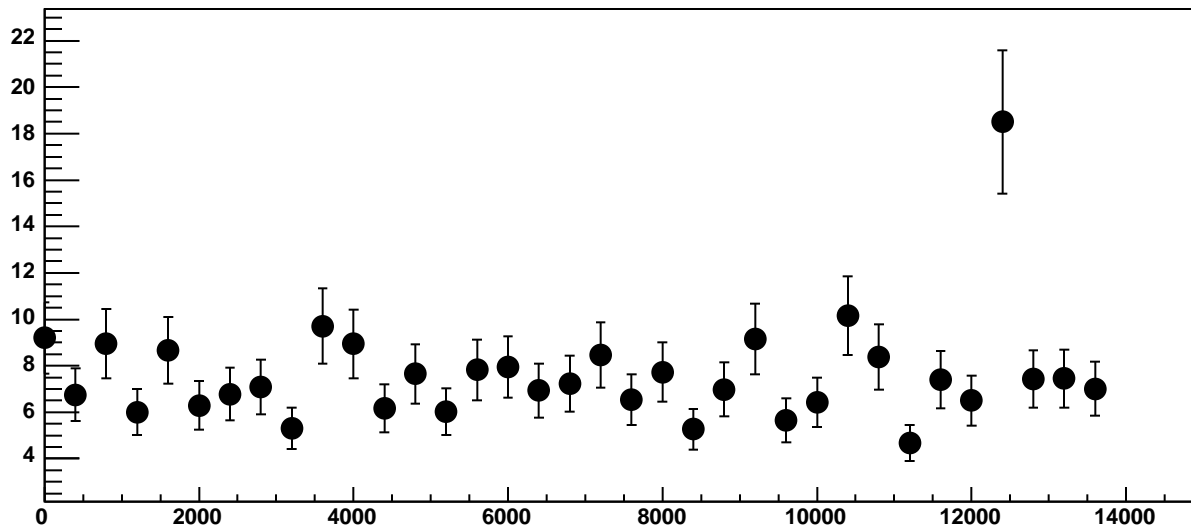
Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



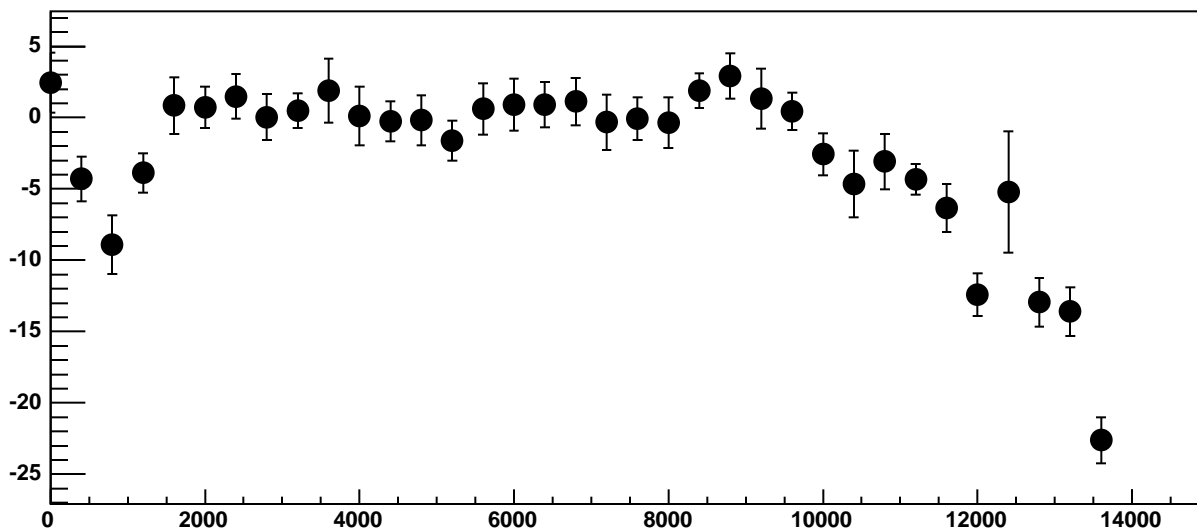
Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



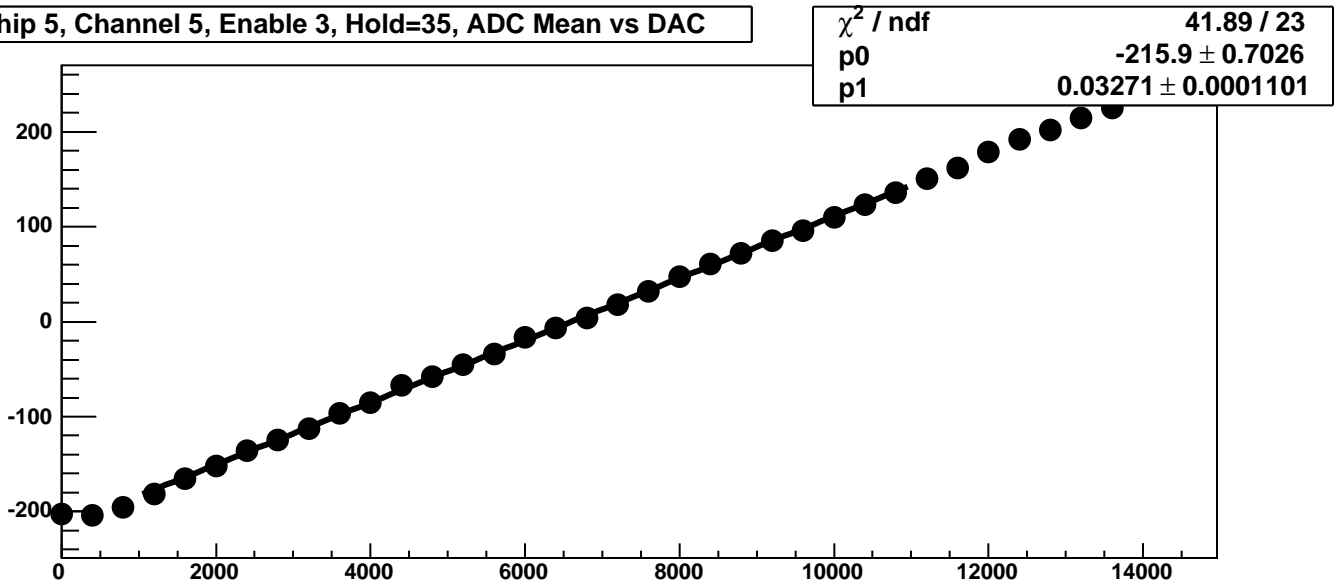
Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



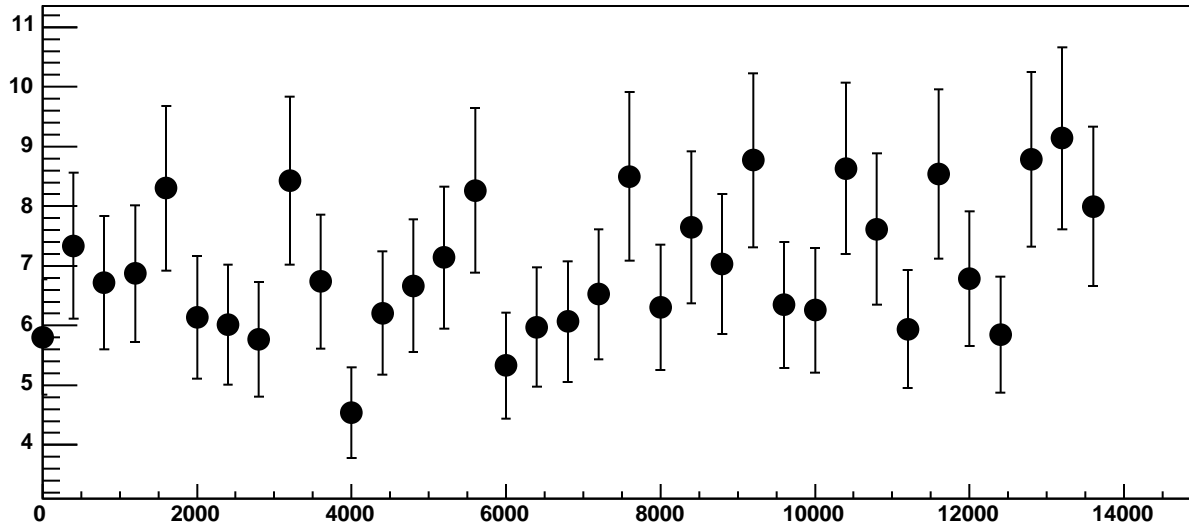
Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



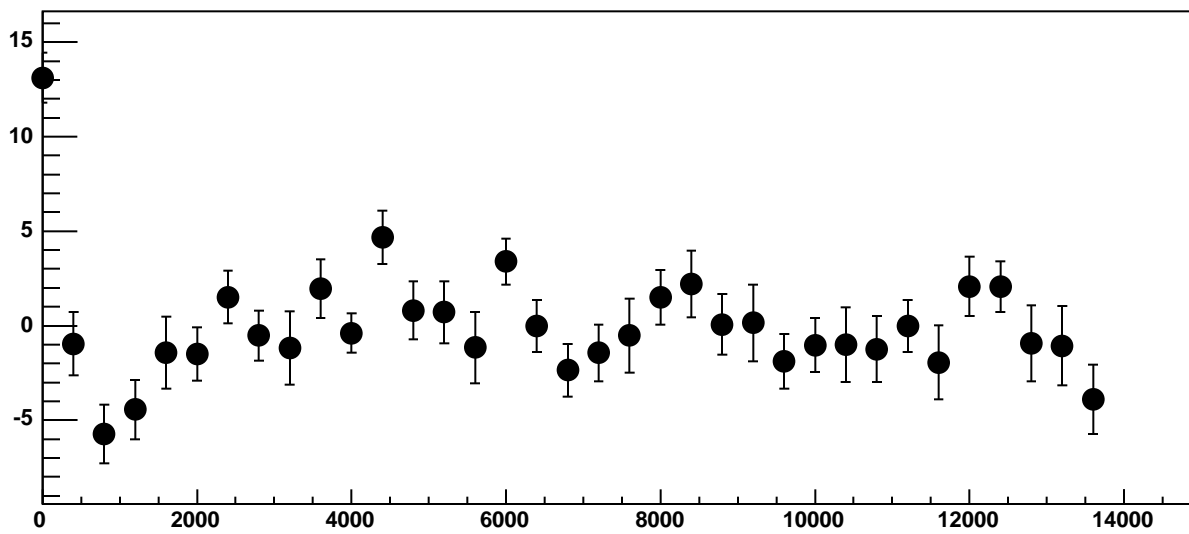
Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



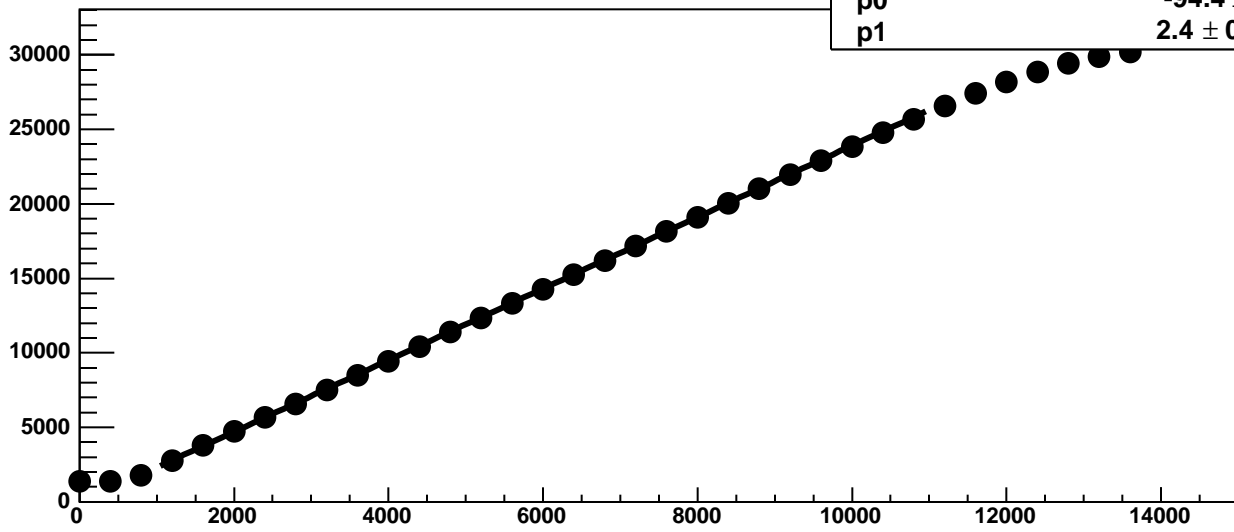
Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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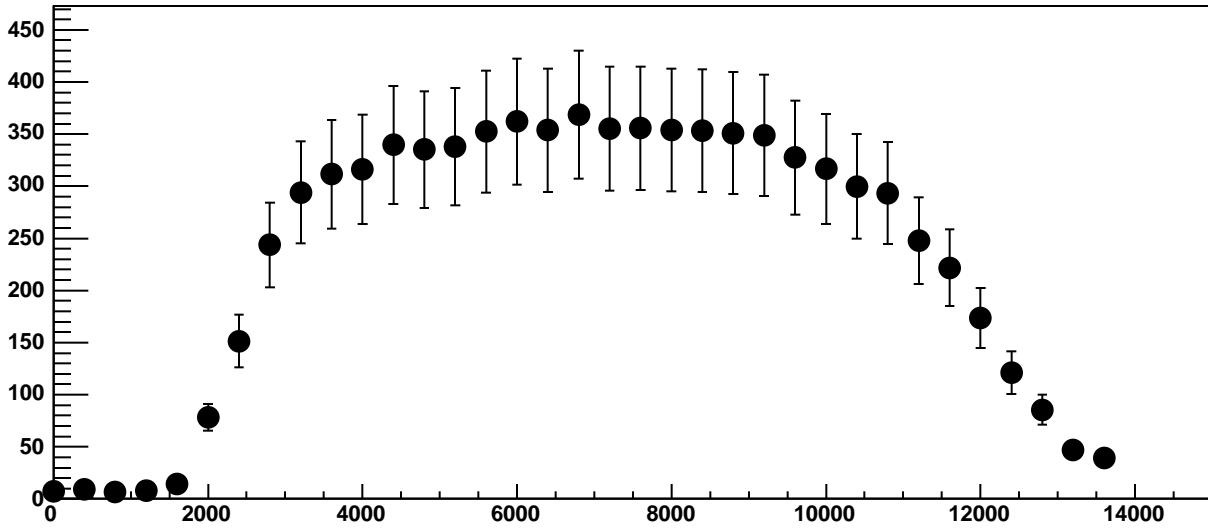
p0

$-94.4 \pm 3.927$

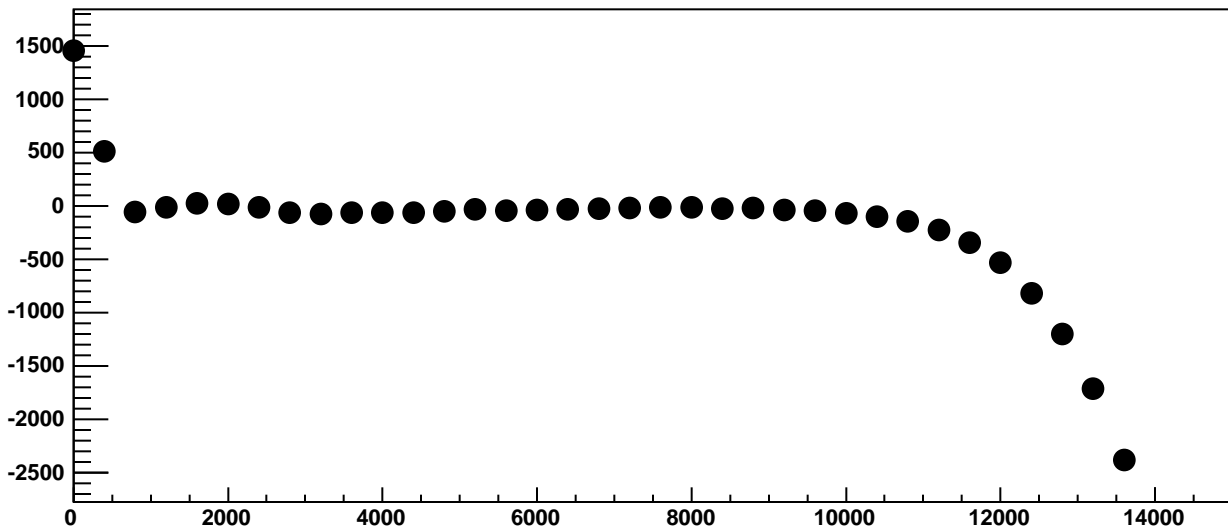
p1

$2.4 \pm 0.00263$

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC



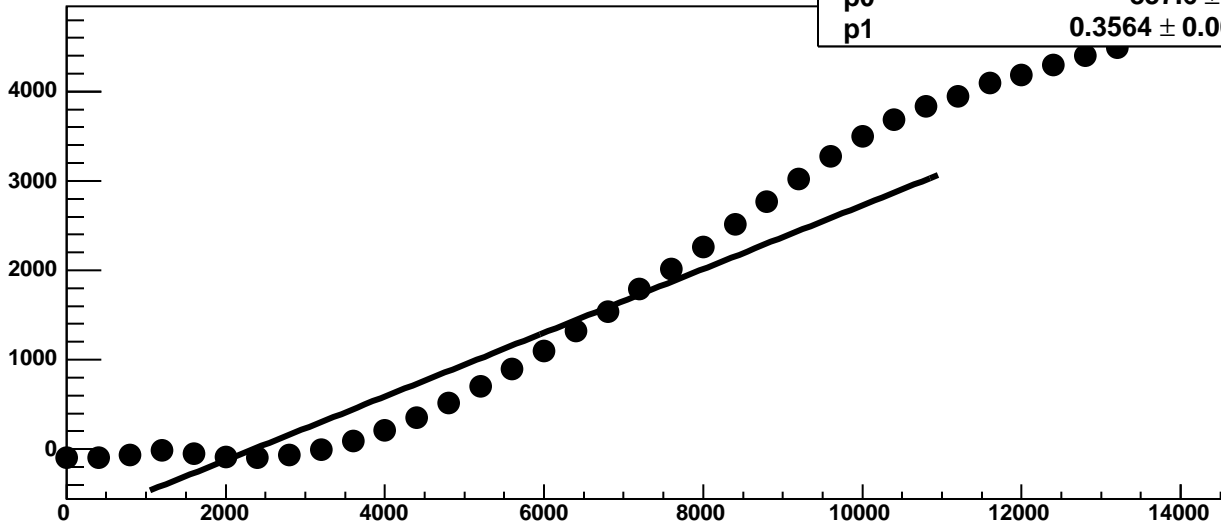
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



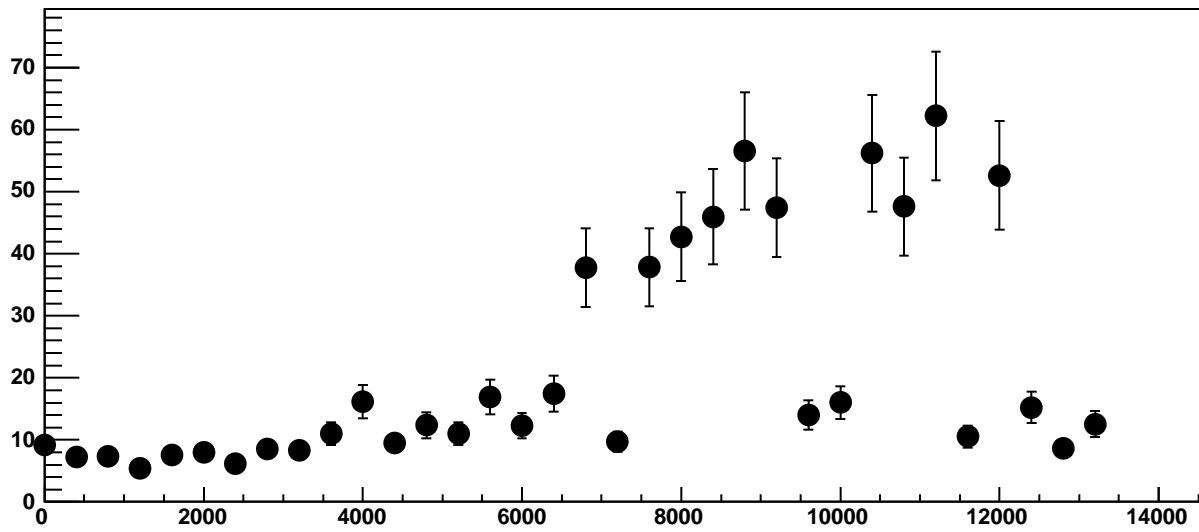


Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

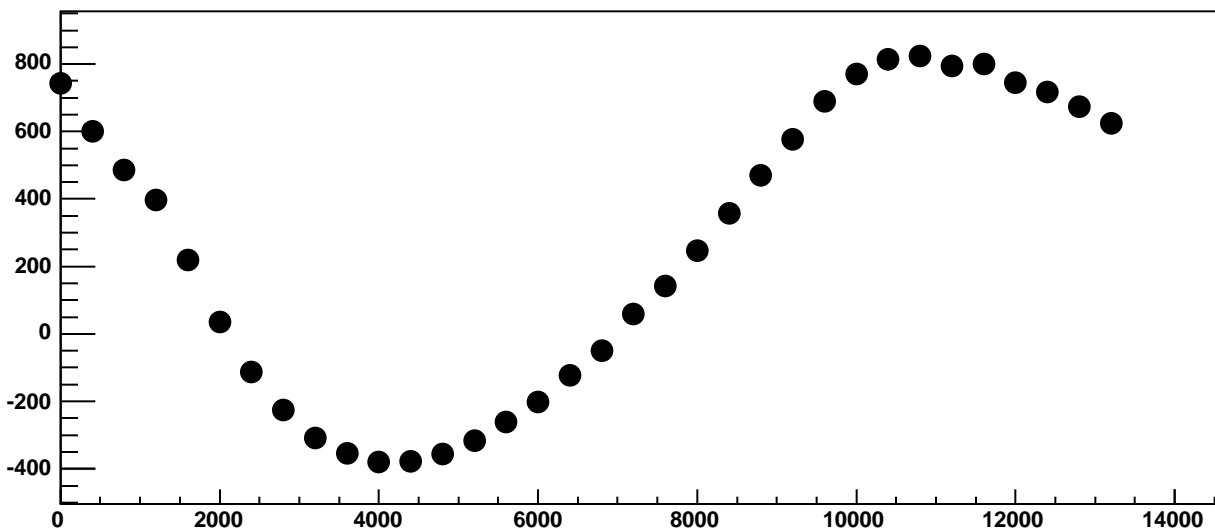
$\chi^2 / \text{ndf}$  3.74e+05 / 23  
p0 -837.6 ± 0.9426  
p1 0.3564 ± 0.0002235



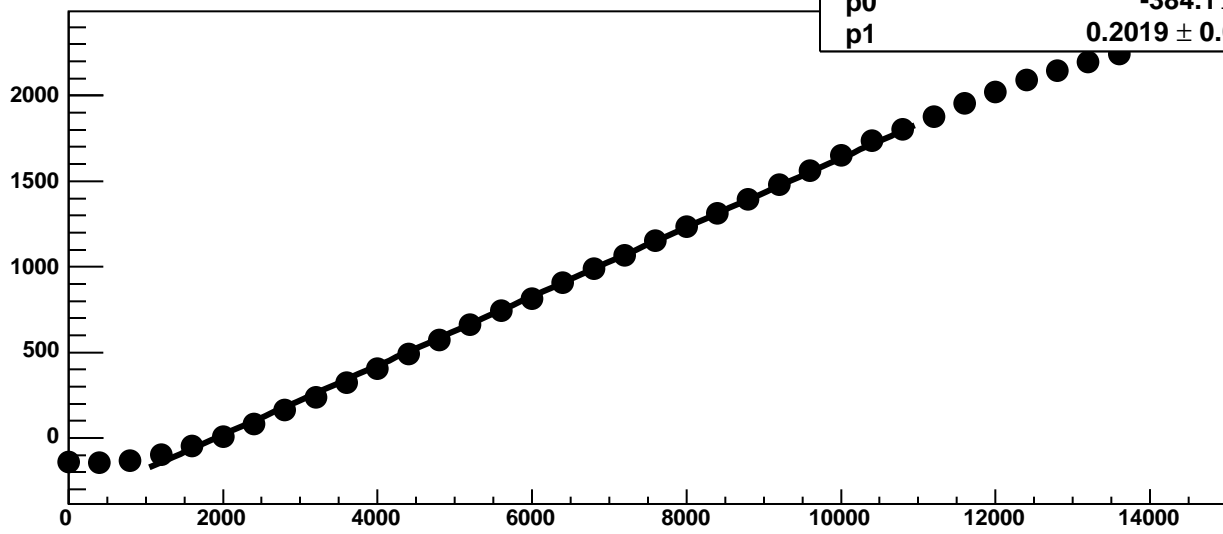
Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

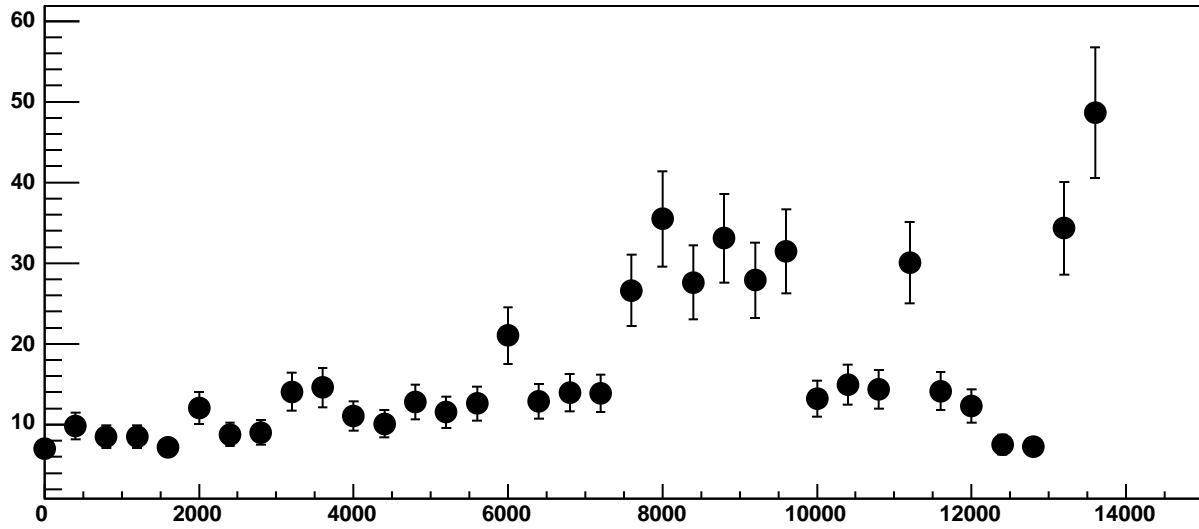


Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

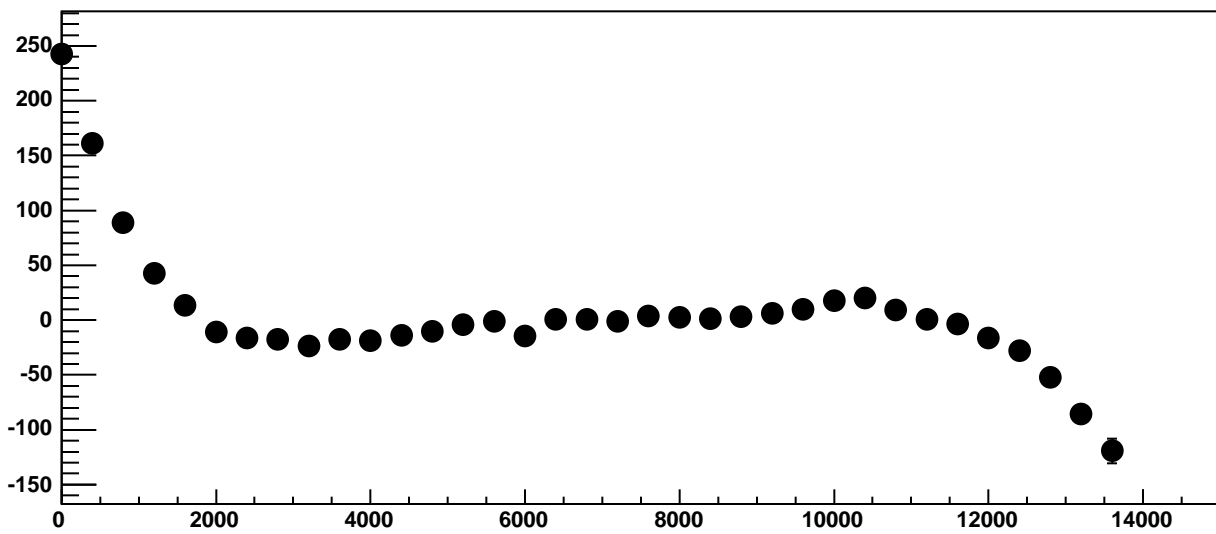


$\chi^2 / \text{ndf}$  989.7 / 23  
p0 -384.1 ± 1.094  
p1 0.2019 ± 0.000209

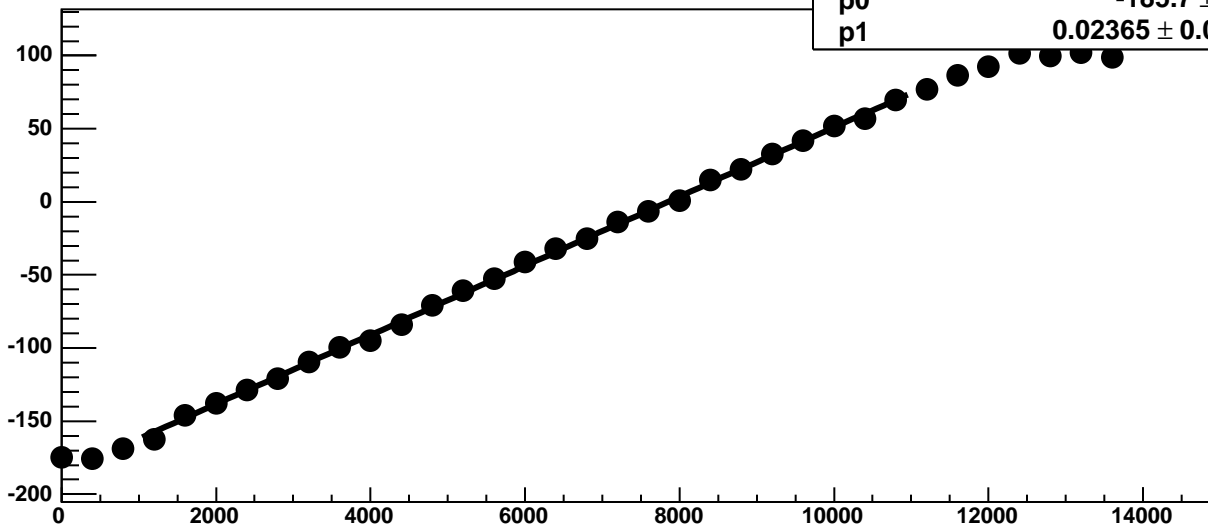
Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



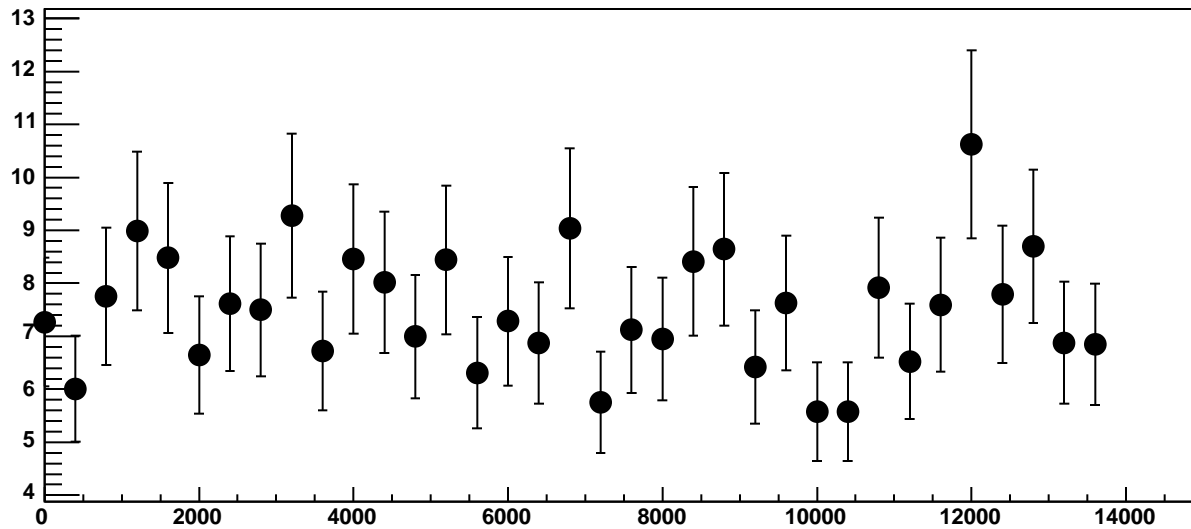
Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



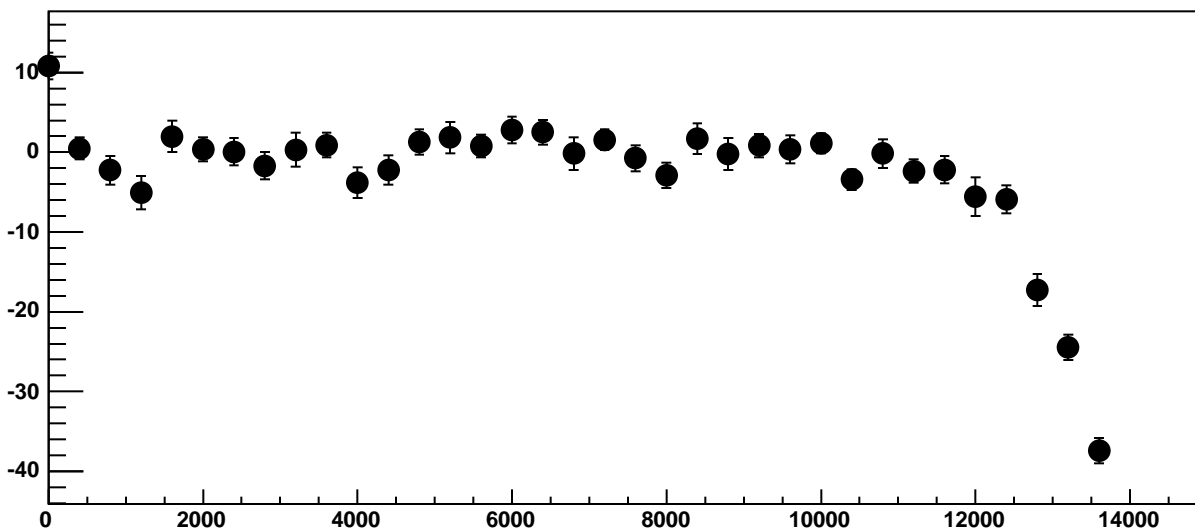
Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



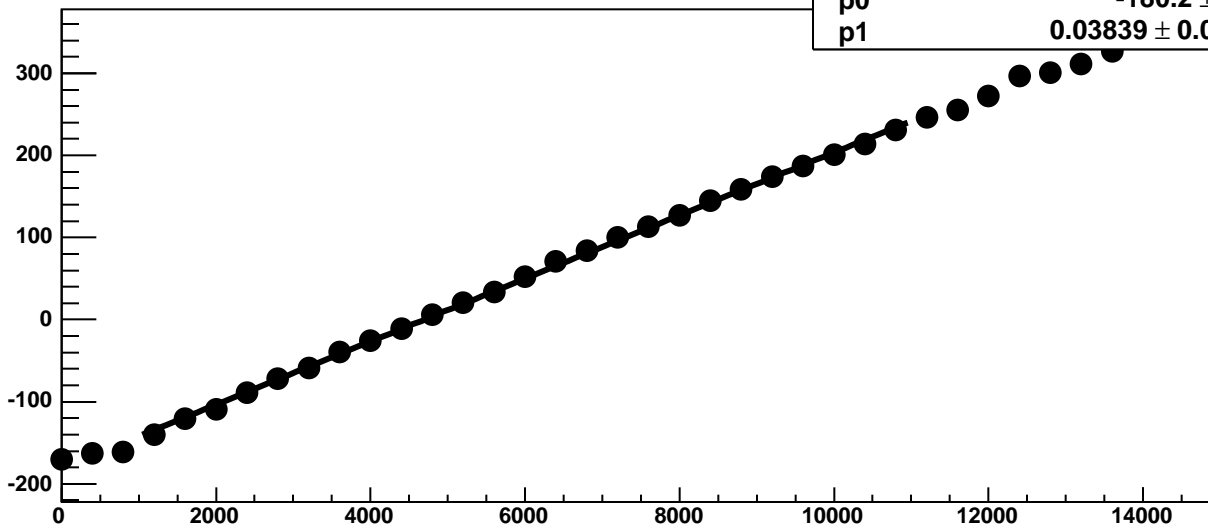
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



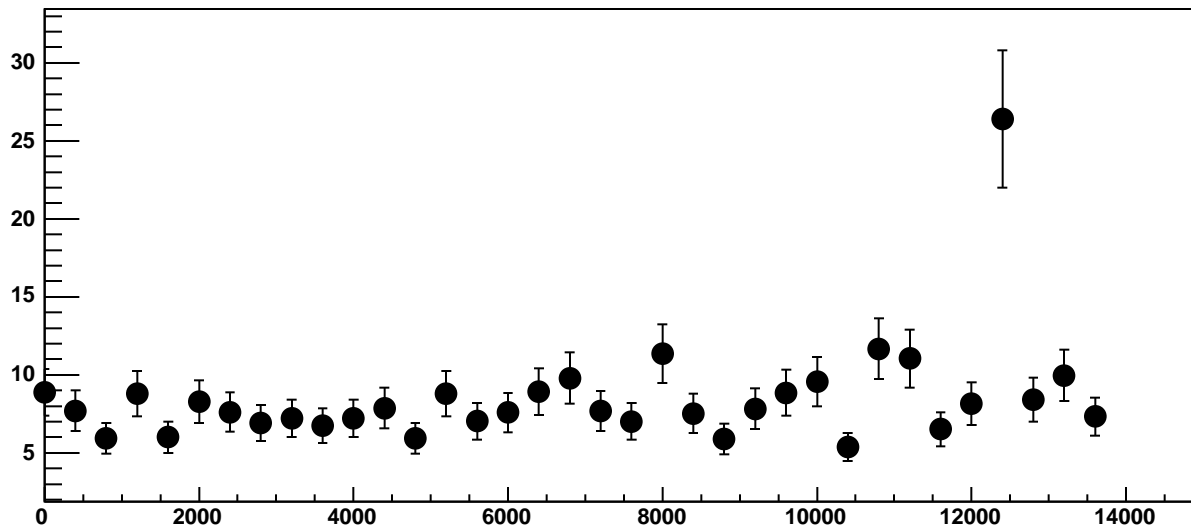
Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



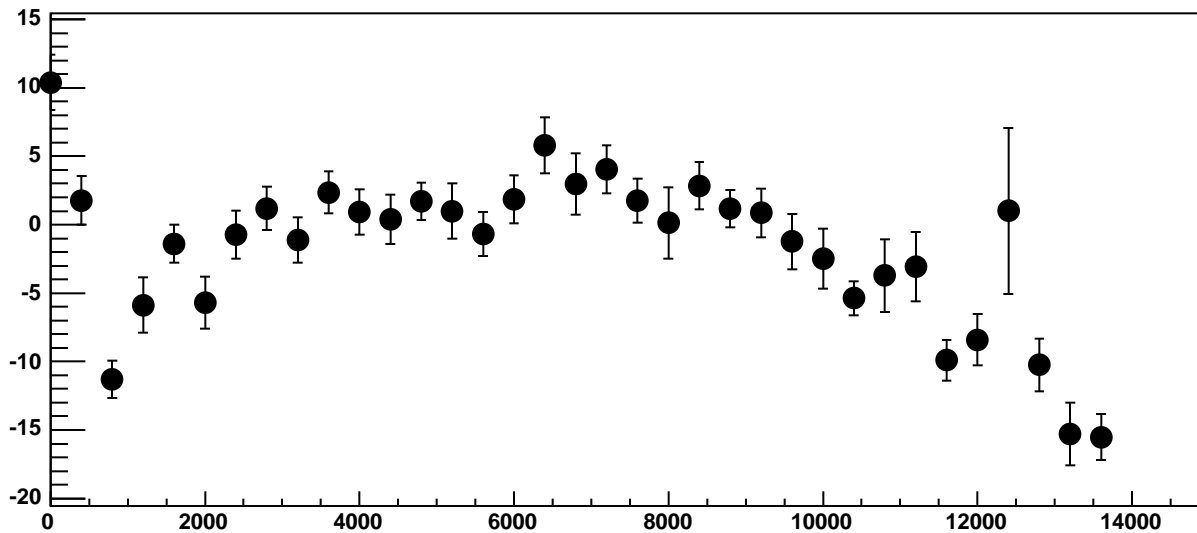
Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



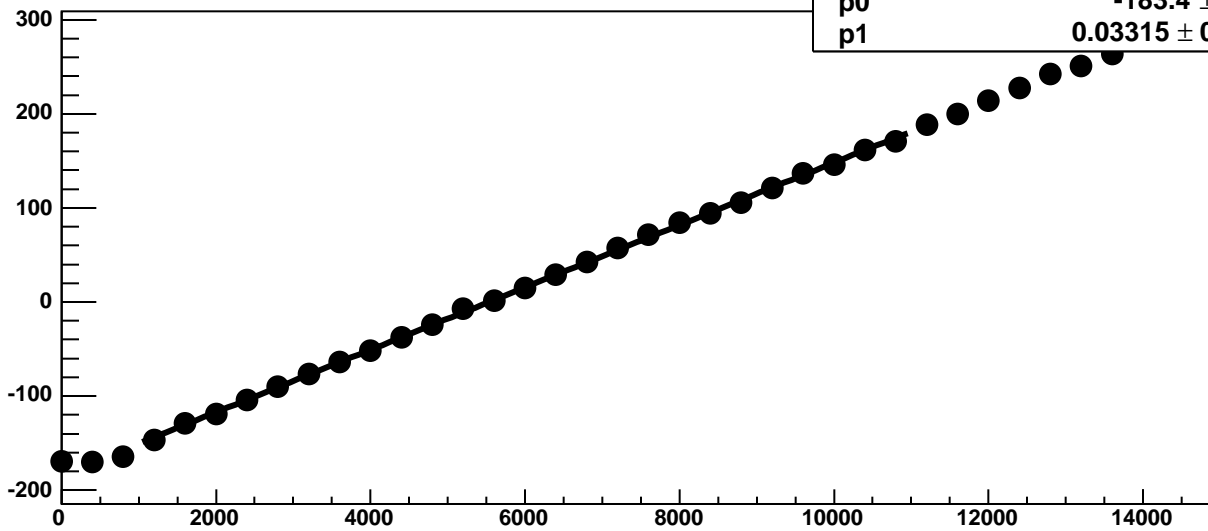
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

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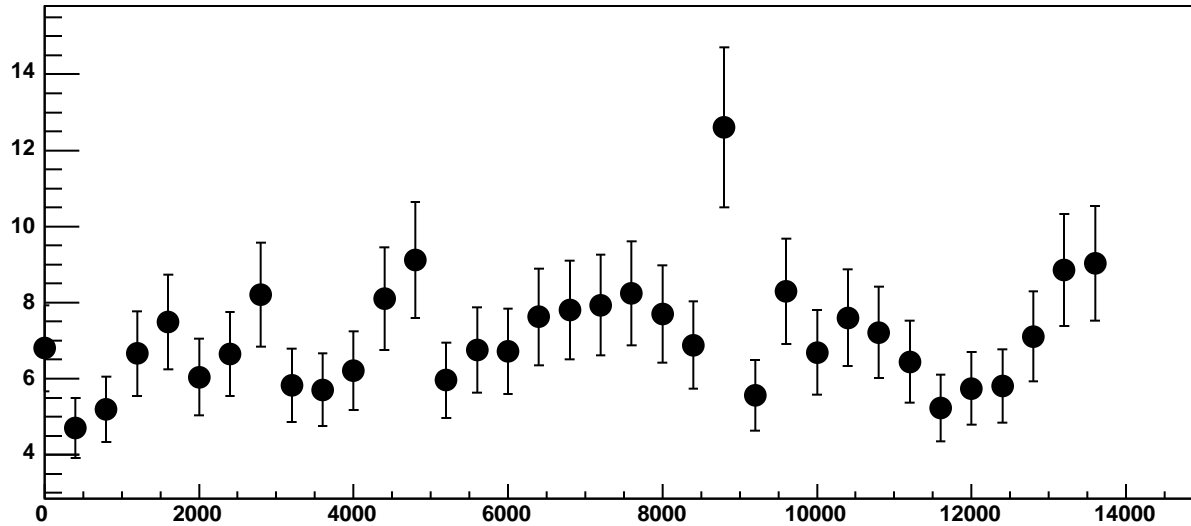
p0

$-183.4 \pm 0.7154$

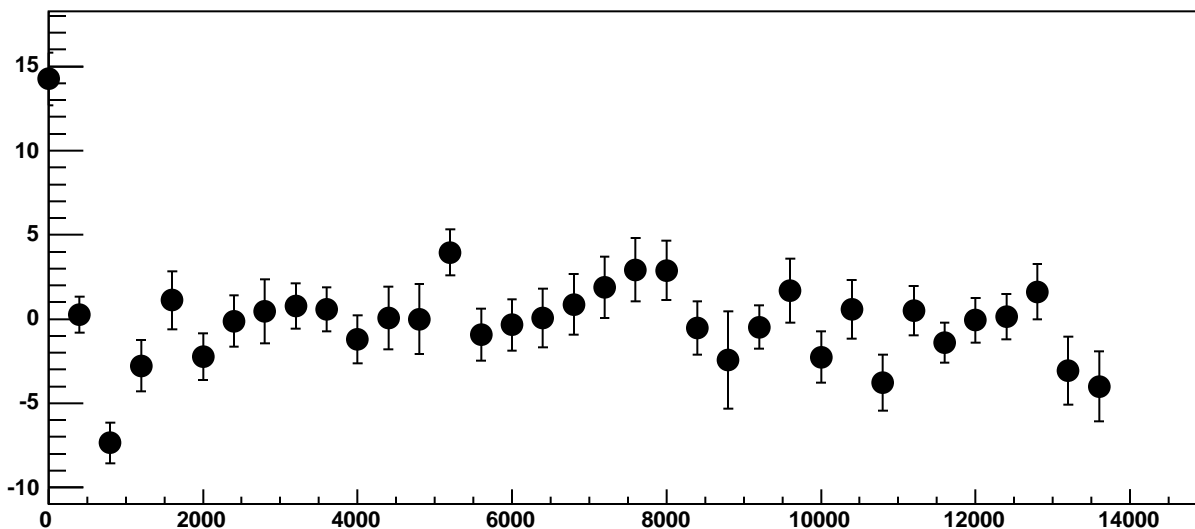
p1

$0.03315 \pm 0.000111$

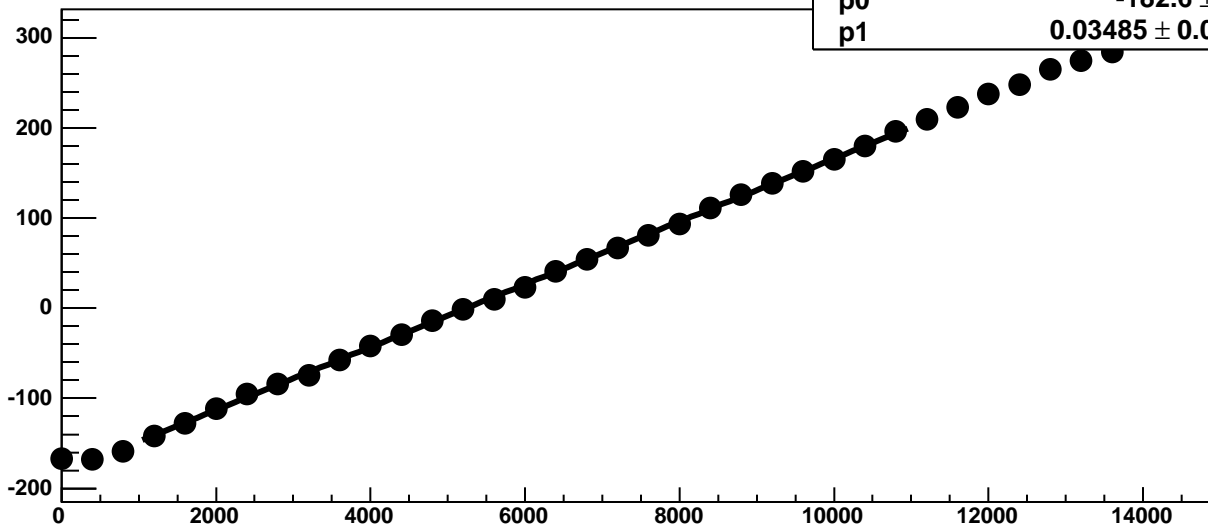
Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

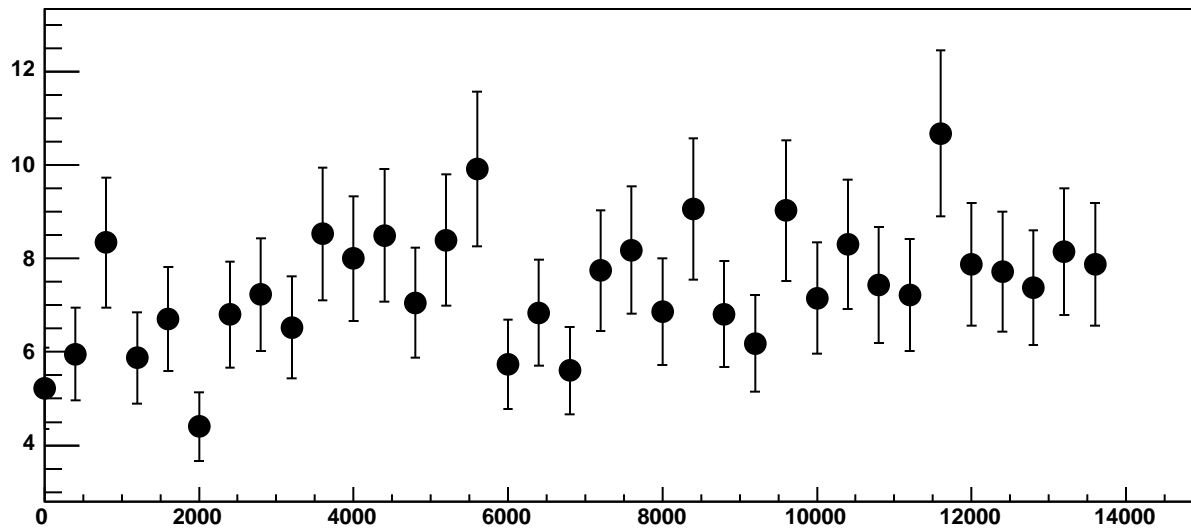


Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

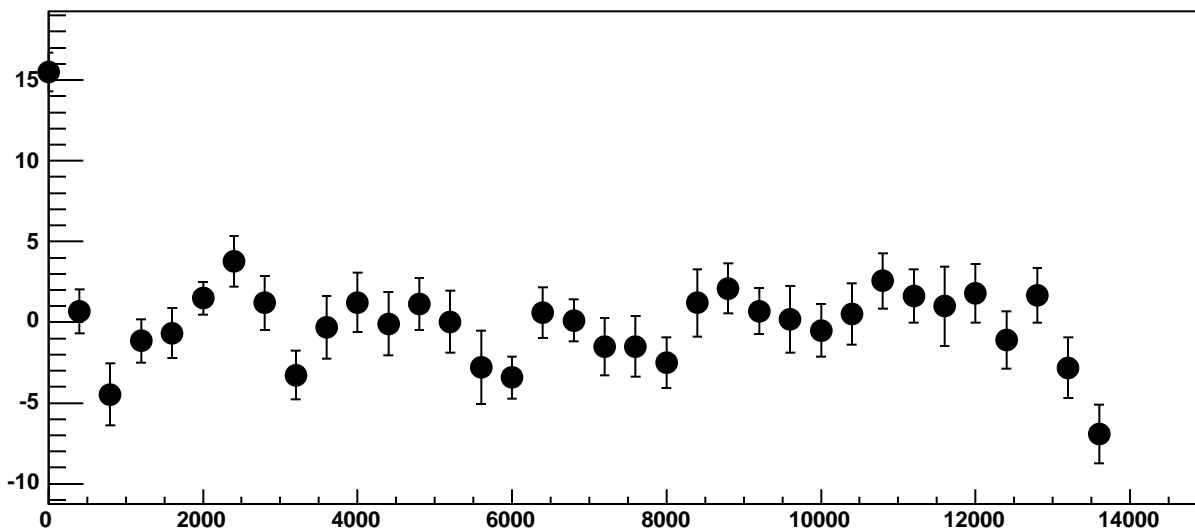


$\chi^2 / \text{ndf}$  32.33 / 23  
p0 -182.6 ± 0.6806  
p1 0.03485 ± 0.0001074

Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

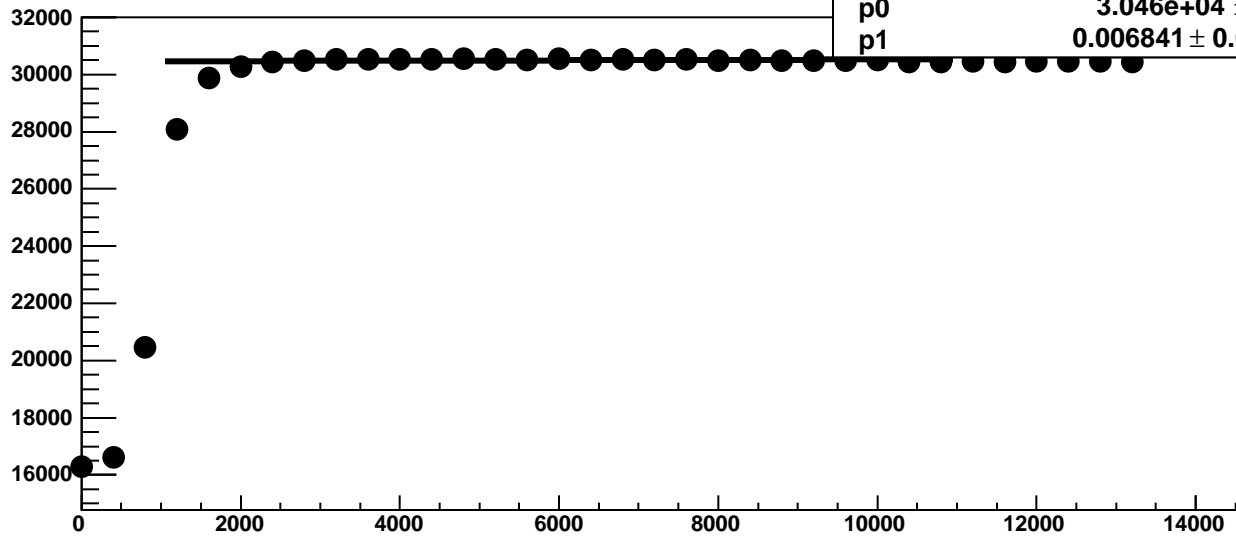
2259 / 23

p0

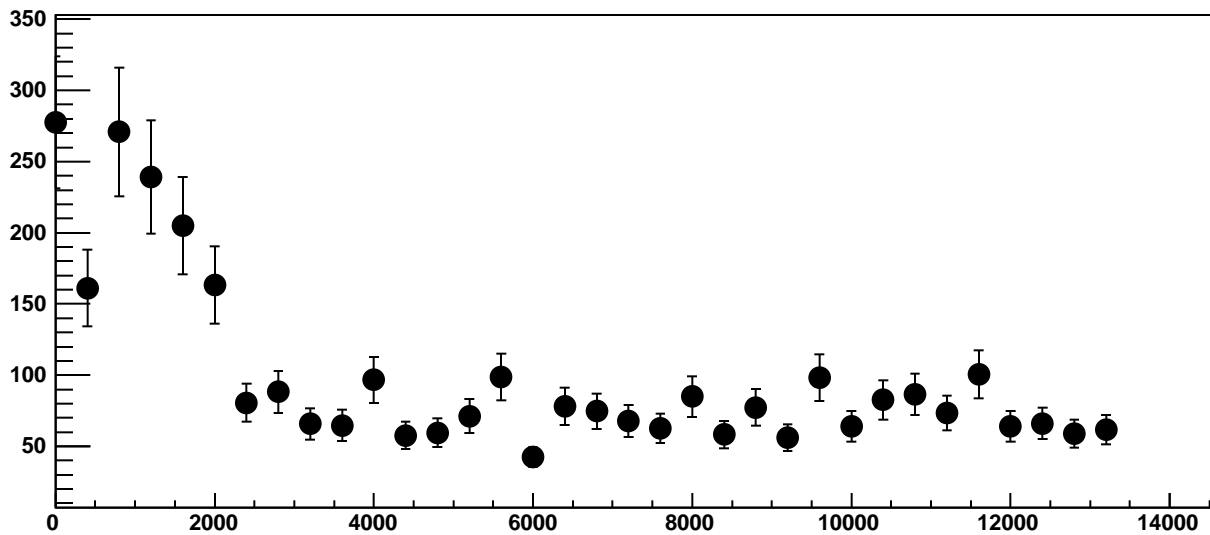
$3.046 \times 10^4 \pm 9.518$

p1

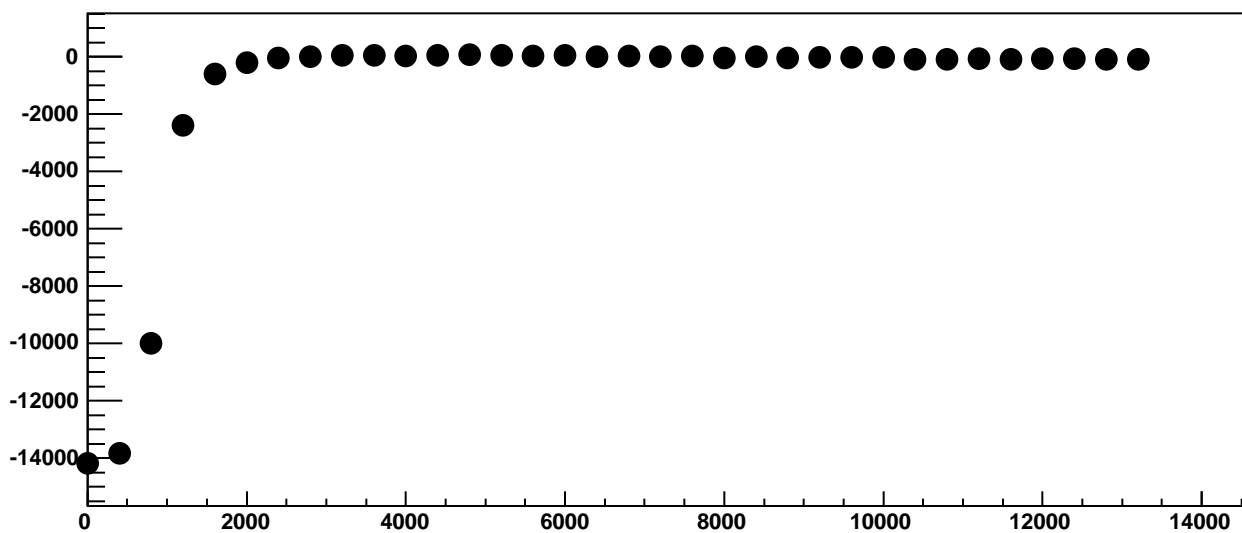
$0.006841 \pm 0.001378$



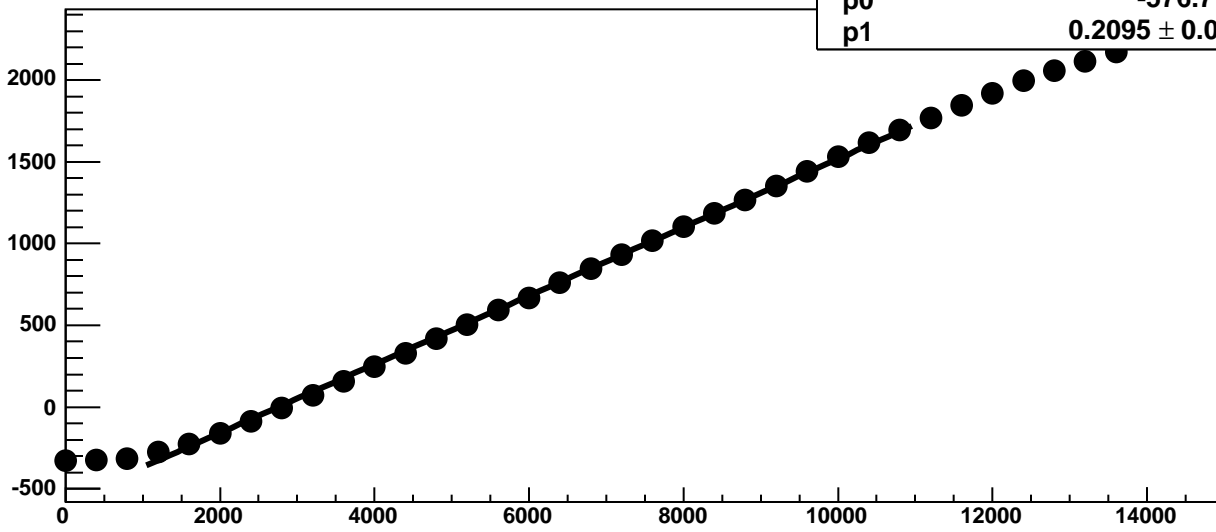
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



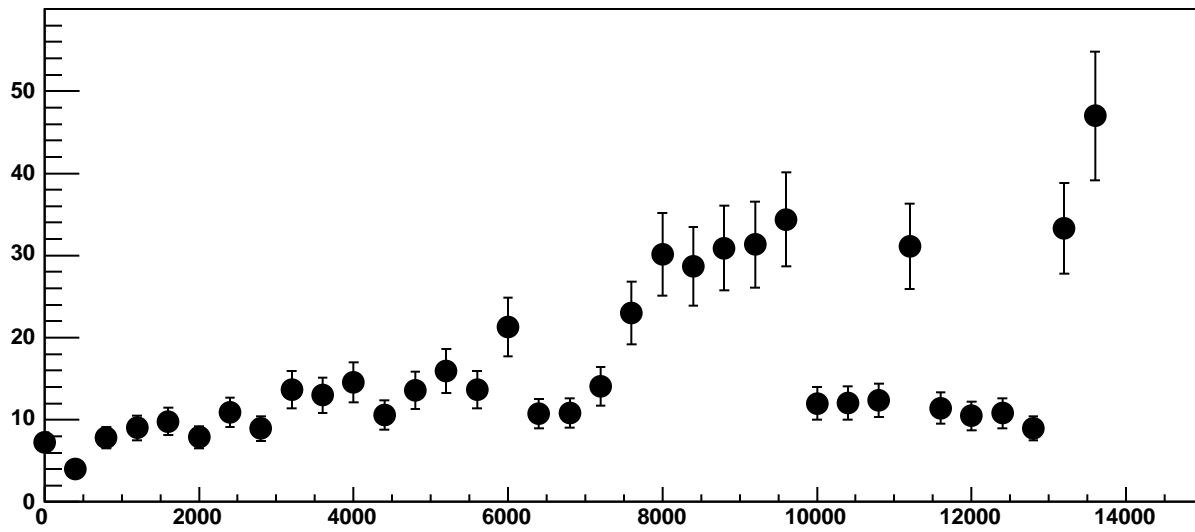
Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



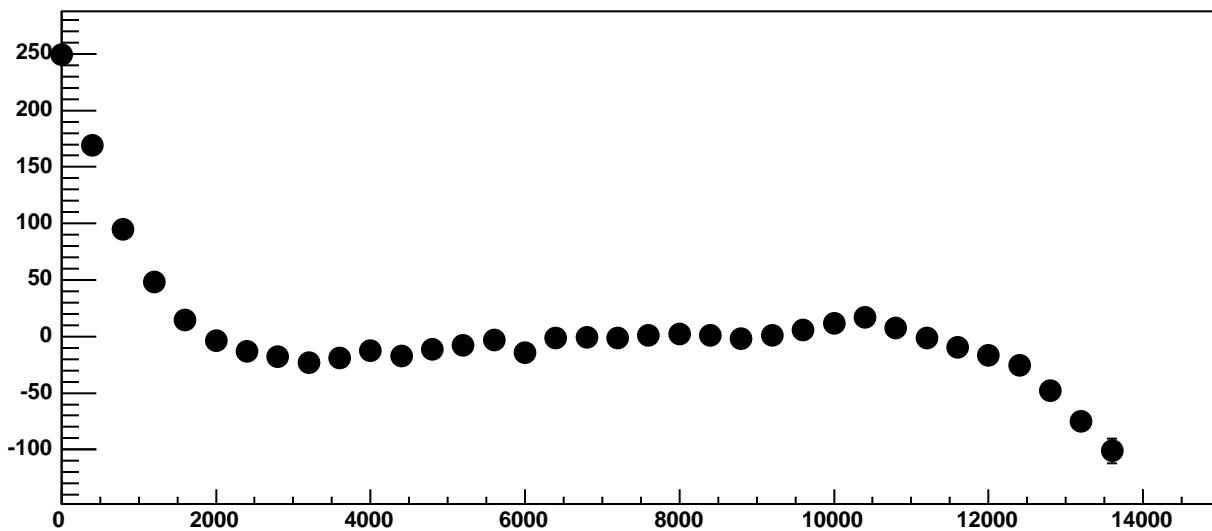
Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

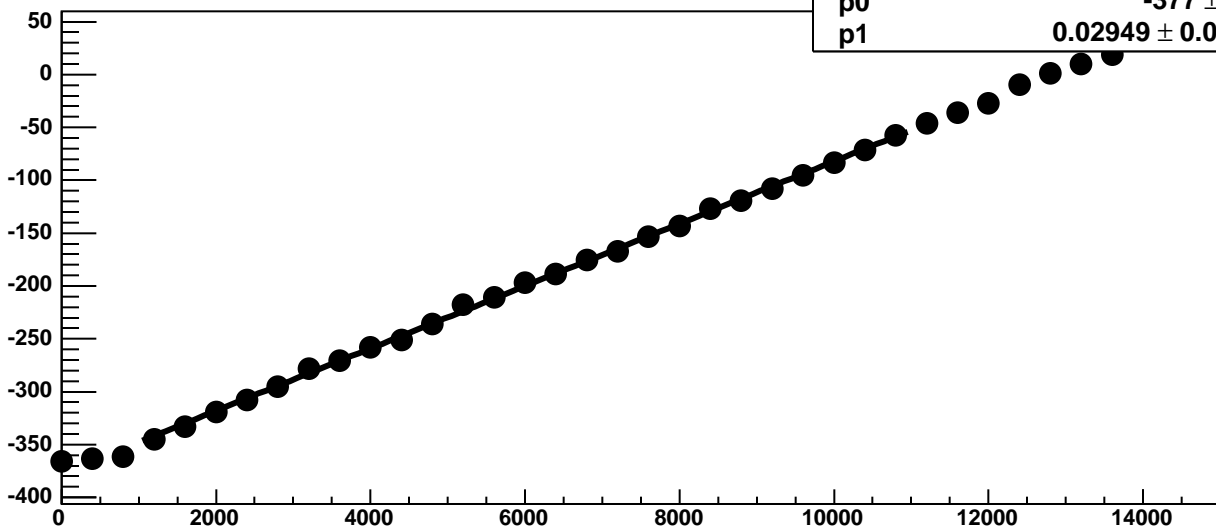


Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



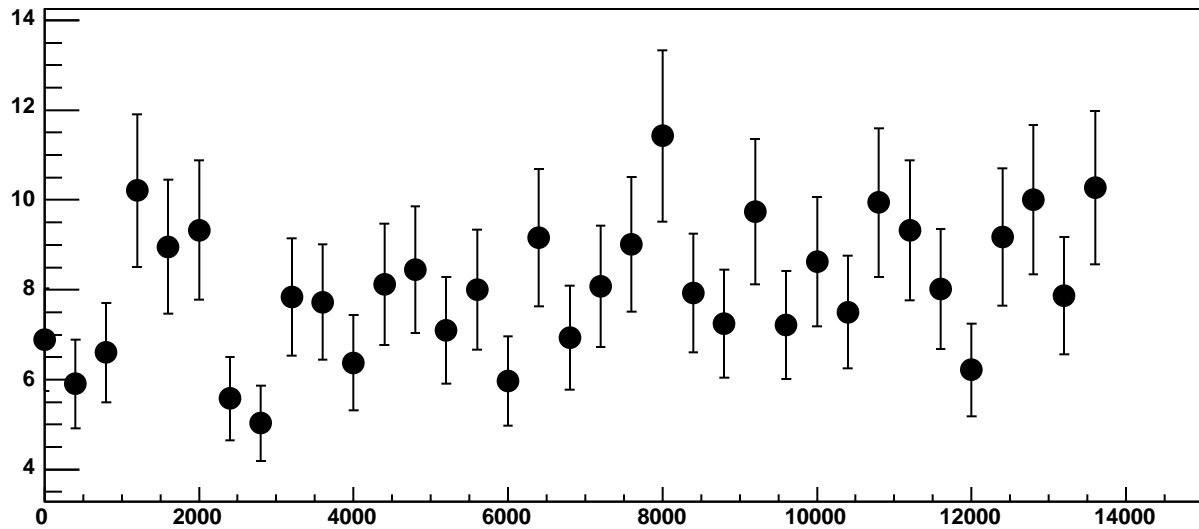


Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

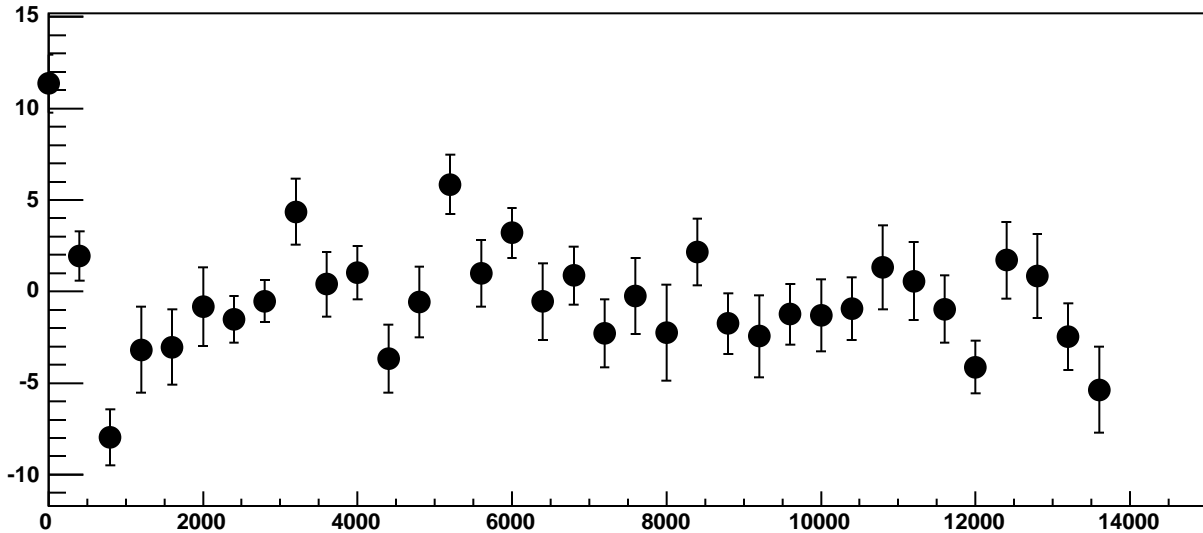


$\chi^2 / \text{ndf}$  42.8 / 23  
p0  $-377 \pm 0.7971$   
p1  $0.02949 \pm 0.0001261$

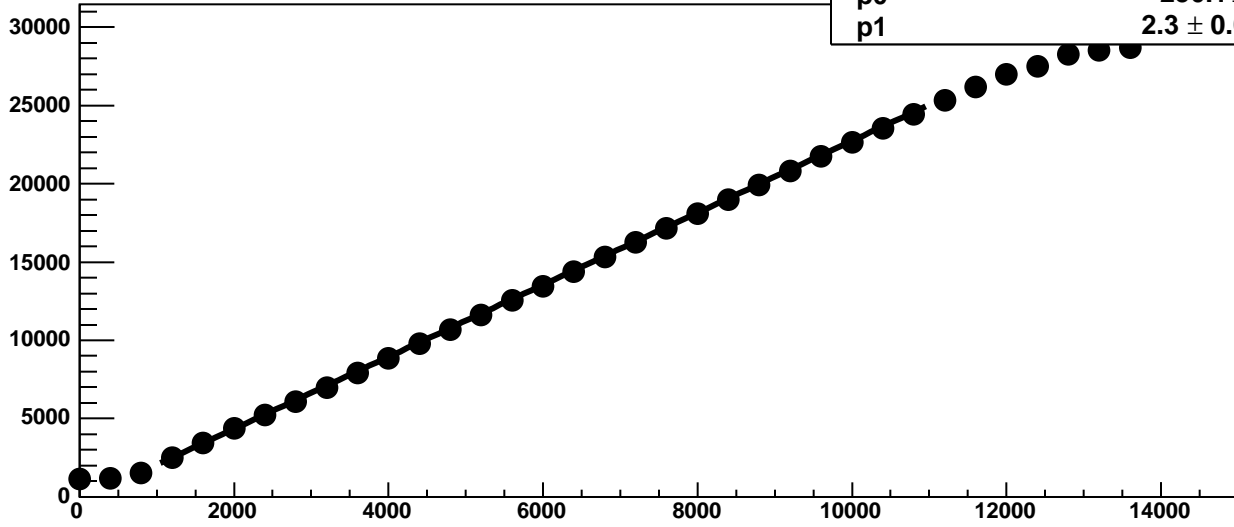
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

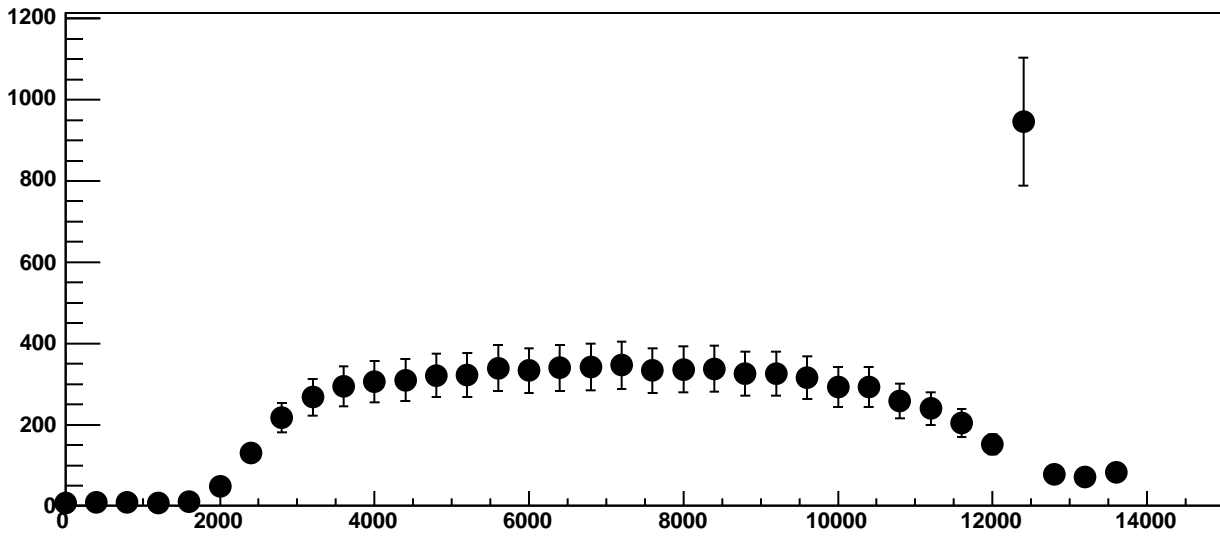


Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC

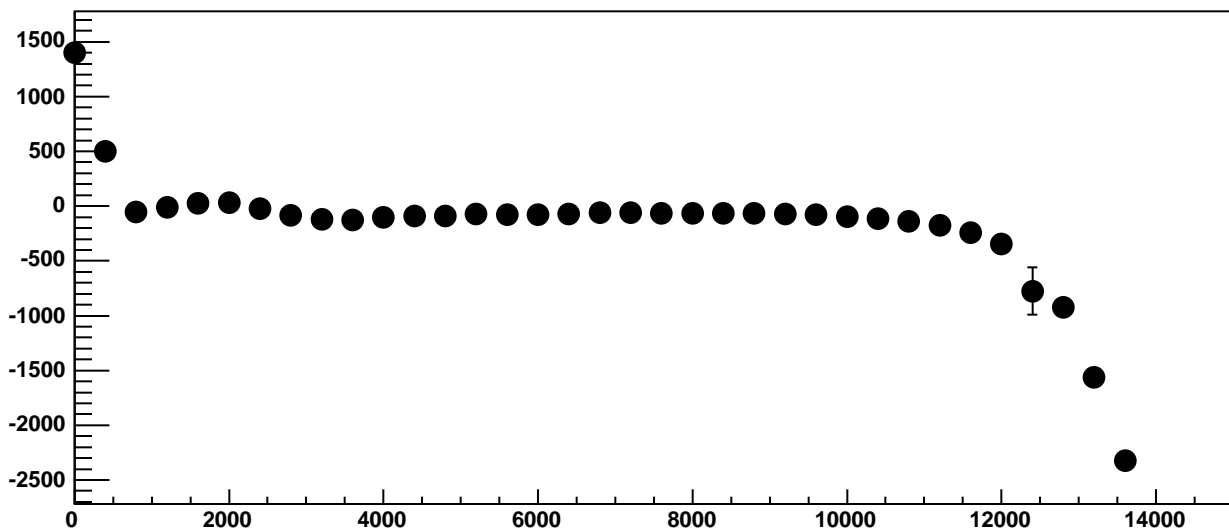


$\chi^2 / \text{ndf}$  214.2 / 23  
p0 -250.1 ± 3.567  
p1 2.3 ± 0.002404

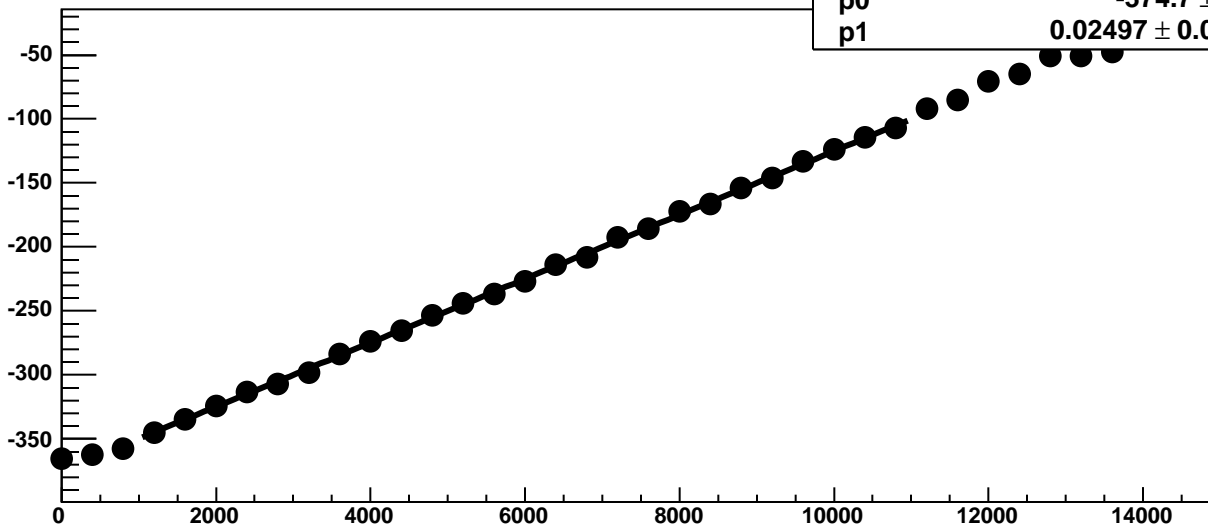
Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

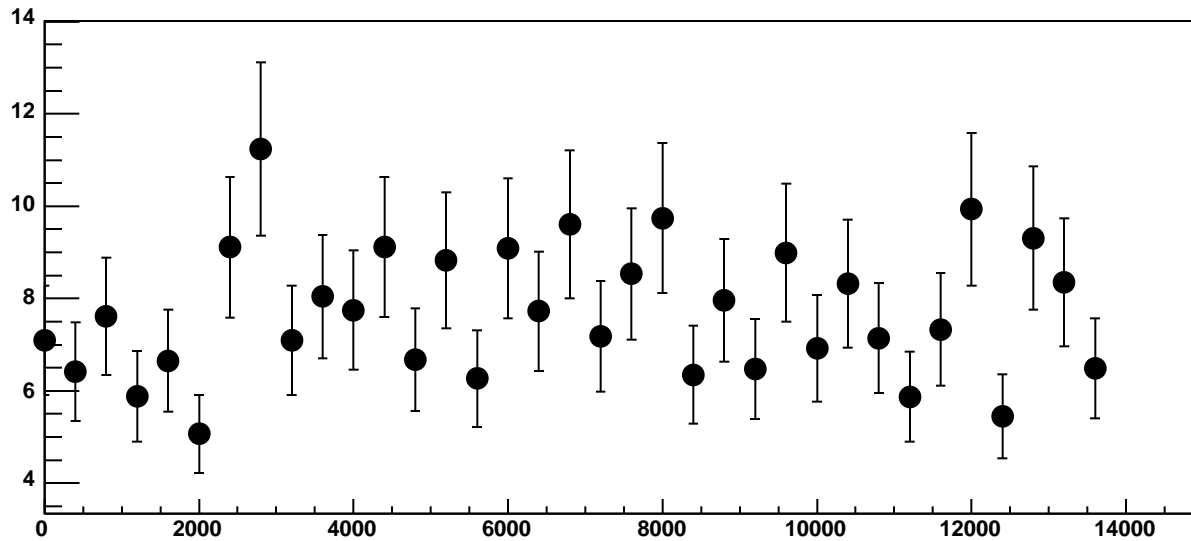


Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

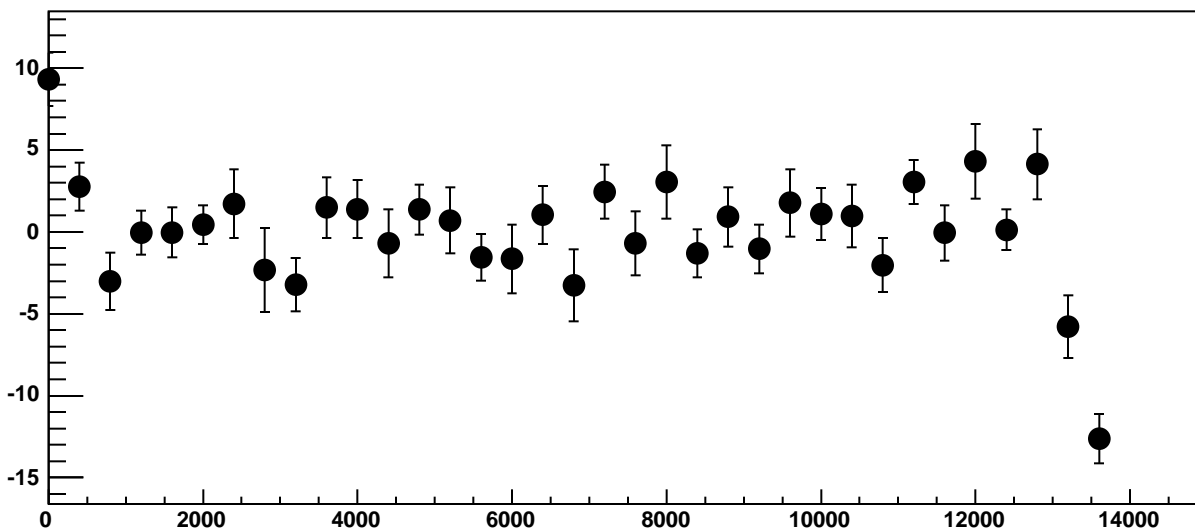


$\chi^2 / \text{ndf}$  20.86 / 23  
p0  $-374.7 \pm 0.7315$   
p1  $0.02497 \pm 0.0001123$

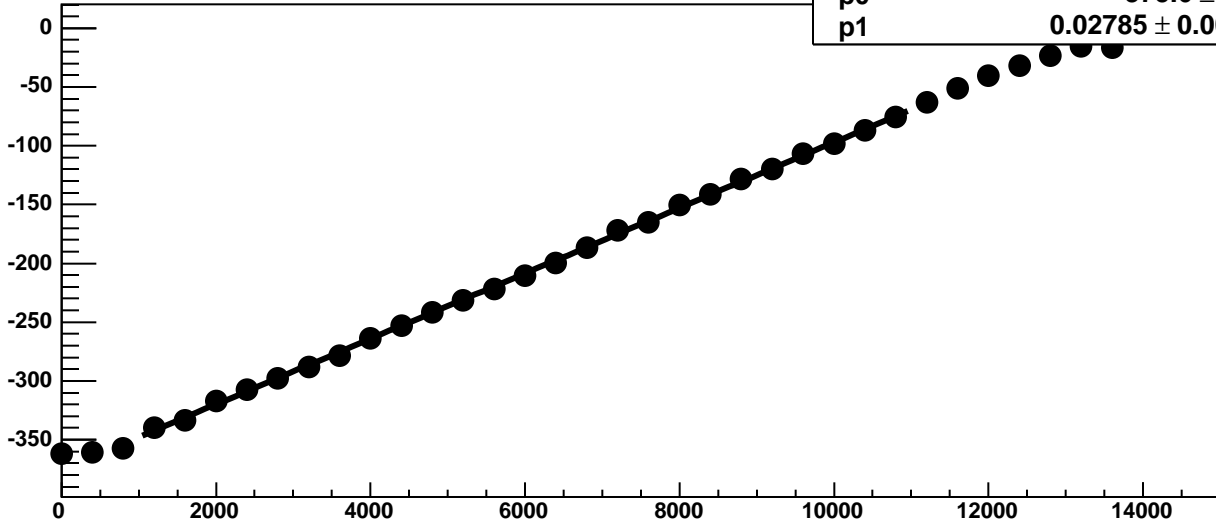
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



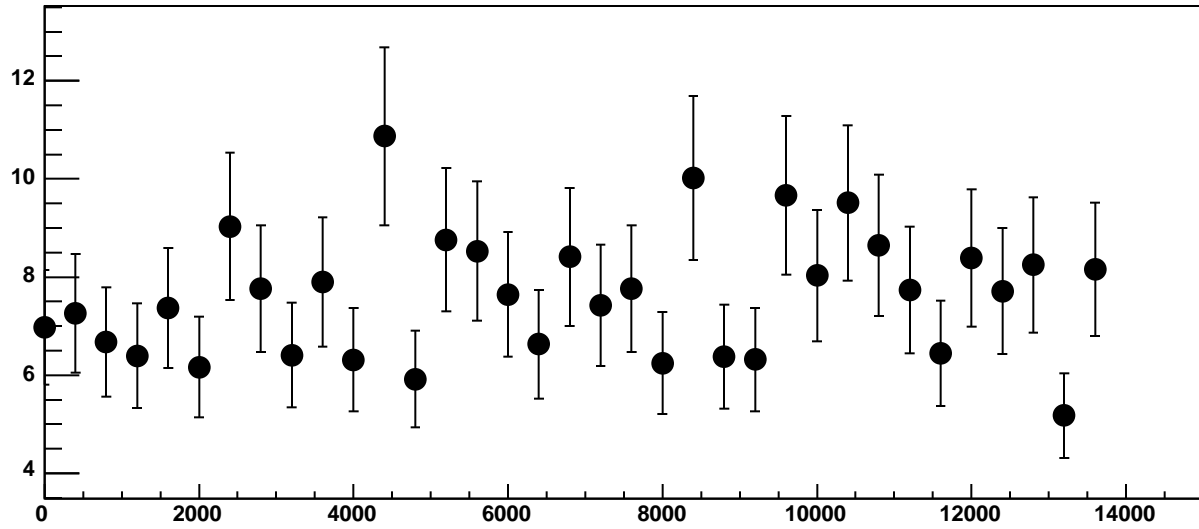
Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



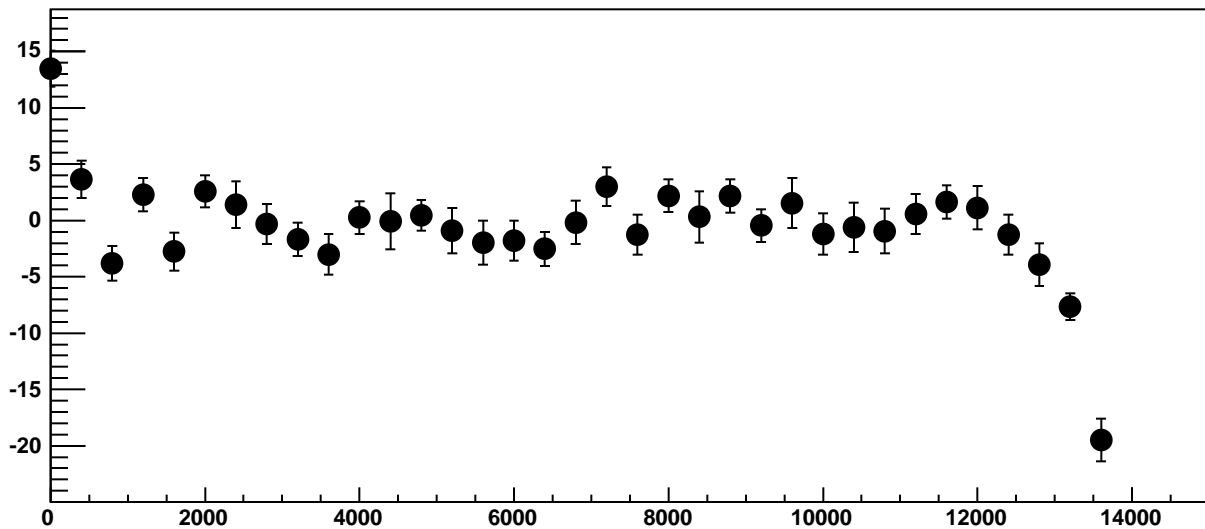
Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



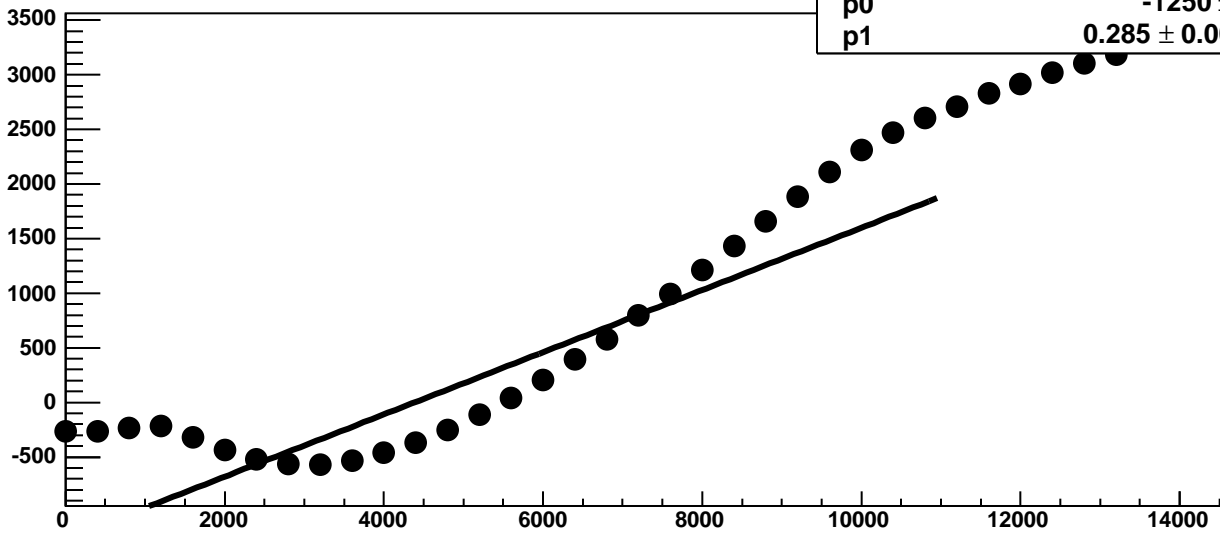
Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



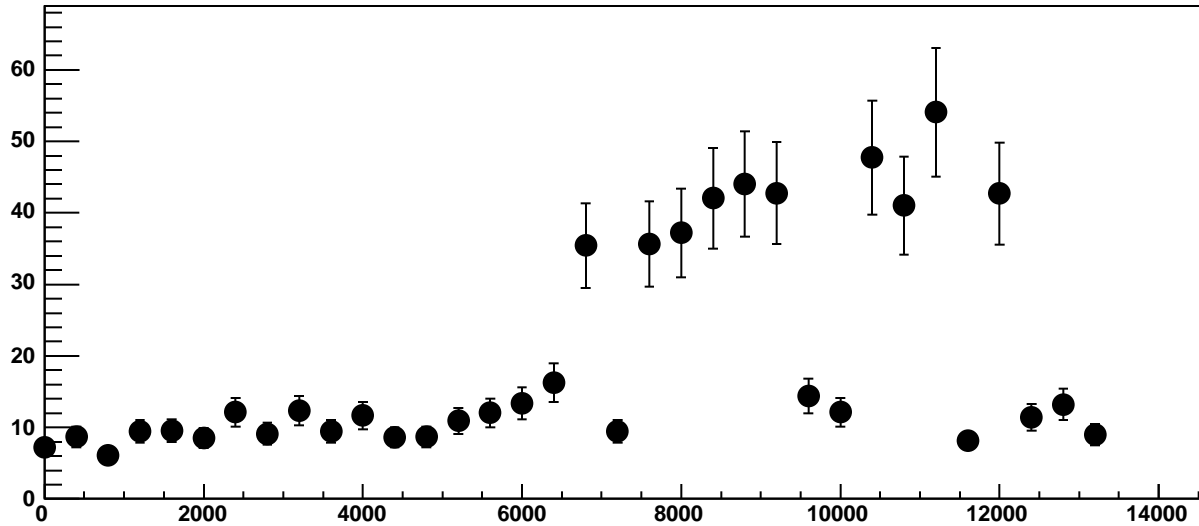
Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



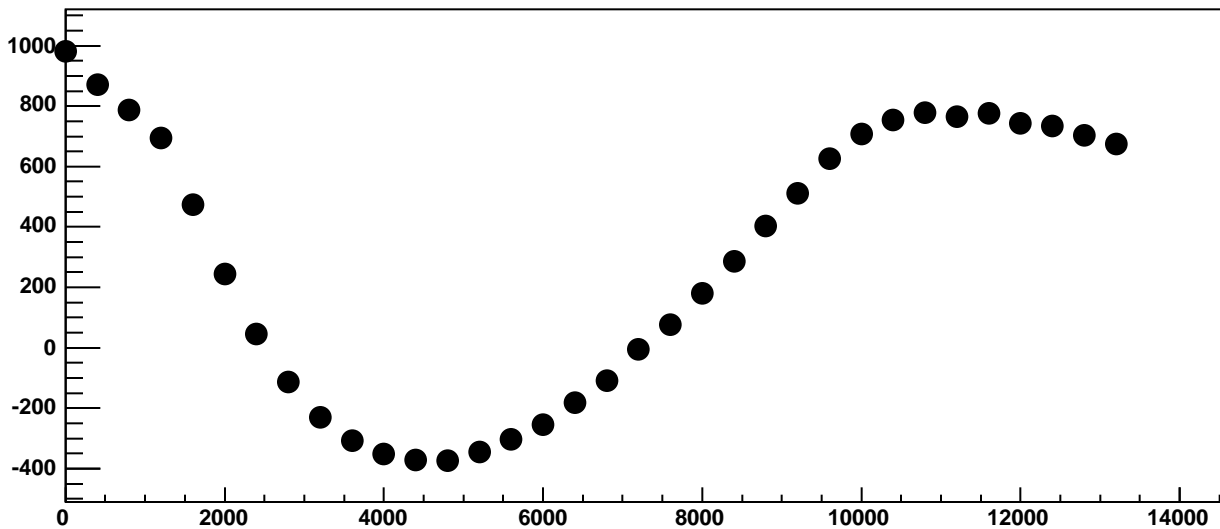
Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



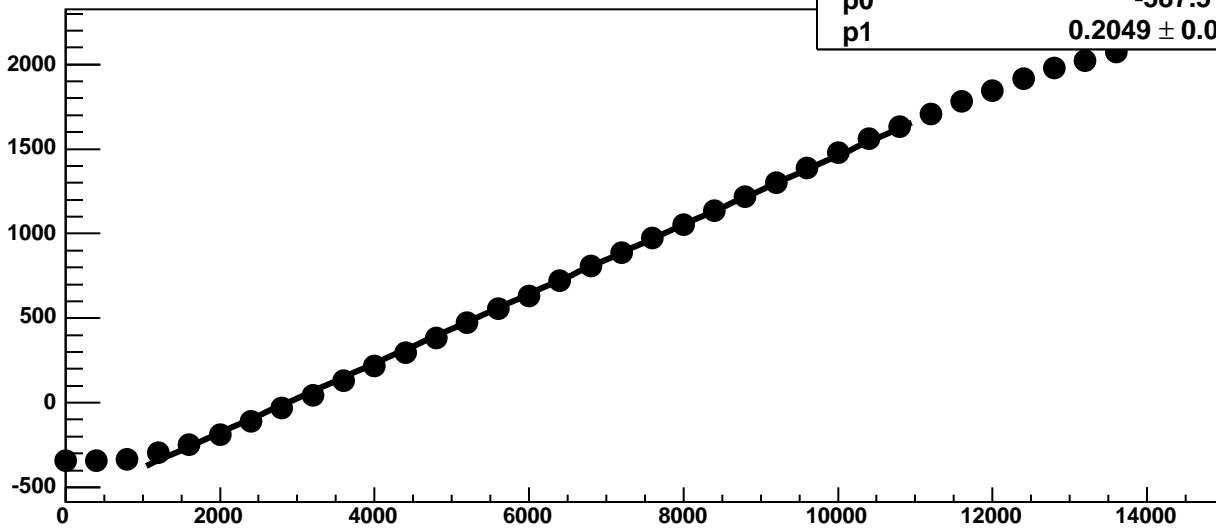
Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

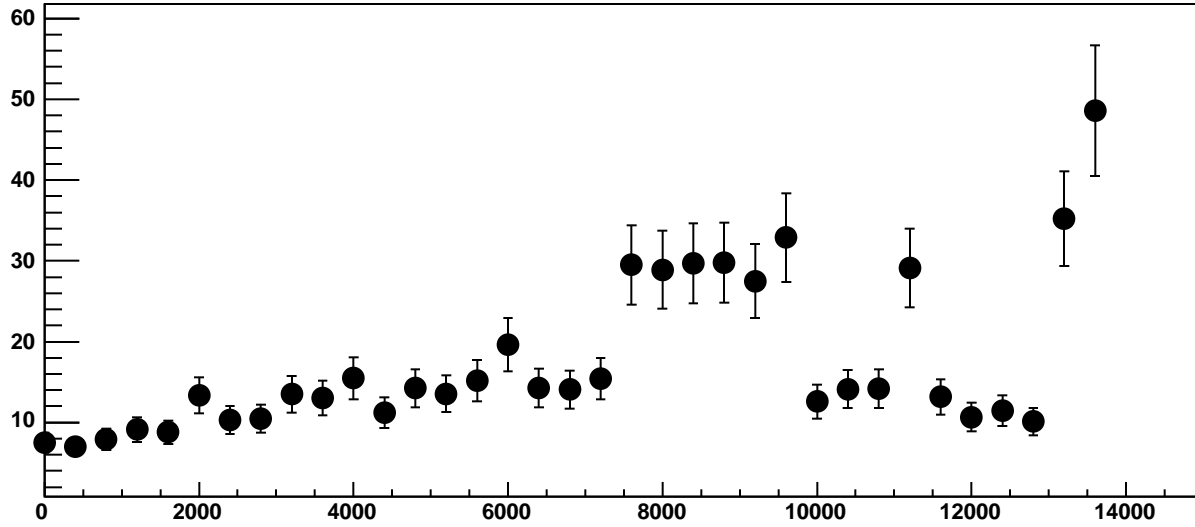


Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

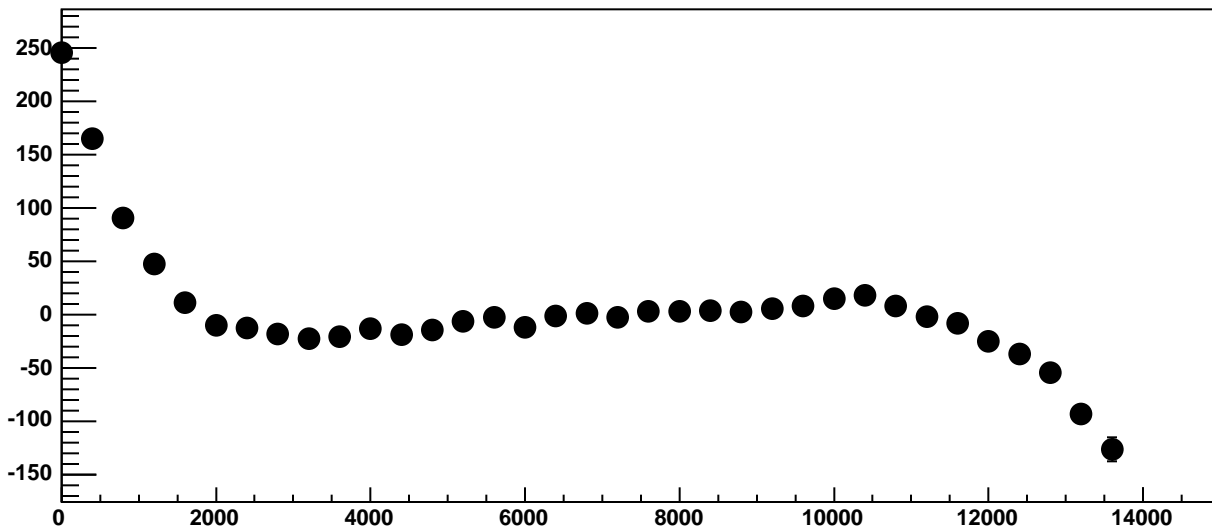


$\chi^2 / \text{ndf}$  903.4 / 23  
p0  $-587.5 \pm 1.208$   
p1  $0.2049 \pm 0.0002167$

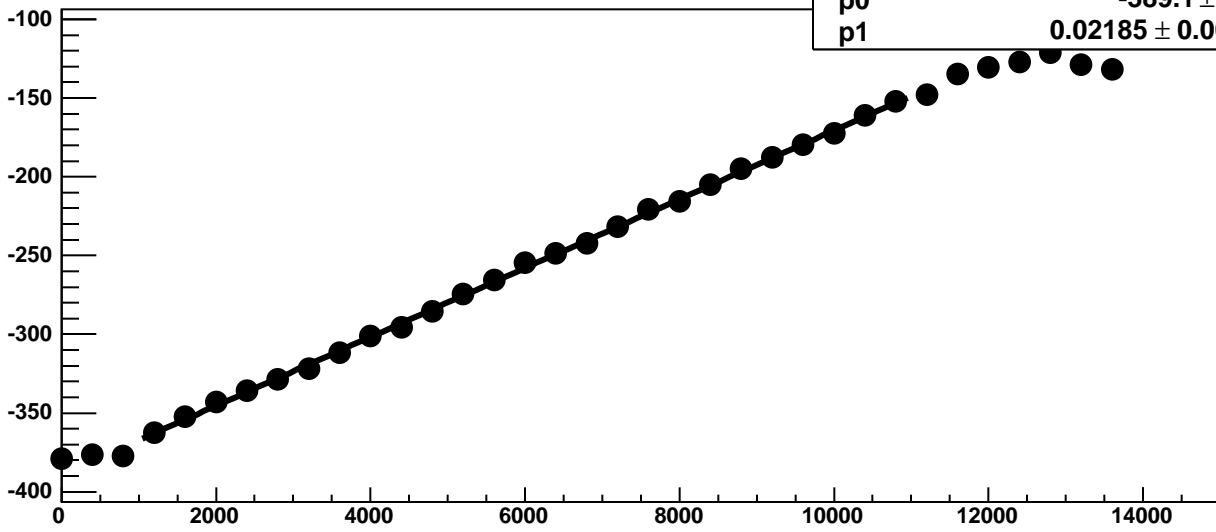
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

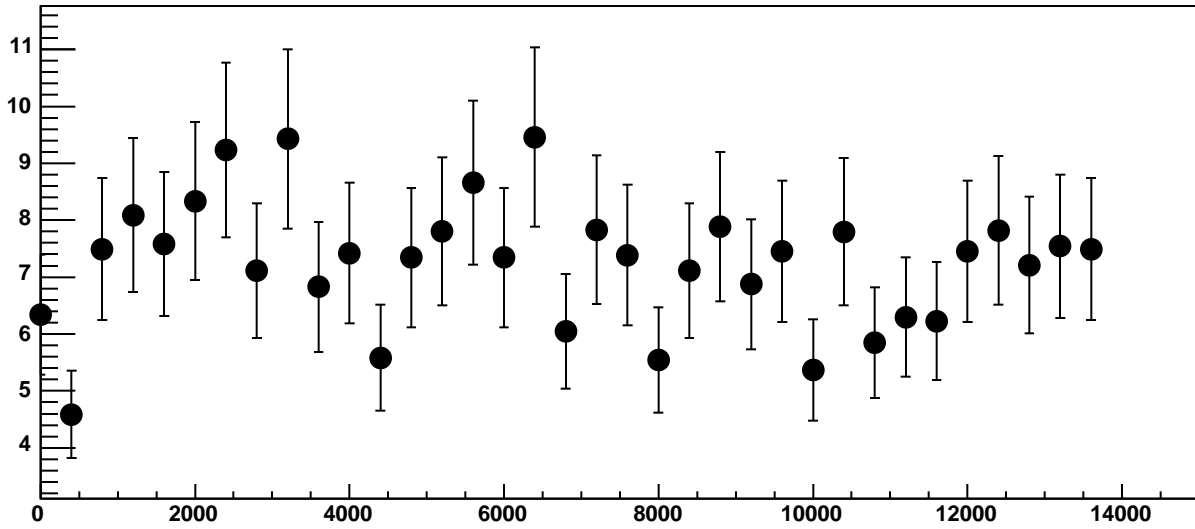


Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

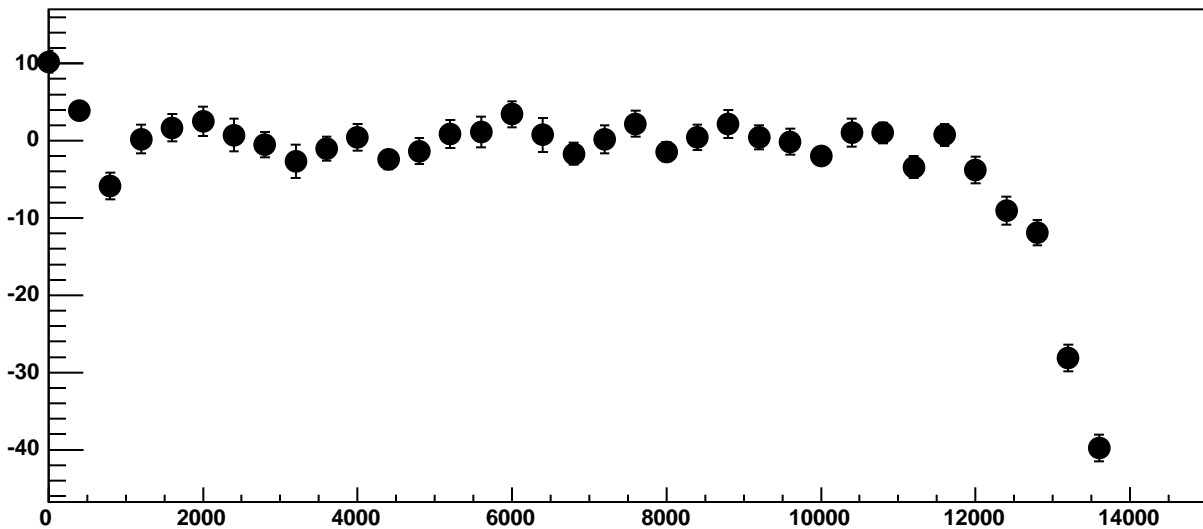


$\chi^2 / \text{ndf}$  23.61 / 23  
p0  $-389.1 \pm 0.7983$   
p1  $0.02185 \pm 0.0001138$

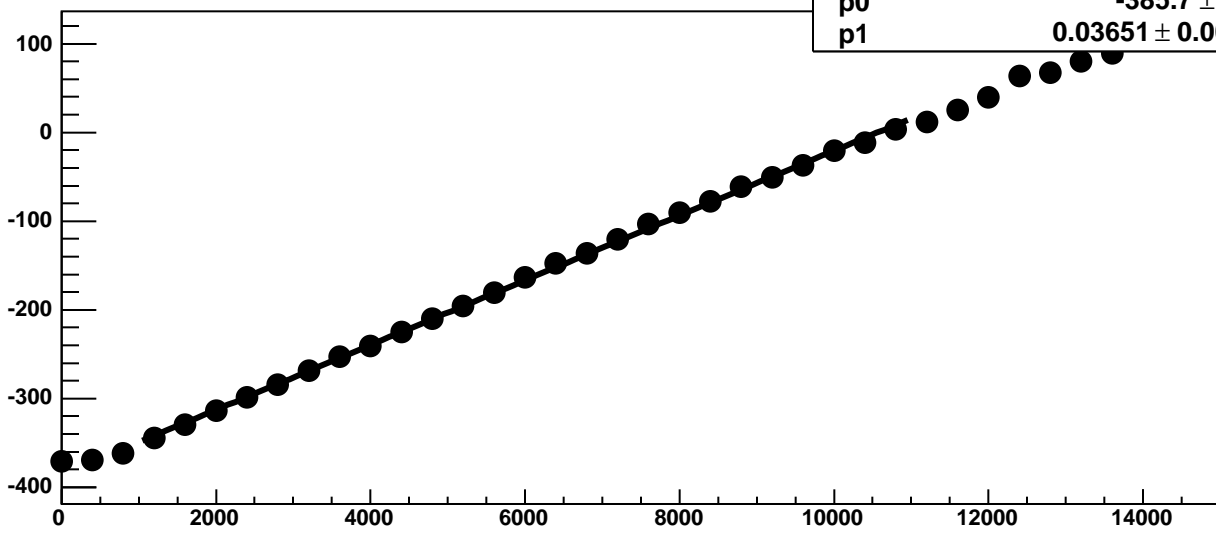
Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

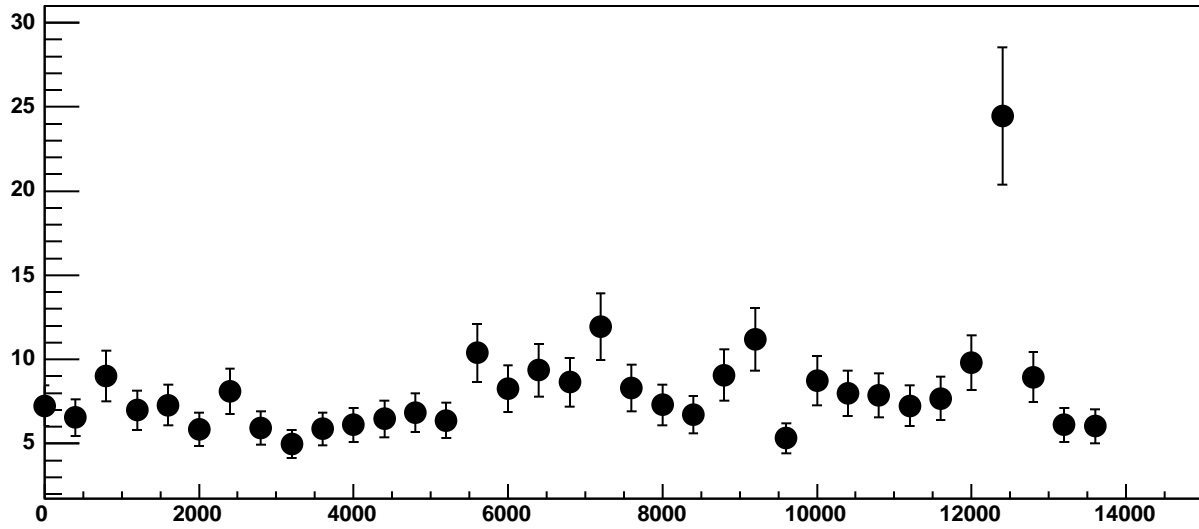


Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

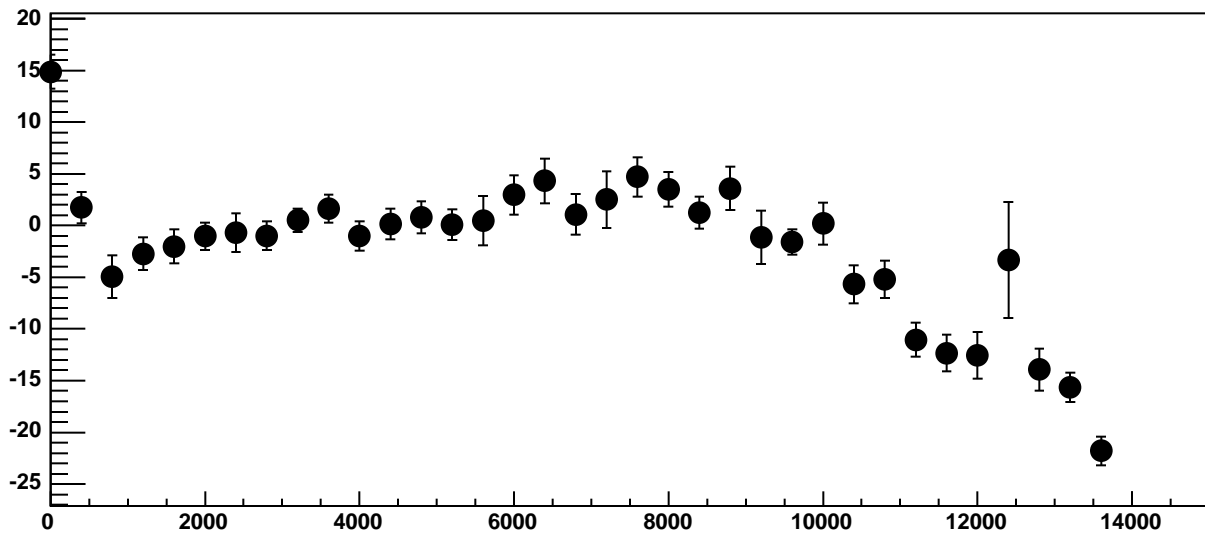


$\chi^2 / \text{ndf}$  49.84 / 23  
p0  $-385.7 \pm 0.7009$   
p1  $0.03651 \pm 0.0001126$

Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

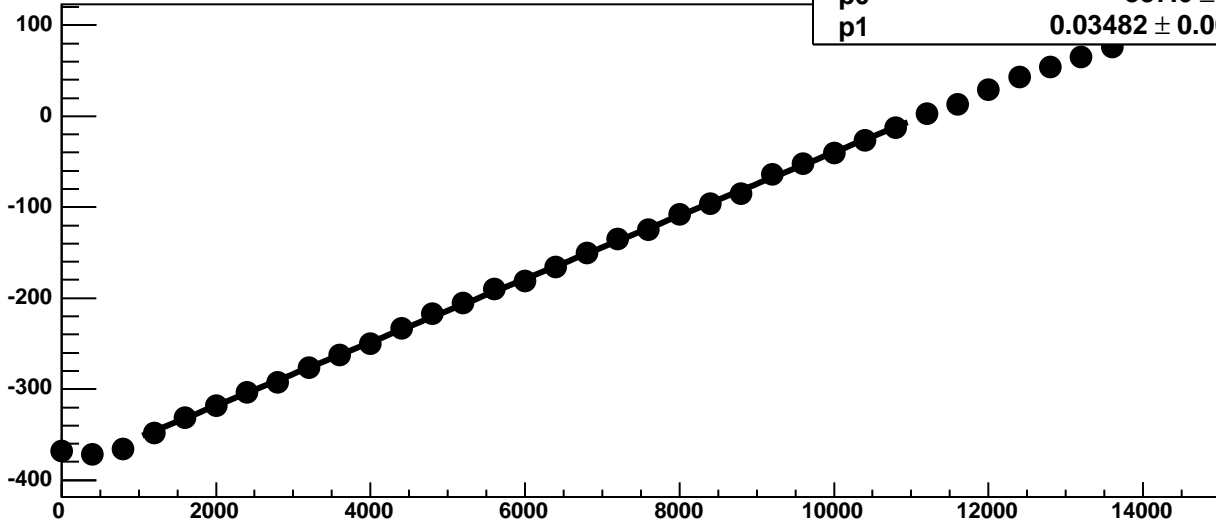


Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



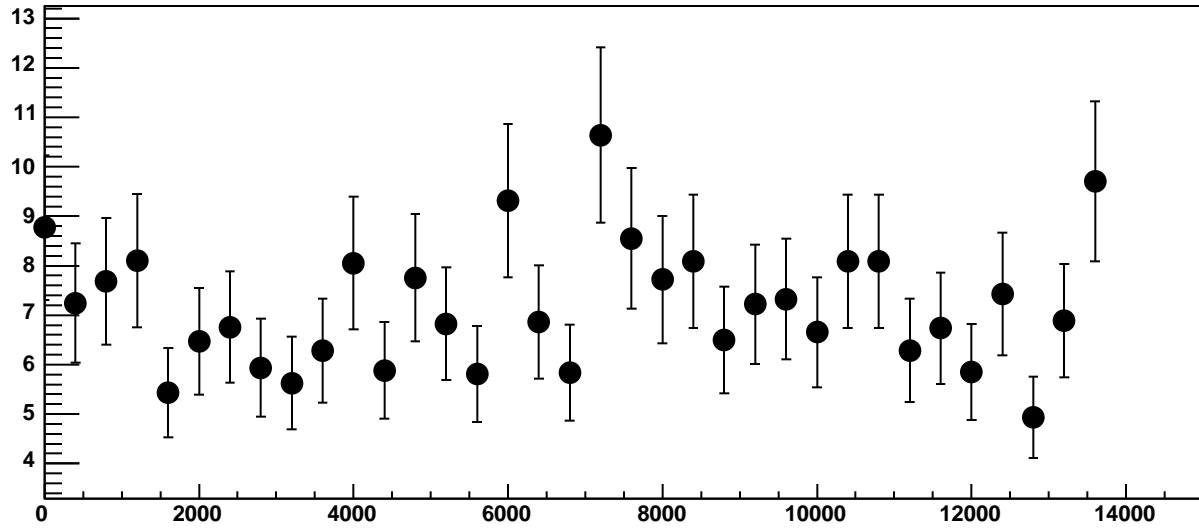


Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

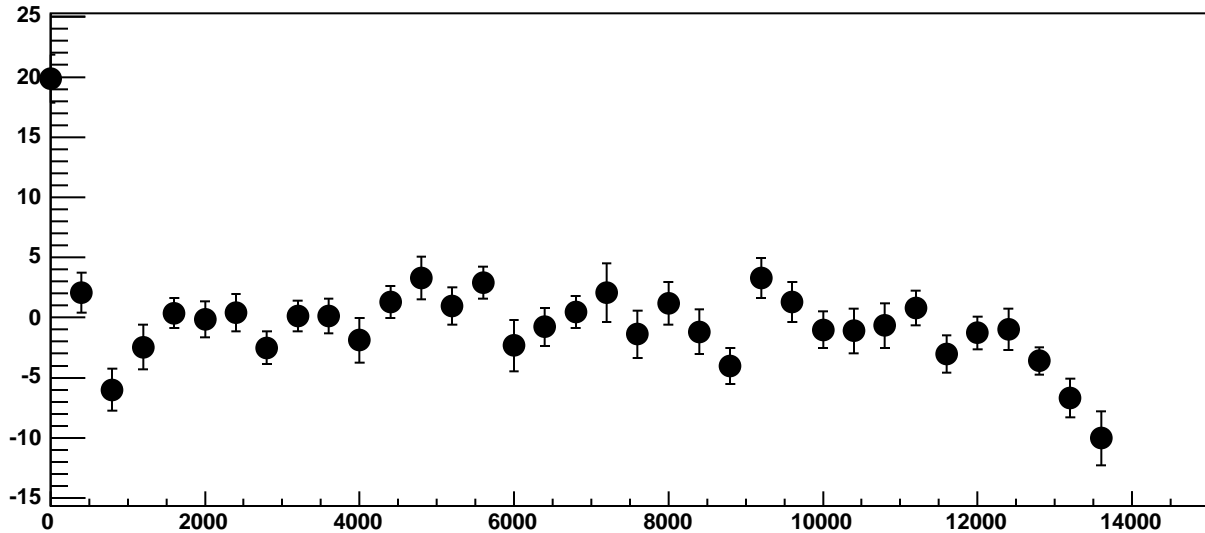


$\chi^2 / \text{ndf}$  32.08 / 23  
p0  $-387.6 \pm 0.6963$   
p1  $0.03482 \pm 0.0001104$

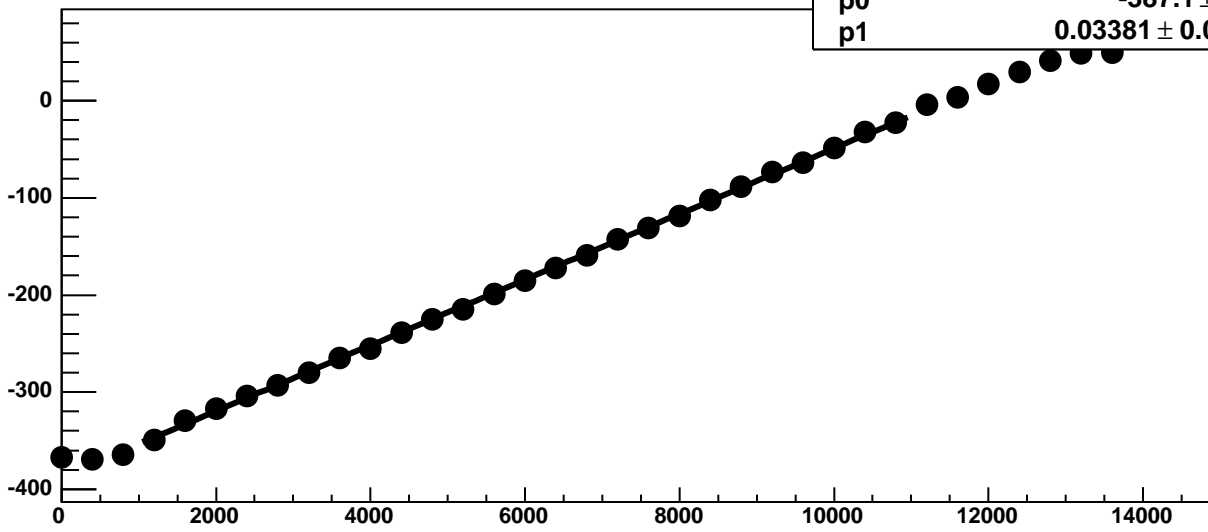
Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

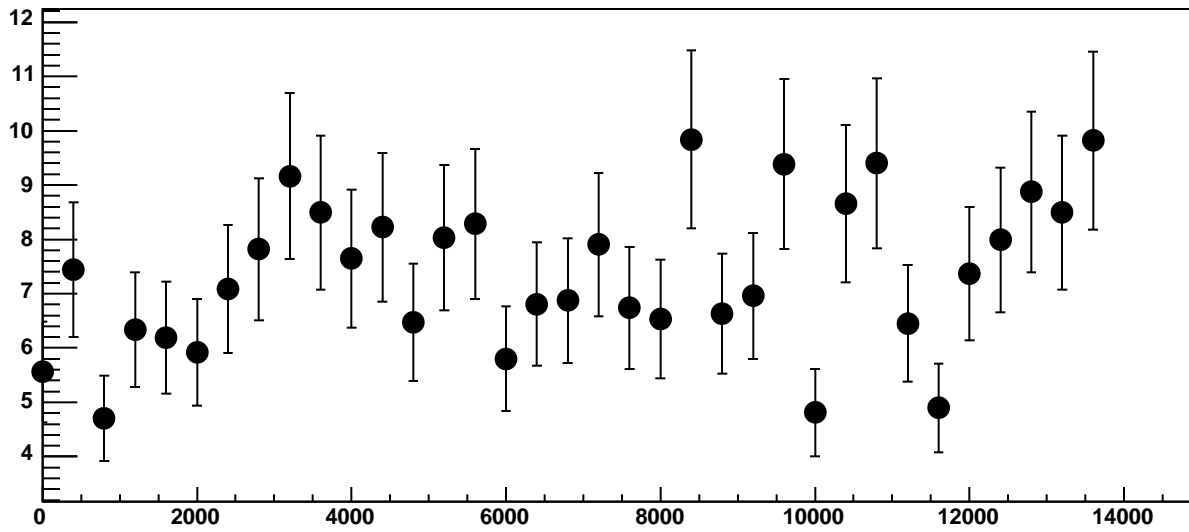


Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

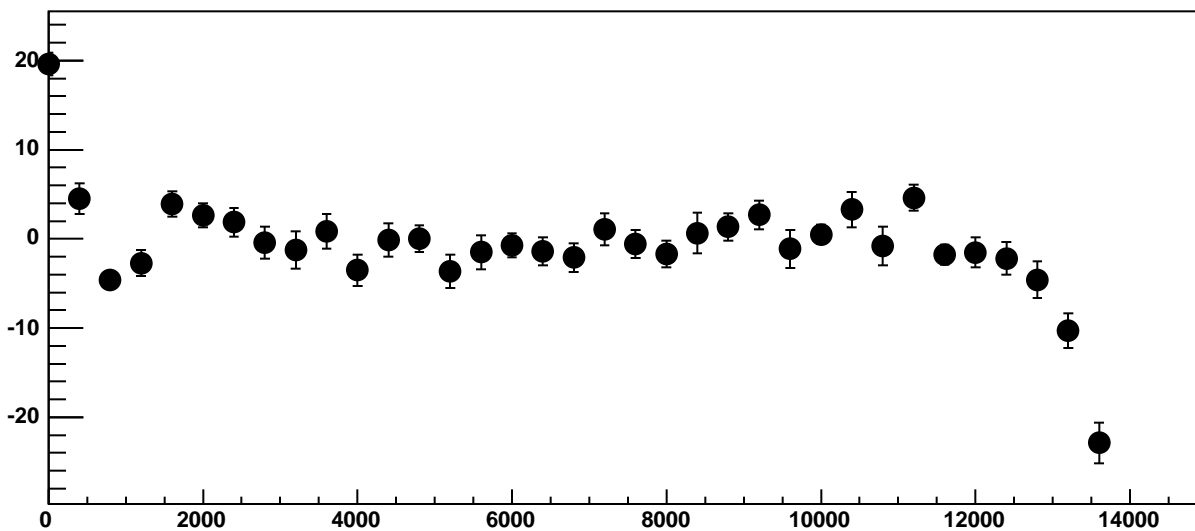


$\chi^2 / \text{ndf}$  37.02 / 23  
p0  $-387.1 \pm 0.7336$   
p1  $0.03381 \pm 0.0001109$

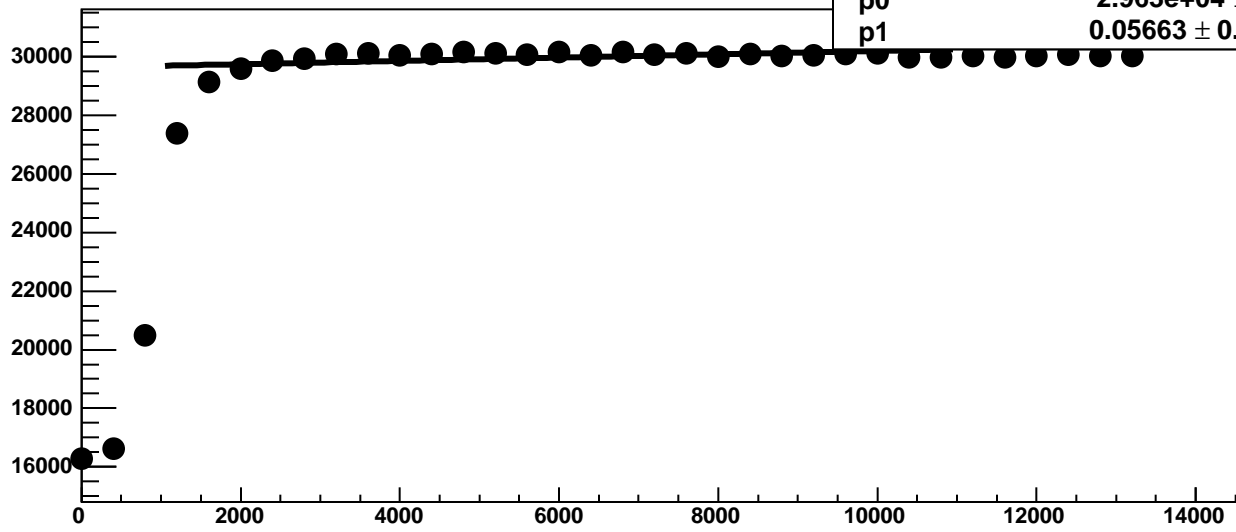
Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1855 / 23

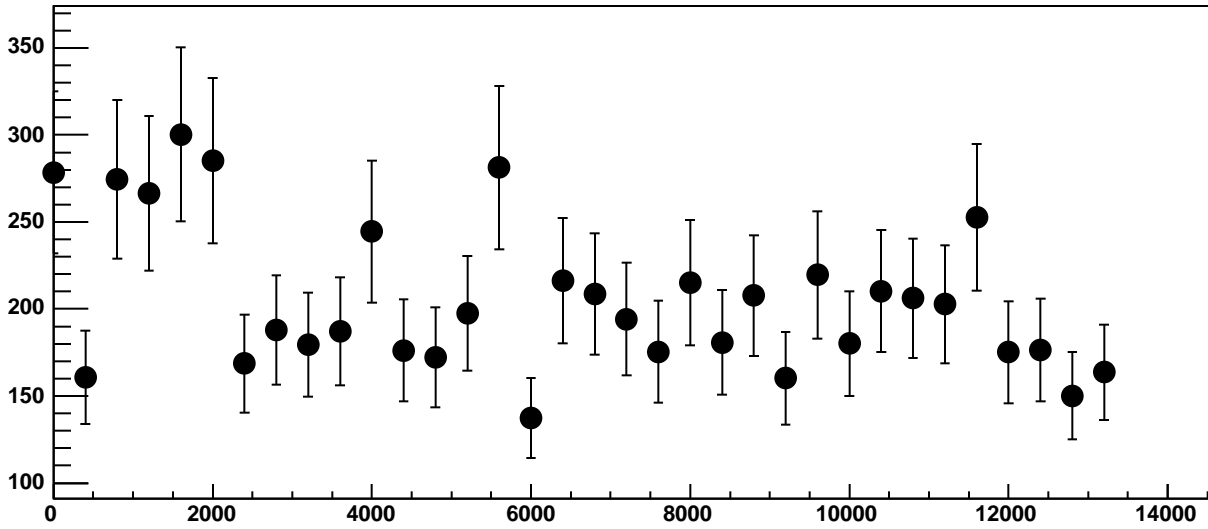
p0

$2.963e+04 \pm 22.45$

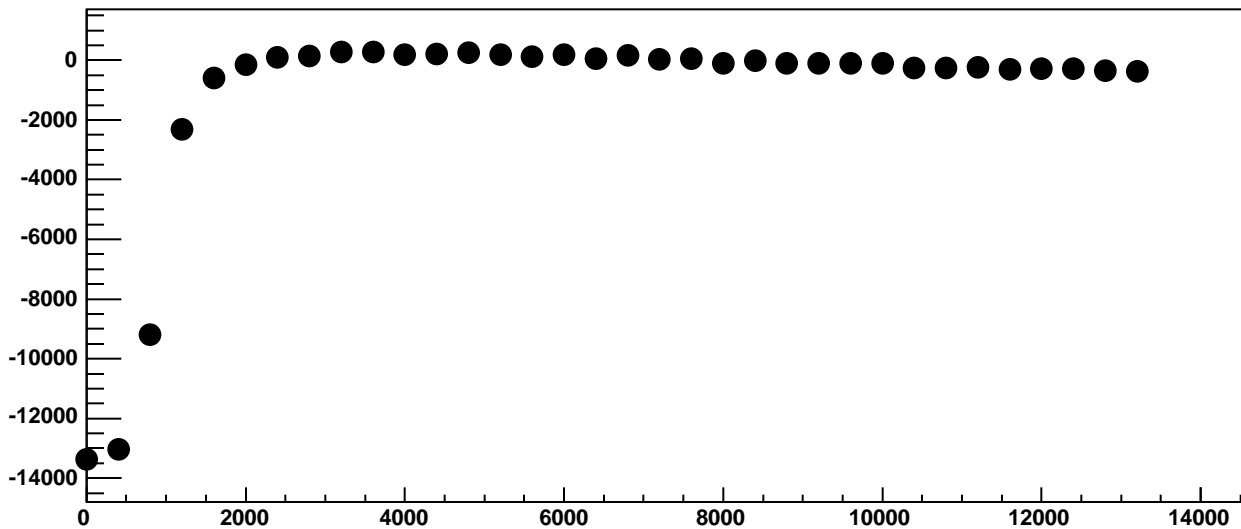
p1

$0.05663 \pm 0.003311$

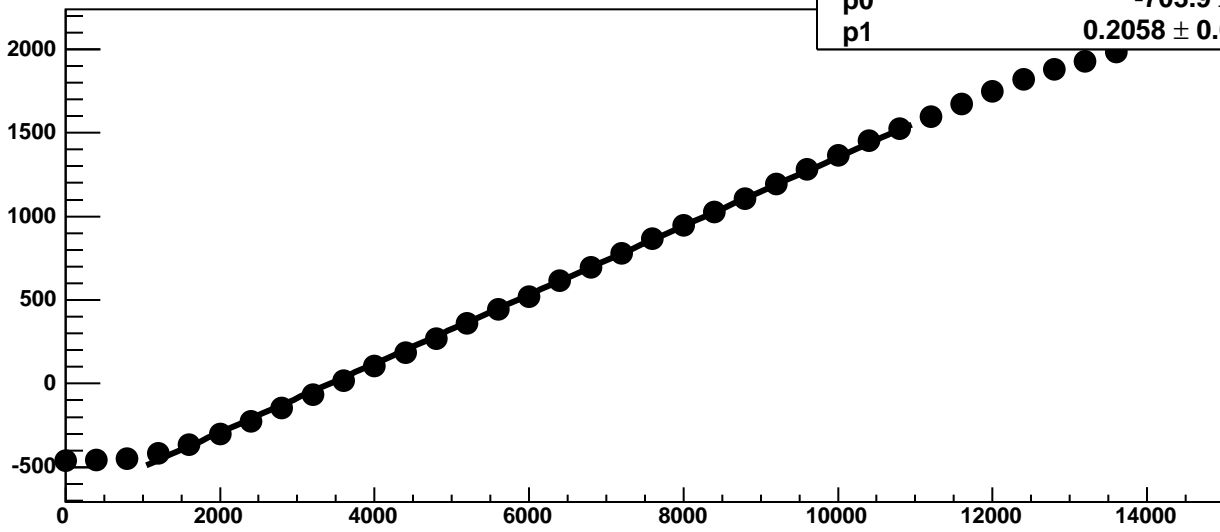
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



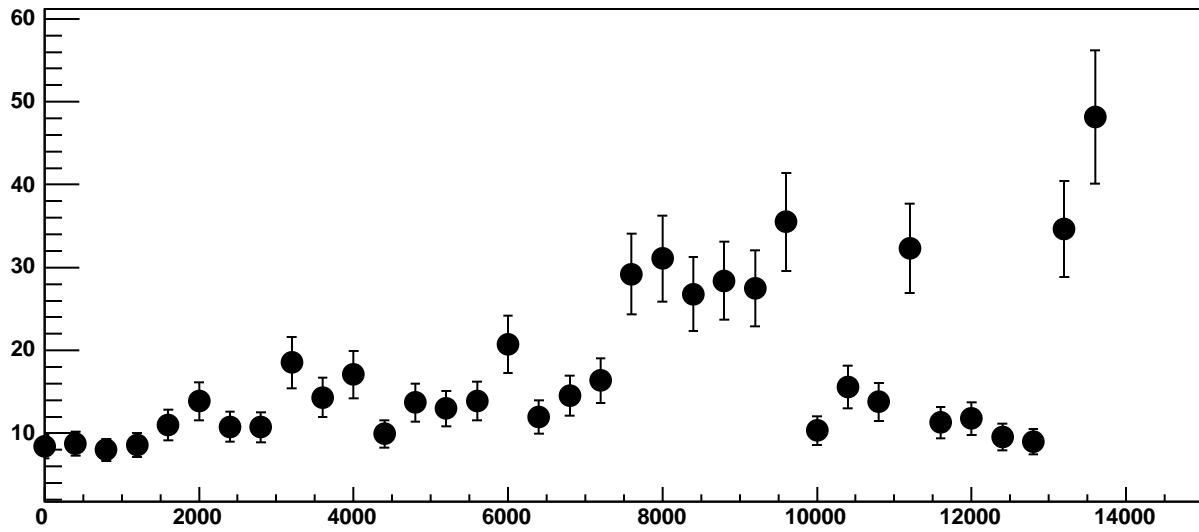
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC



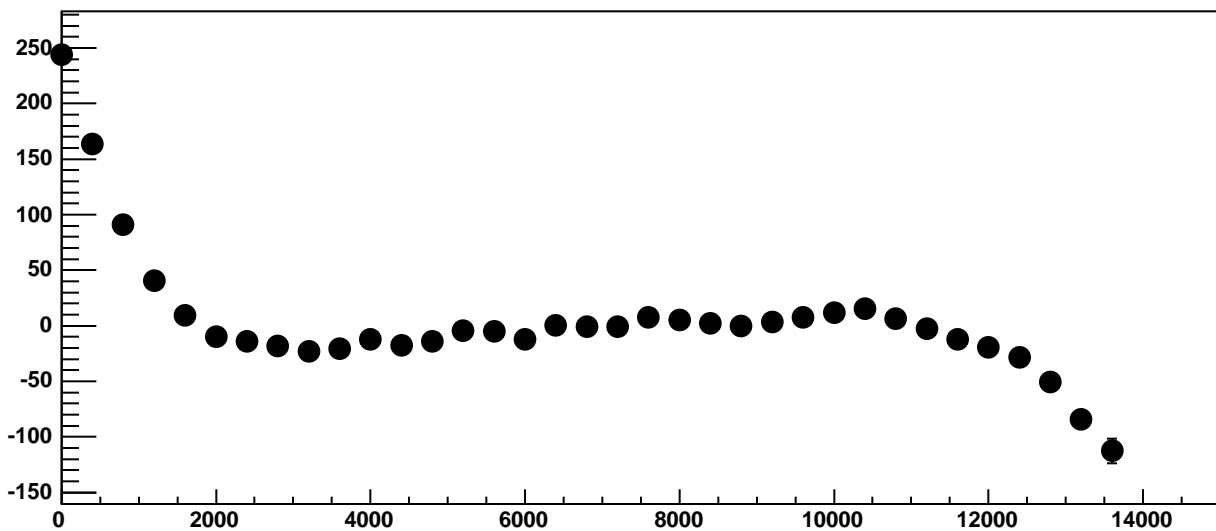
Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC



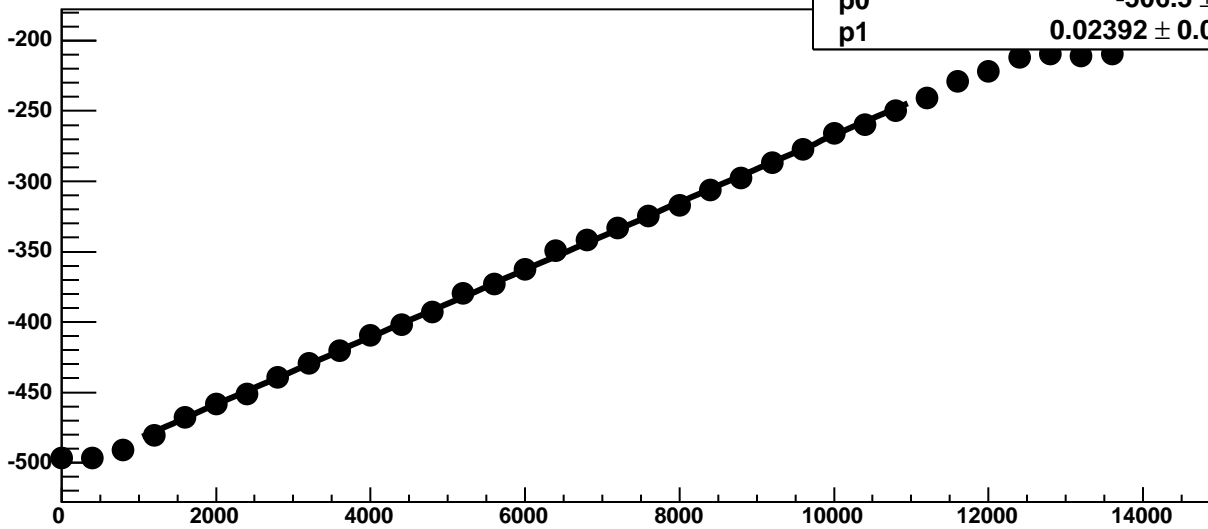
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



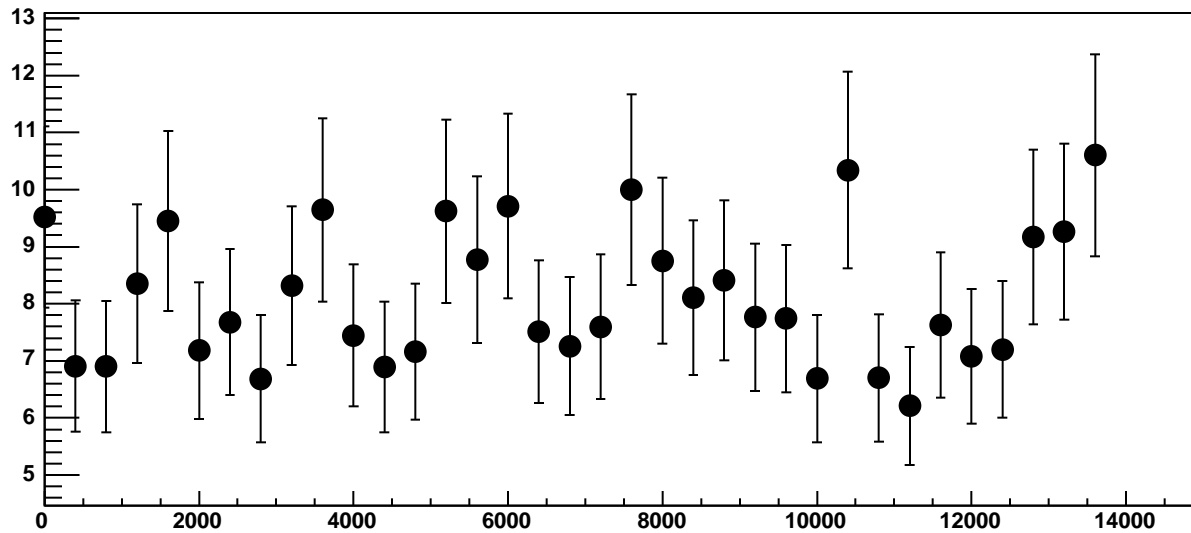
Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC



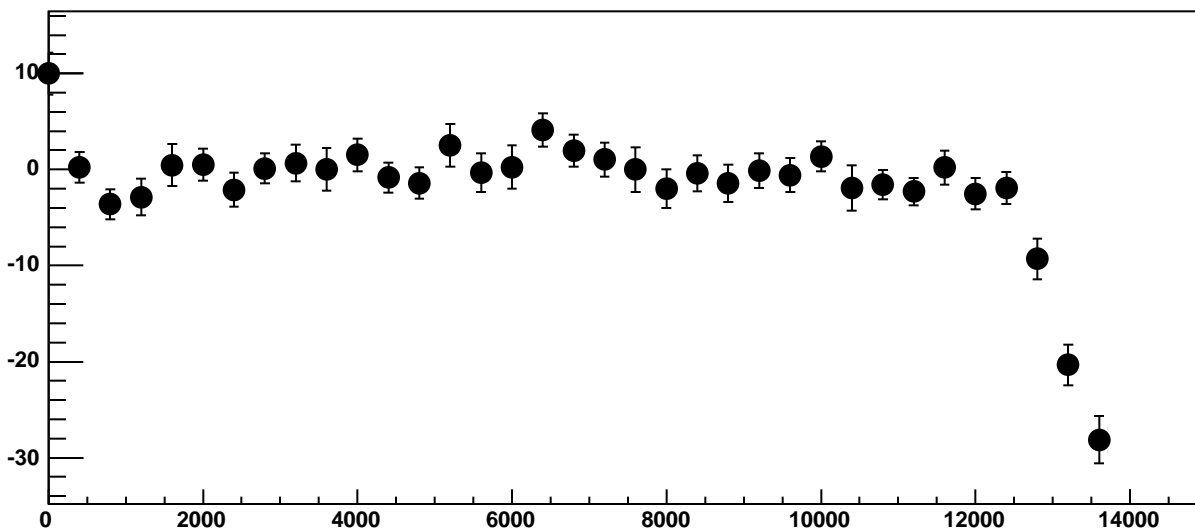
Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



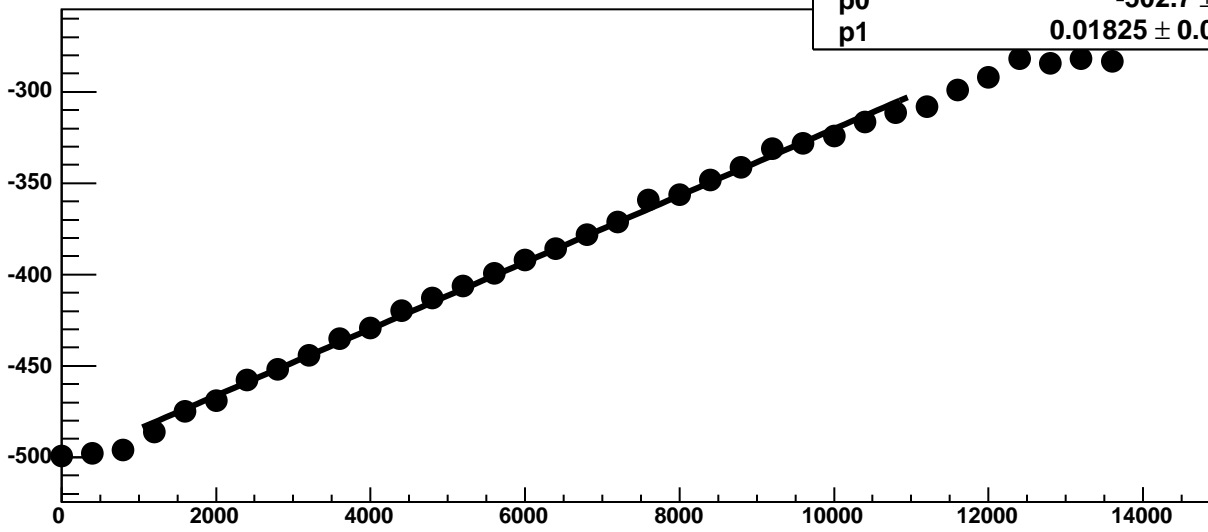
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



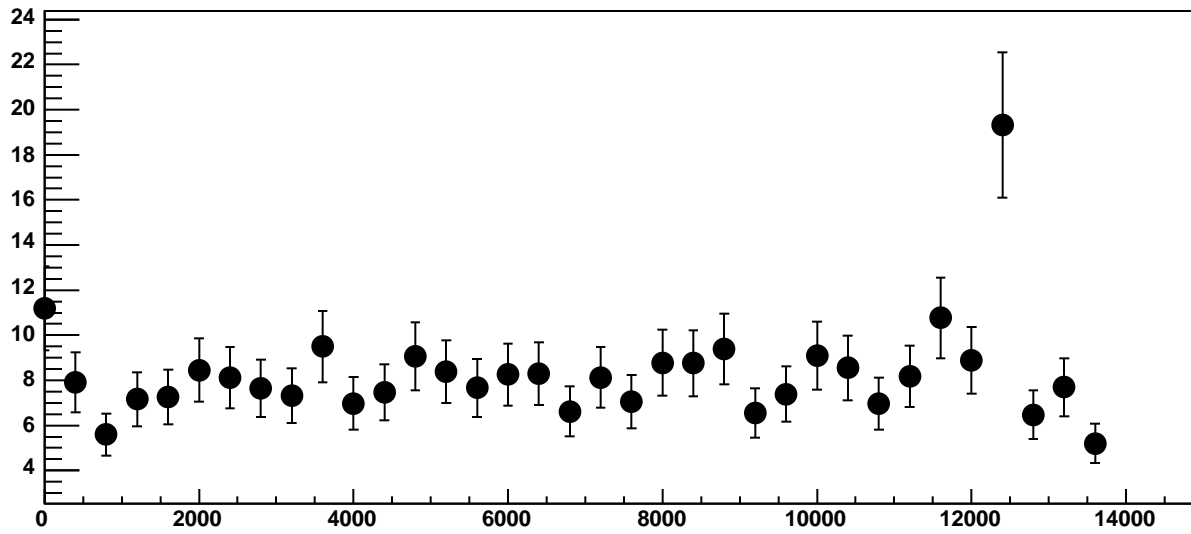
Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



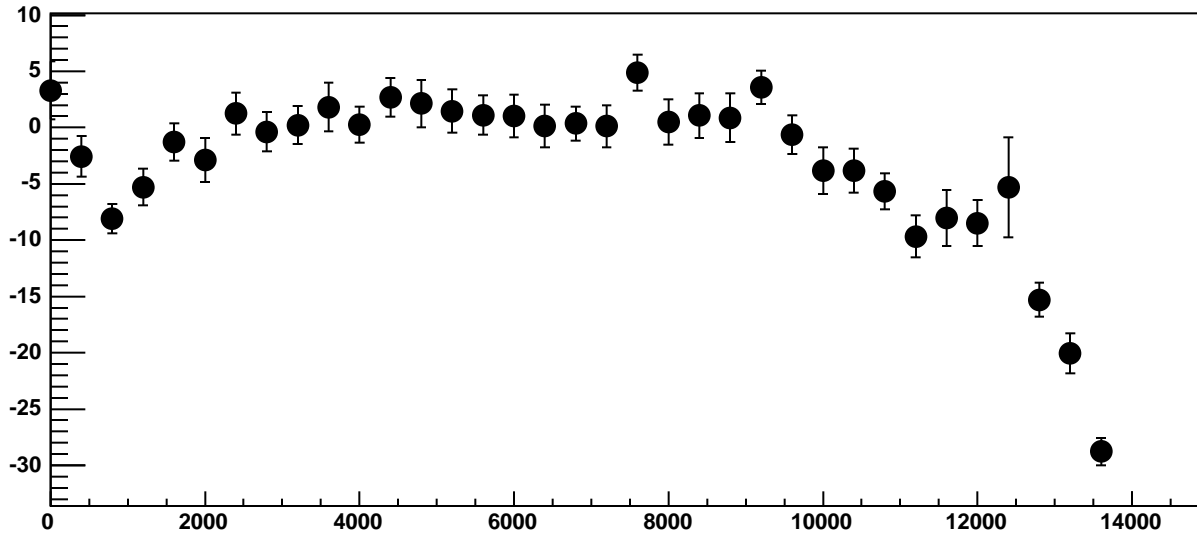
Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



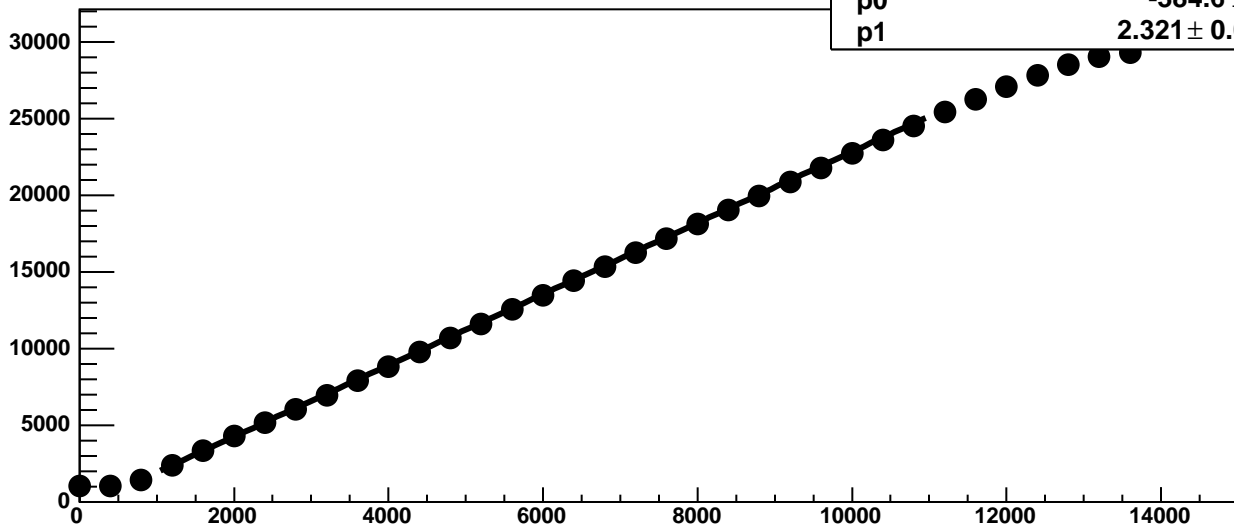
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



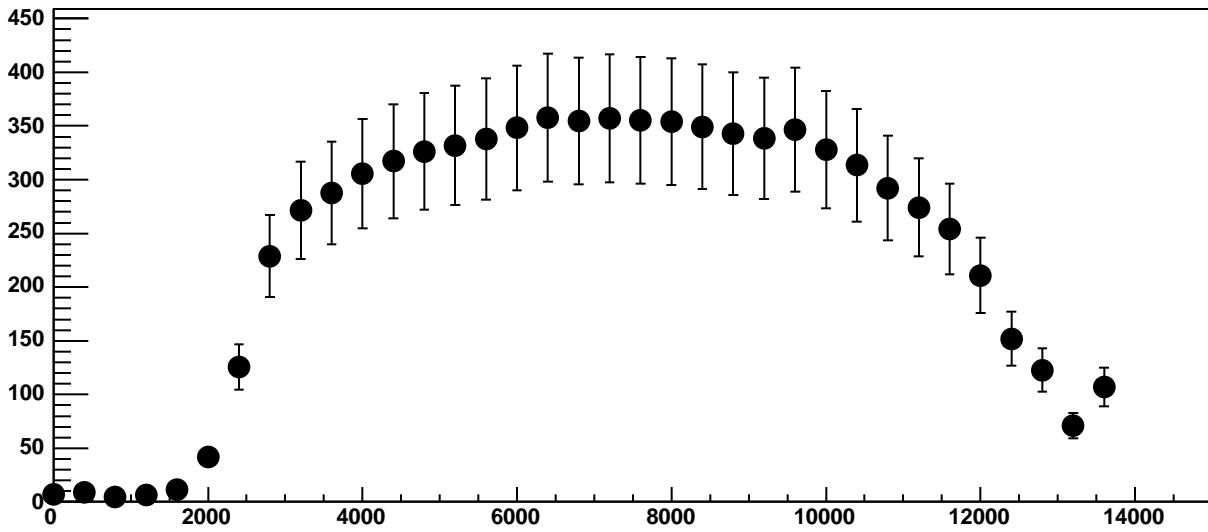
Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



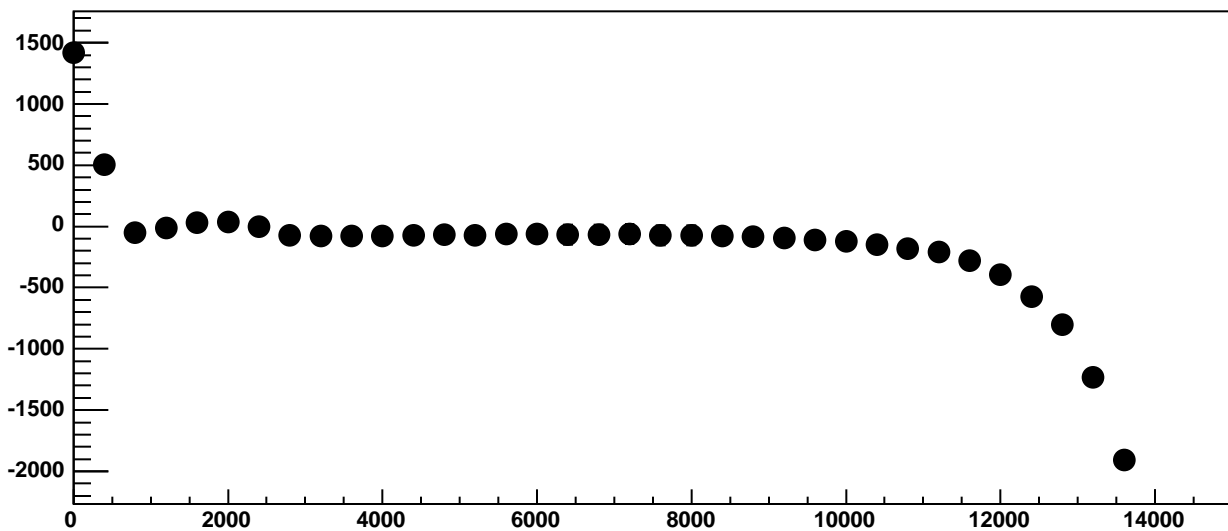
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



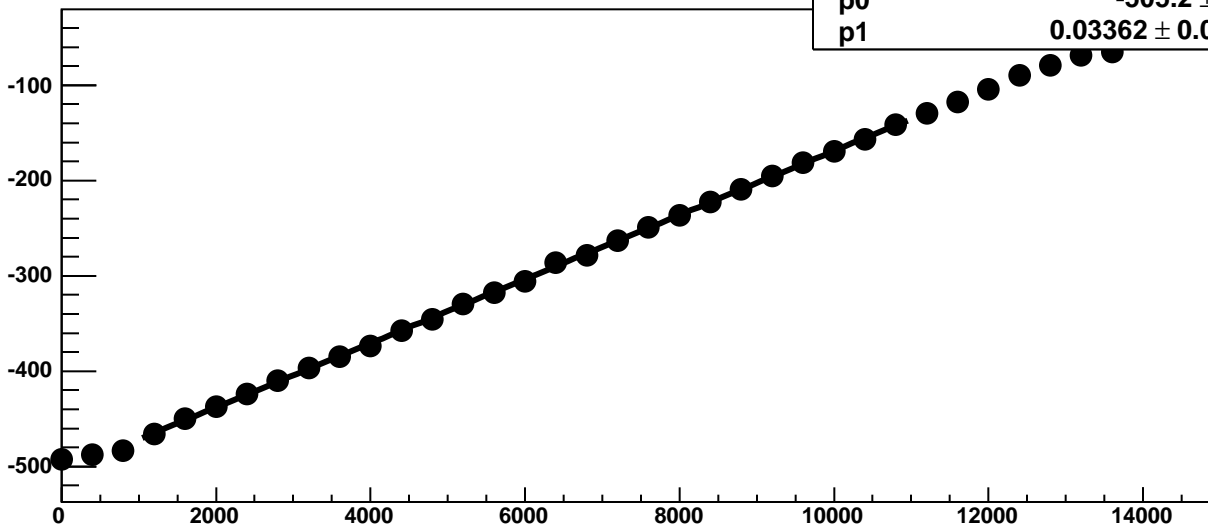
Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

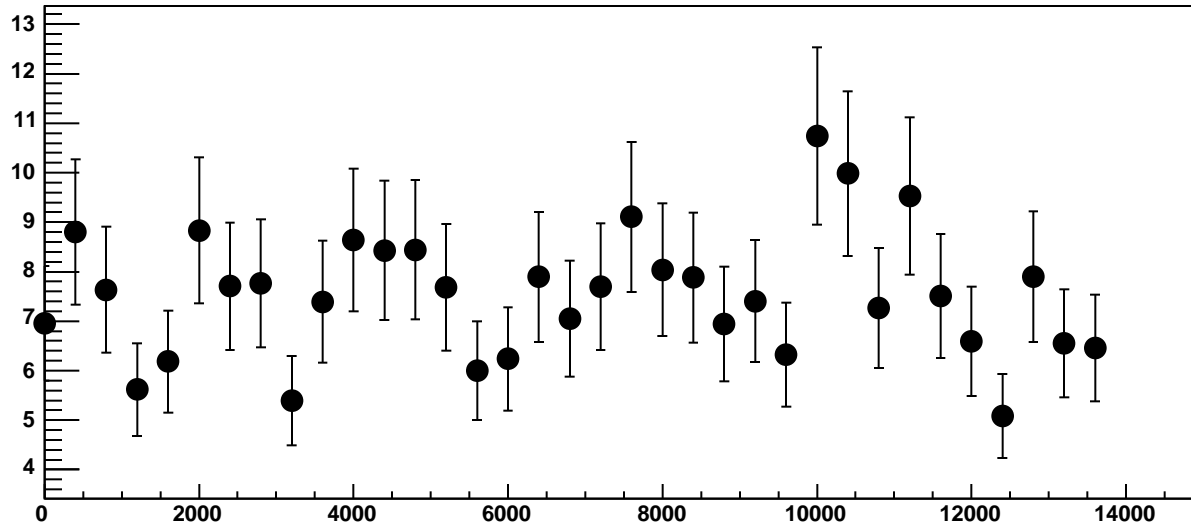


Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

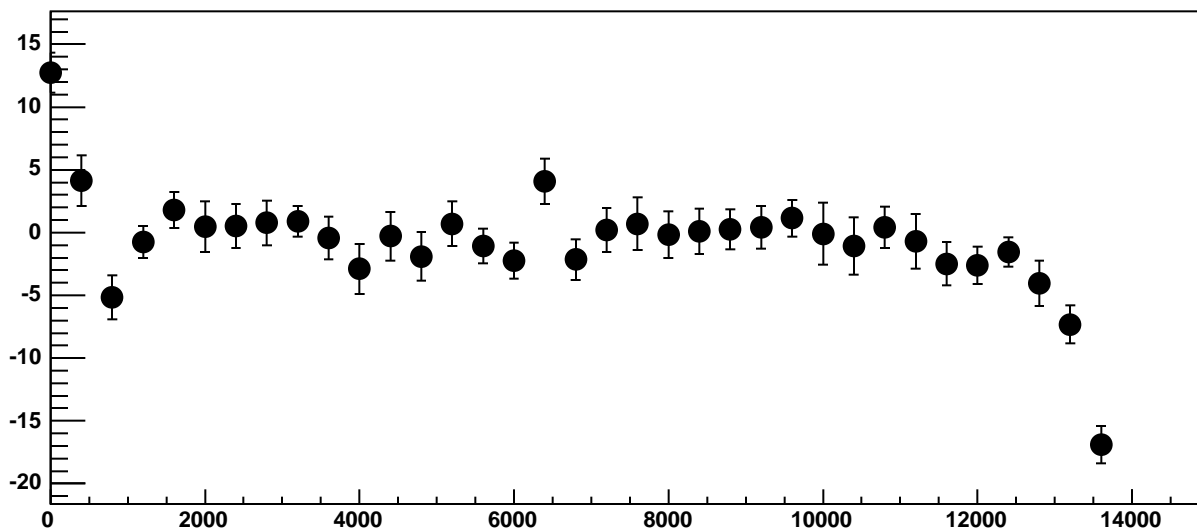


$\chi^2 / \text{ndf}$  17.2 / 23  
p0  $-505.2 \pm 0.7357$   
p1  $0.03362 \pm 0.0001155$

Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

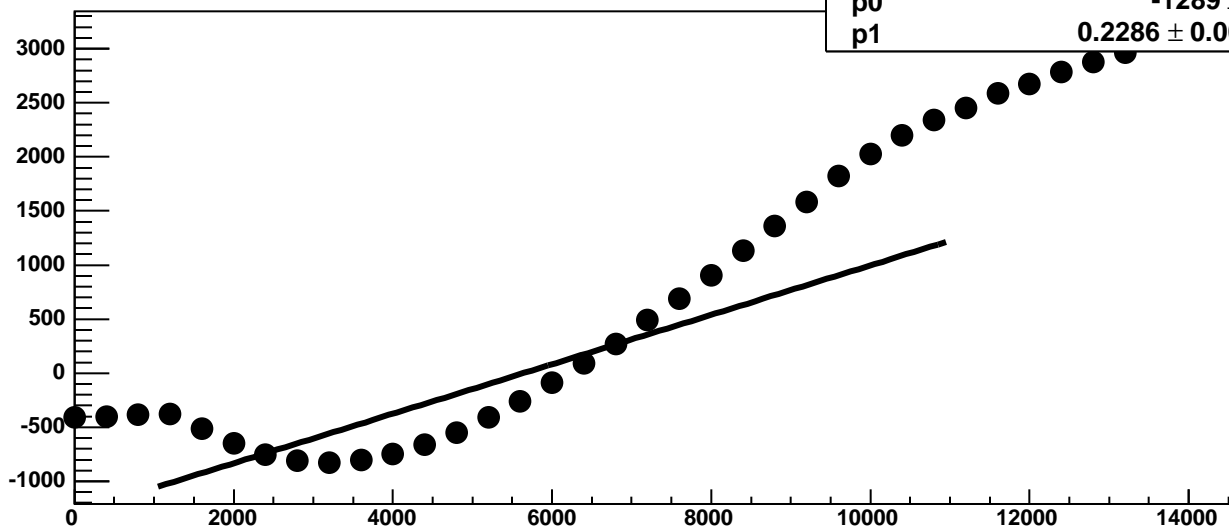


Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



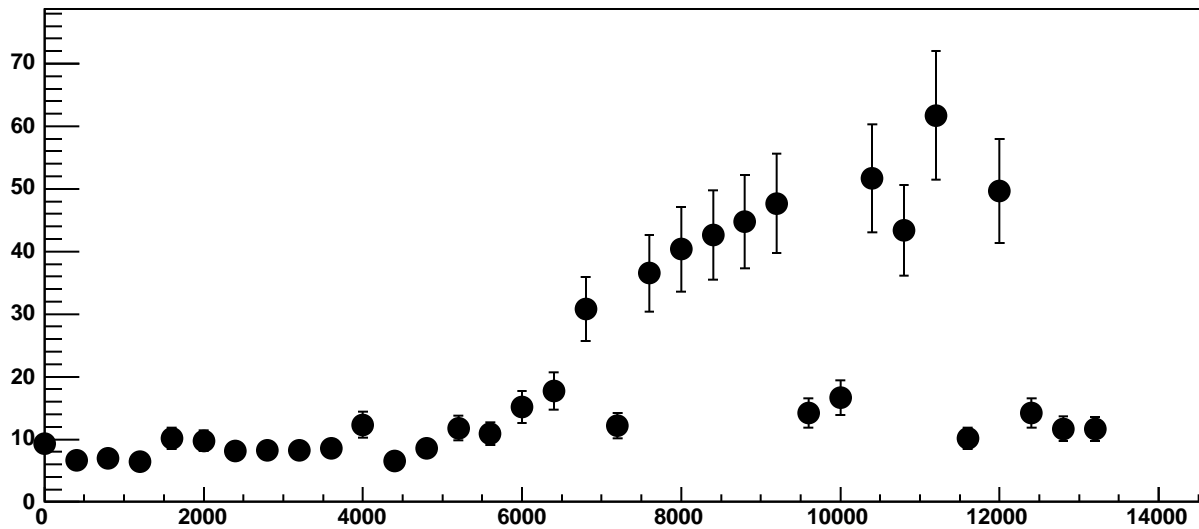


Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

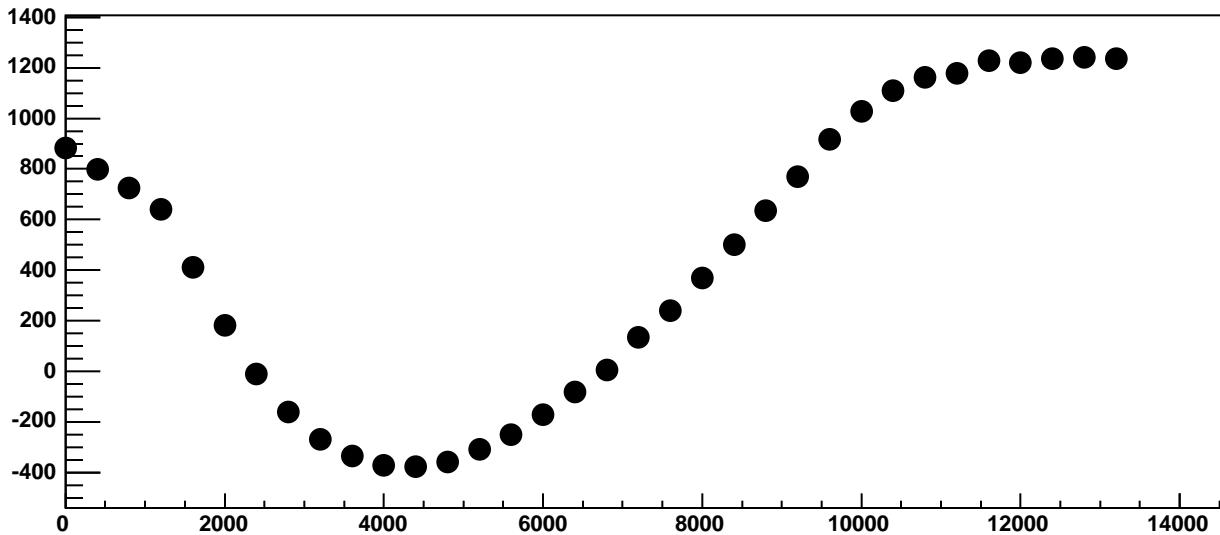


$\chi^2 / \text{ndf}$  6.099e+05 / 23  
p0 -1289 ± 1.085  
p1 0.2286 ± 0.0002432

Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

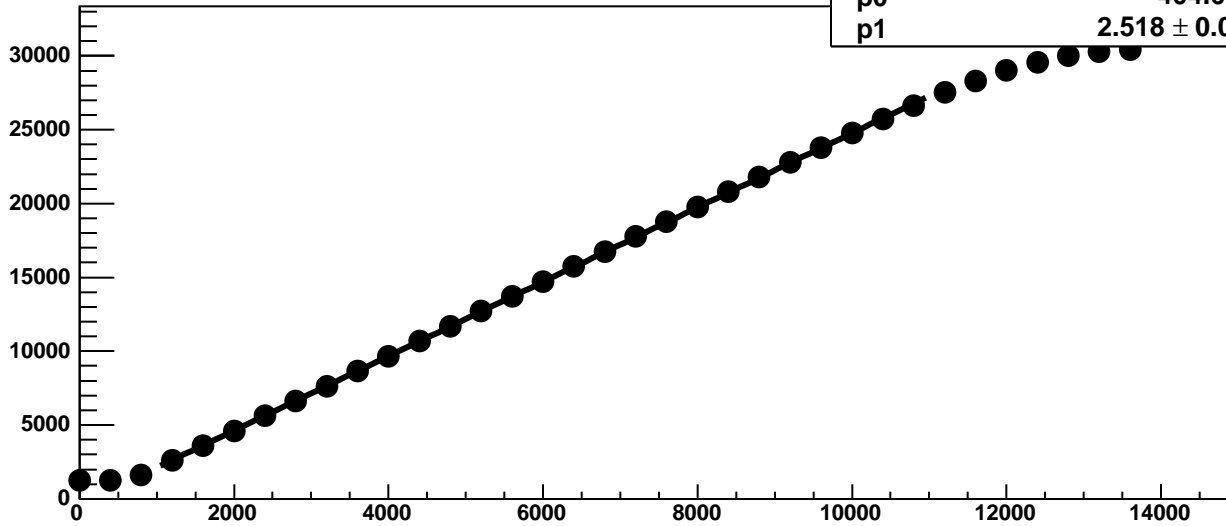


Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

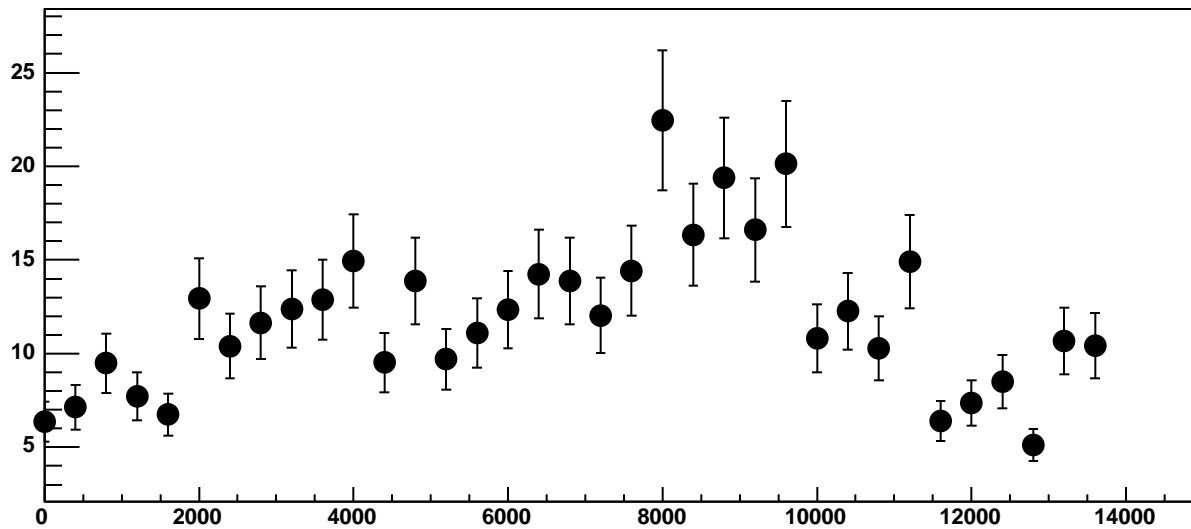


Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

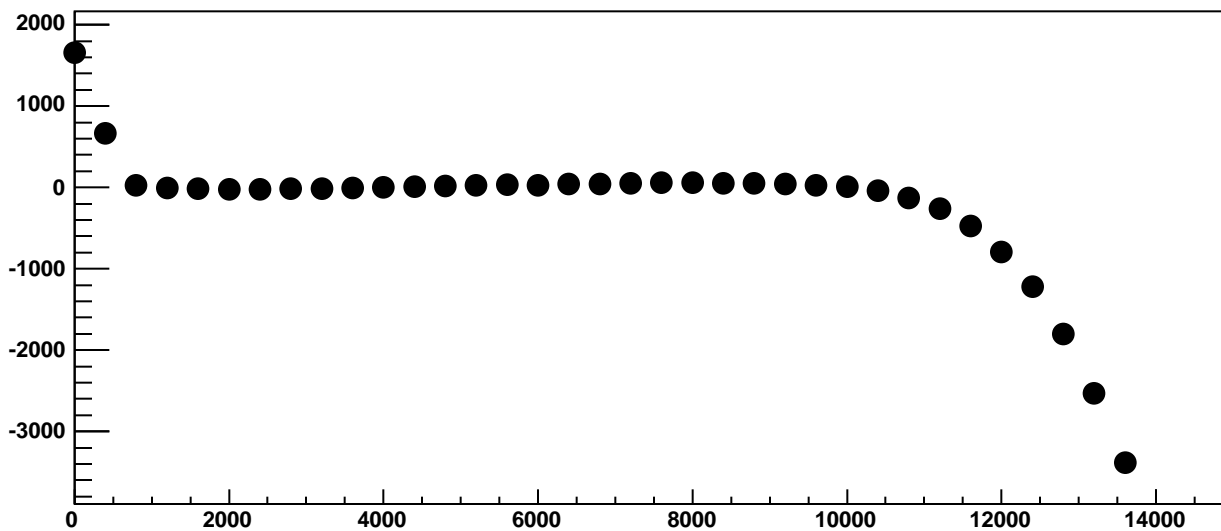
$\chi^2 / \text{ndf}$  5553 / 23  
p0  $-404.6 \pm 1.039$   
p1  $2.518 \pm 0.0001743$



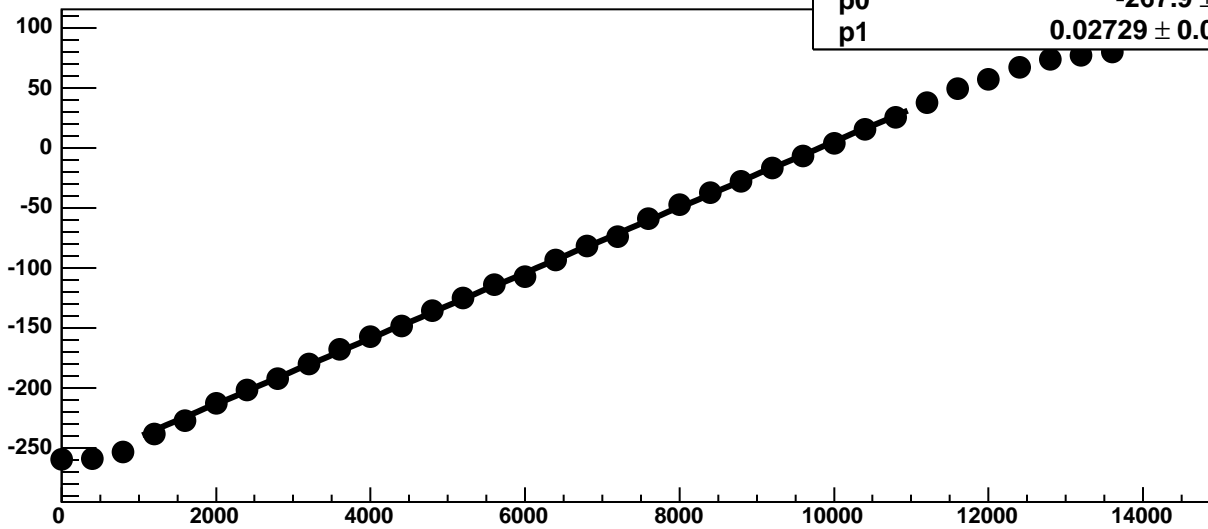
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



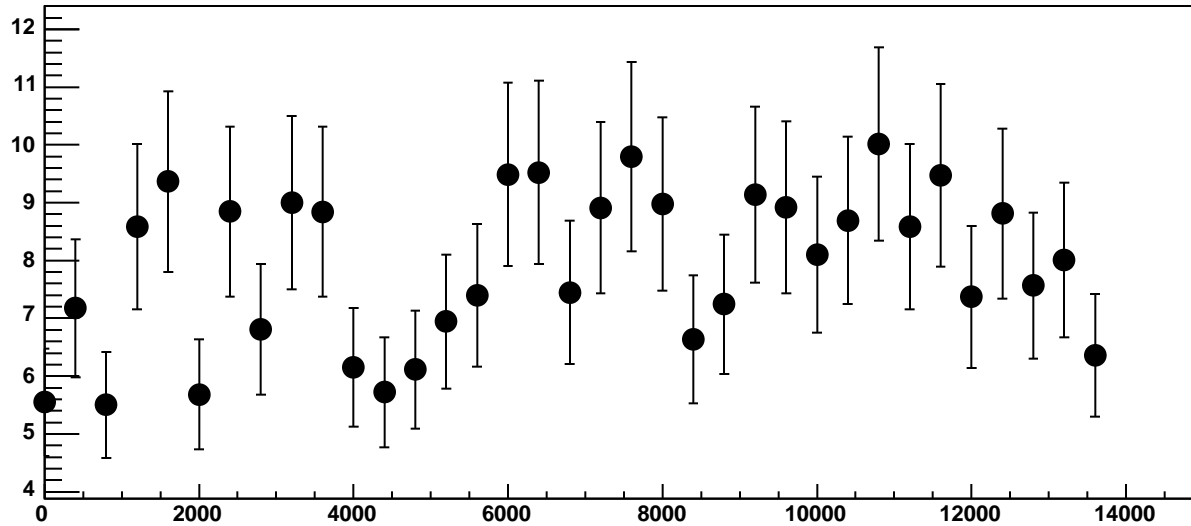
Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



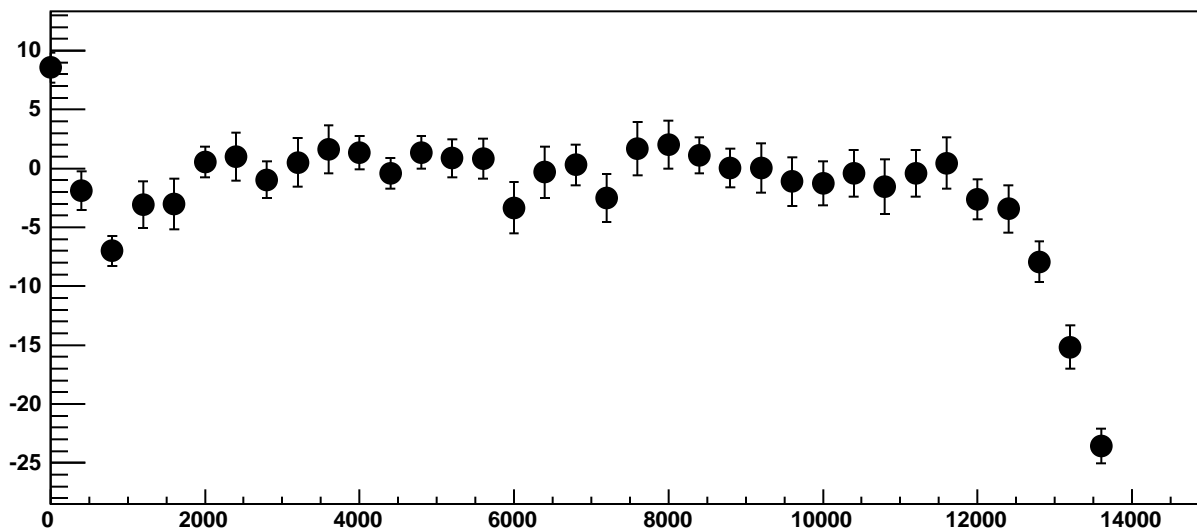
Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



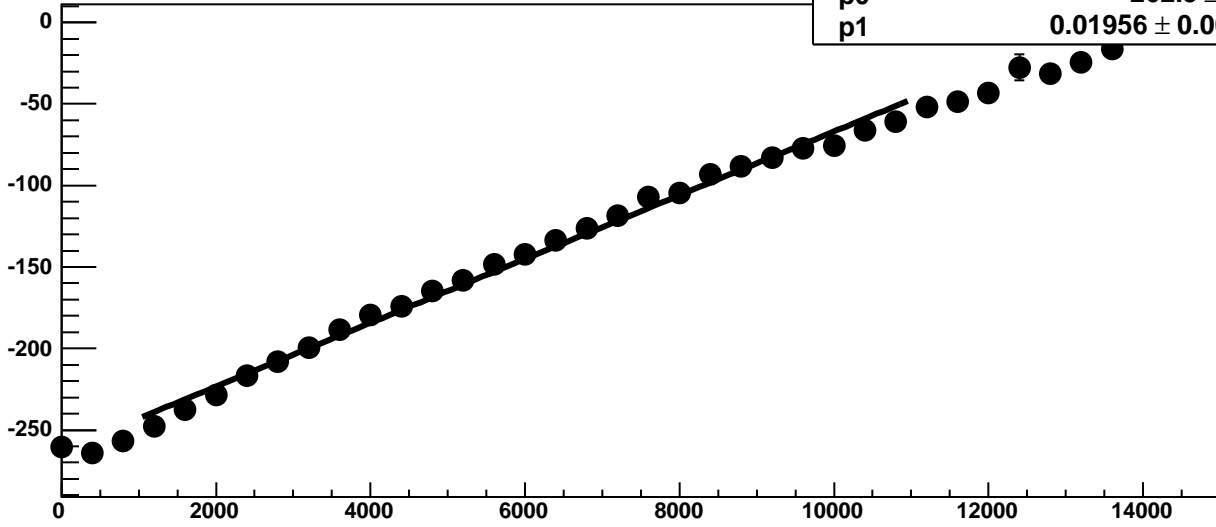
Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



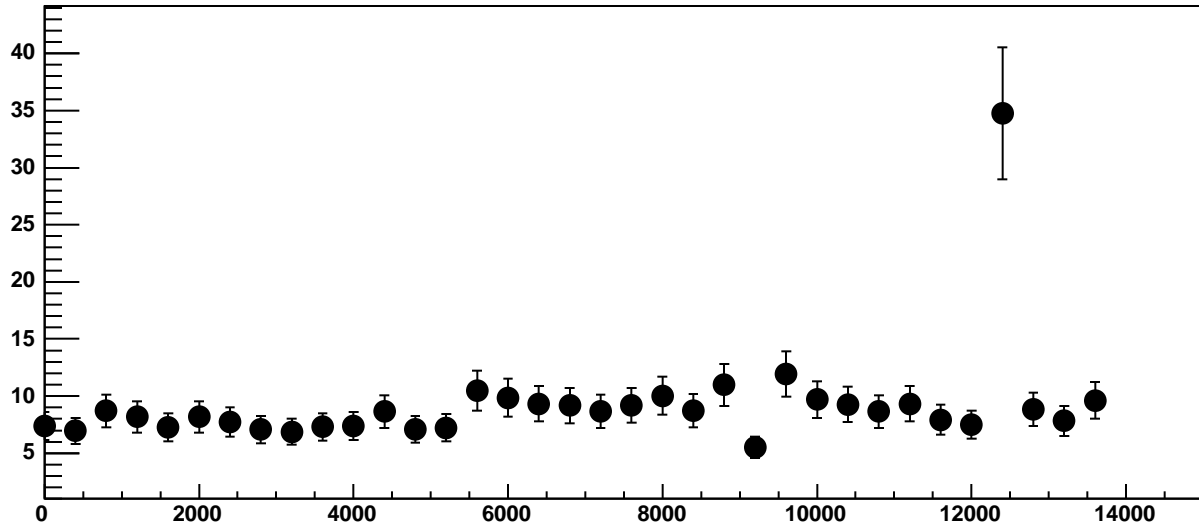
Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



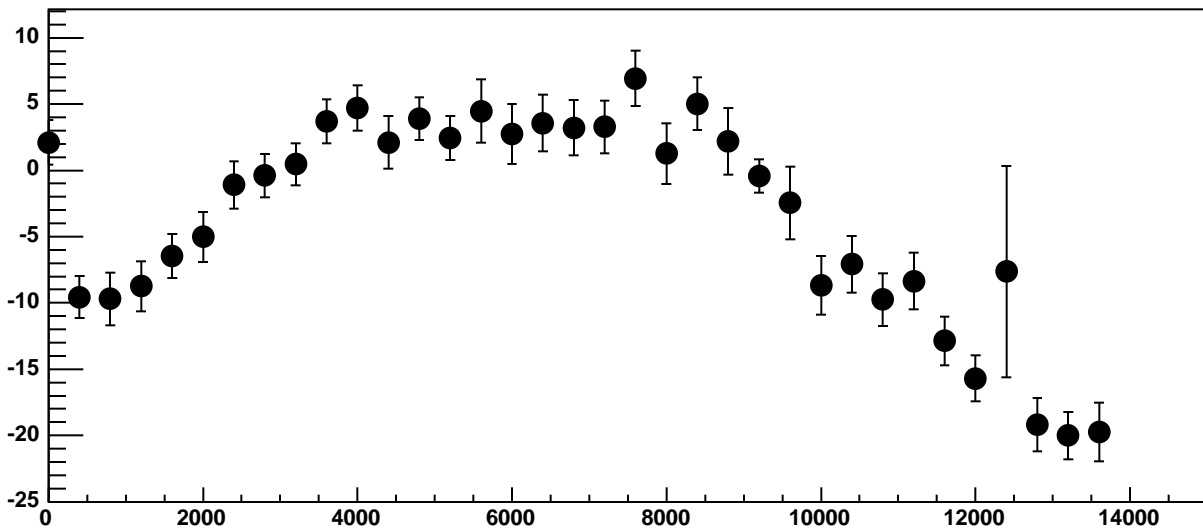
Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



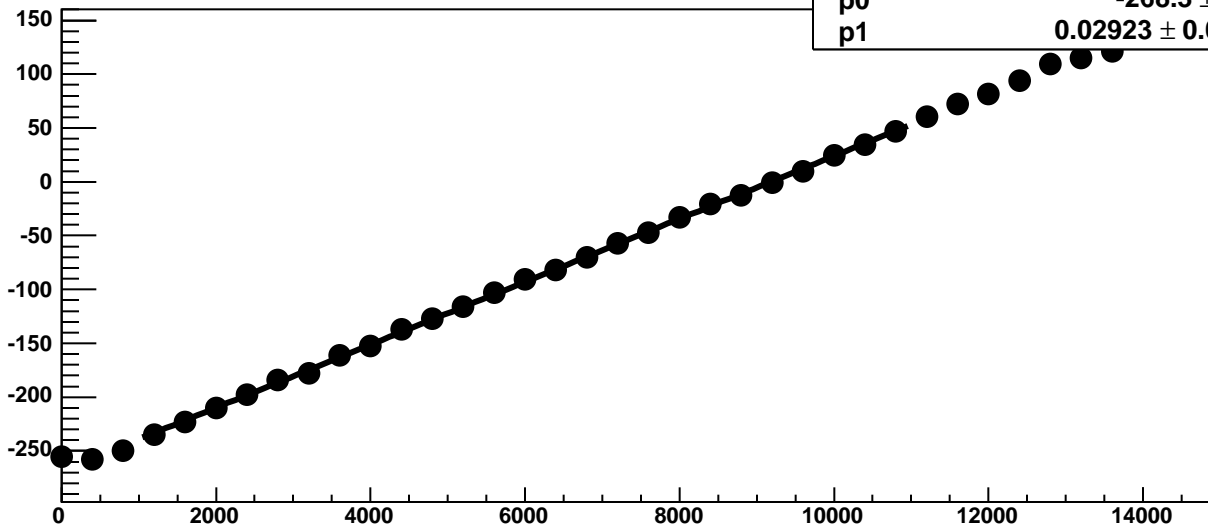
Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

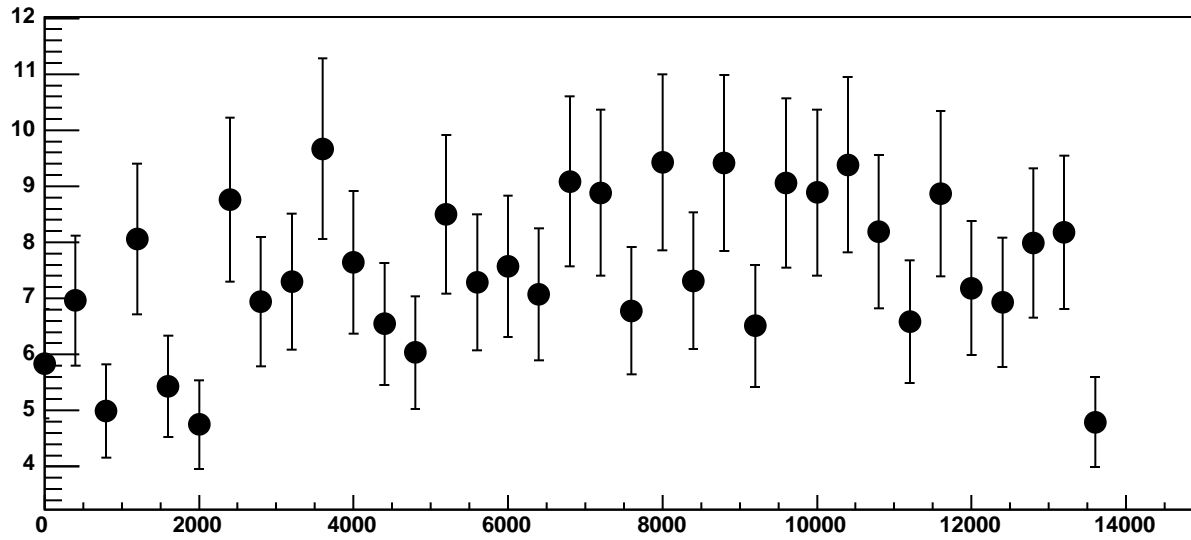


Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

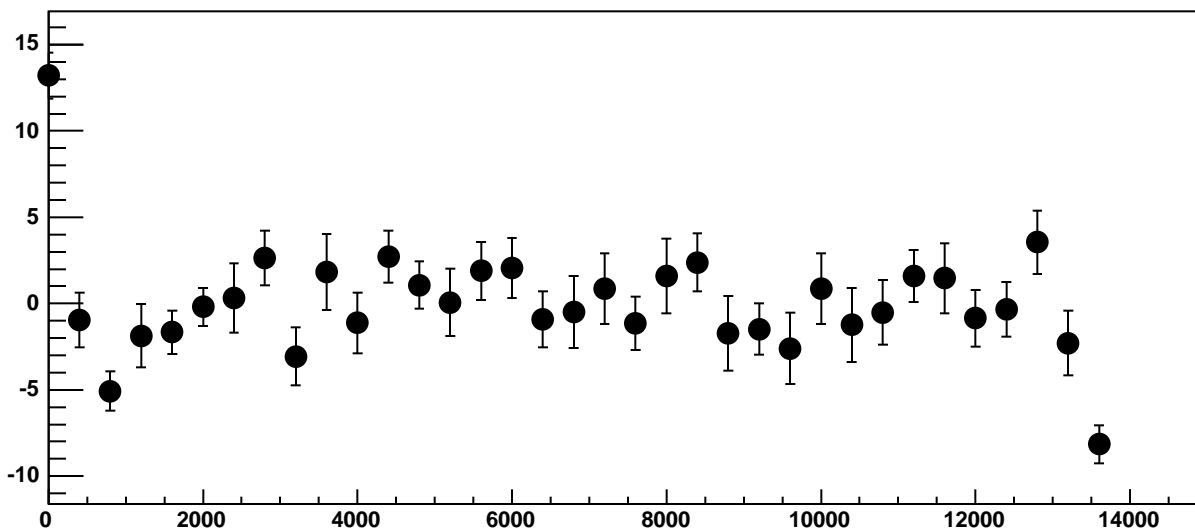


$\chi^2 / \text{ndf}$  23.96 / 23  
p0 -268.3 ± 0.7215  
p1 0.02923 ± 0.0001171

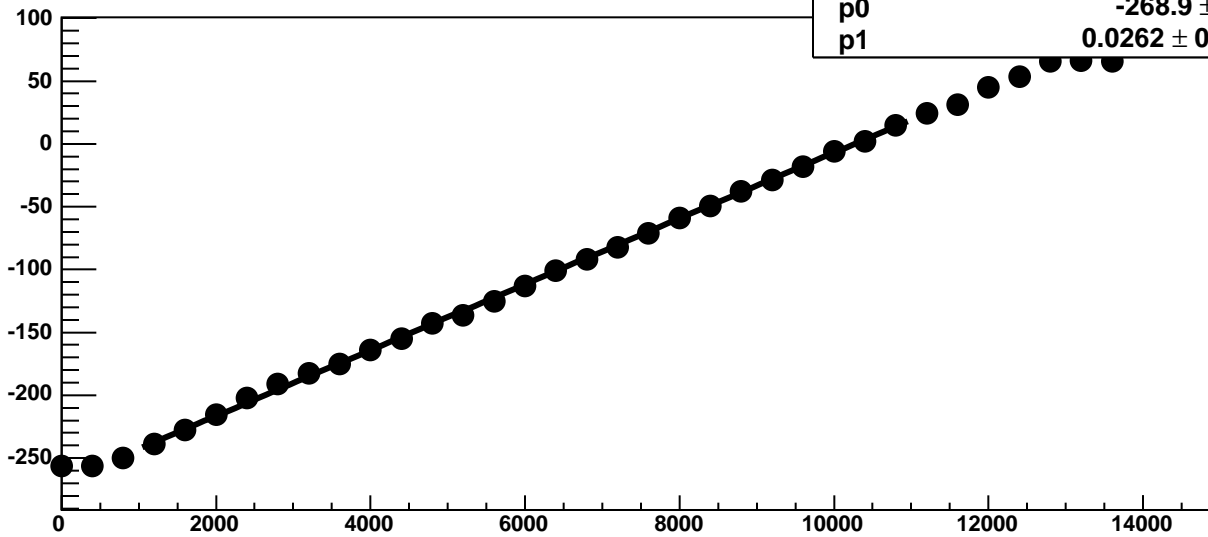
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

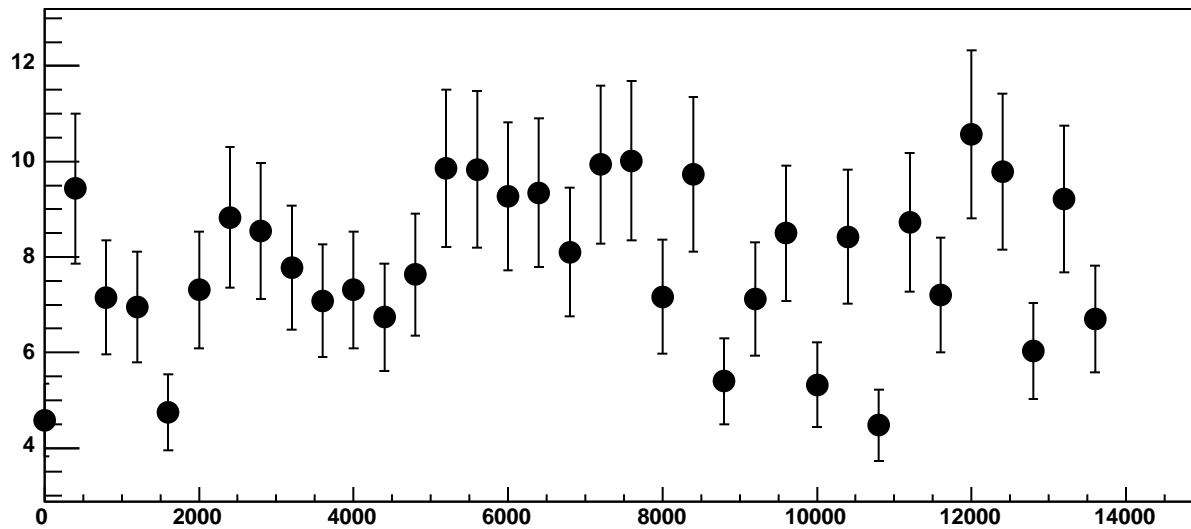


Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

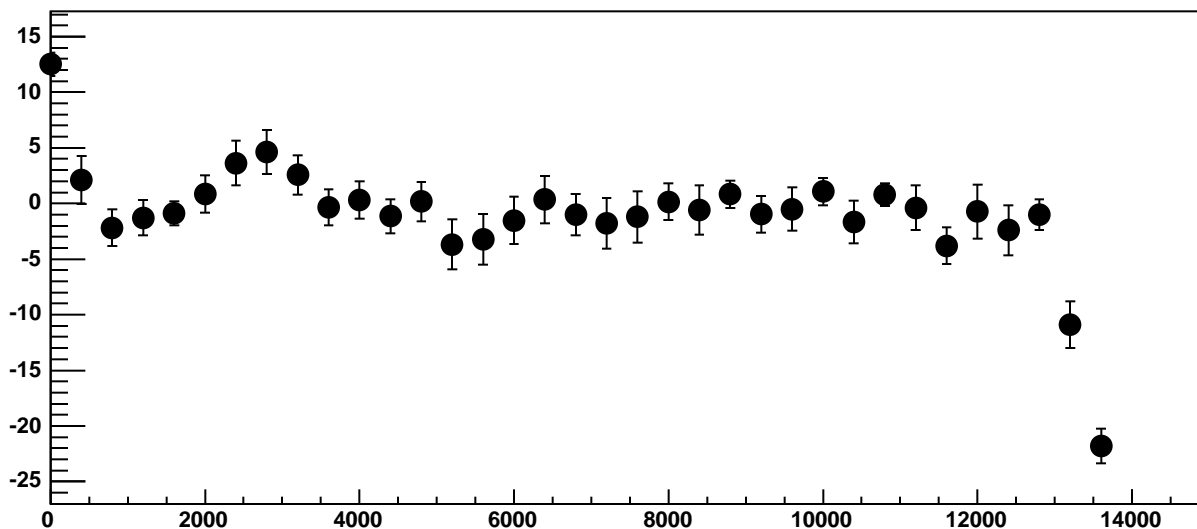


$\chi^2 / \text{ndf}$  22.49 / 23  
p0  $-268.9 \pm 0.7093$   
p1  $0.0262 \pm 0.000101$

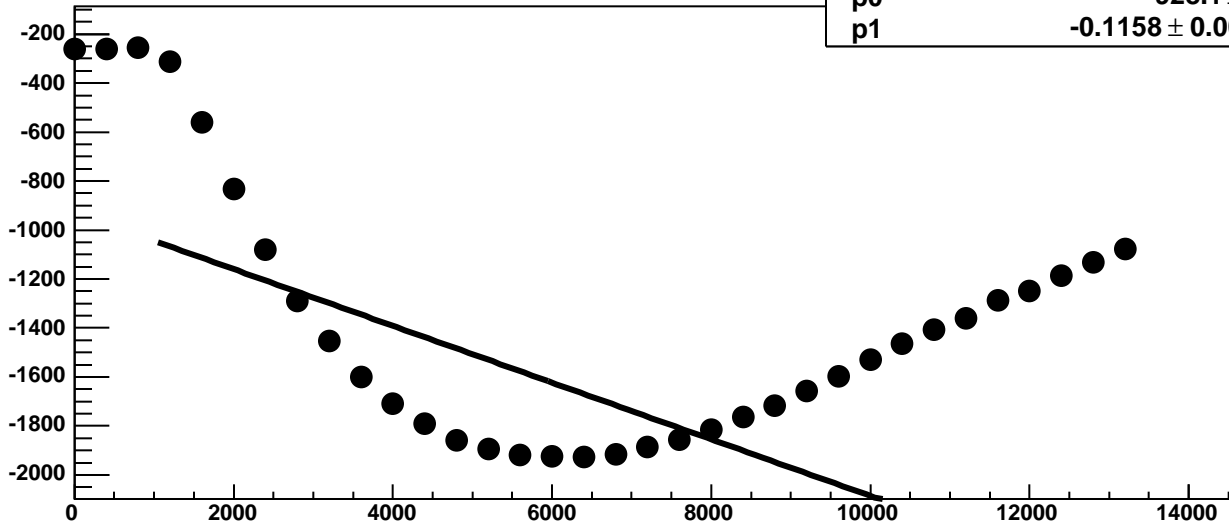
Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

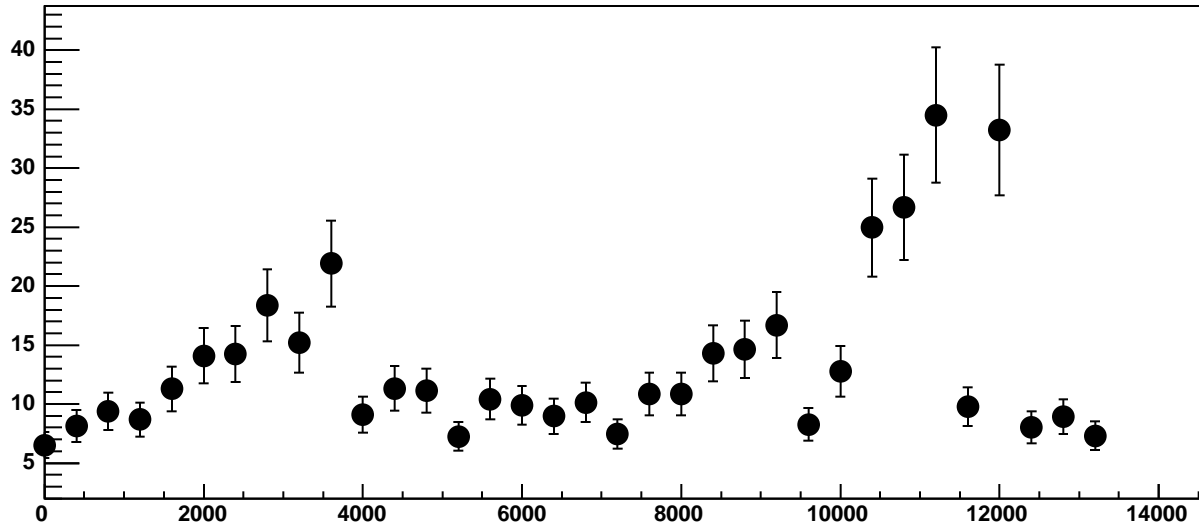


Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

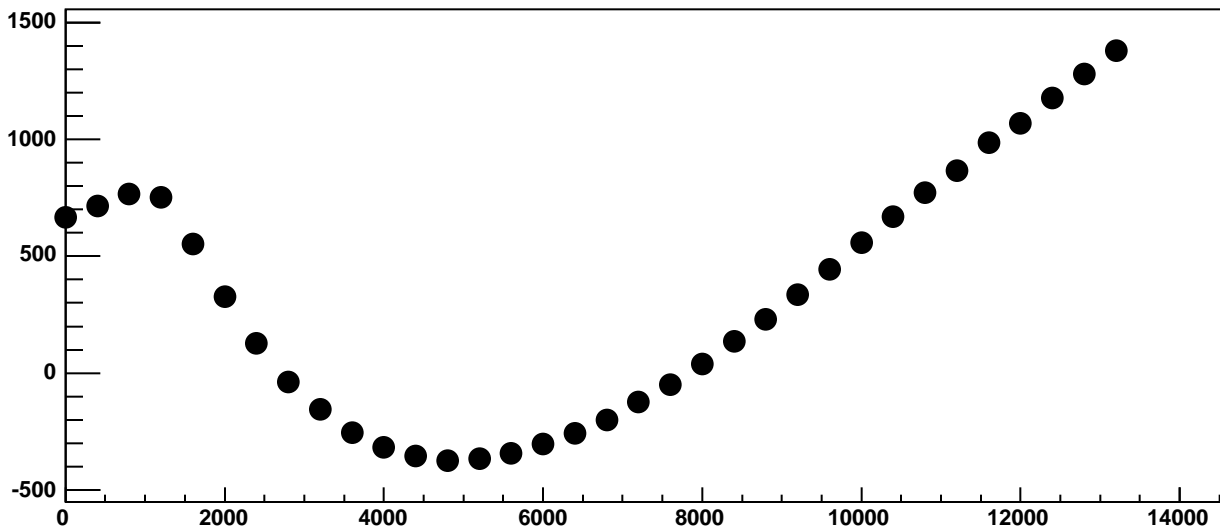


$\chi^2 / \text{ndf}$	5.181e+05 / 23
p0	-928.1 ± 1.275
p1	-0.1158 ± 0.0002006

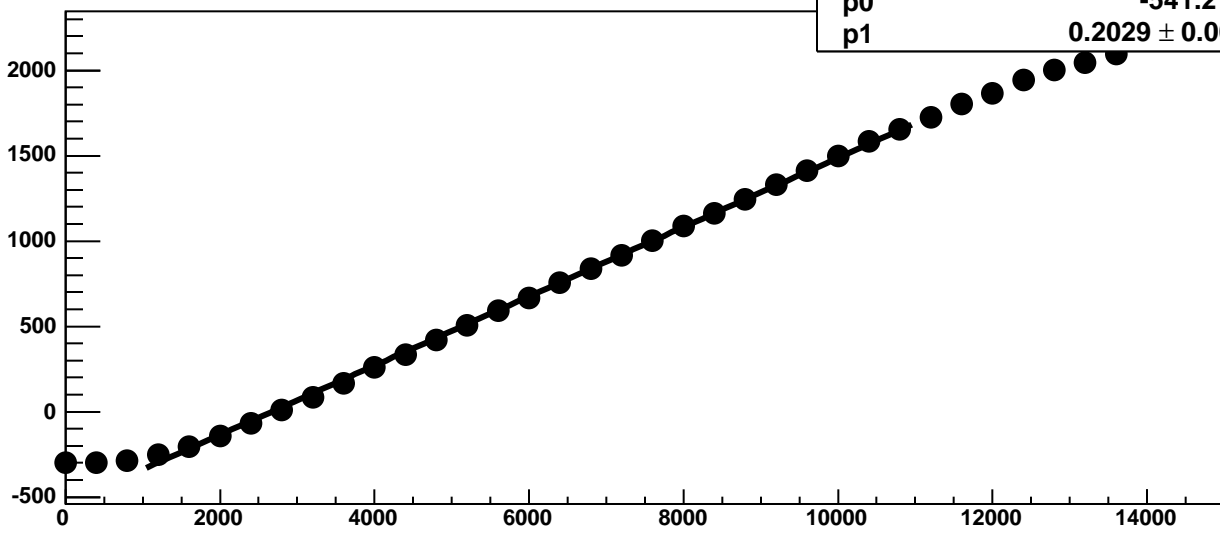
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



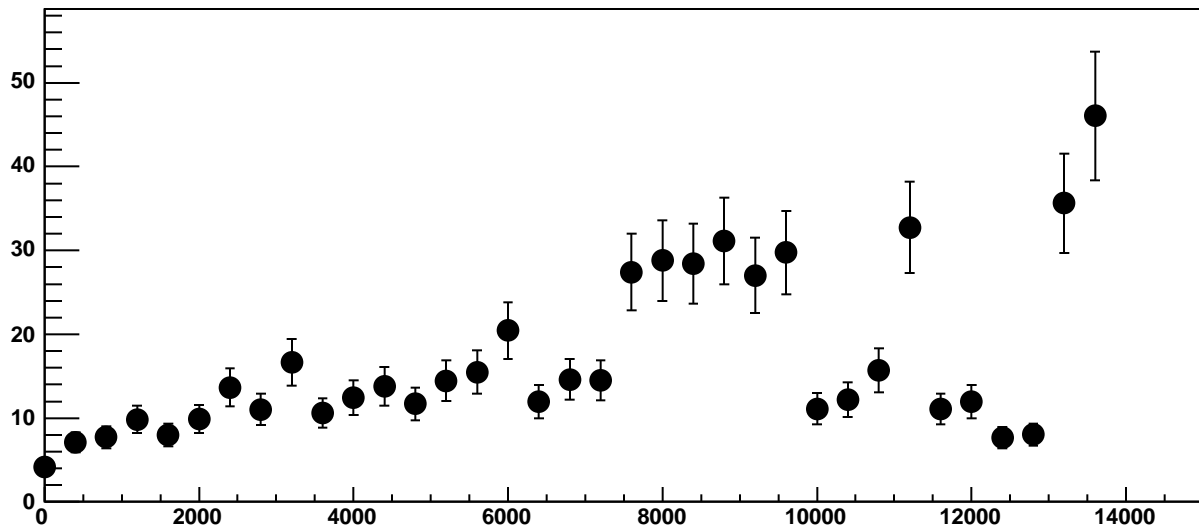
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



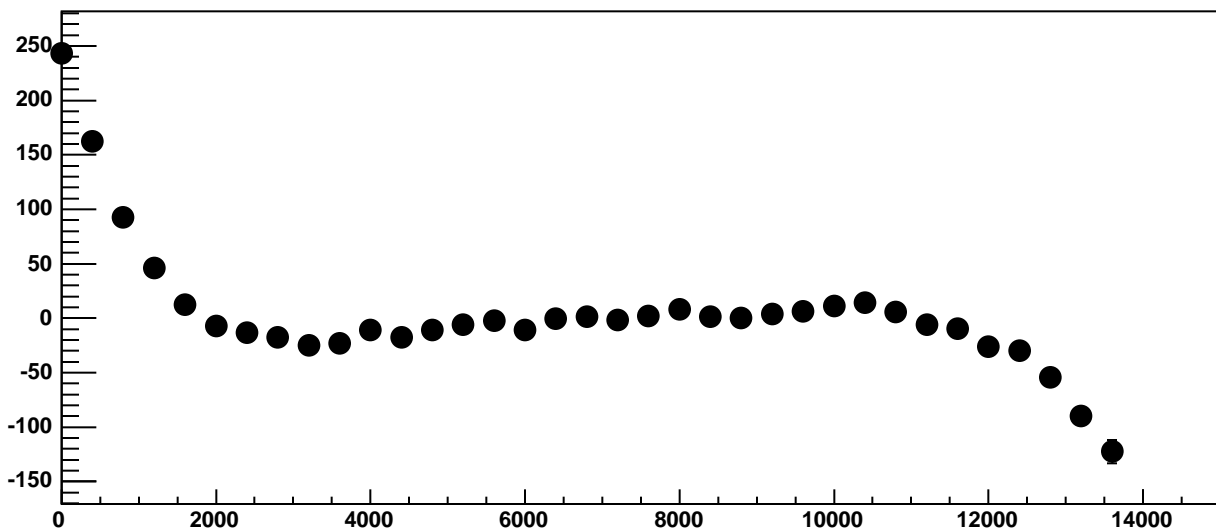
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

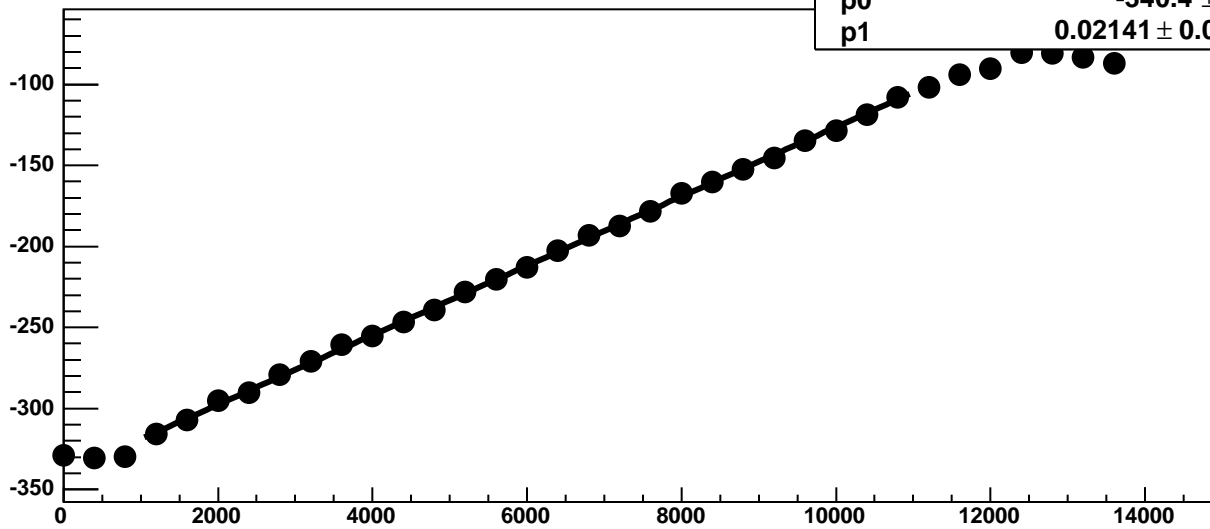


Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

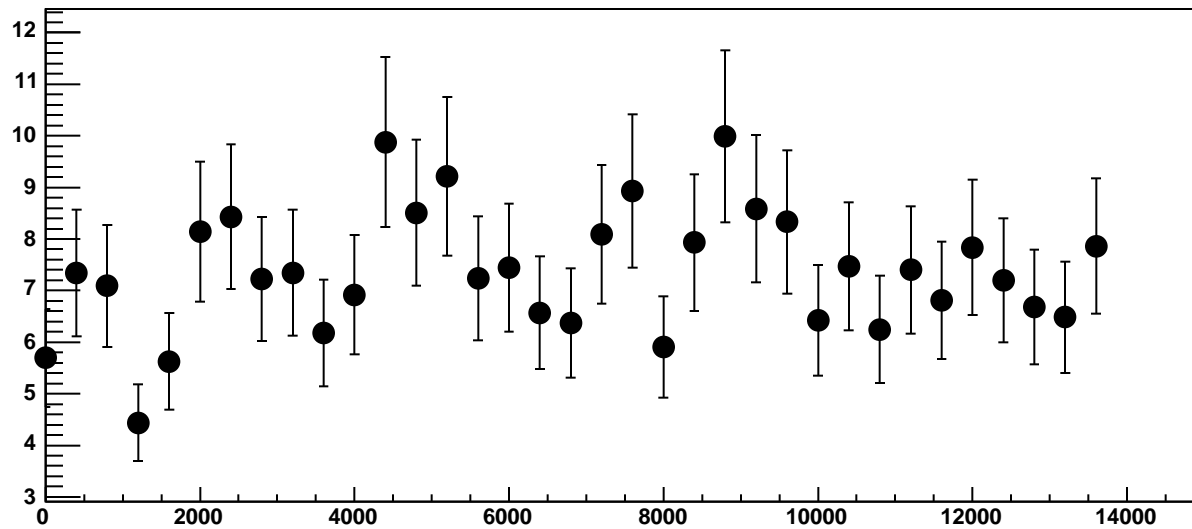




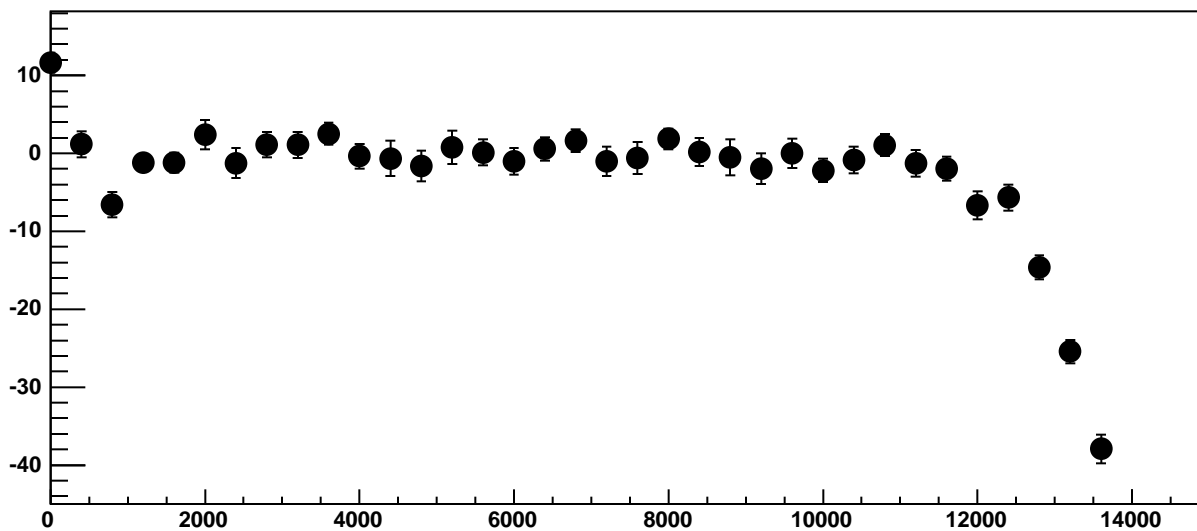
Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



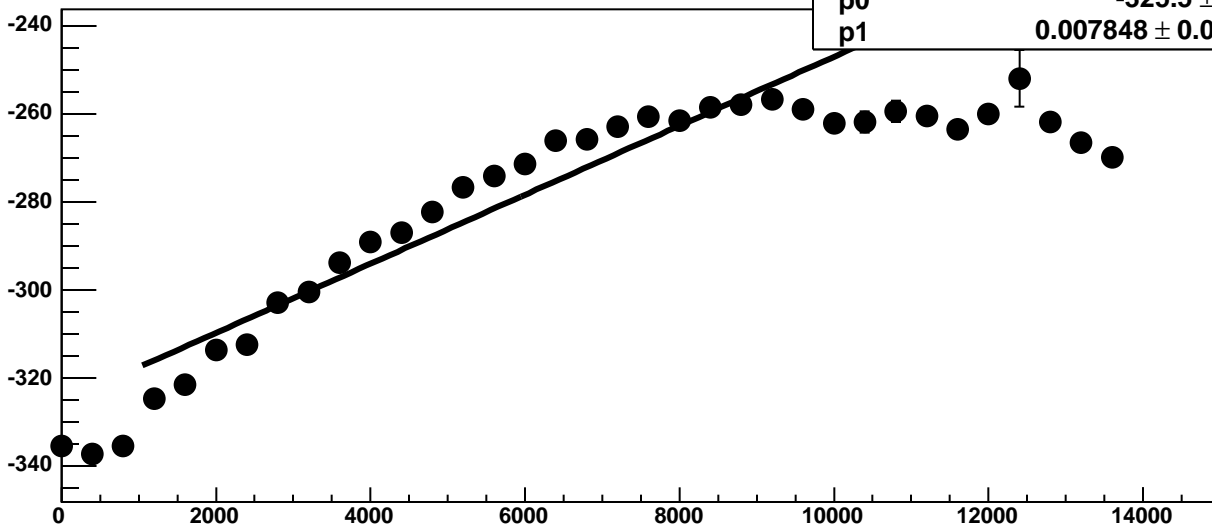
Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



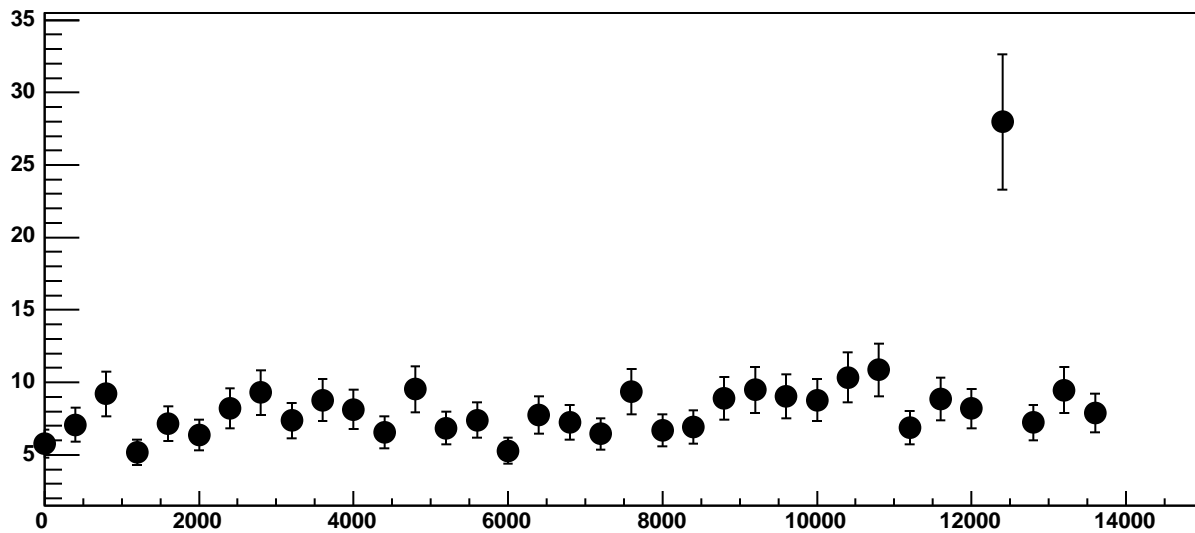
Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



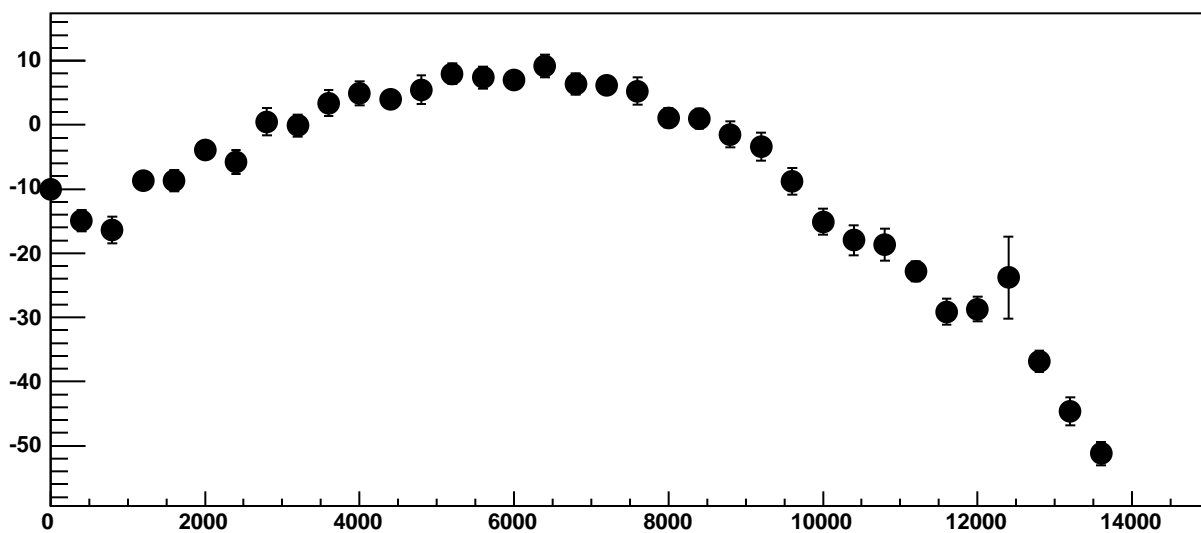
Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

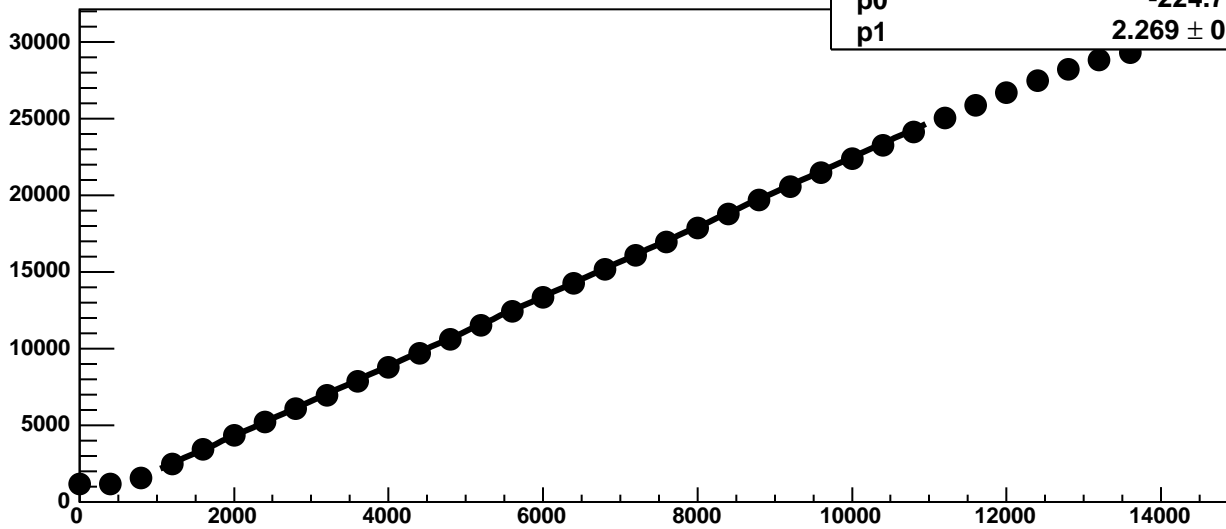


Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

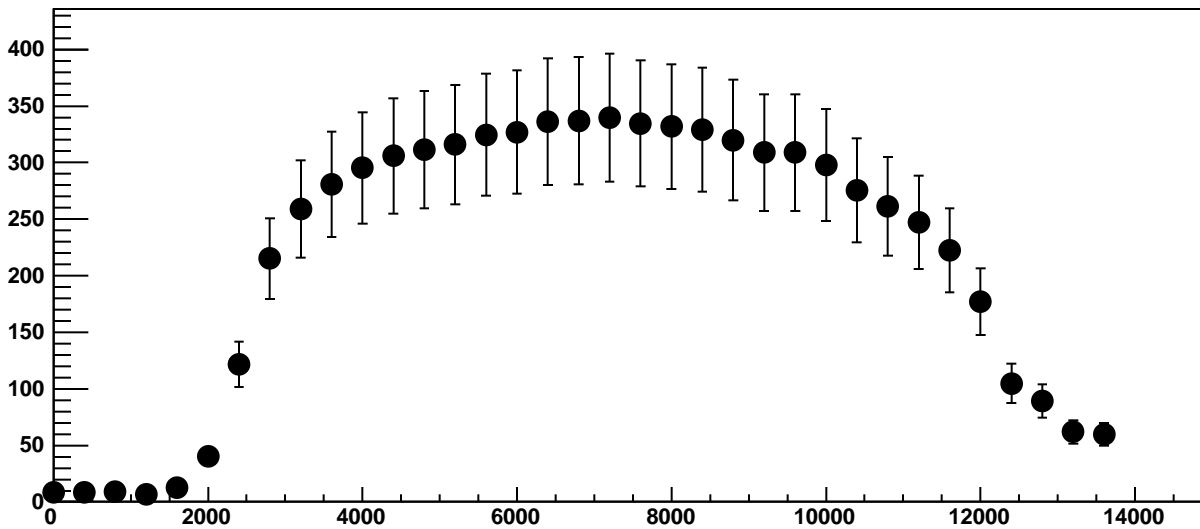


Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

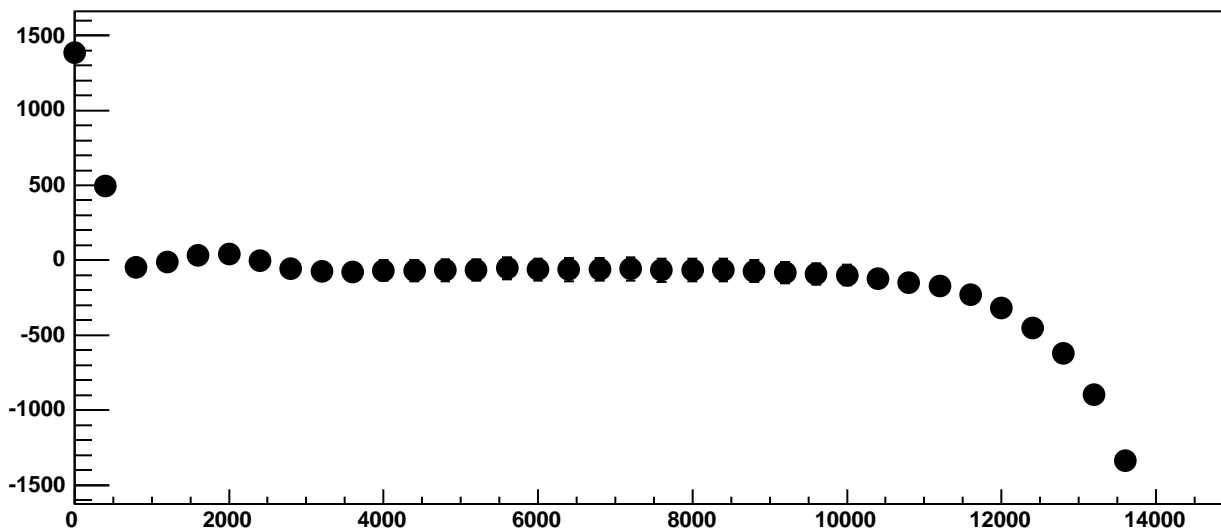
$\chi^2 / \text{ndf}$  238.7 / 23  
p0  $-224.7 \pm 3.494$   
p1  $2.269 \pm 0.002376$



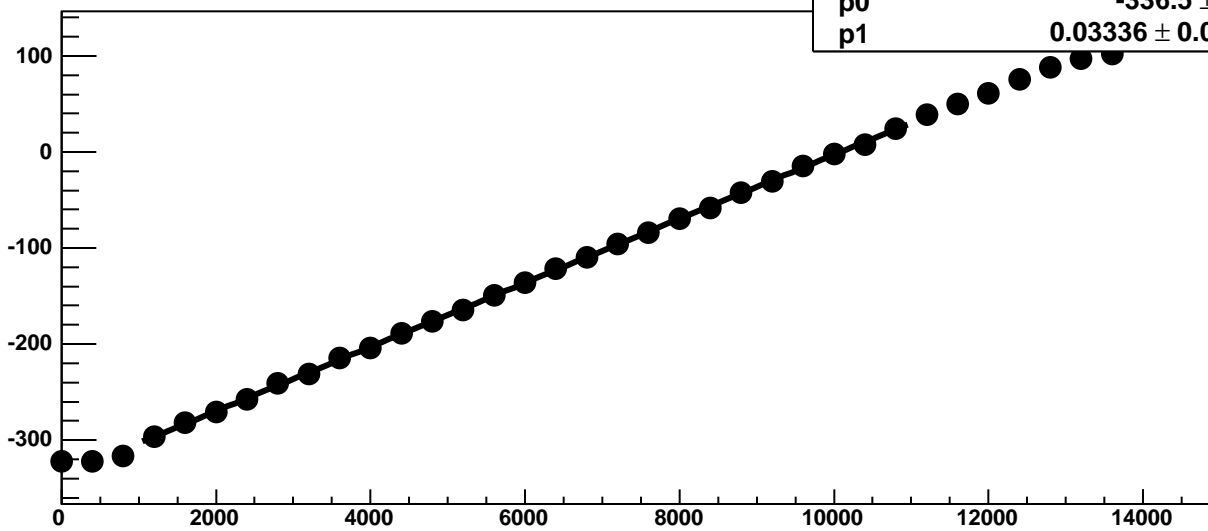
Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

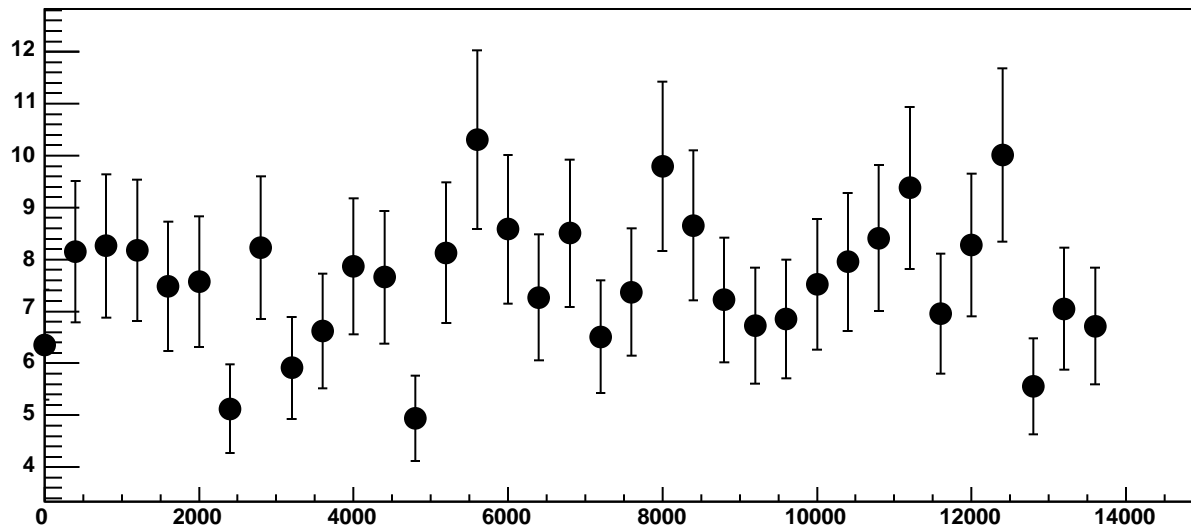


Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

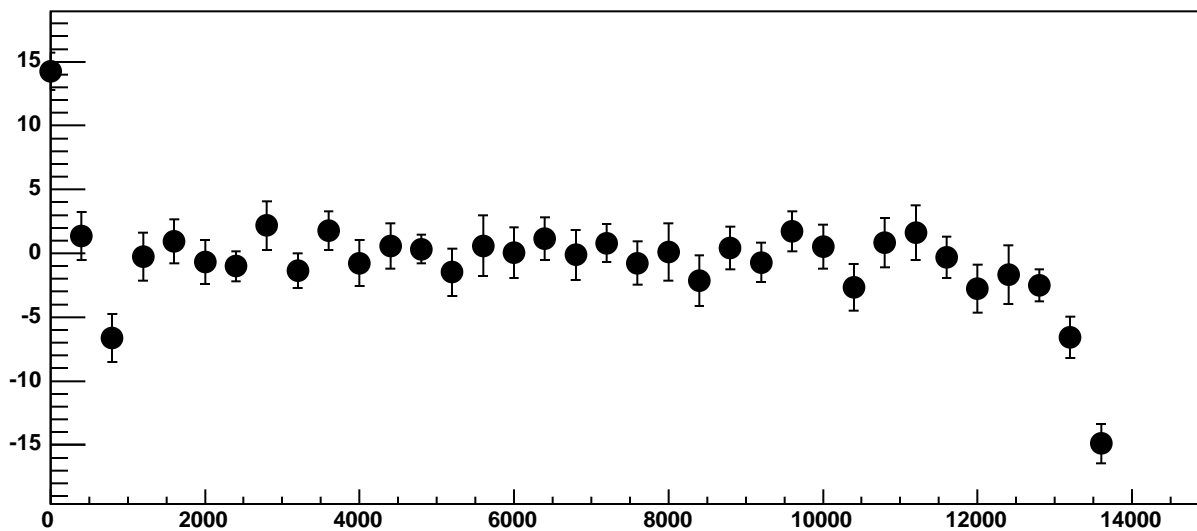


$\chi^2 / \text{ndf}$  11.98 / 23  
p0  $-336.5 \pm 0.7452$   
p1  $0.03336 \pm 0.0001163$

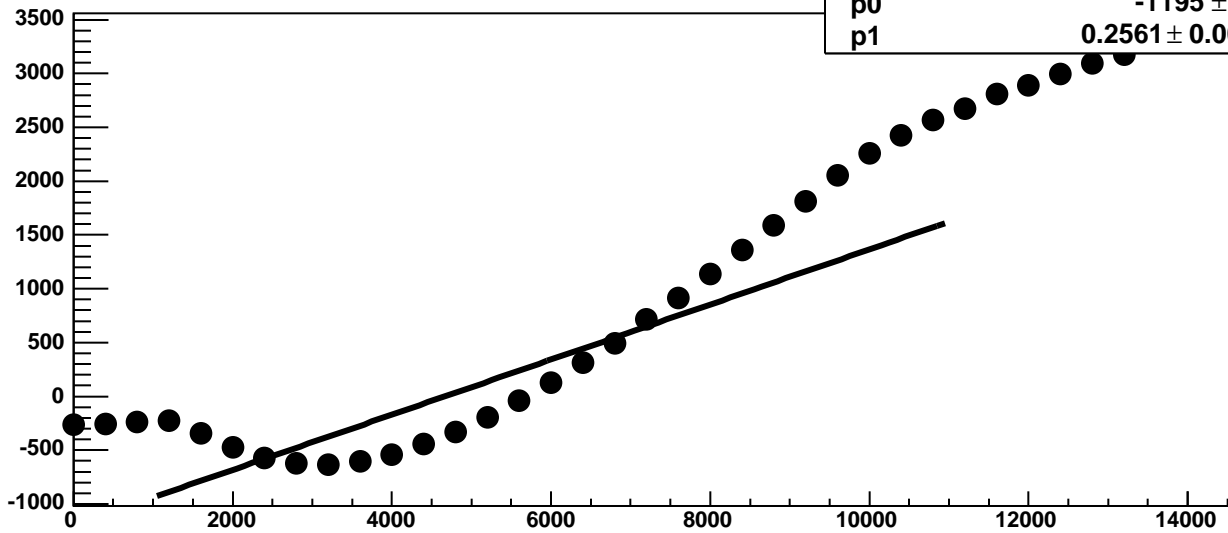
Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

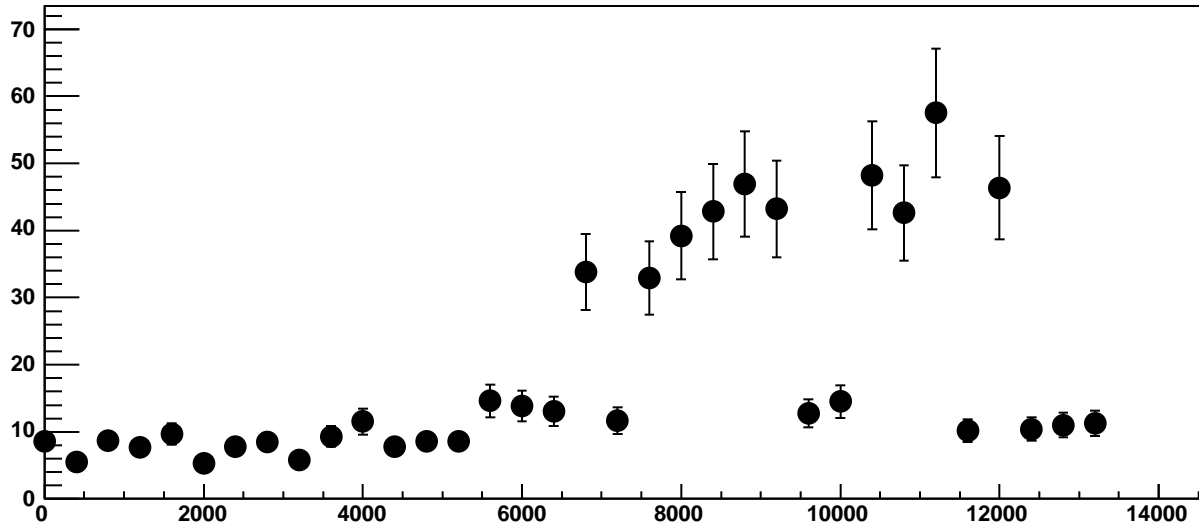


Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

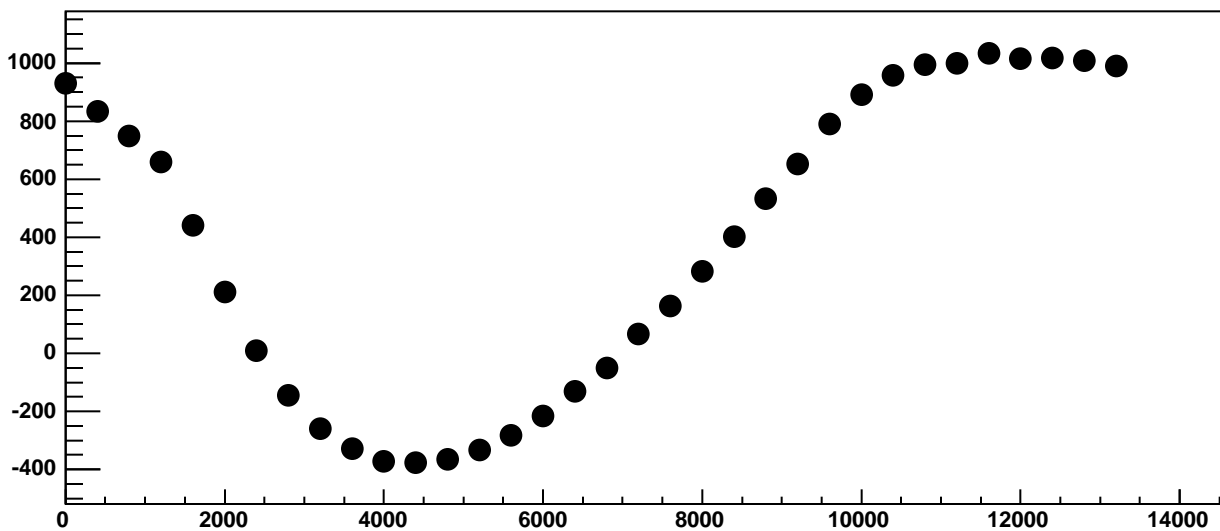


$\chi^2 / \text{ndf}$  5.904e+05 / 23  
p0 -1195 ± 0.9817  
p1 0.2561 ± 0.0002234

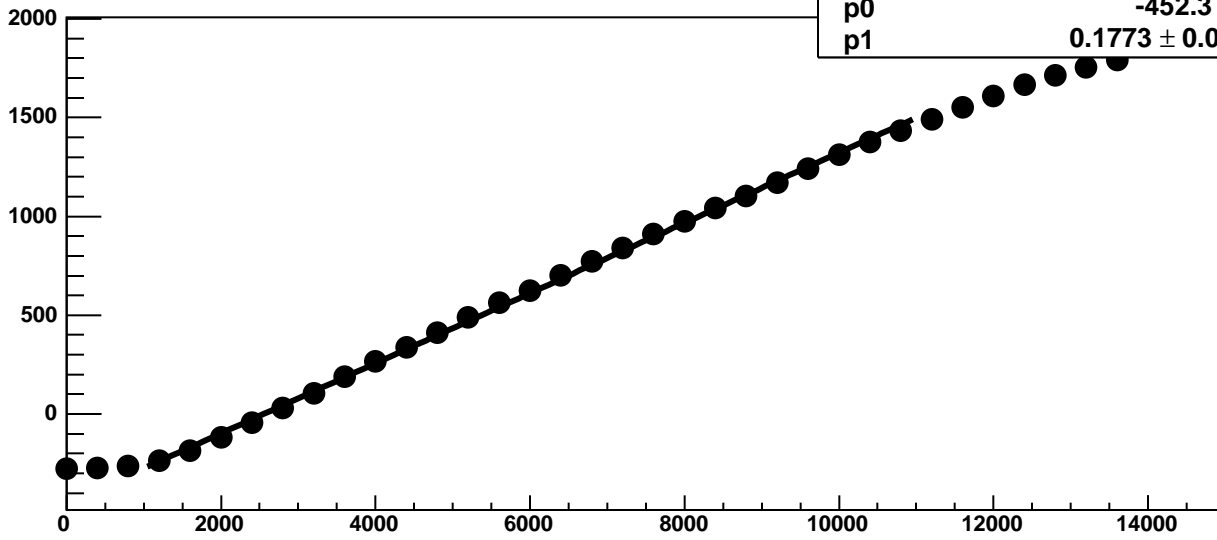
Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

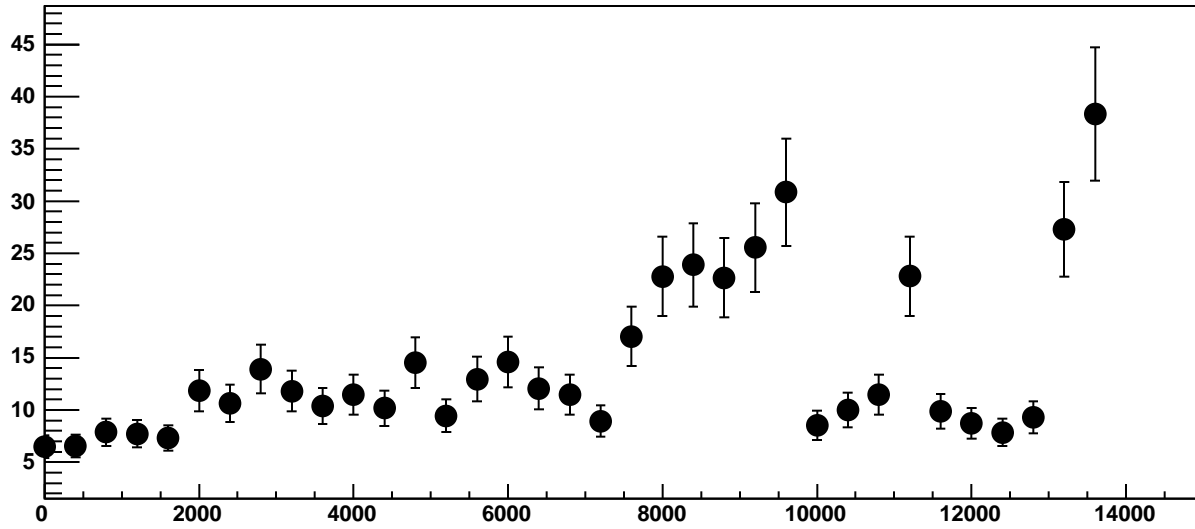


Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

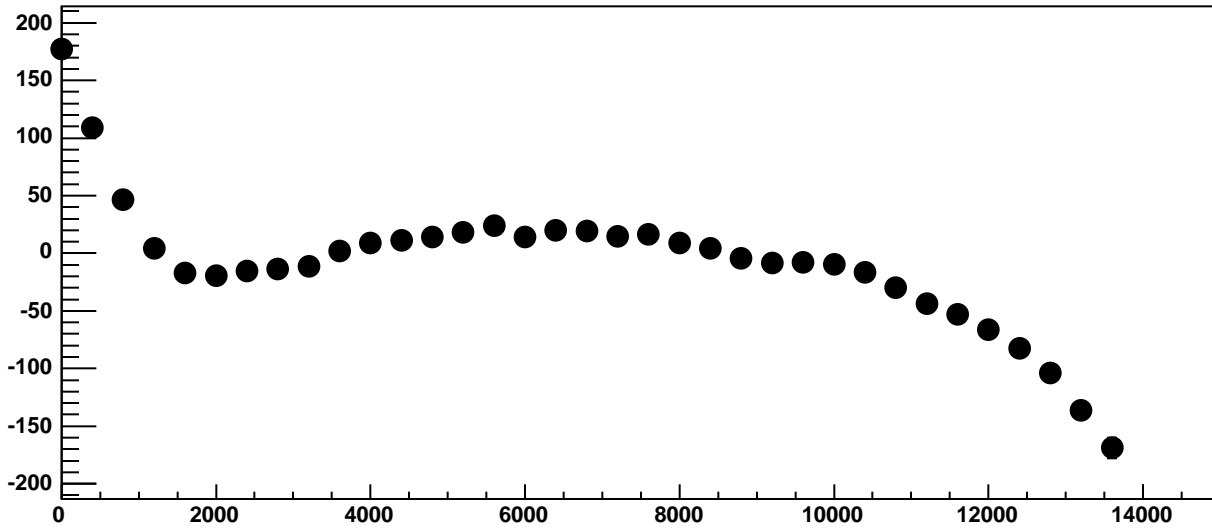


$\chi^2 / \text{ndf}$  828.3 / 23  
p0  $-452.3 \pm 1.045$   
p1  $0.1773 \pm 0.0001719$

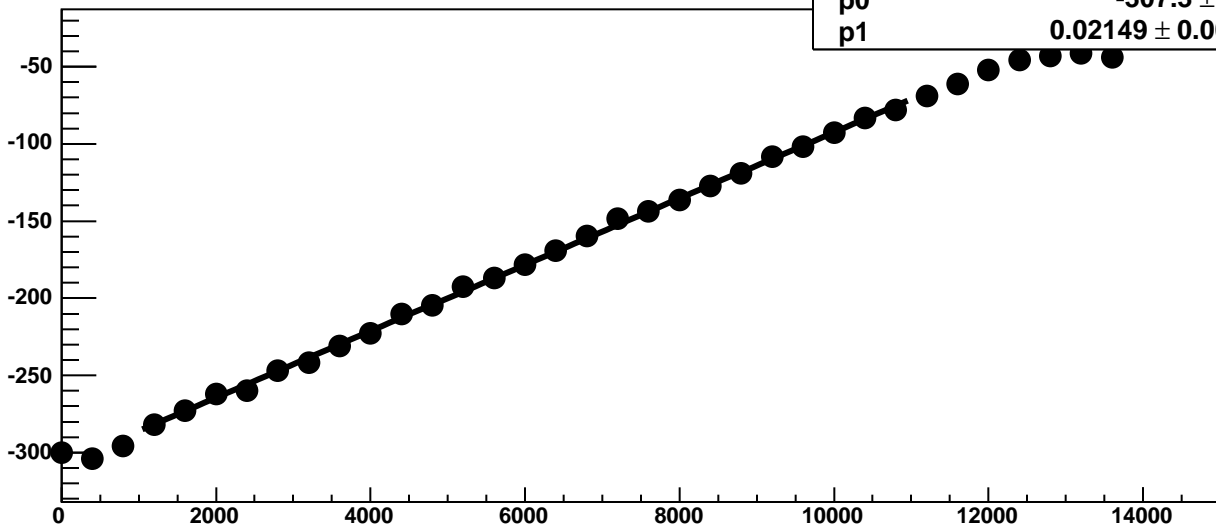
Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



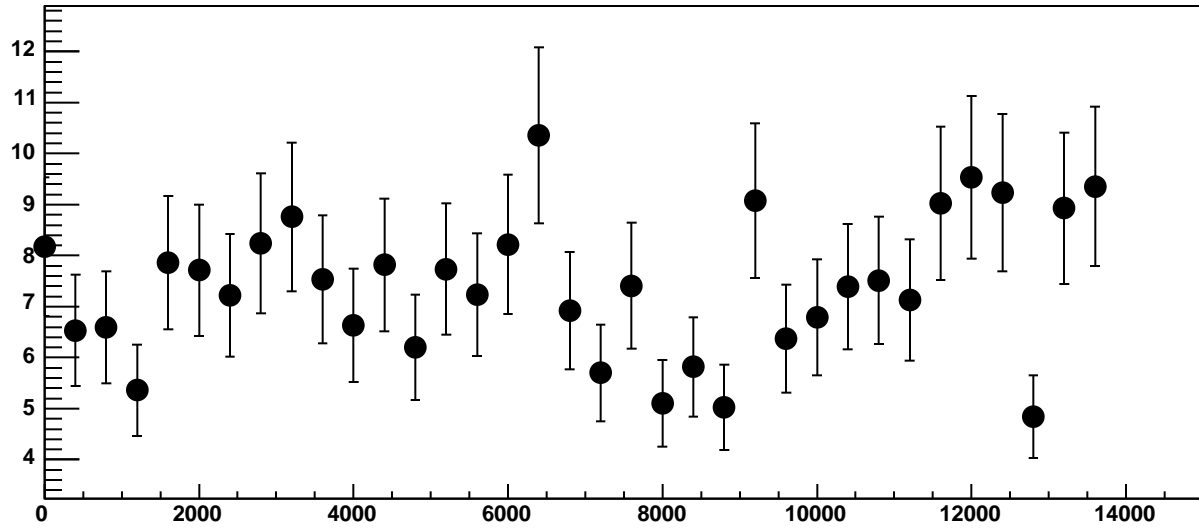
Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



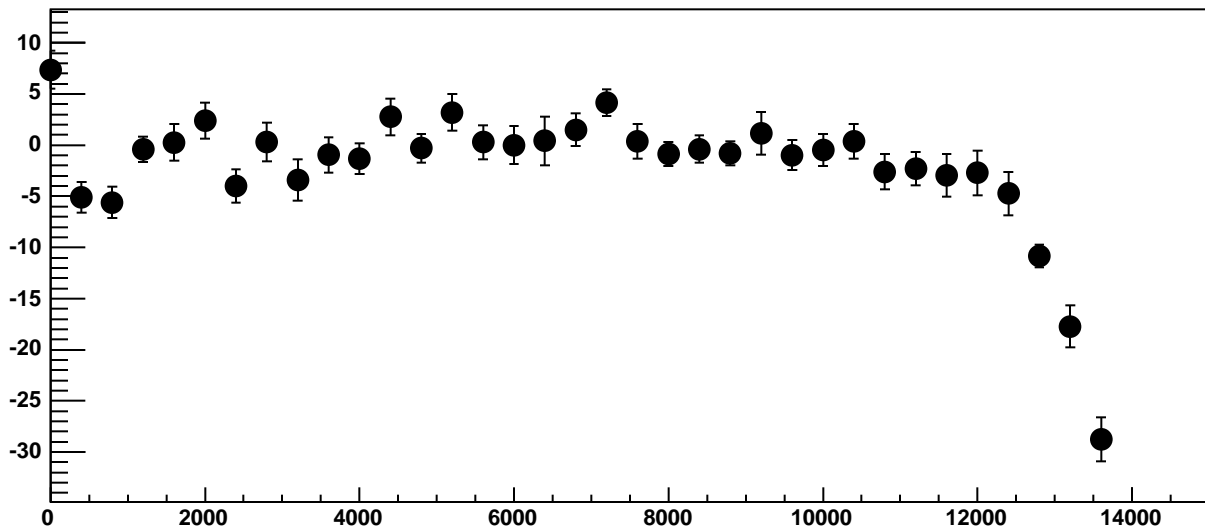
Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC



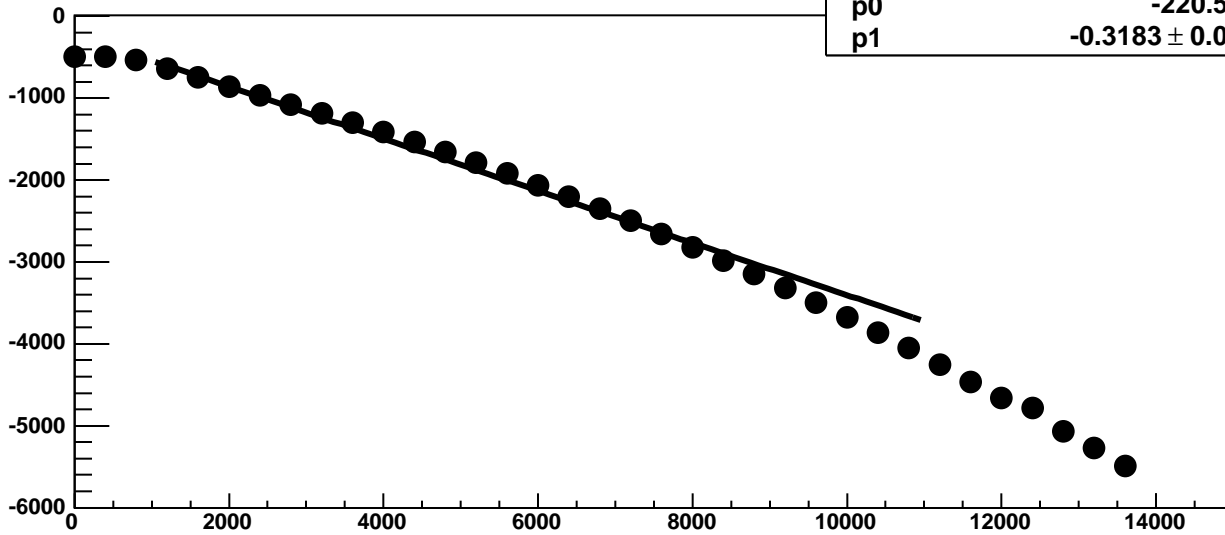
Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4729 / 23

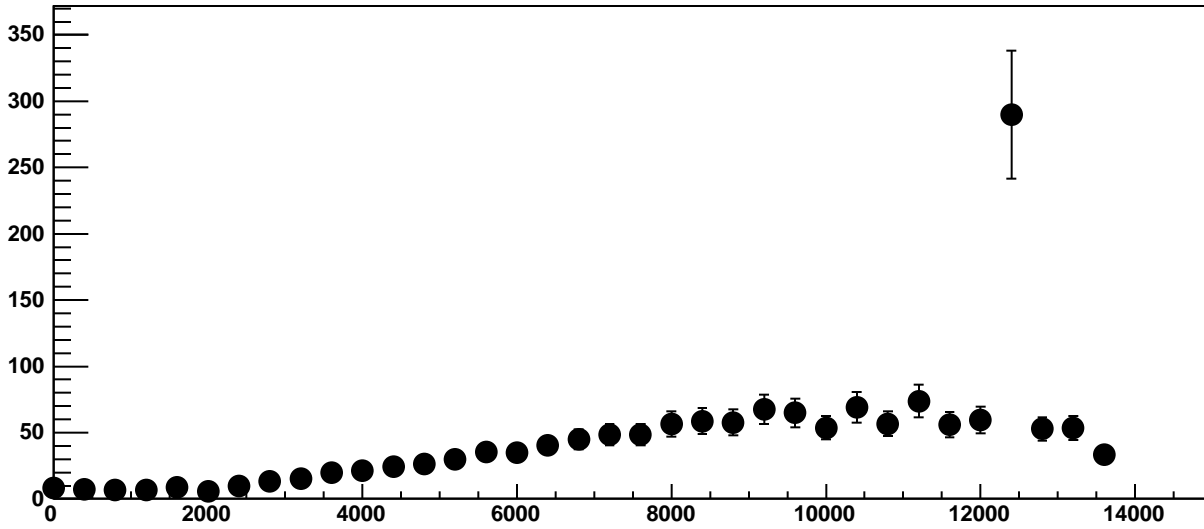
p0

$-220.5 \pm 1.35$

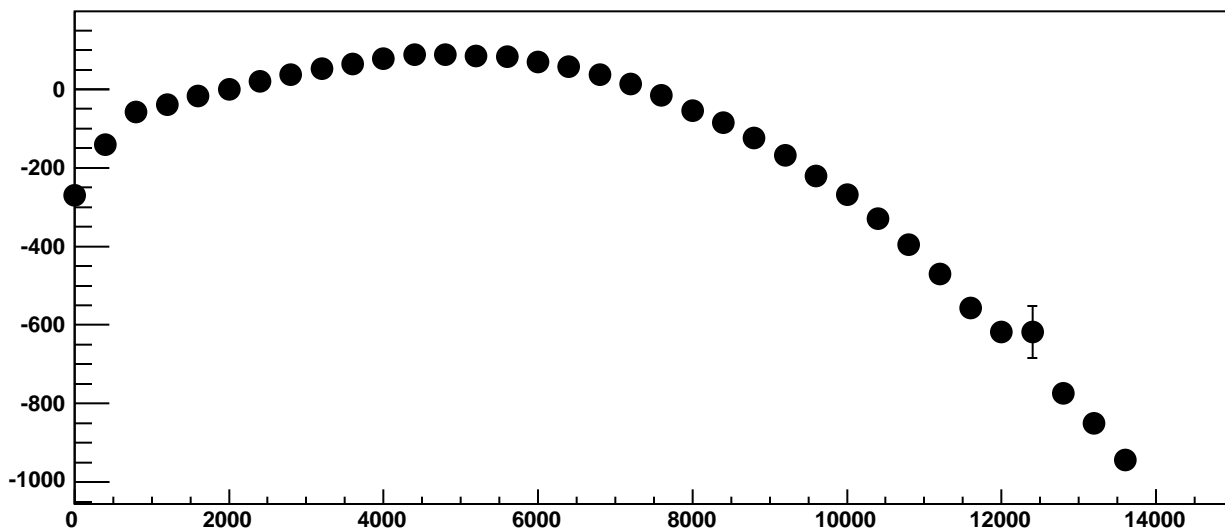
p1

$-0.3183 \pm 0.0004602$

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

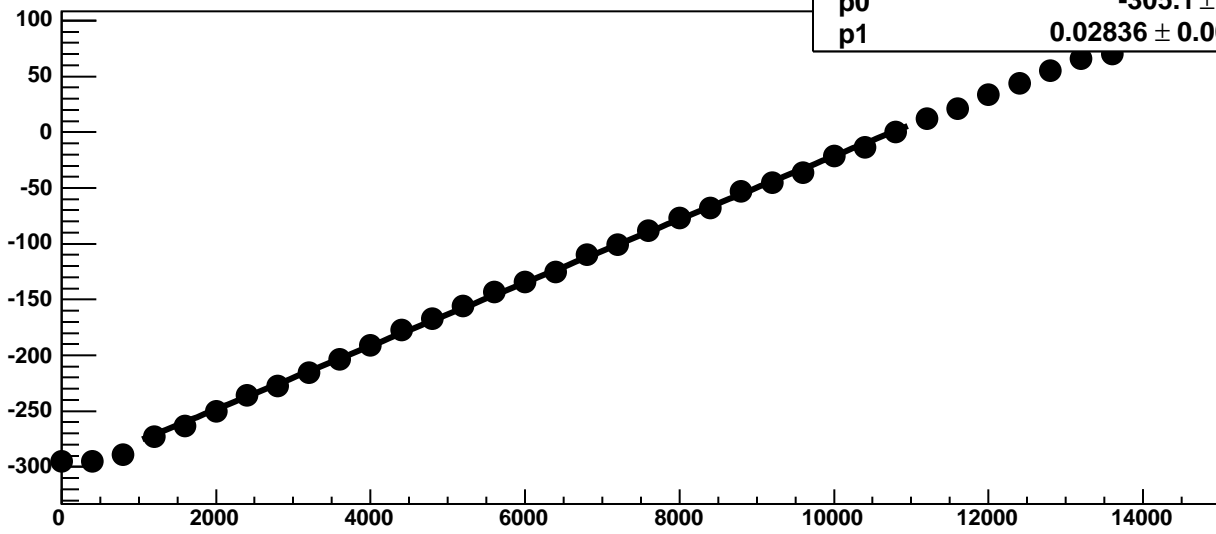


Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



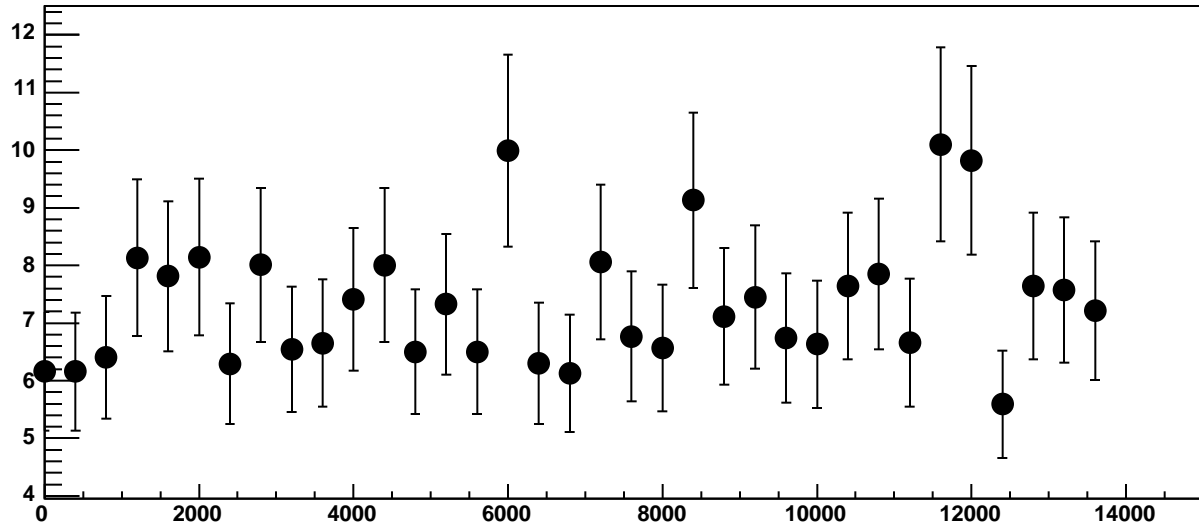


Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

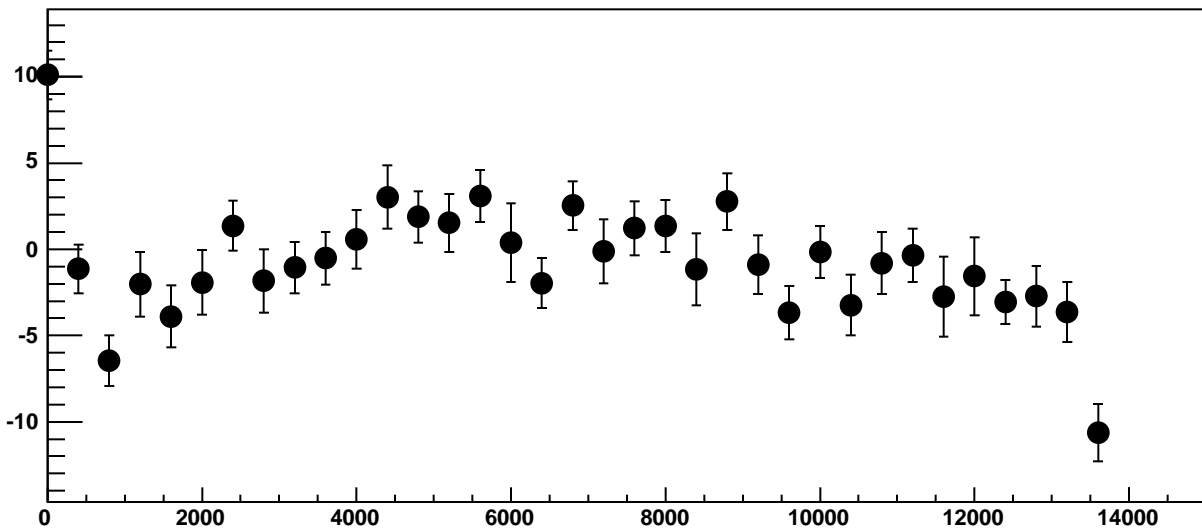


$\chi^2 / \text{ndf}$  38.19 / 23  
p0  $-305.1 \pm 0.7812$   
p1  $0.02836 \pm 0.0001173$

Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

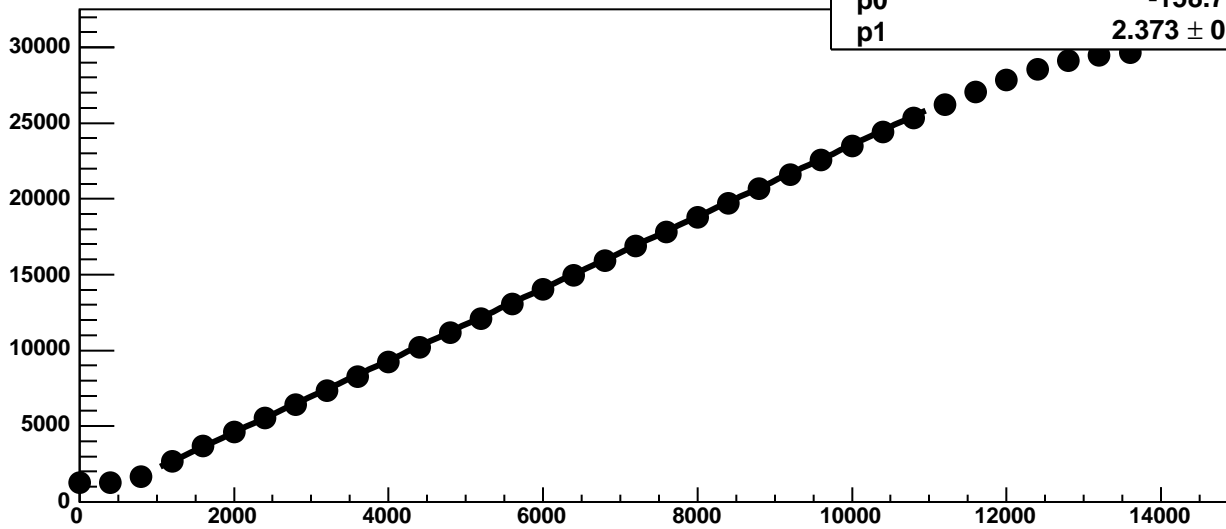


Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

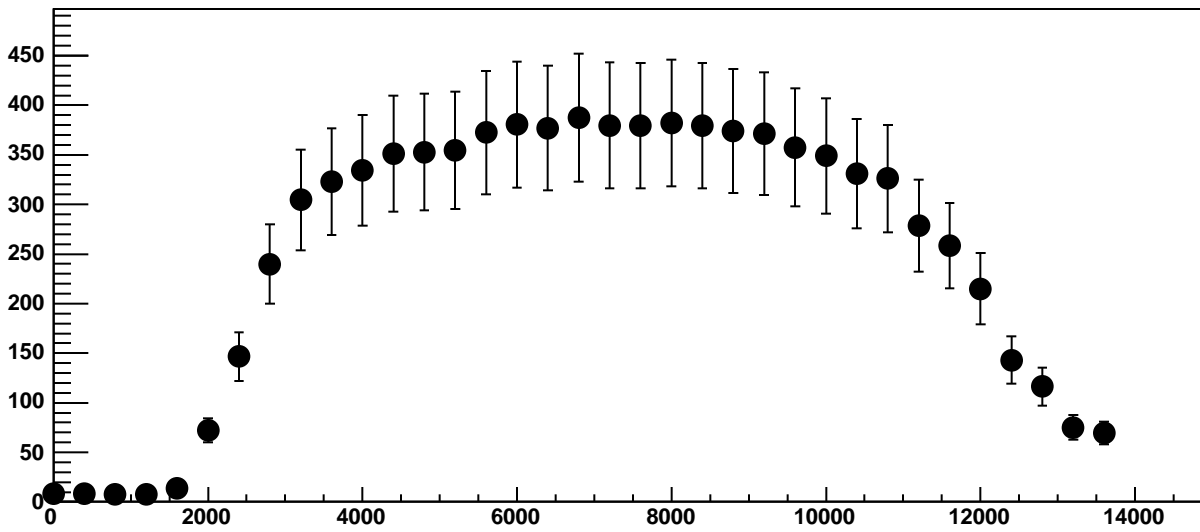


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

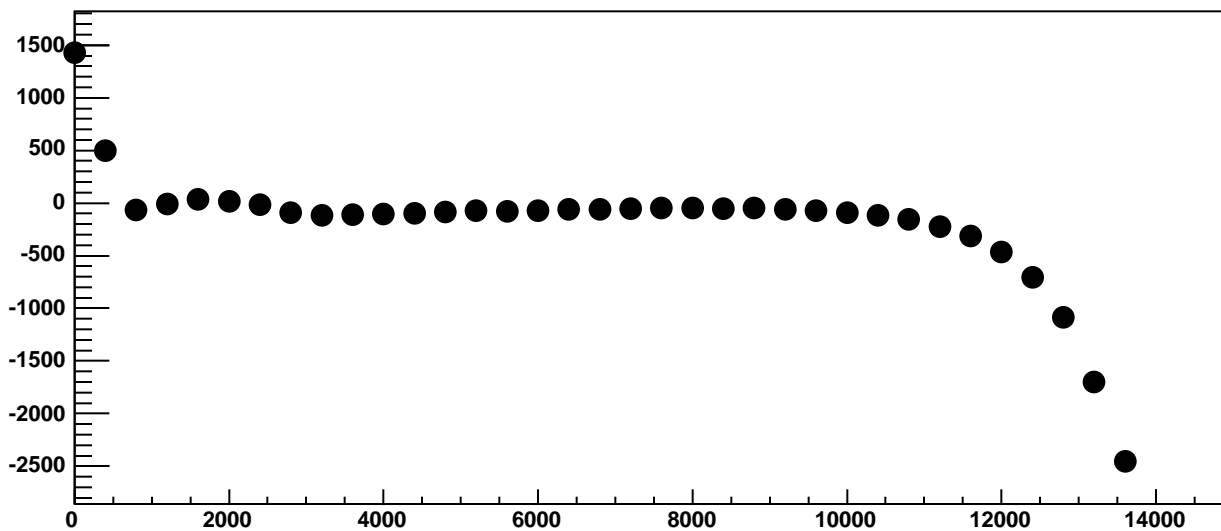
$\chi^2 / \text{ndf}$  187.7 / 23  
p0  $-158.7 \pm 4.104$   
p1  $2.373 \pm 0.002804$



Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

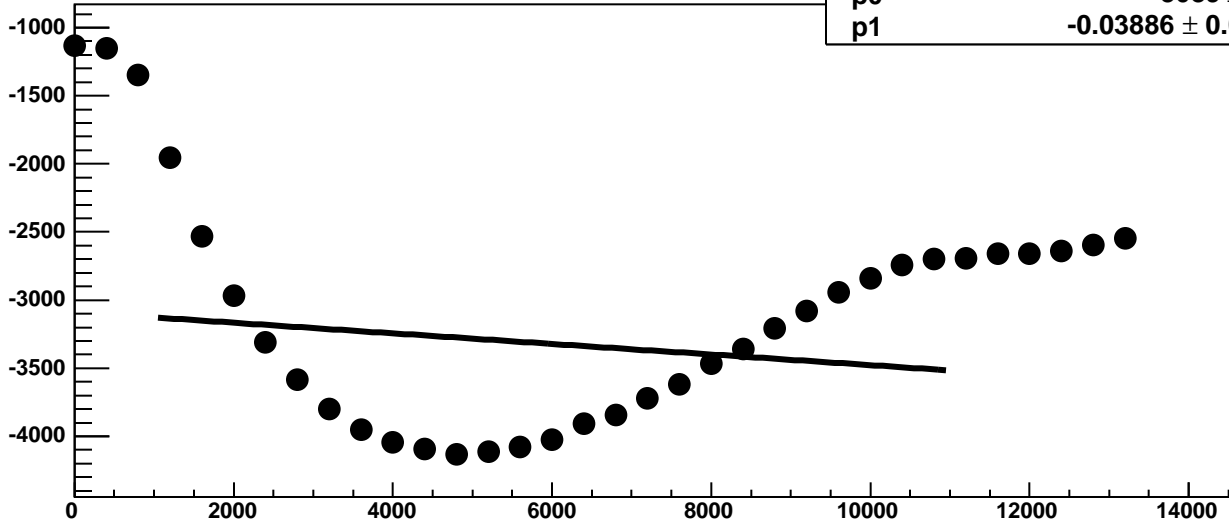


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

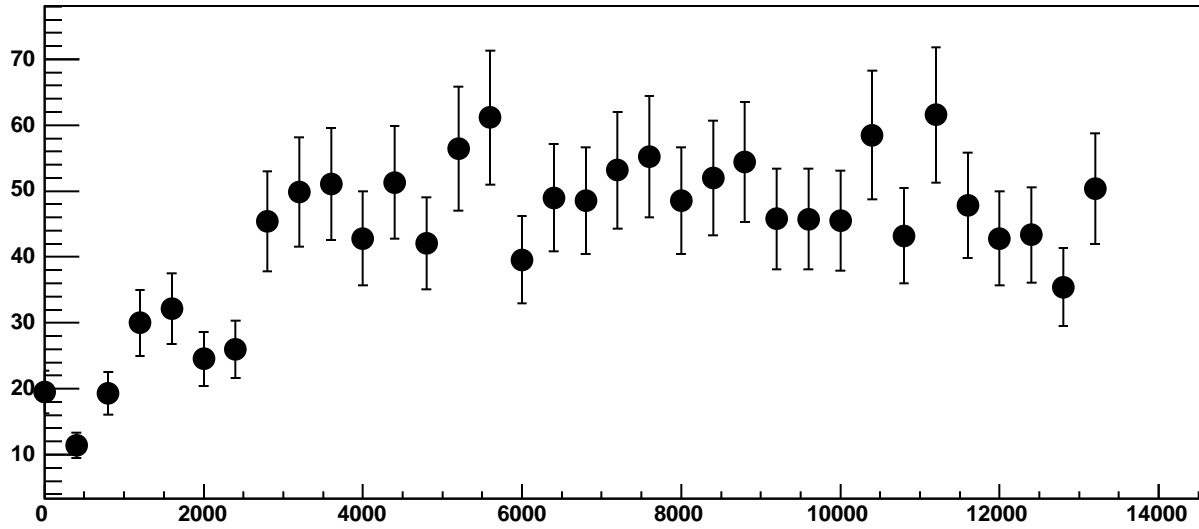


Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

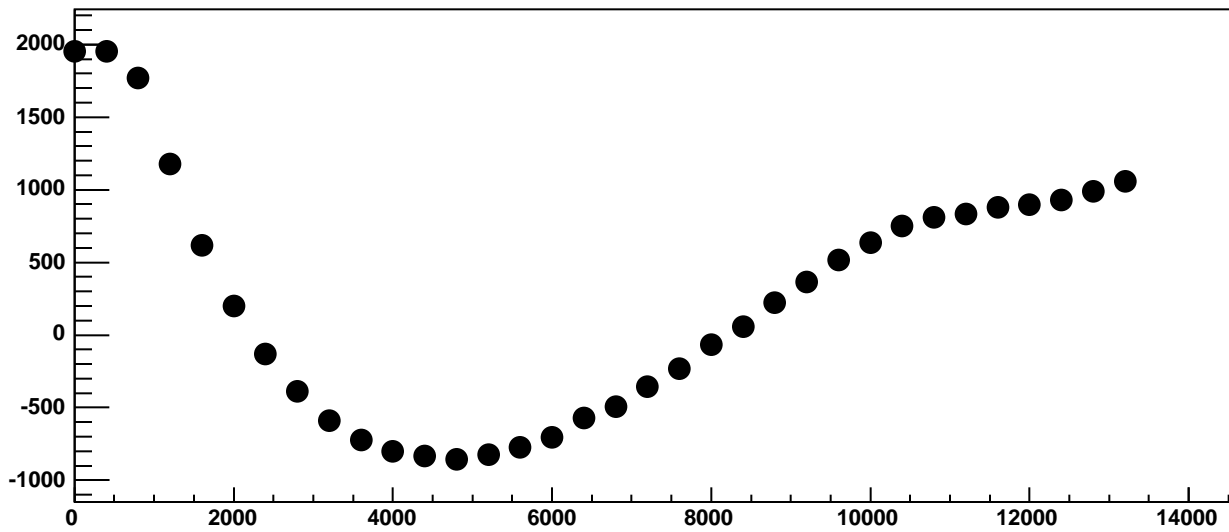
$\chi^2 / \text{ndf}$  1.018e+05 / 23  
p0 -3089 ± 3.654  
p1 -0.03886 ± 0.000626



Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

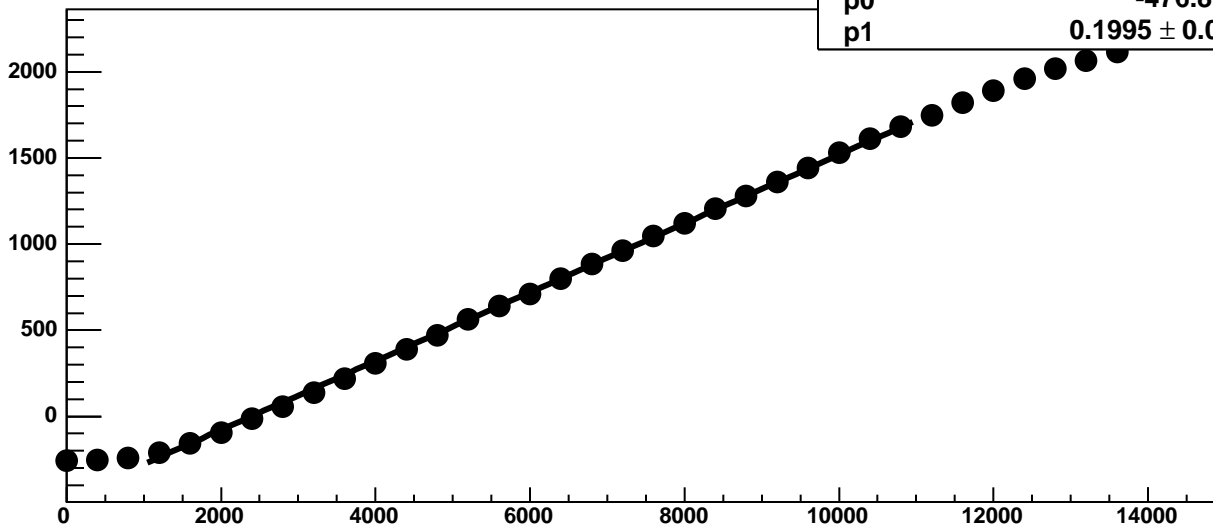


Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

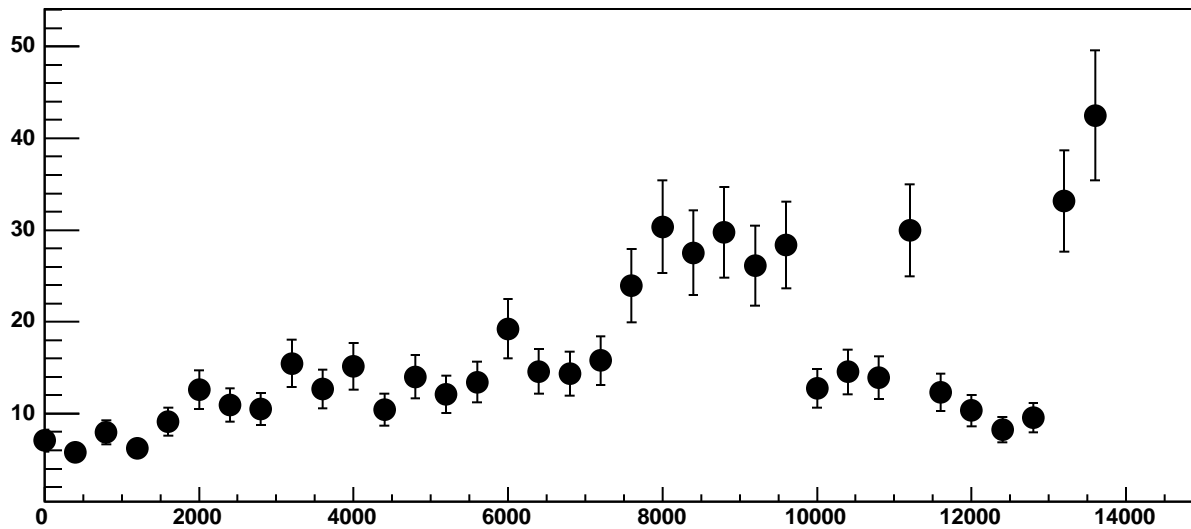


Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

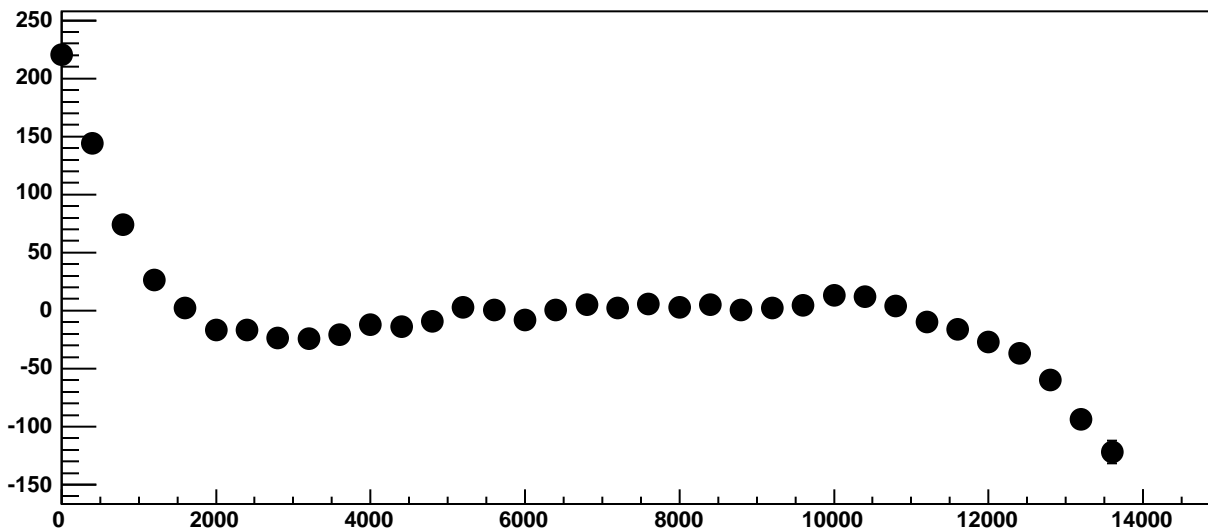
$\chi^2 / \text{ndf}$  698.5 / 23  
p0  $-476.8 \pm 1.088$   
p1  $0.1995 \pm 0.0002032$



Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

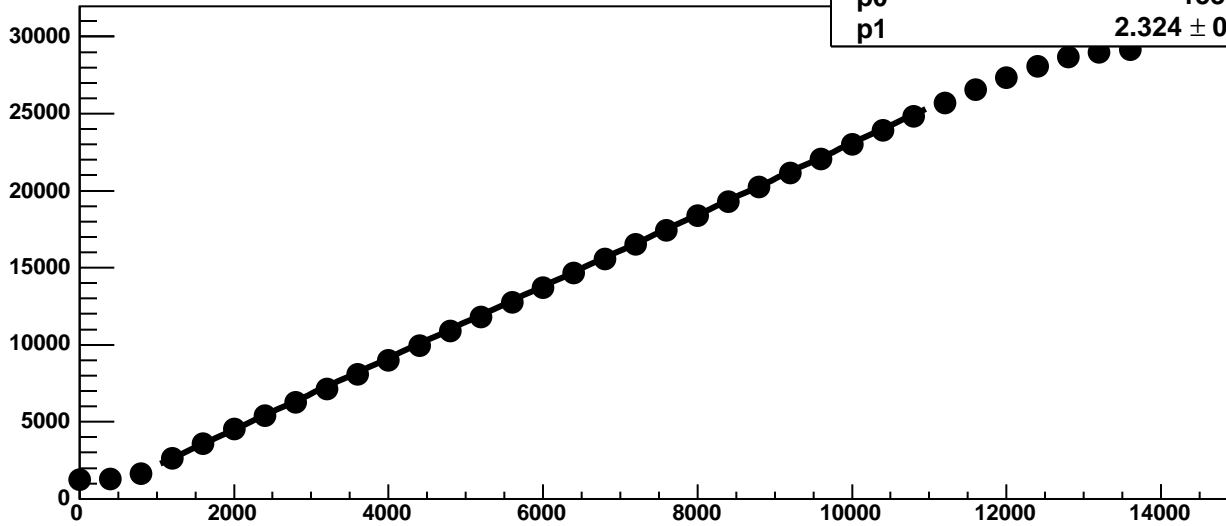


Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

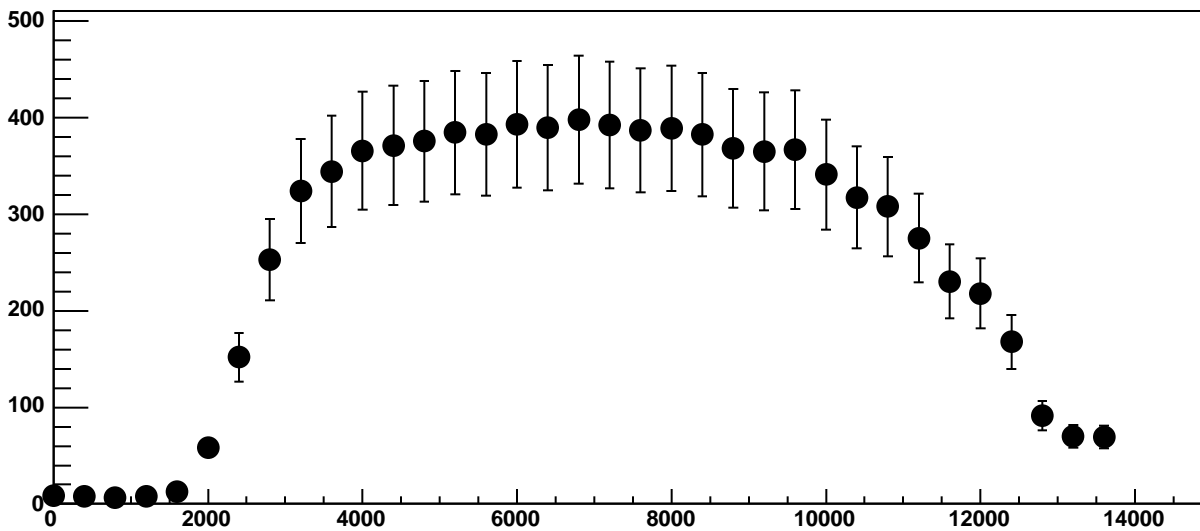


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC

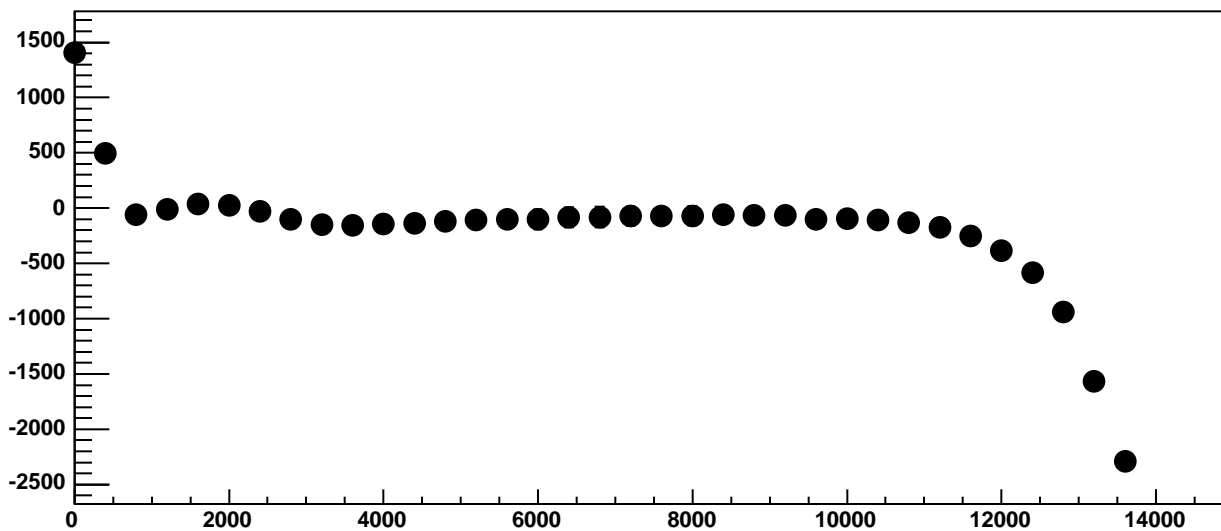
$\chi^2 / \text{ndf}$  225.9 / 23  
p0  $-153 \pm 4.026$   
p1  $2.324 \pm 0.002771$



Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

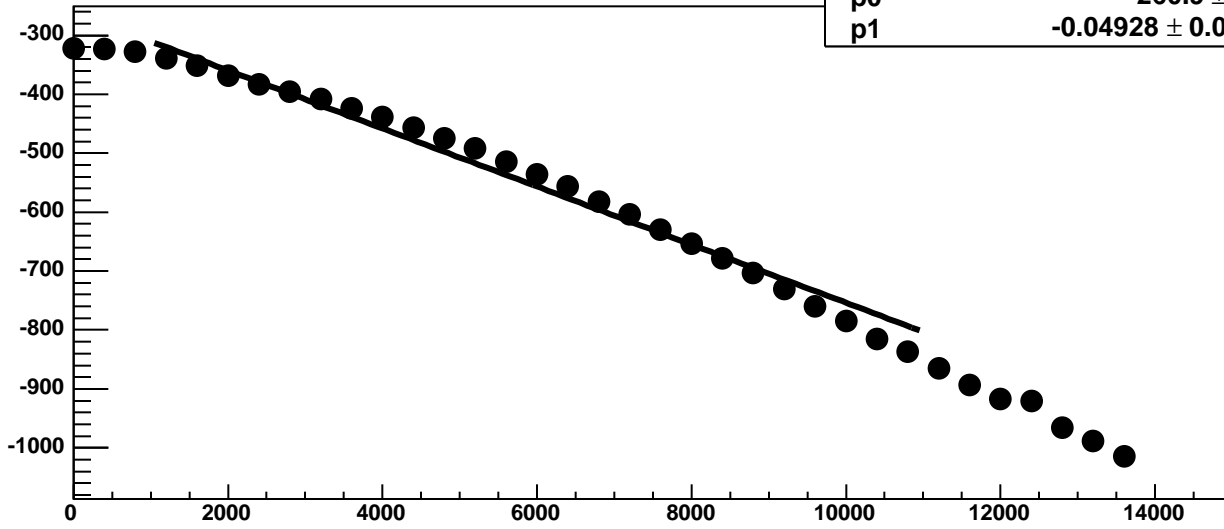


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

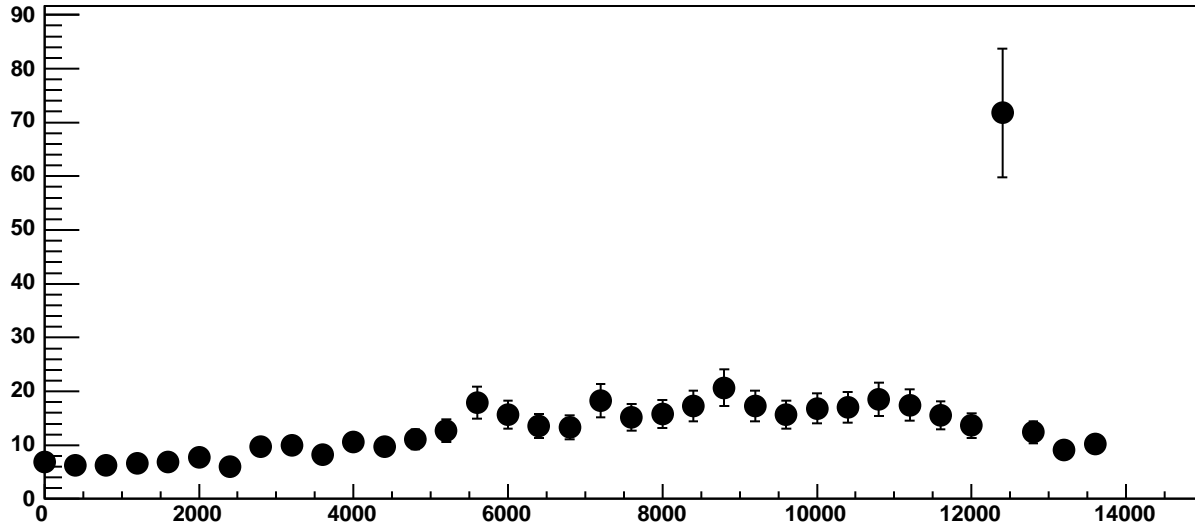


Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

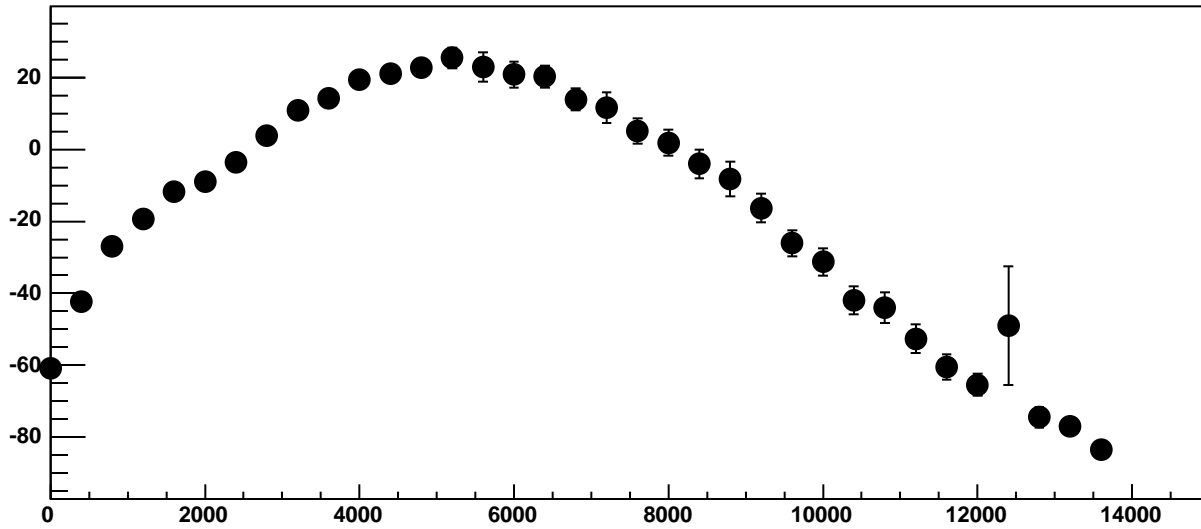
$\chi^2 / \text{ndf}$  1139 / 23  
p0  $-260.9 \pm 0.9186$   
p1  $-0.04928 \pm 0.0001896$



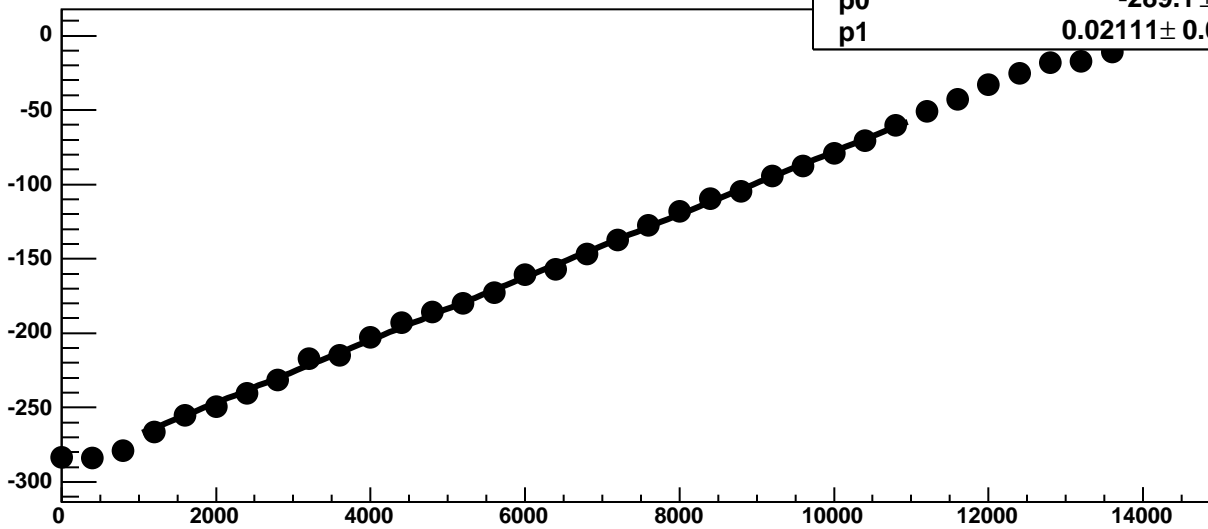
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

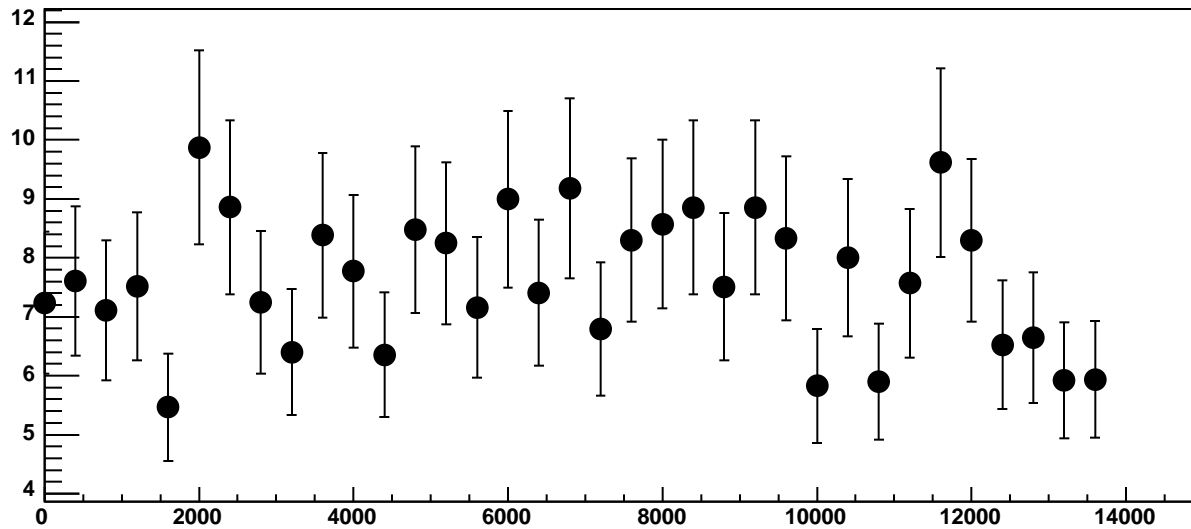


Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

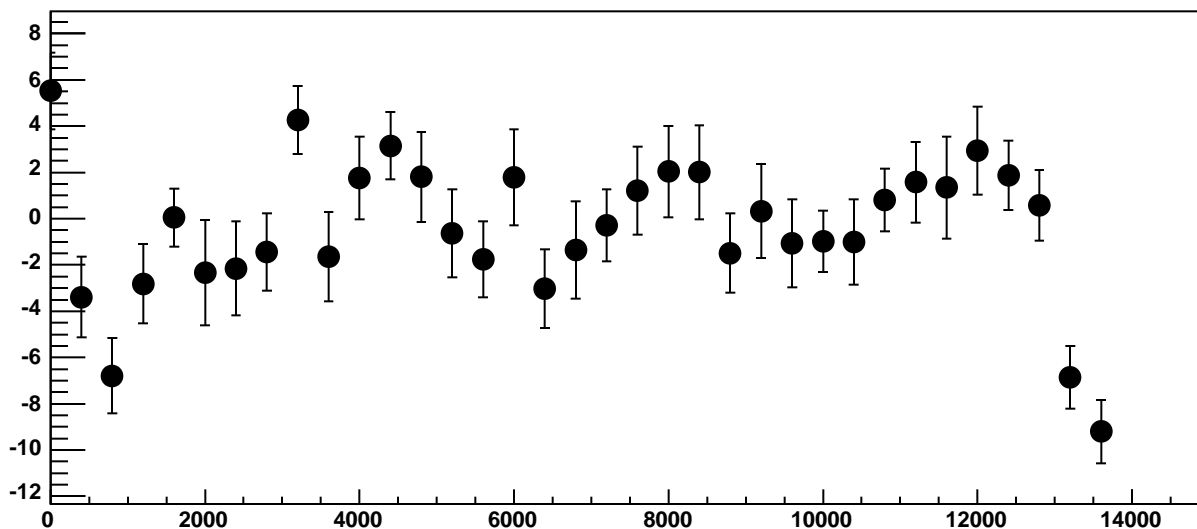


$\chi^2 / \text{ndf}$  31.68 / 23  
p0 -289.1 ± 0.7605  
p1 0.02111 ± 0.0001131

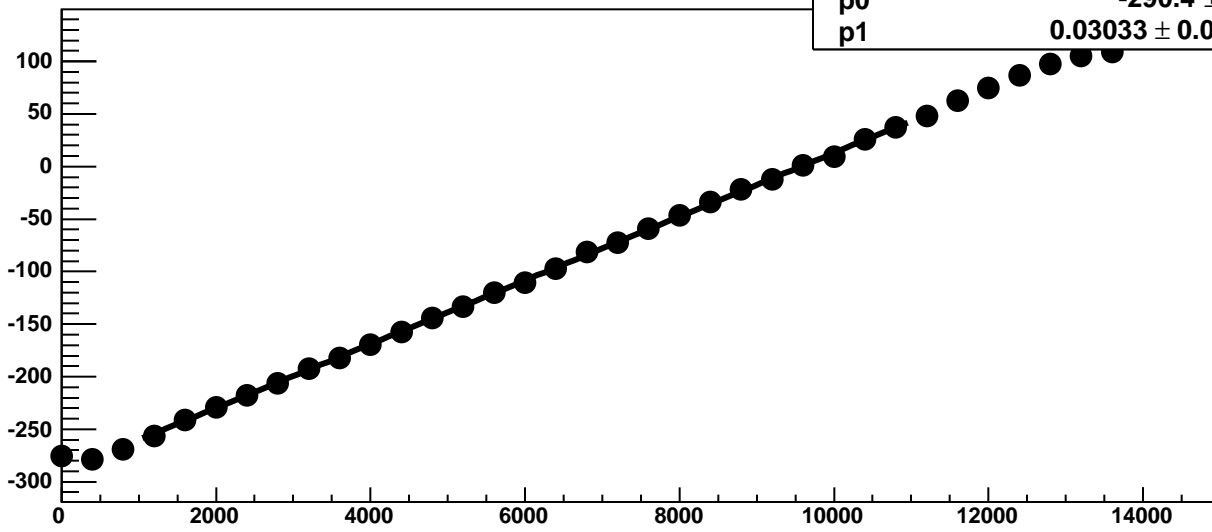
Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

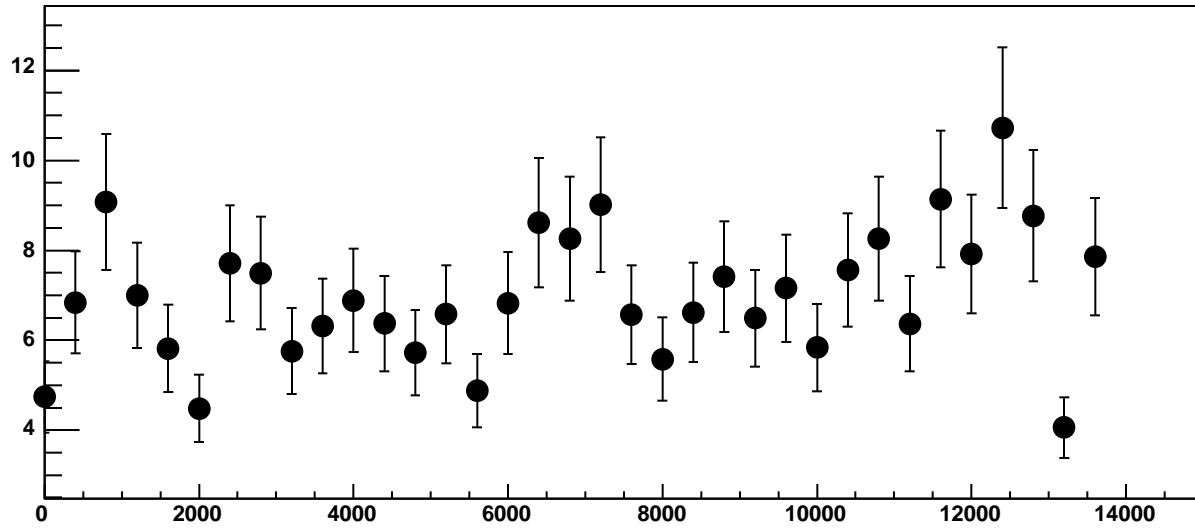


Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

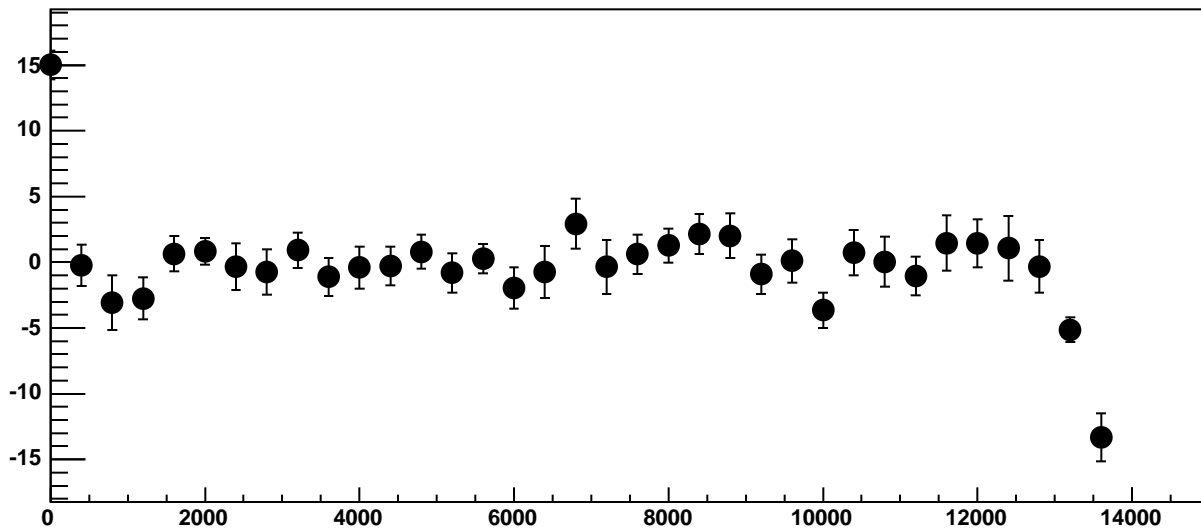


$\chi^2 / \text{ndf}$  22.59 / 23  
p0  $-290.4 \pm 0.6591$   
p1  $0.03033 \pm 0.0001036$

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



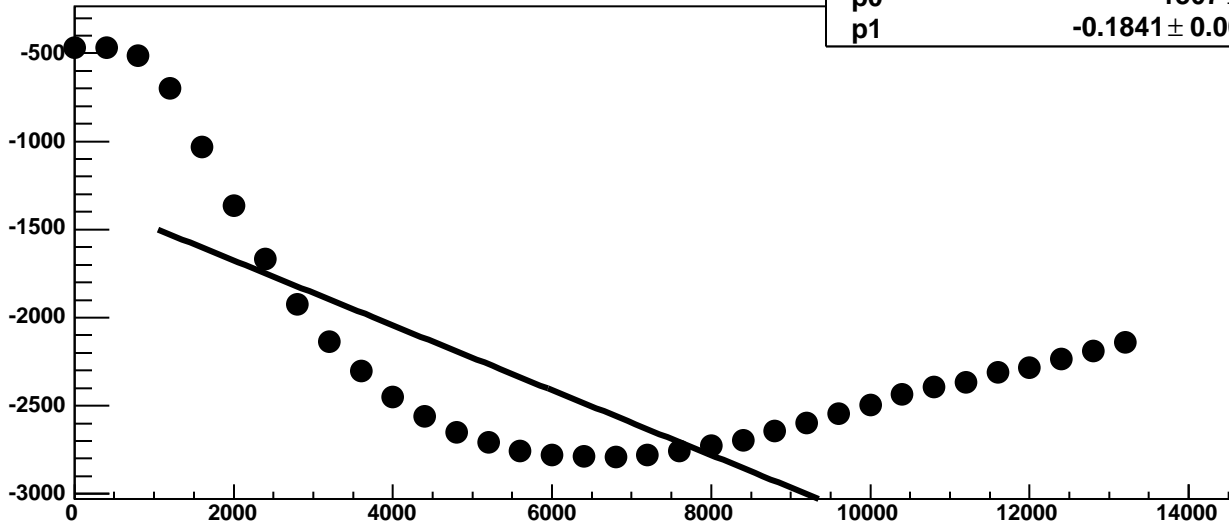
Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



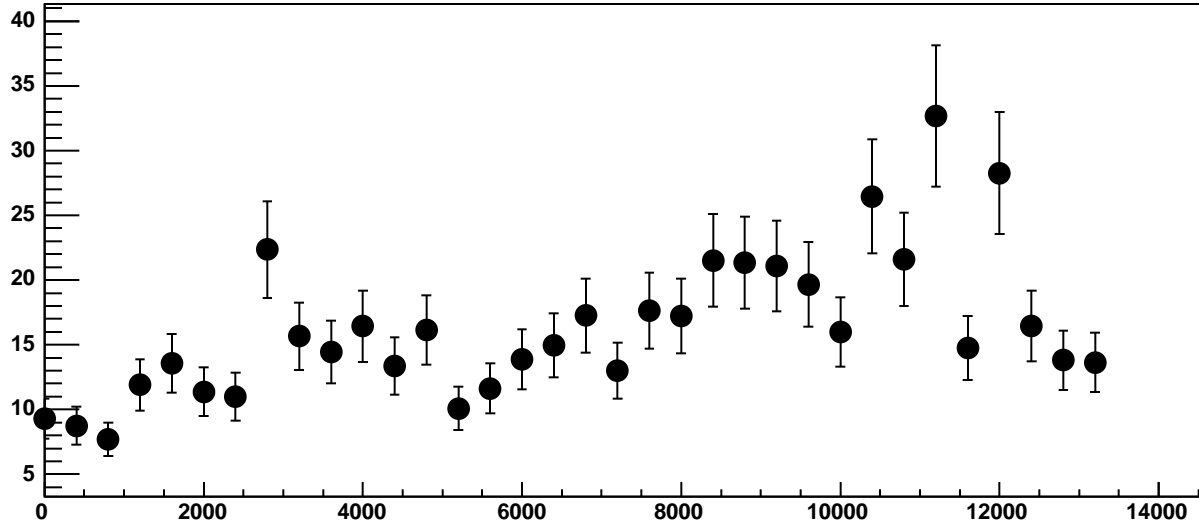


Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

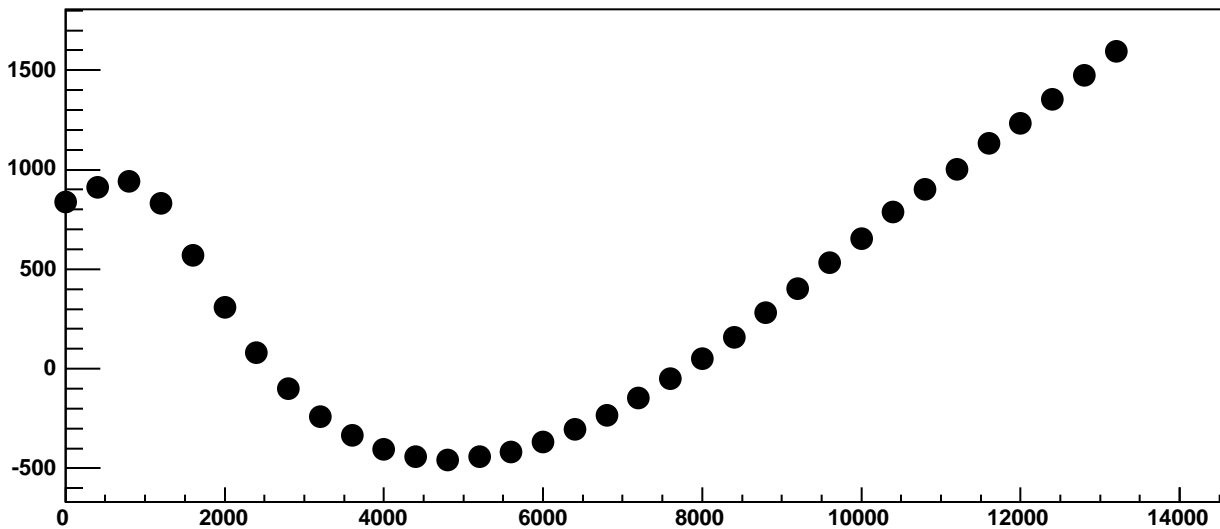
$\chi^2 / \text{ndf}$  3.995e+05 / 23  
p0 -1307 ± 1.488  
p1 -0.1841 ± 0.0002569



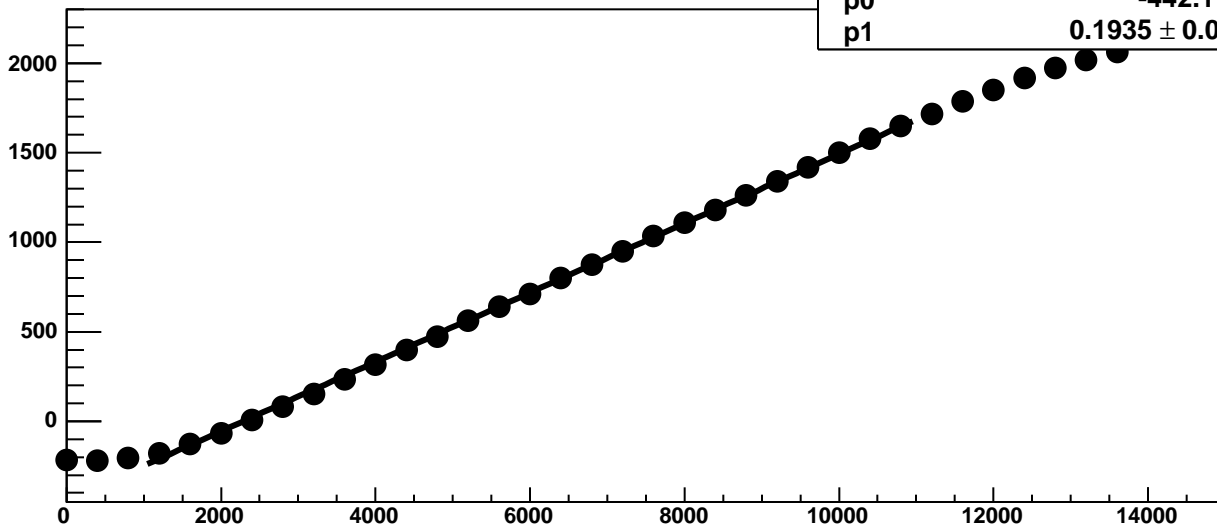
Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



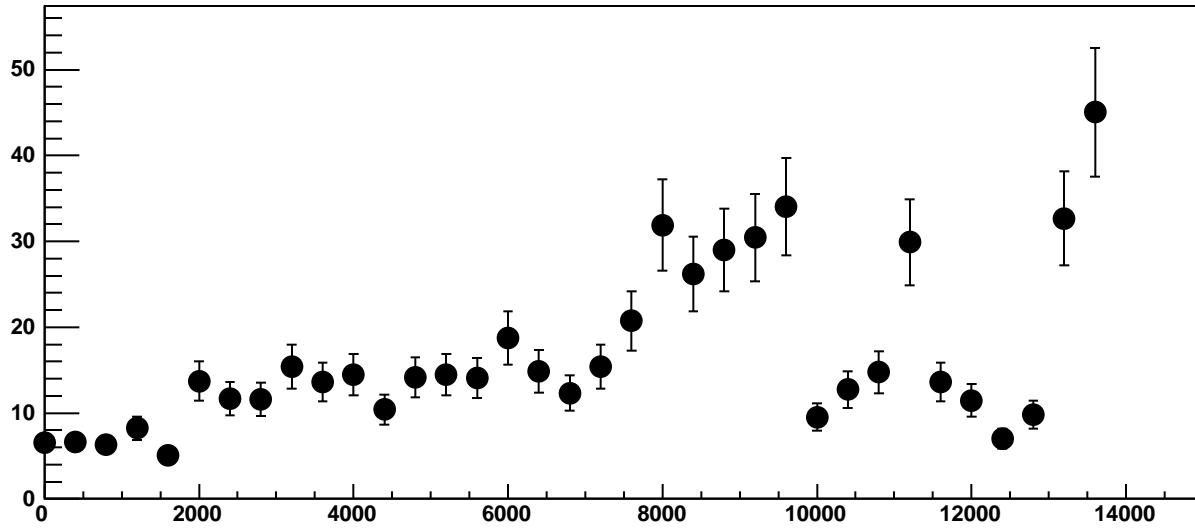
Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



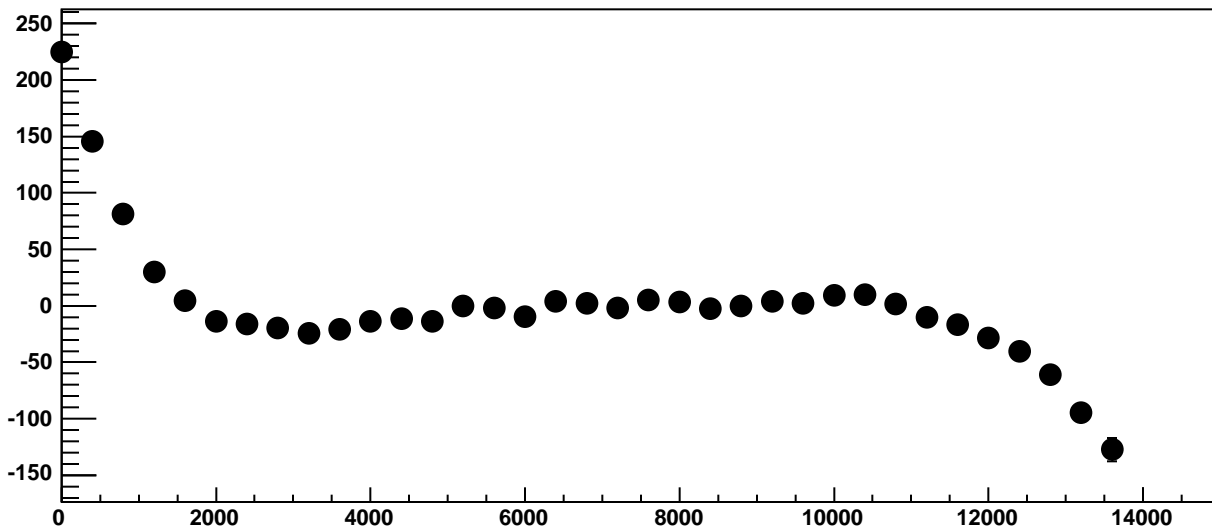
Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



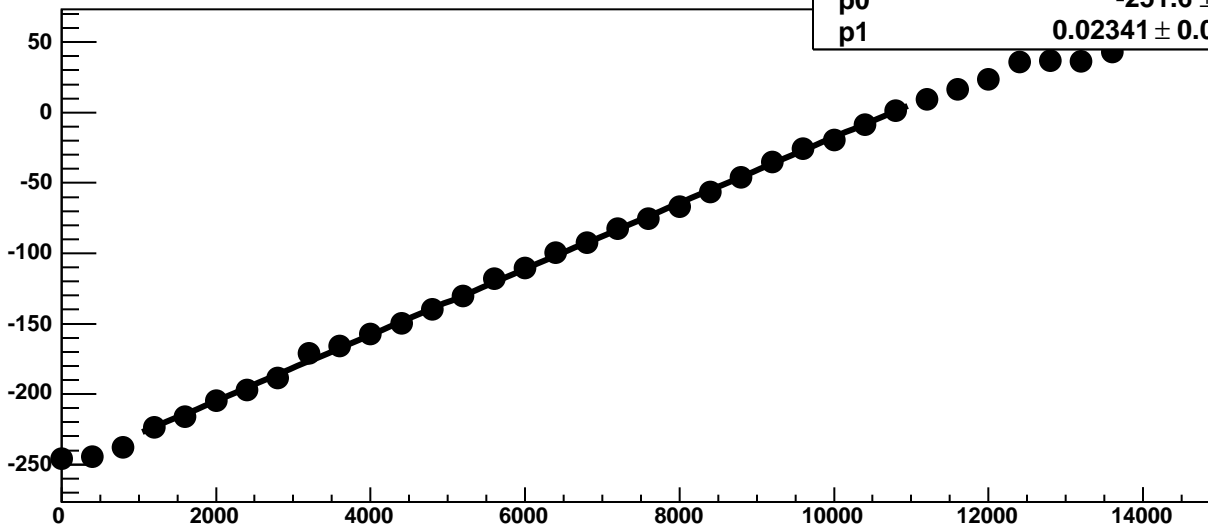
Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

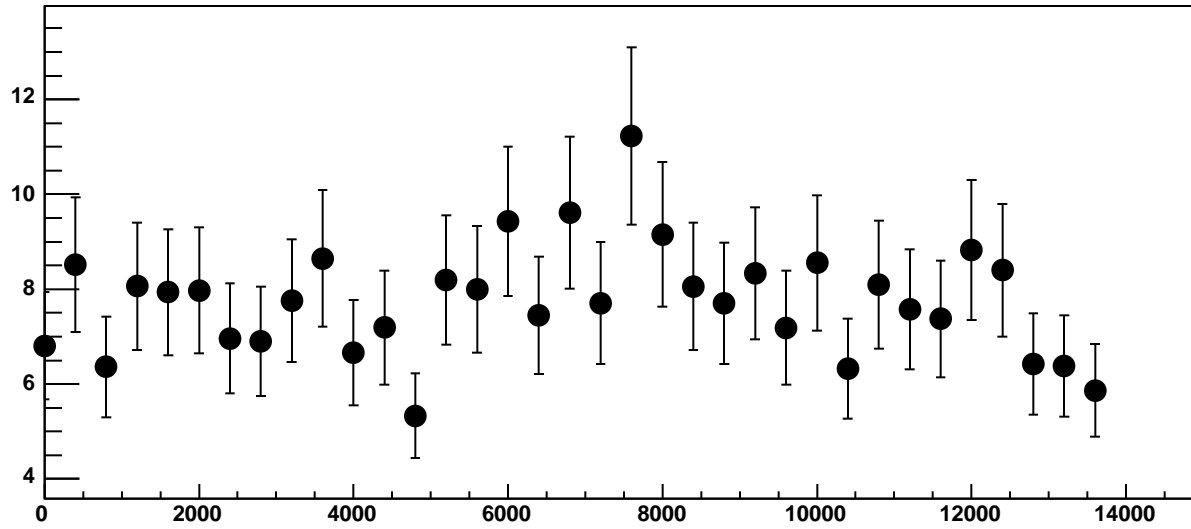


Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

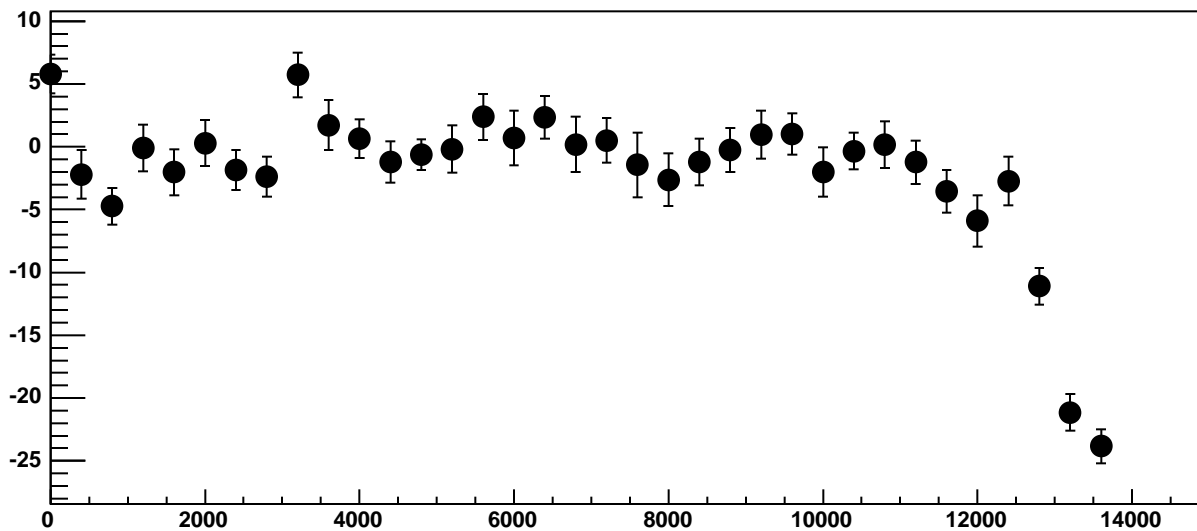


$\chi^2 / \text{ndf}$  24.87 / 23  
p0  $-251.6 \pm 0.7954$   
p1  $0.02341 \pm 0.0001213$

Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

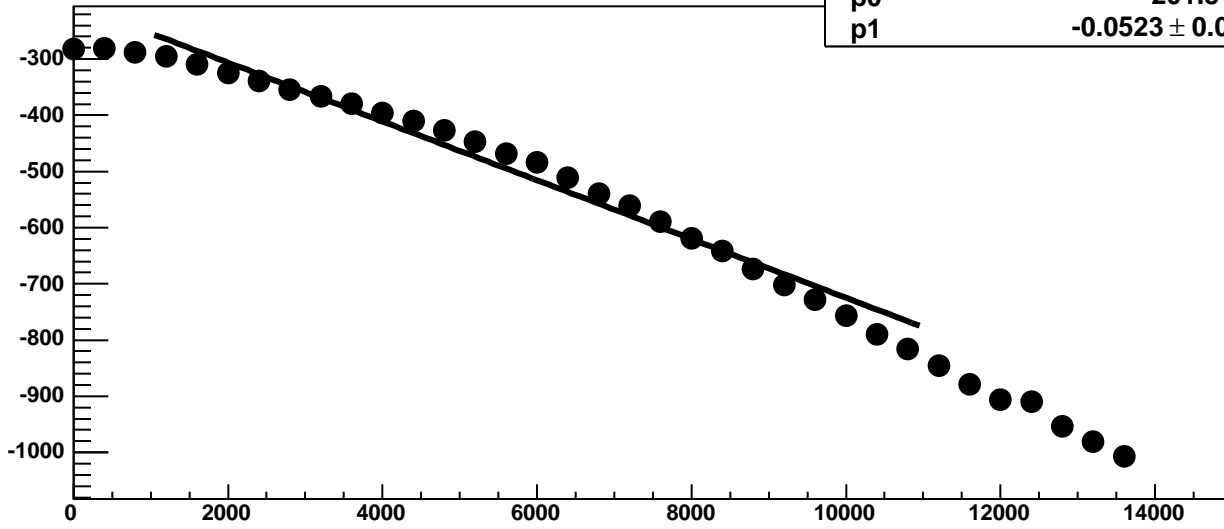
2146 / 23

p0

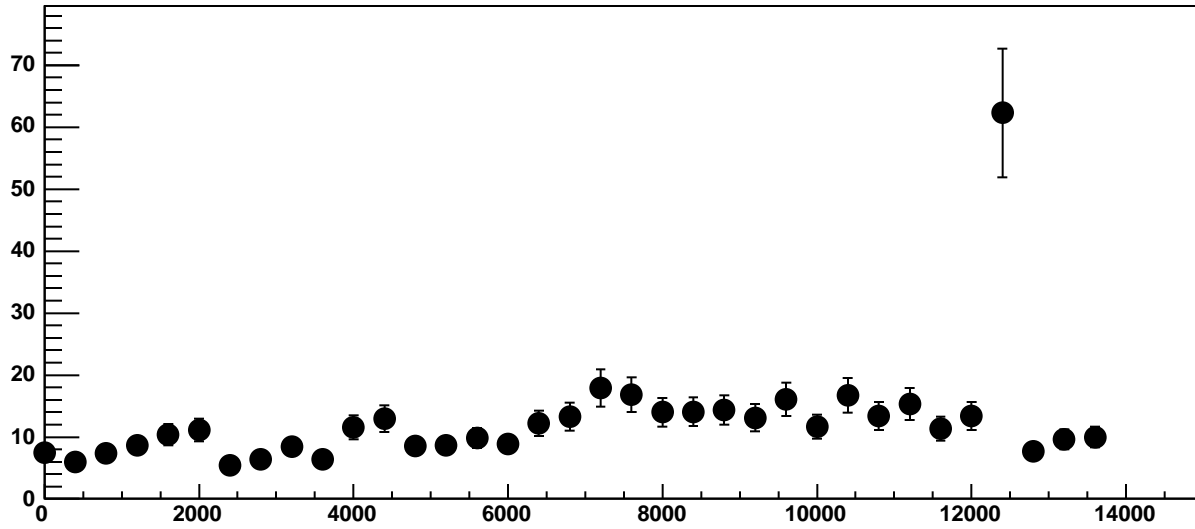
$-201.8 \pm 0.938$

p1

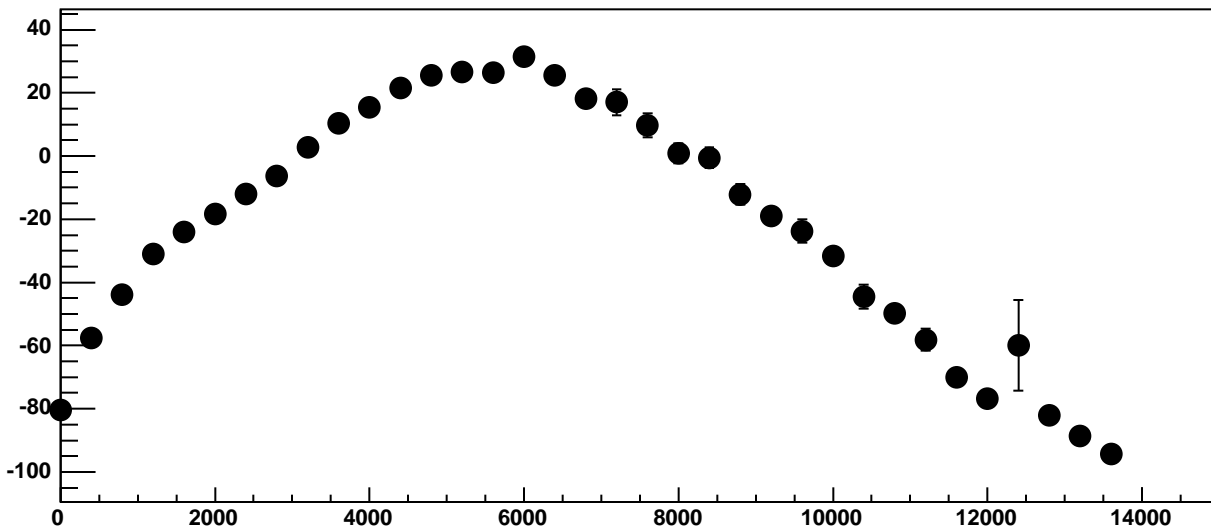
$-0.0523 \pm 0.0001751$



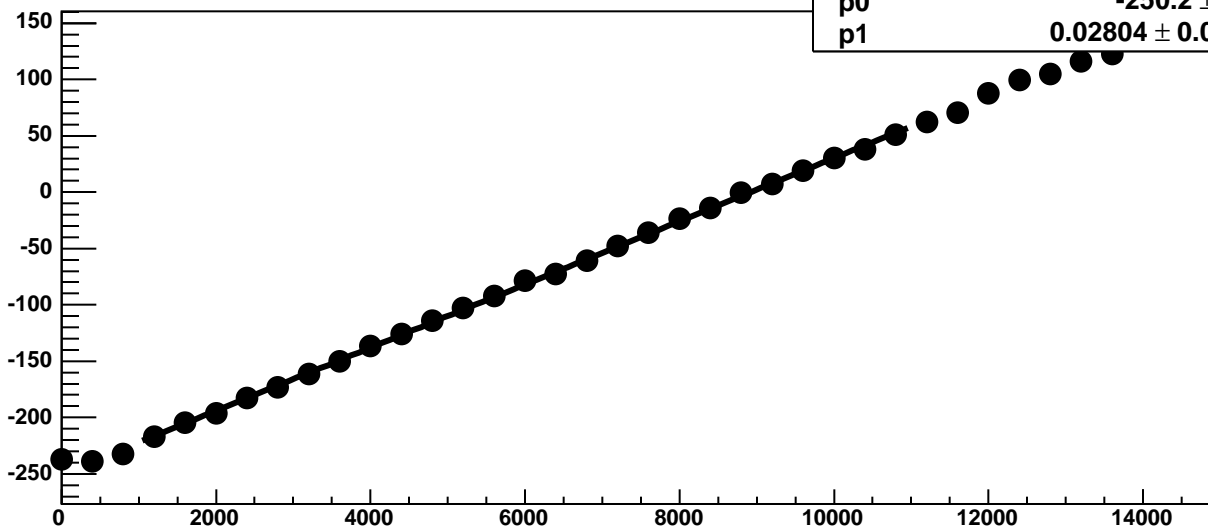
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

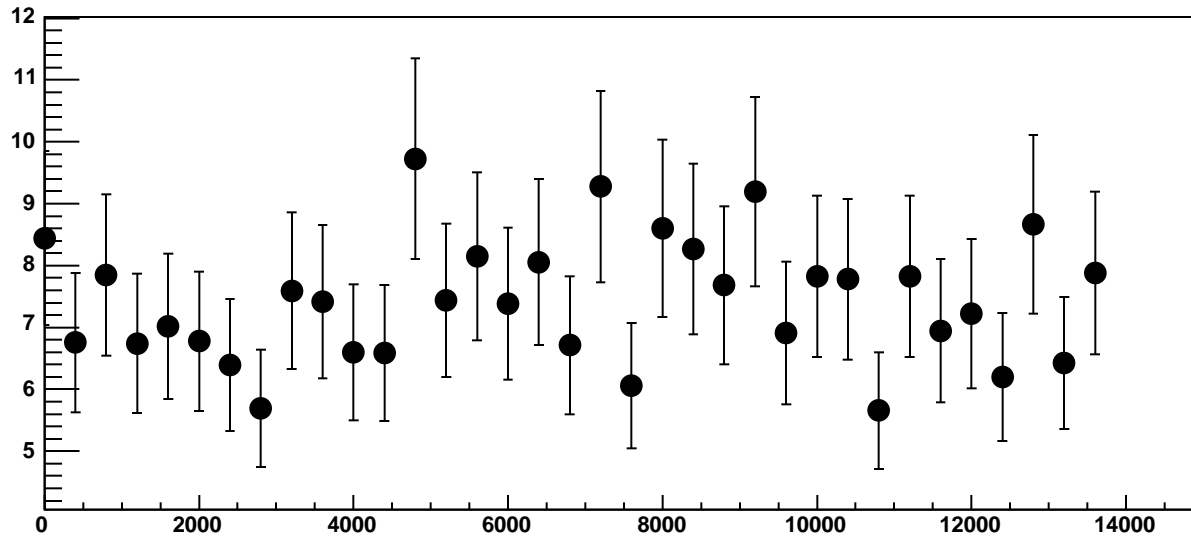


Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

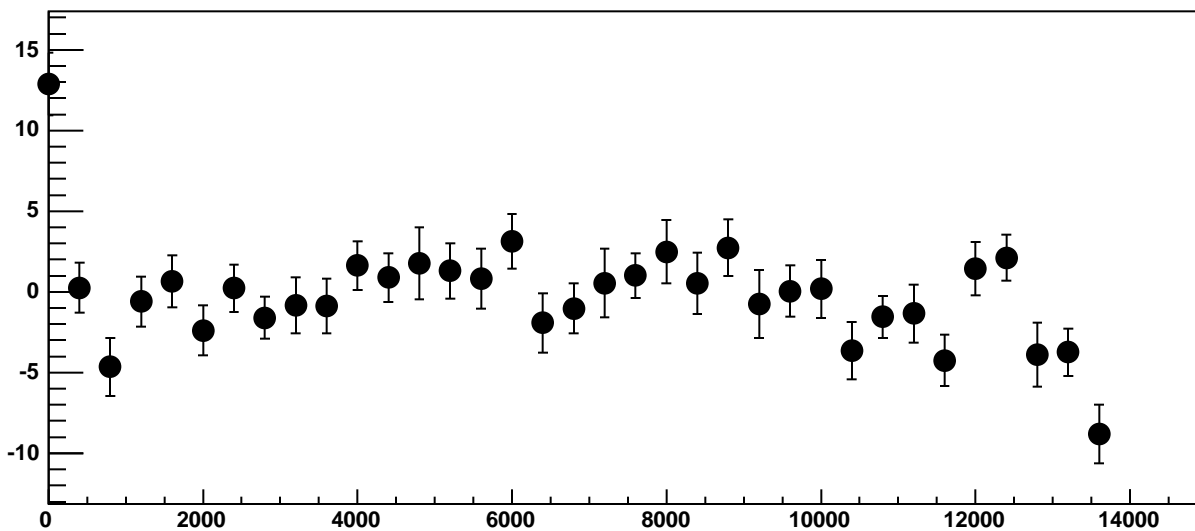


$\chi^2 / \text{ndf}$  23.05 / 23  
p0  $-250.2 \pm 0.7206$   
p1  $0.02804 \pm 0.0001099$

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

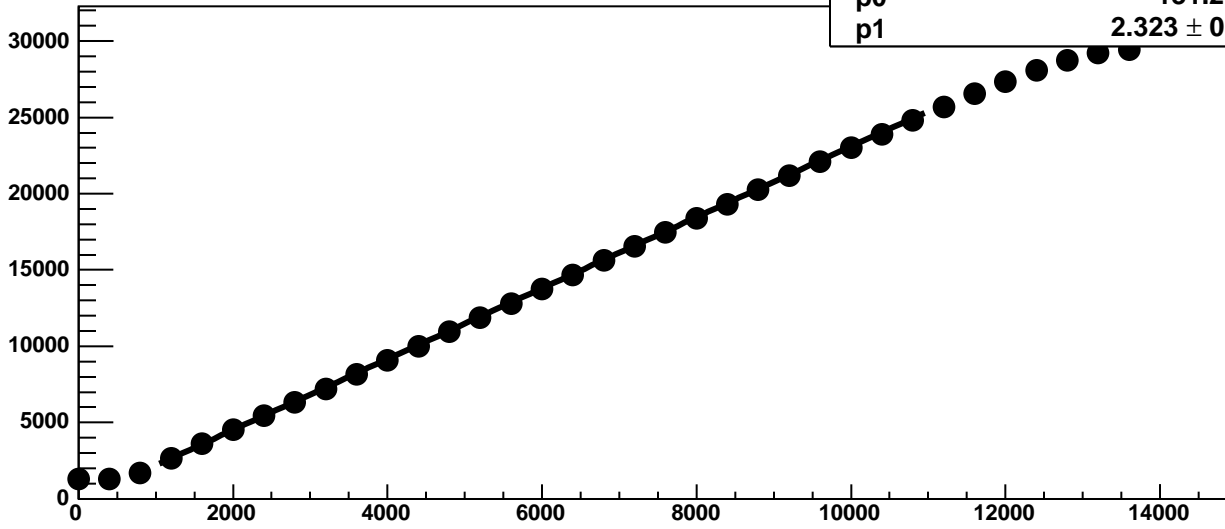


Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

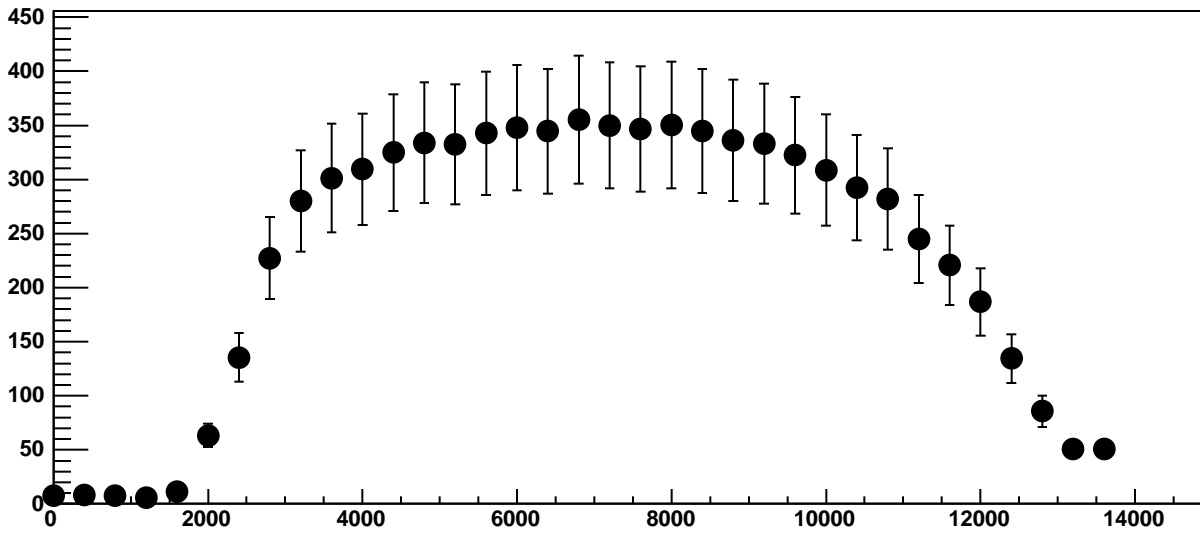


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

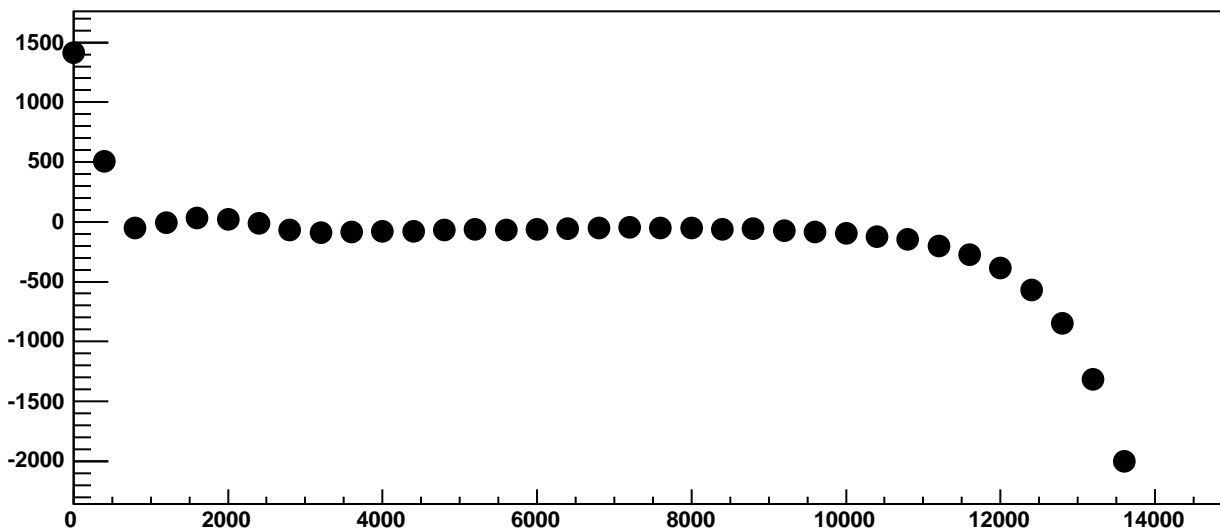
$\chi^2 / \text{ndf}$  182.8 / 23  
p0 -131.2 ± 3.473  
p1 2.323 ± 0.002483



Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC

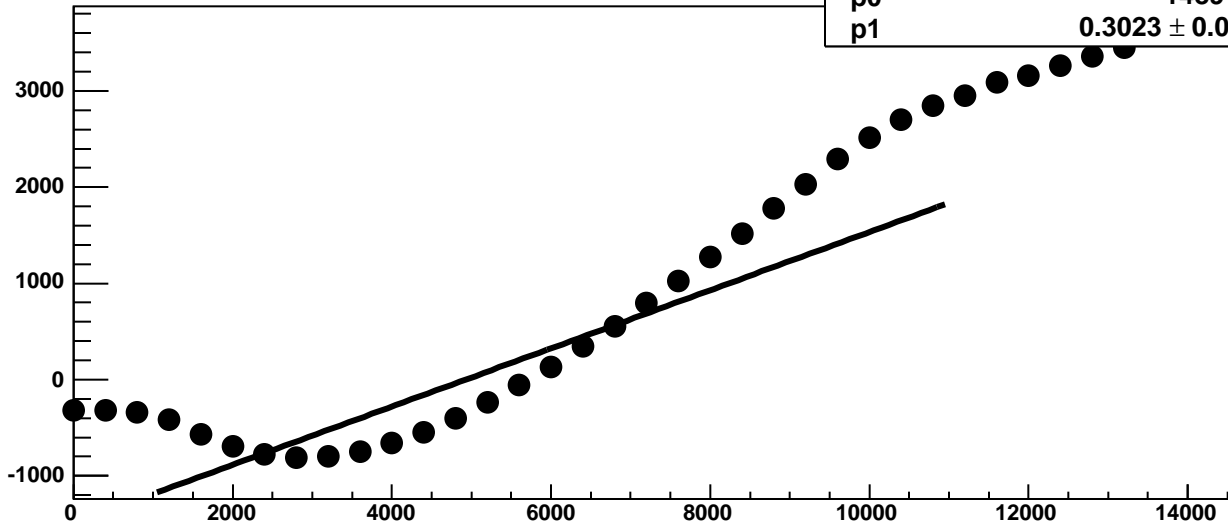


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC

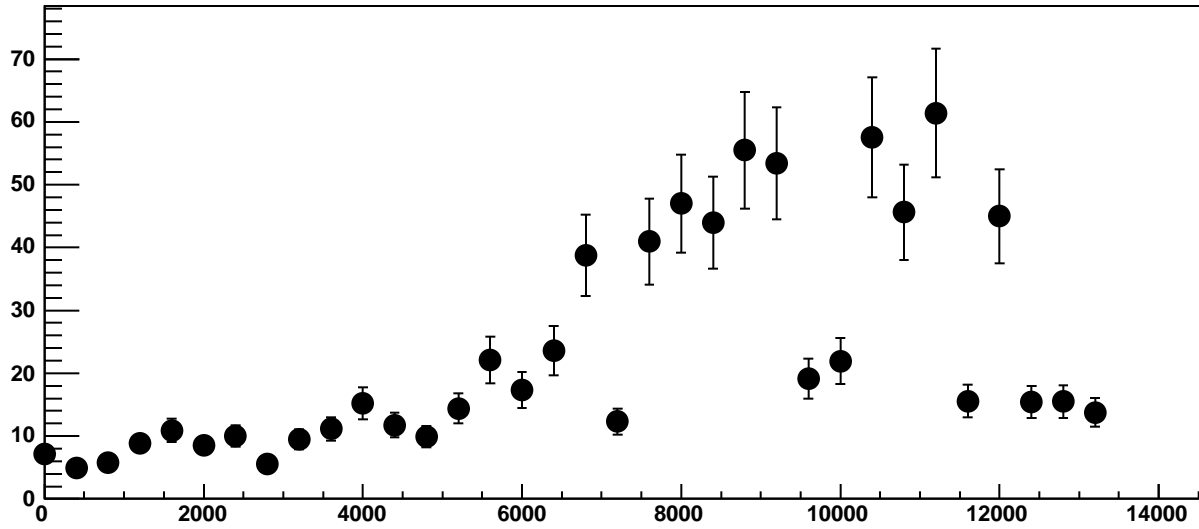


Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

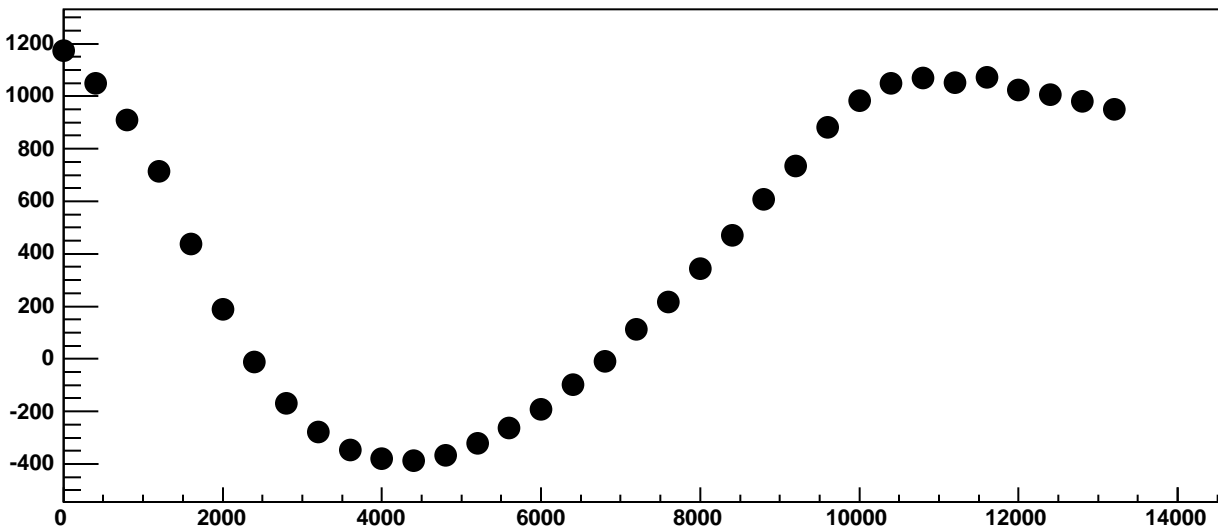
$\chi^2 / \text{ndf}$  3.951e+05 / 23  
p0 -1489 ± 1.221  
p1 0.3023 ± 0.0002921



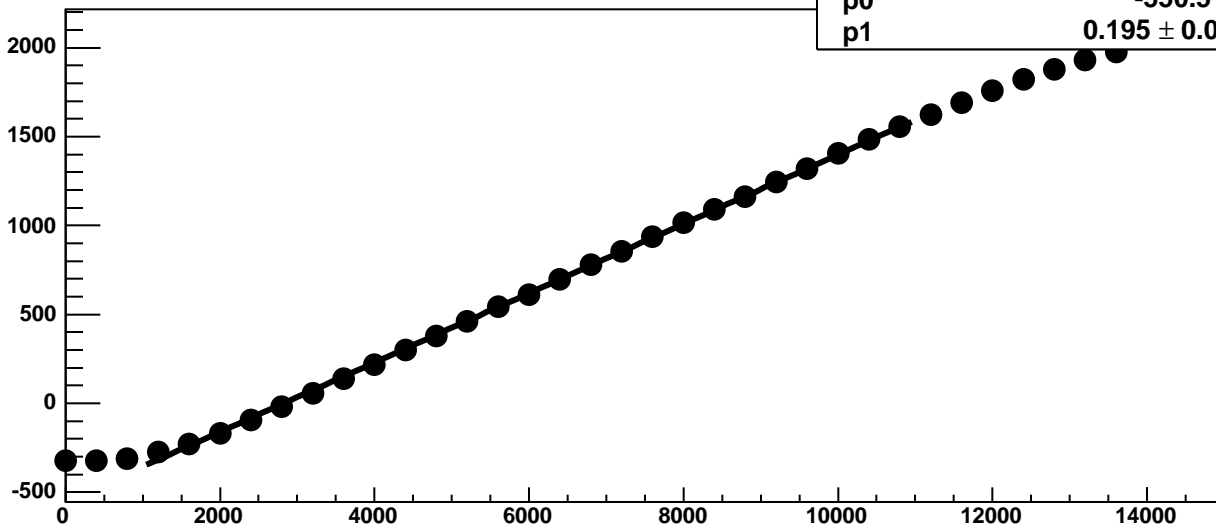
Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

461.6 / 23

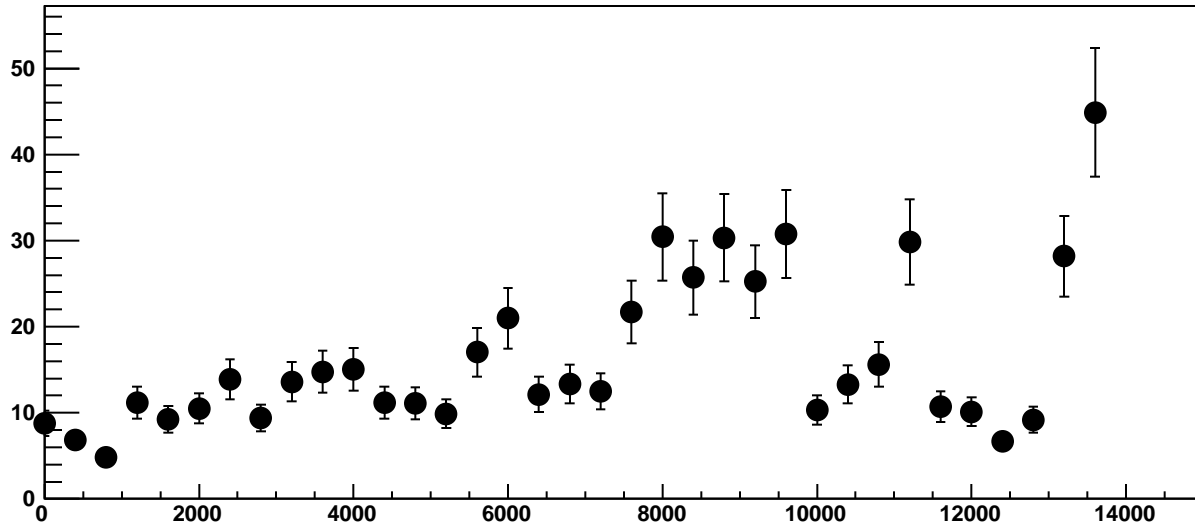
p0

$-550.5 \pm 1.239$

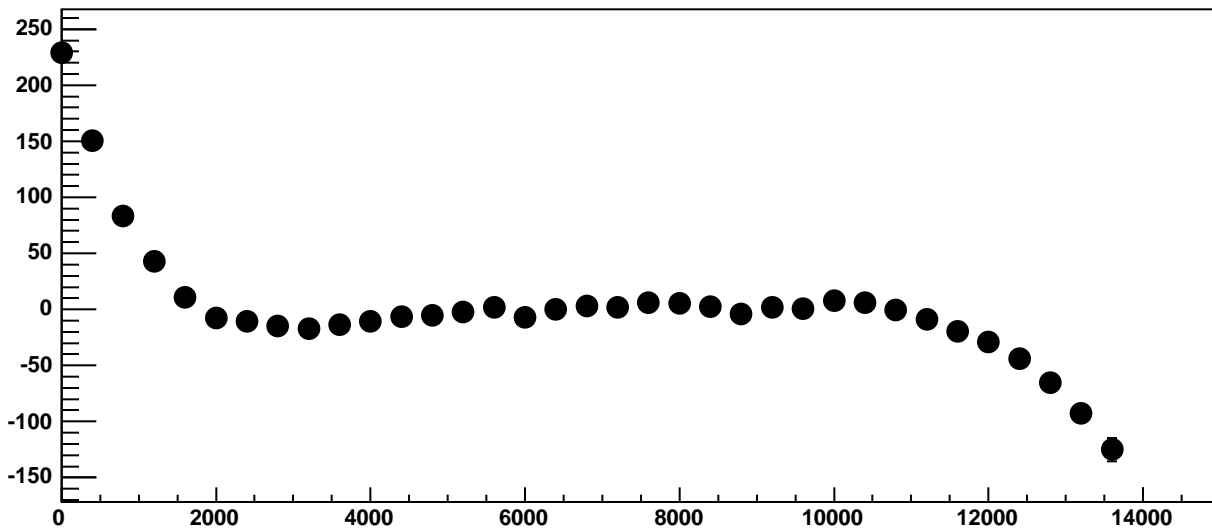
p1

$0.195 \pm 0.0002124$

Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

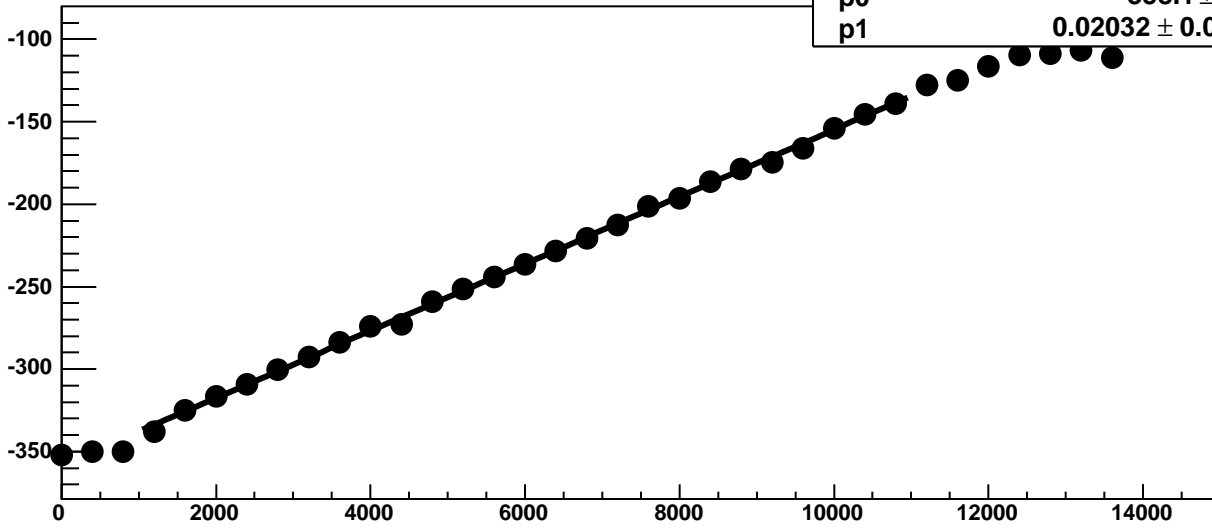


Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

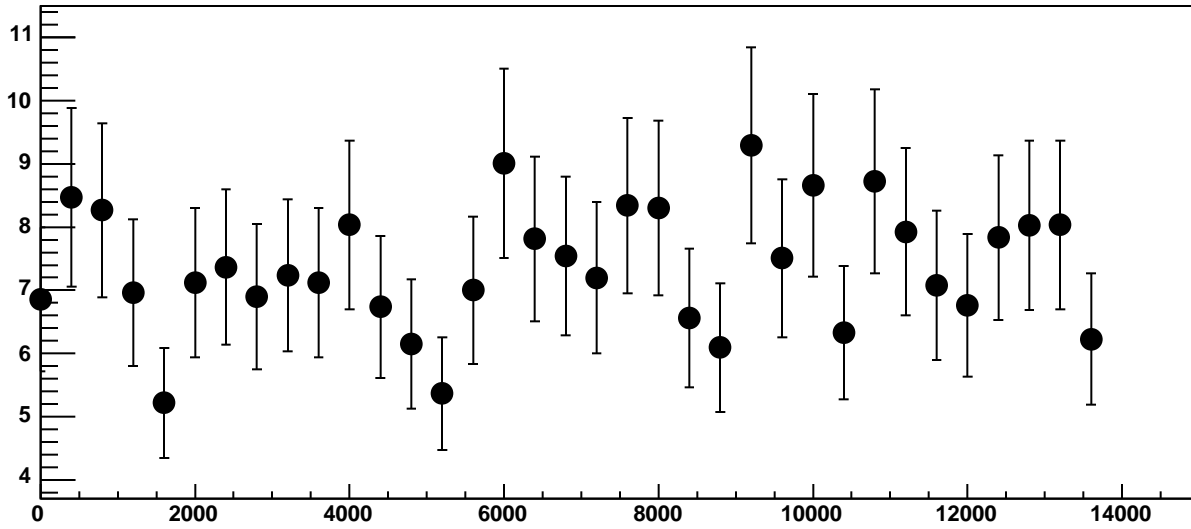




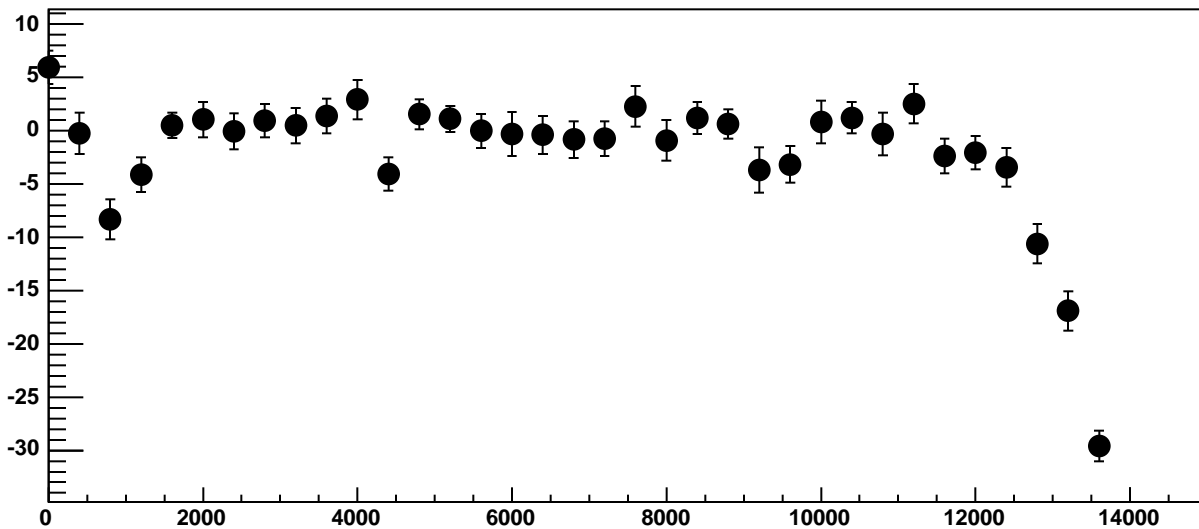
Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC



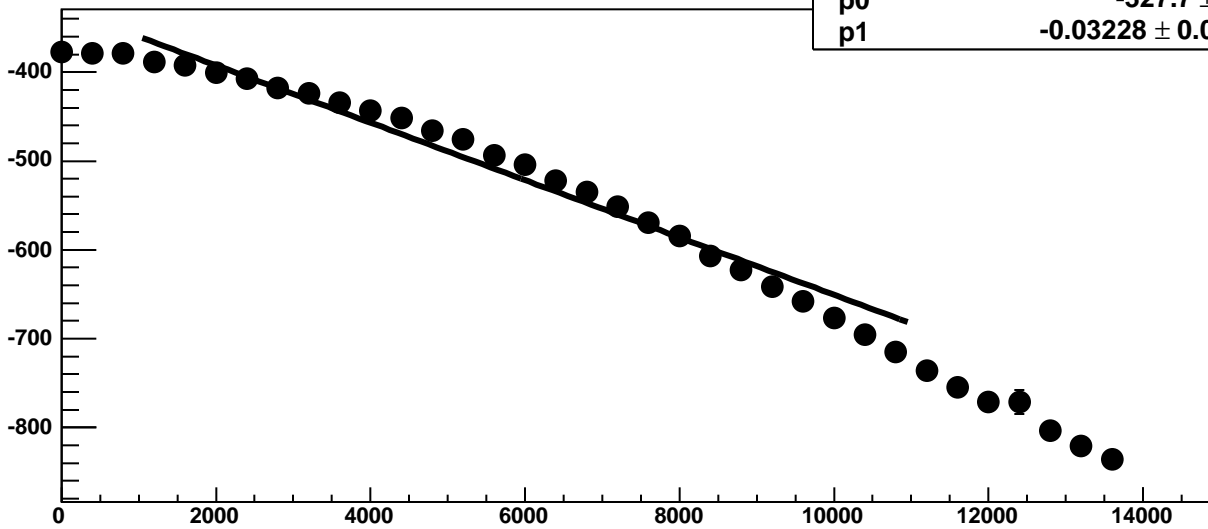
Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

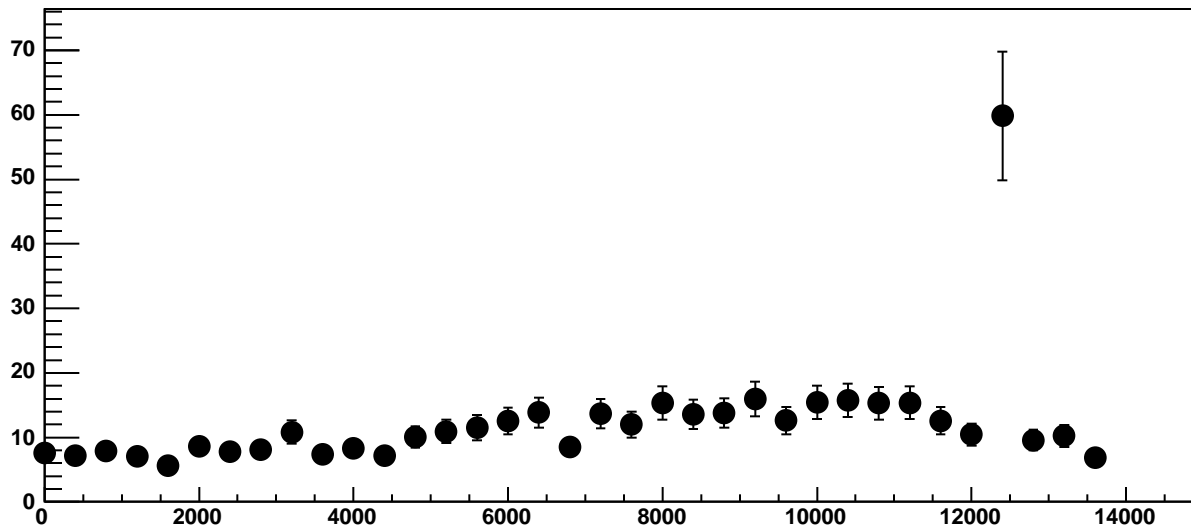


Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

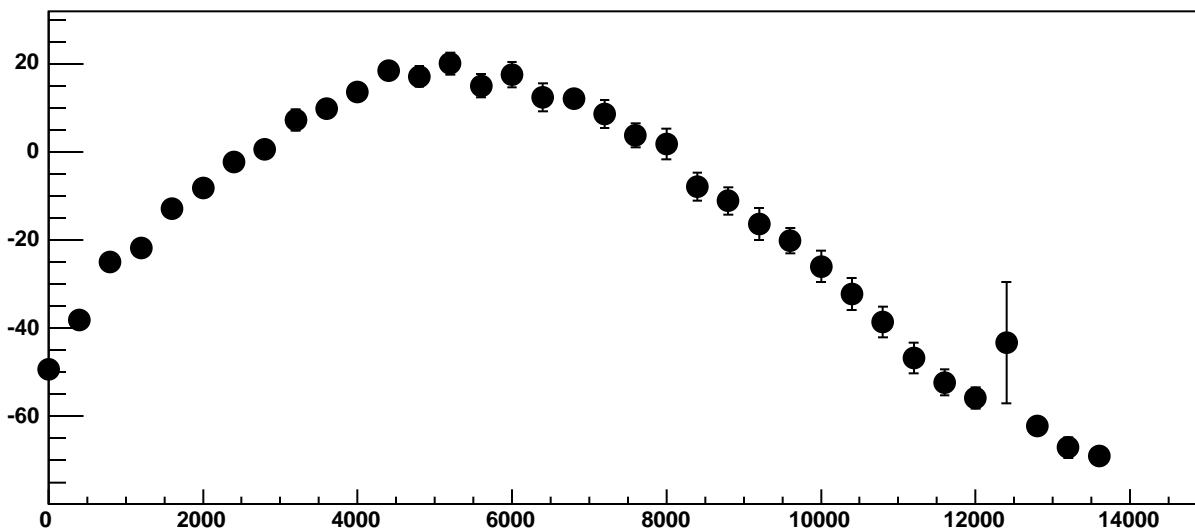


$\chi^2 / \text{ndf}$	1112 / 23
p0	$-327.7 \pm 0.8803$
p1	$-0.03228 \pm 0.0001682$

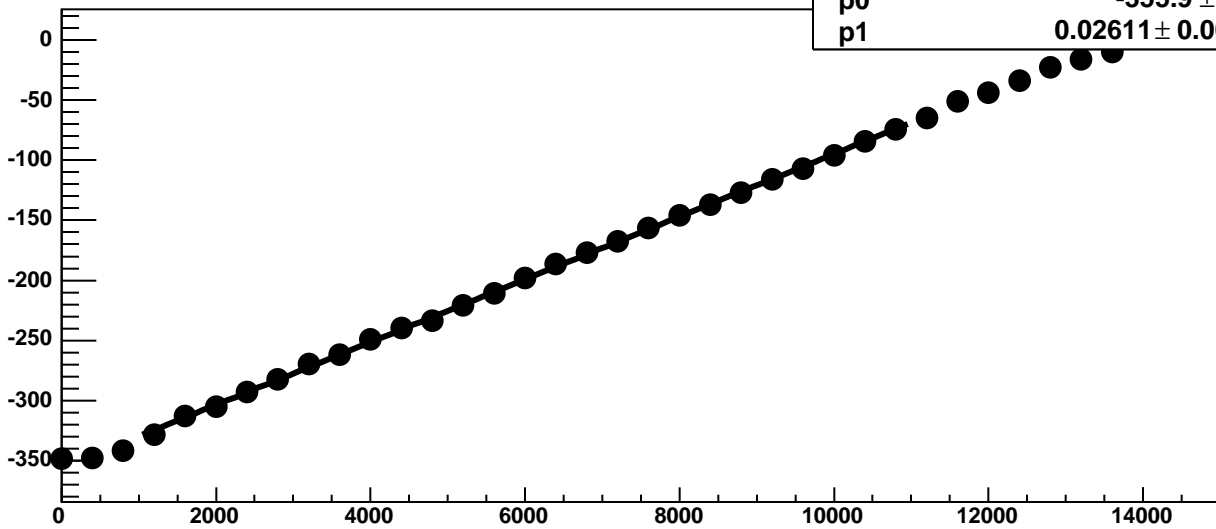
Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

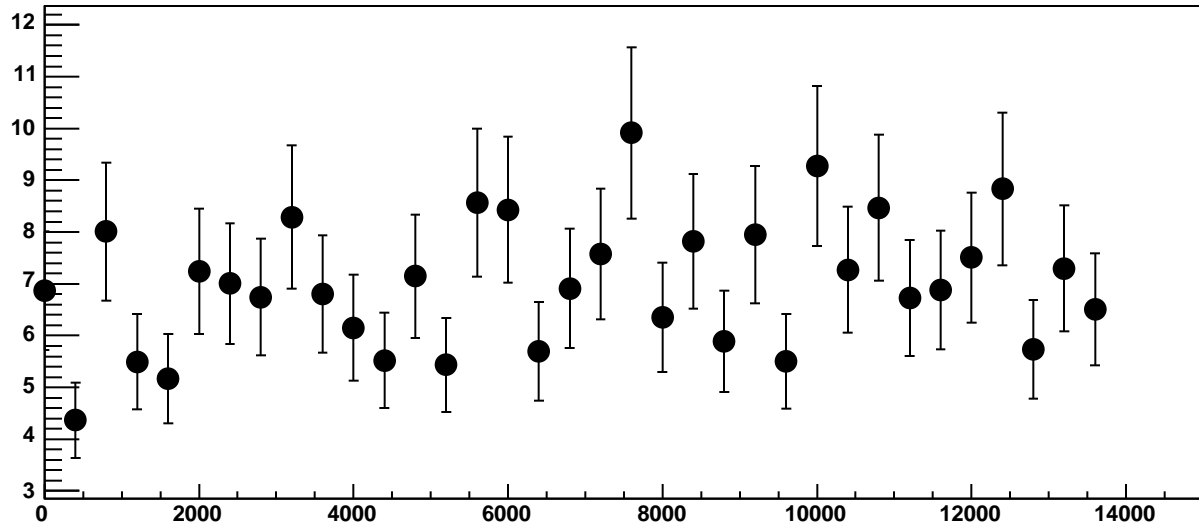


Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

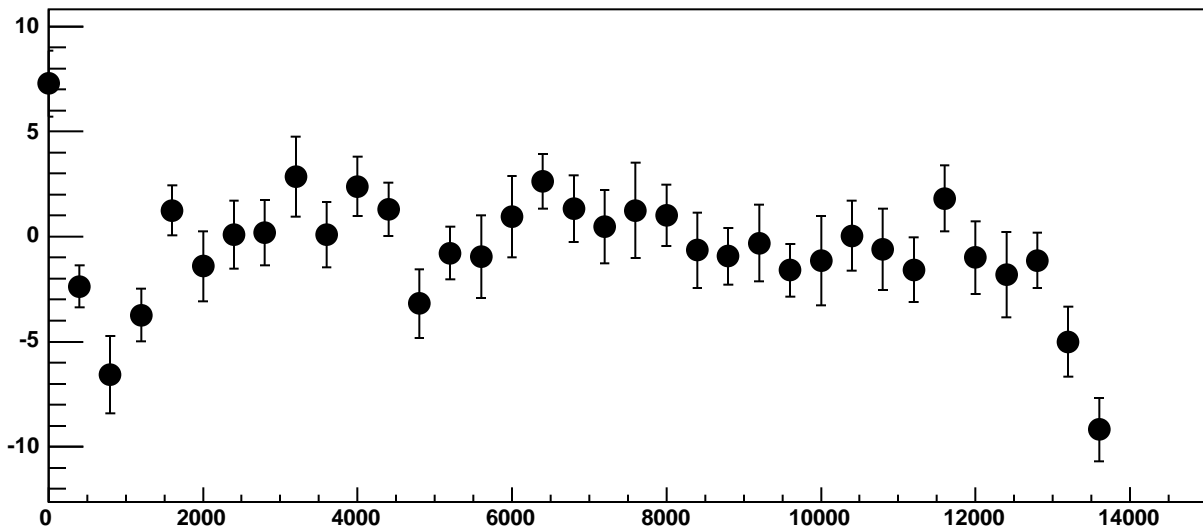


$\chi^2 / \text{ndf}$  29.64 / 23  
p0  $-355.9 \pm 0.6757$   
p1  $0.02611 \pm 0.0001064$

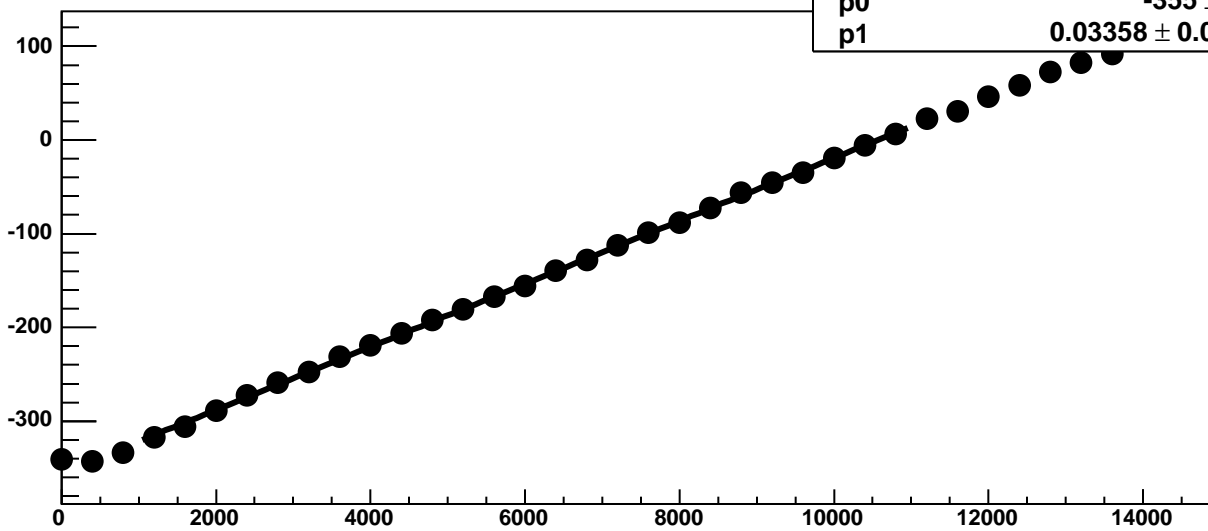
Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

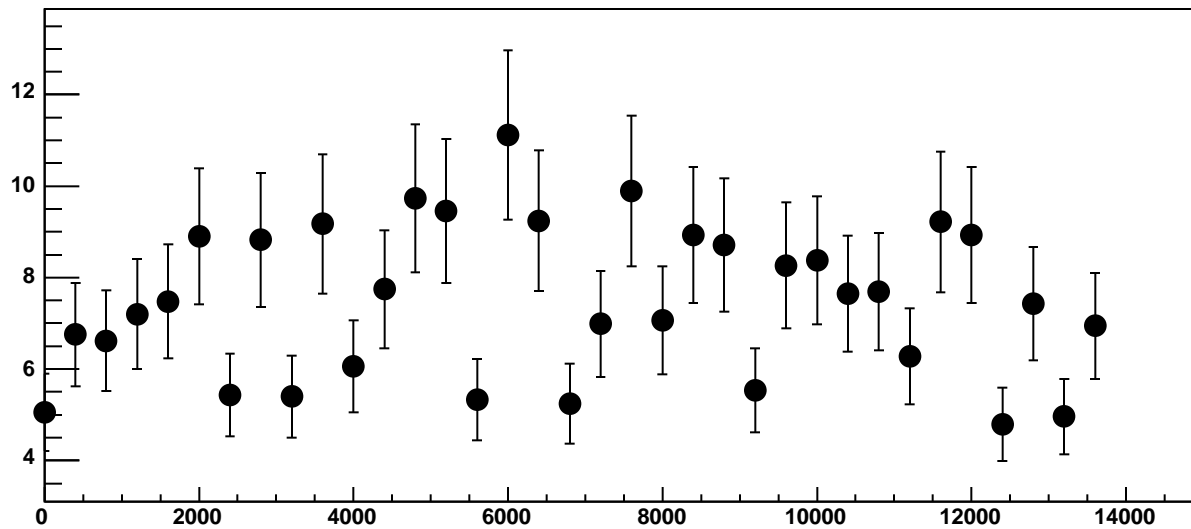


Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

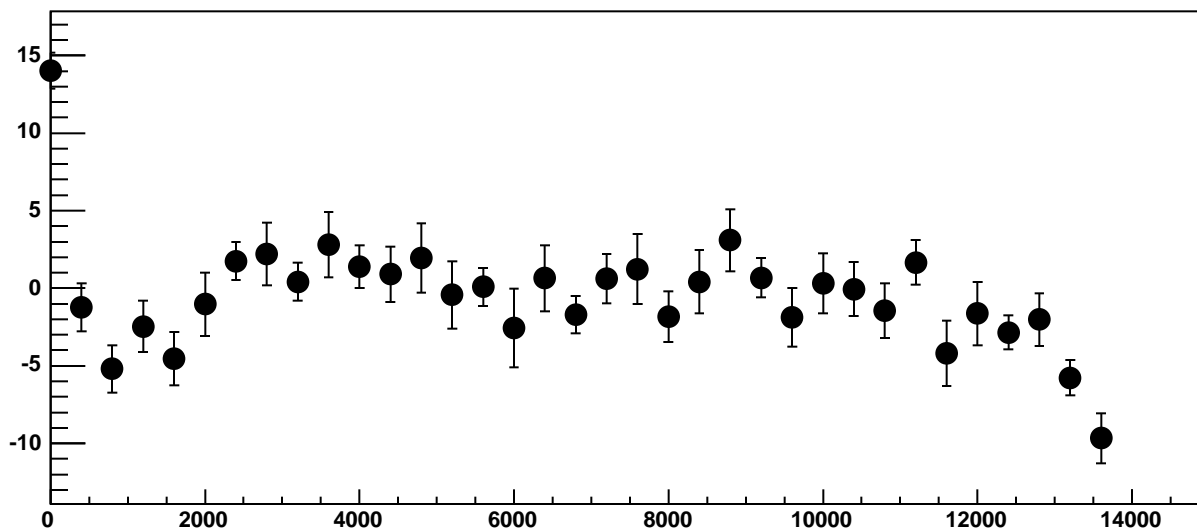


$\chi^2 / \text{ndf}$  25.83 / 23  
p0  $-355 \pm 0.7581$   
p1  $0.03358 \pm 0.0001165$

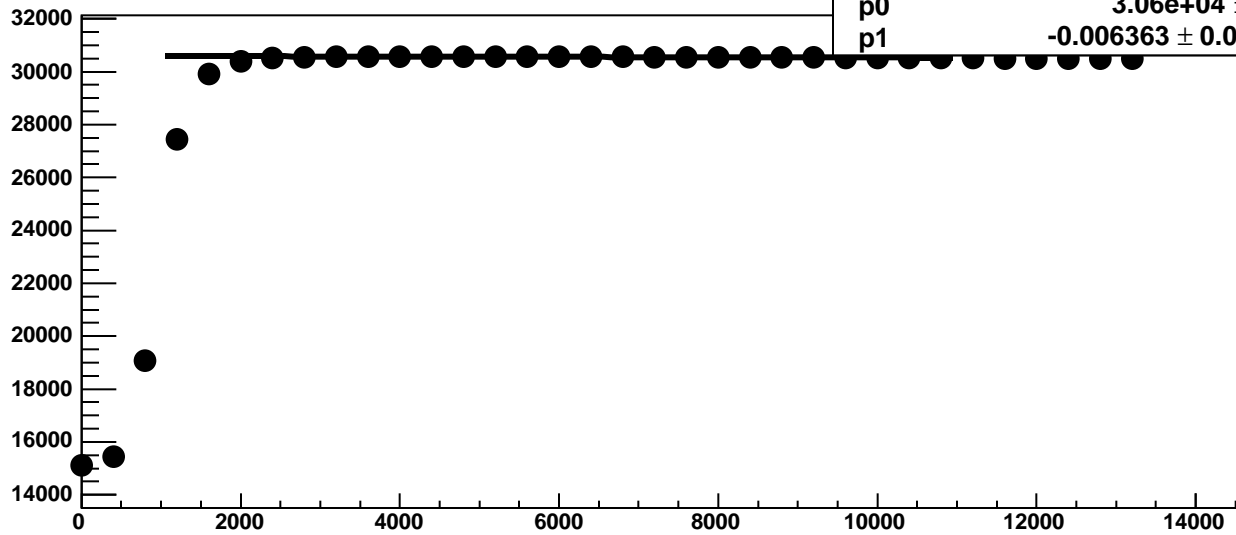
Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



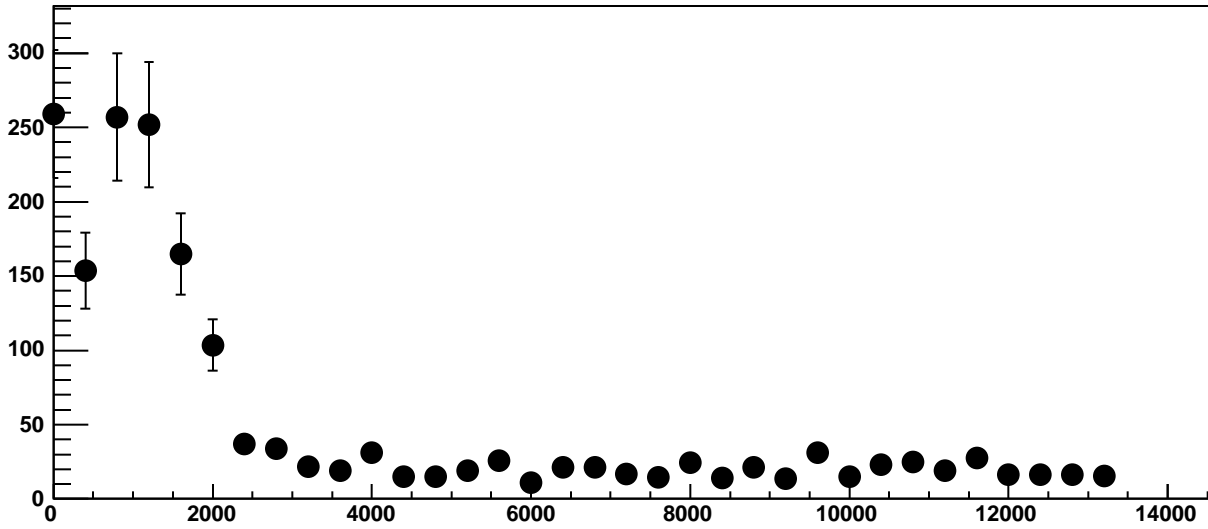
Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



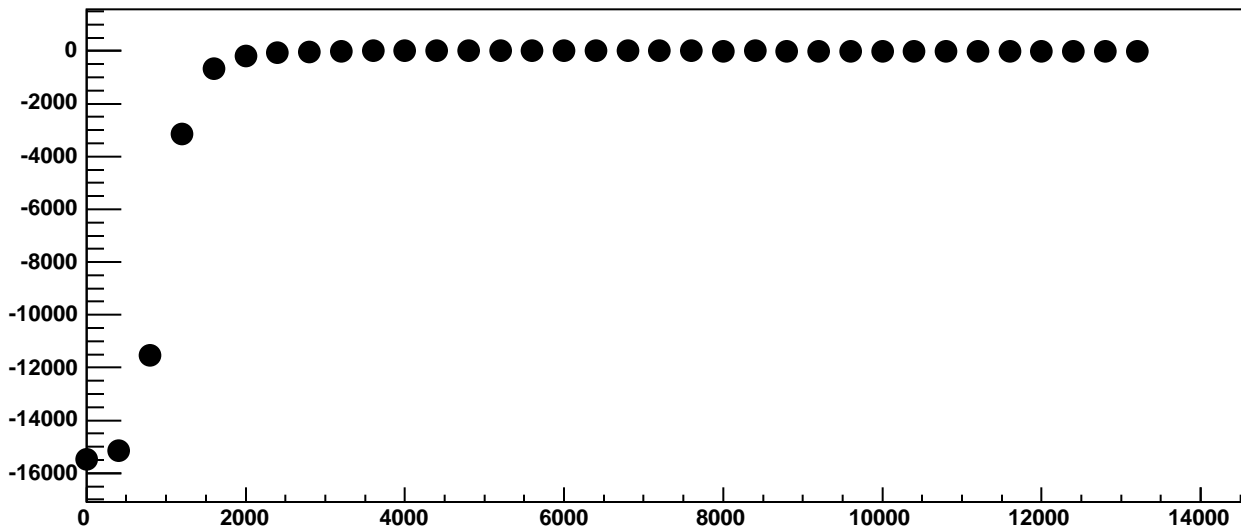
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



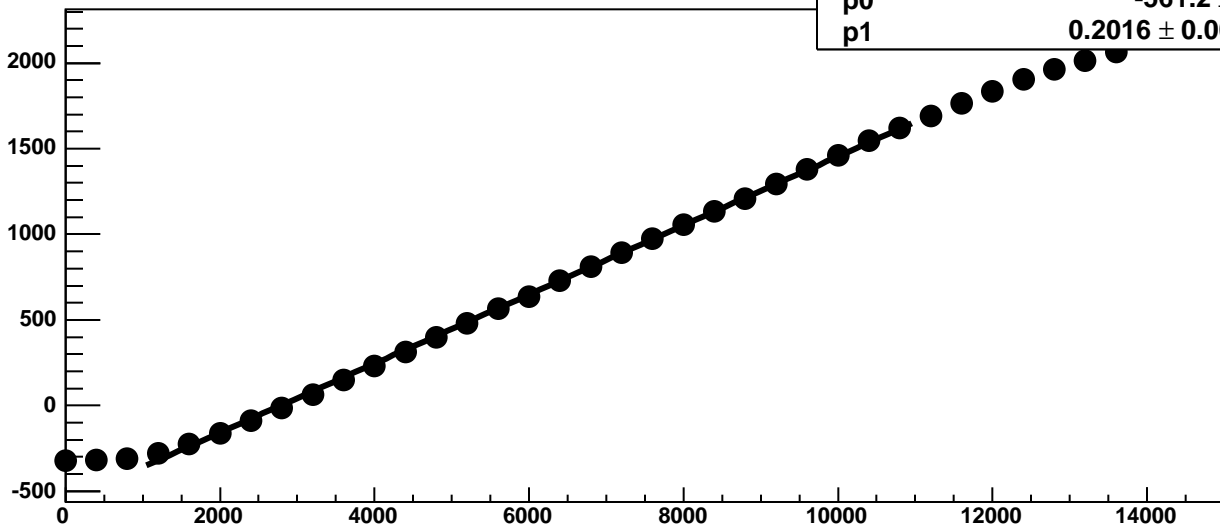
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



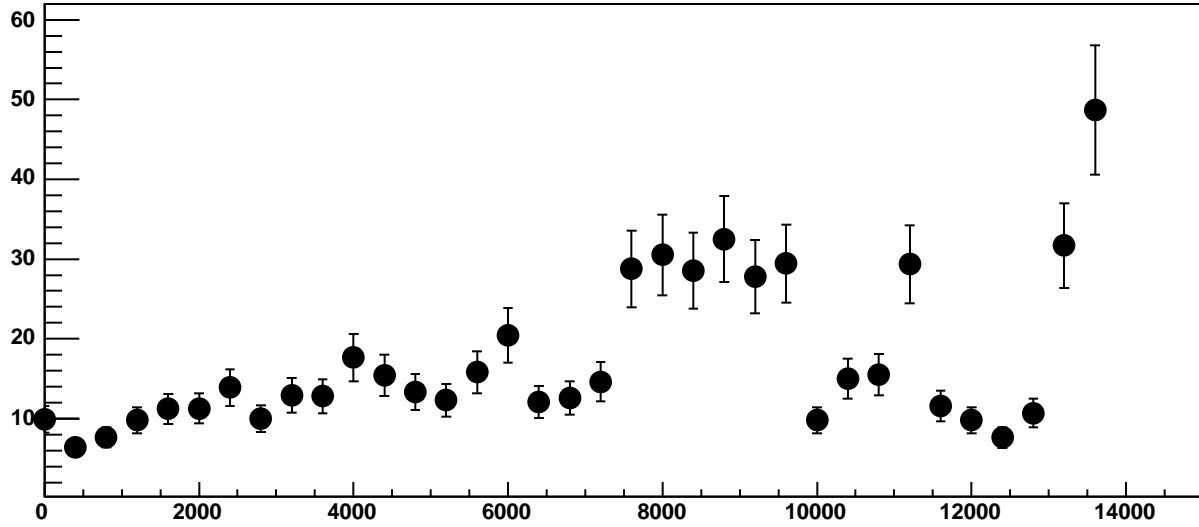
Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



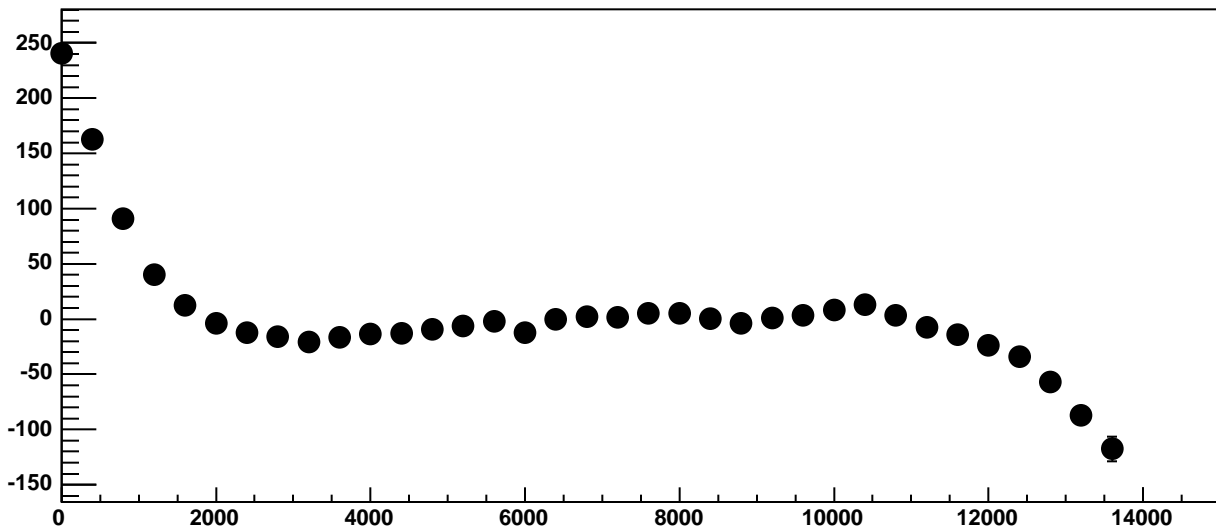
Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



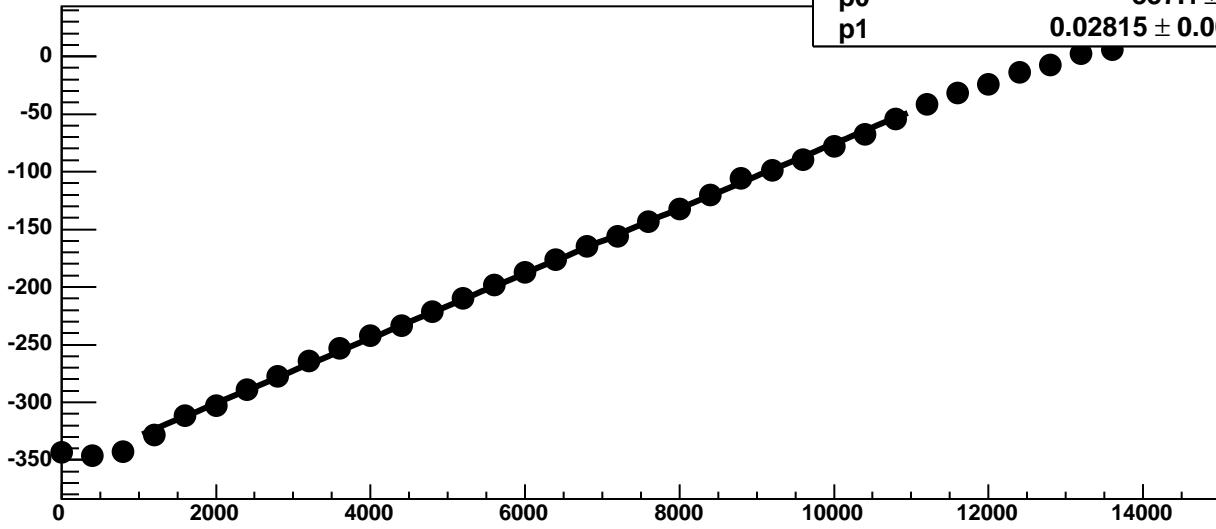
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



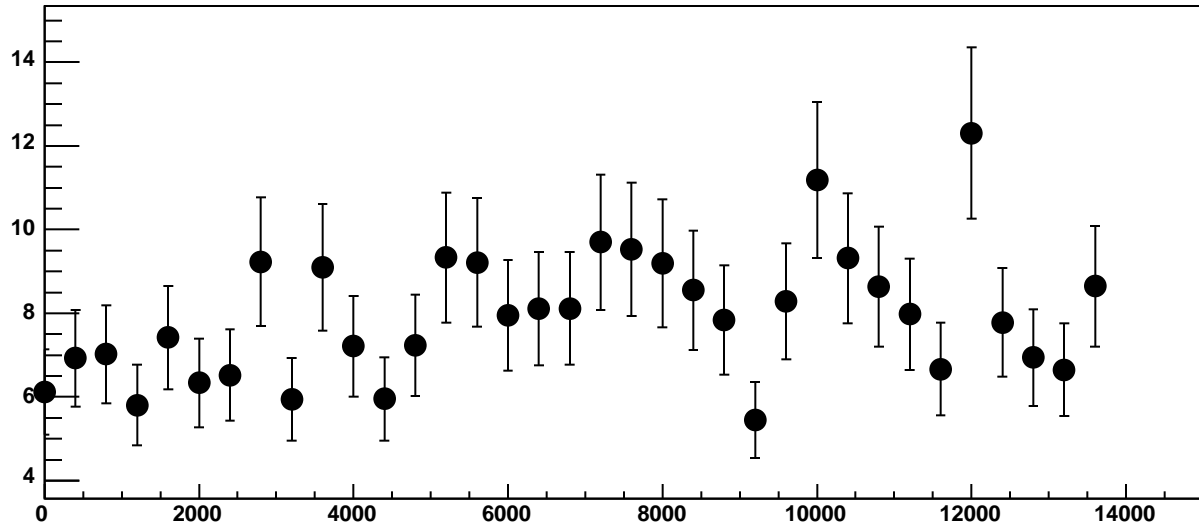
Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



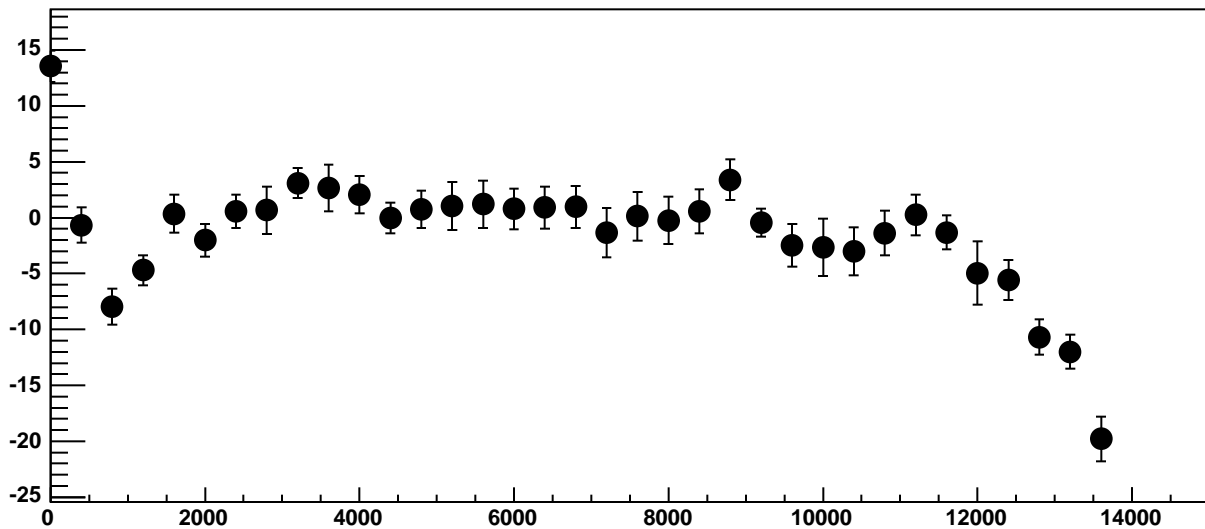
Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



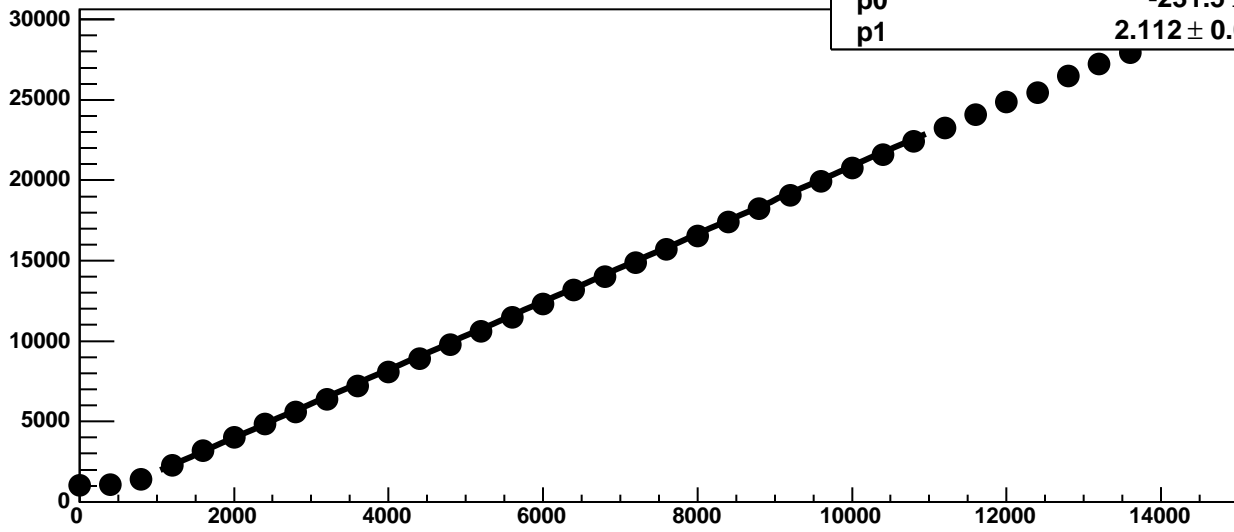
Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

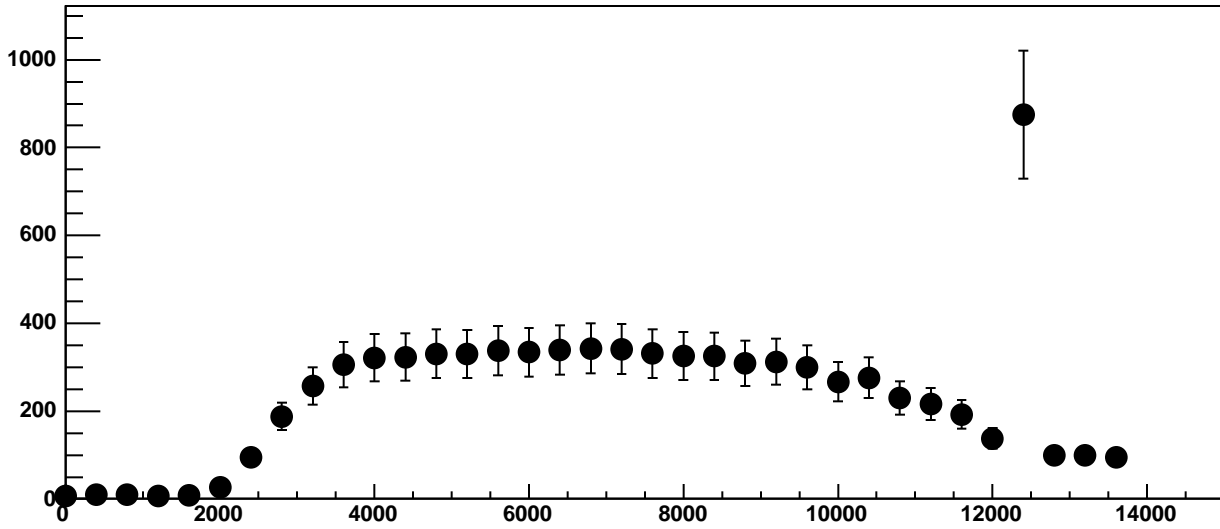


Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

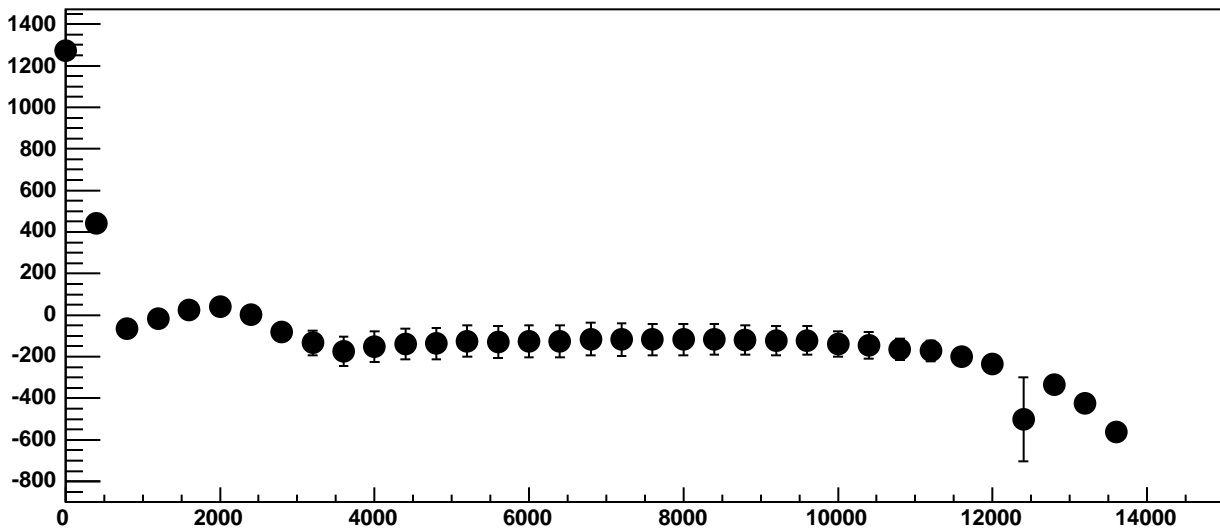


$\chi^2 / \text{ndf}$  396.2 / 23  
p0  $-231.5 \pm 3.439$   
p1  $2.112 \pm 0.002242$

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

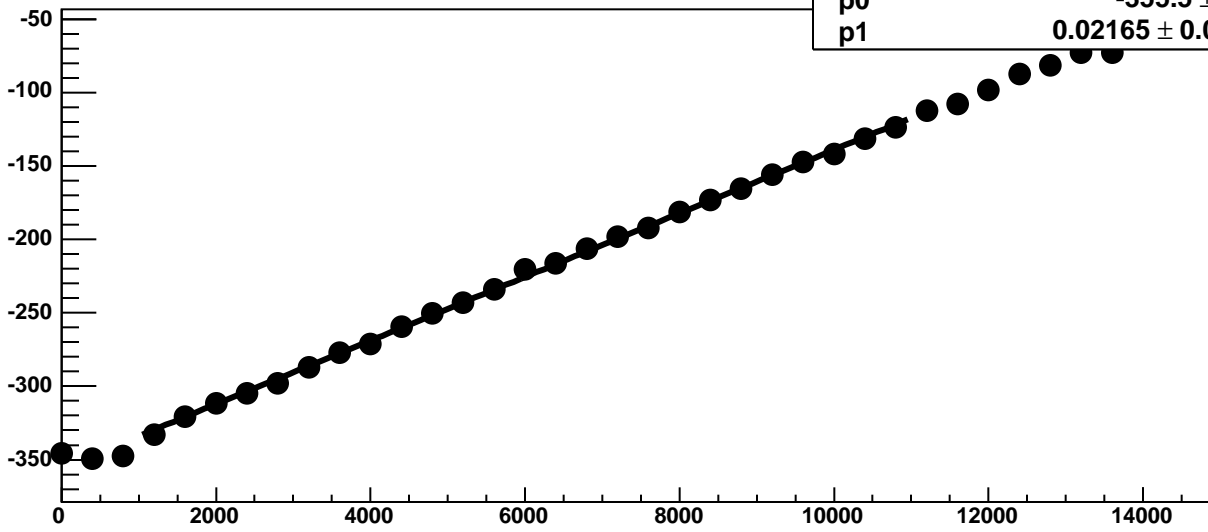


Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



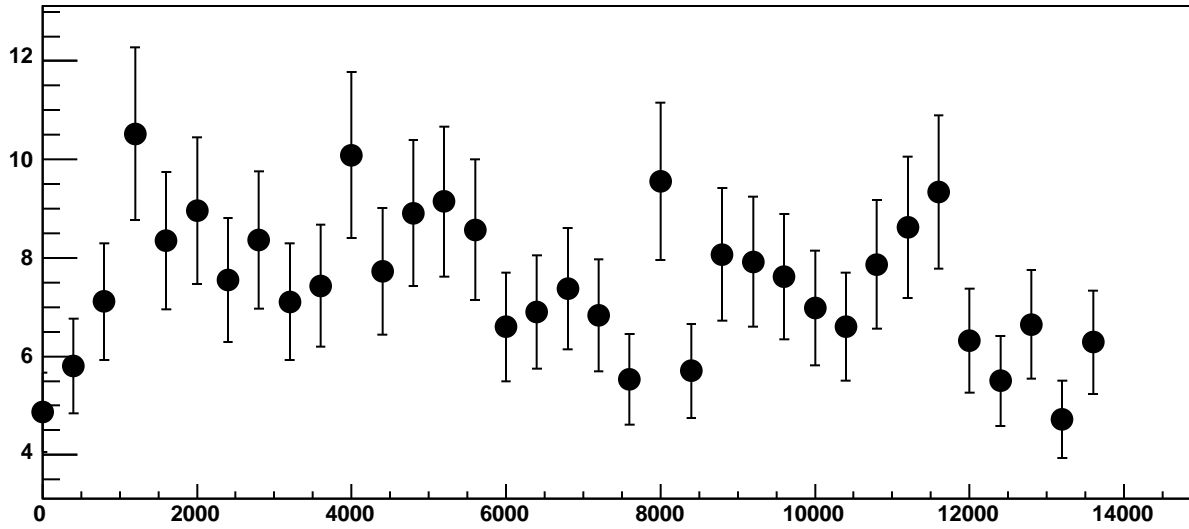


Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

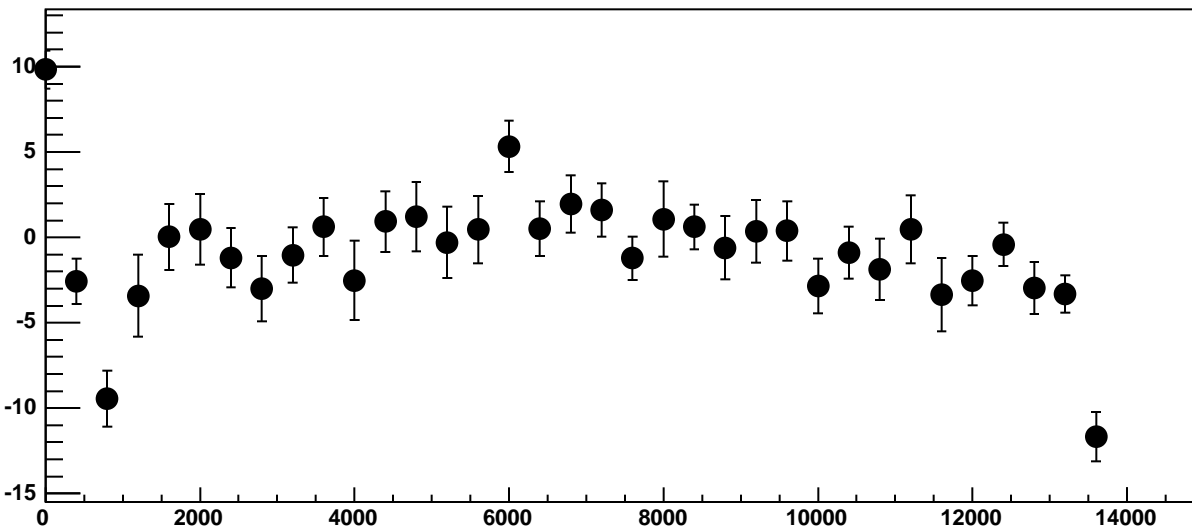


$\chi^2 / \text{ndf}$  28.49 / 23  
p0  $-355.5 \pm 0.8726$   
p1  $0.02165 \pm 0.0001251$

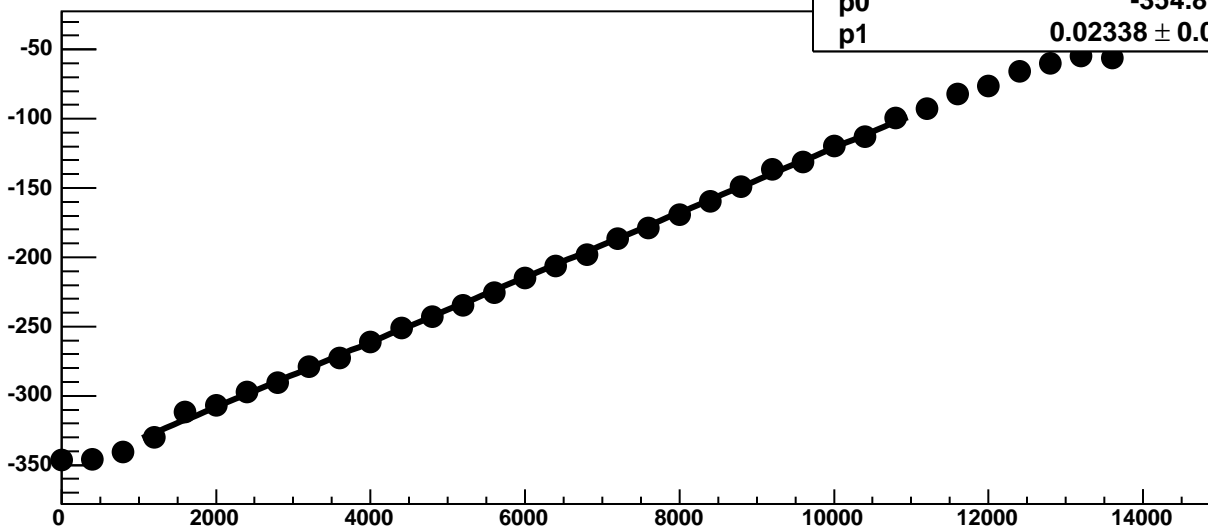
Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

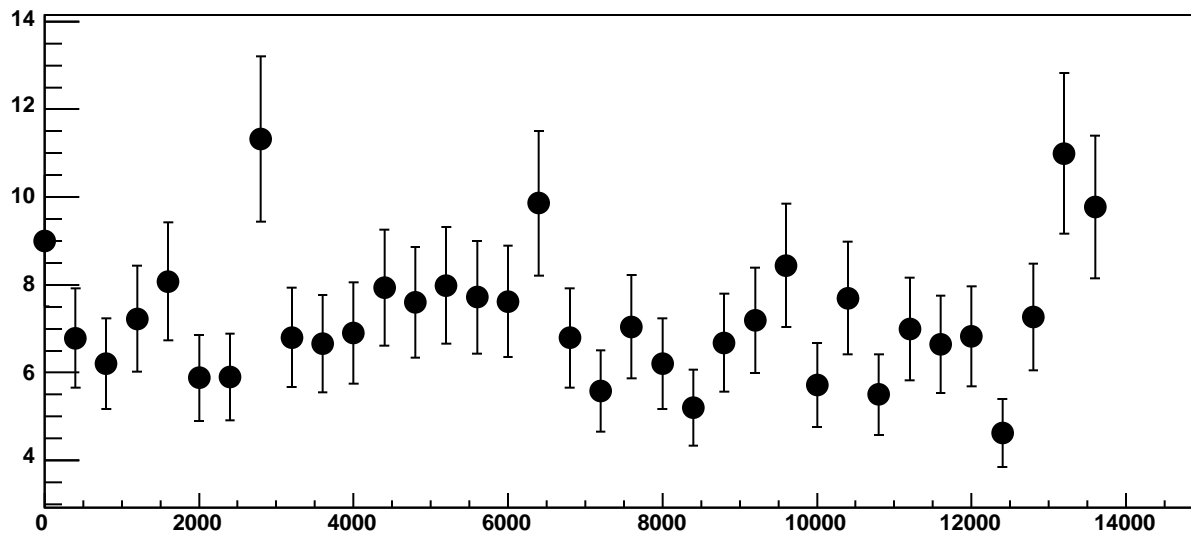


Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

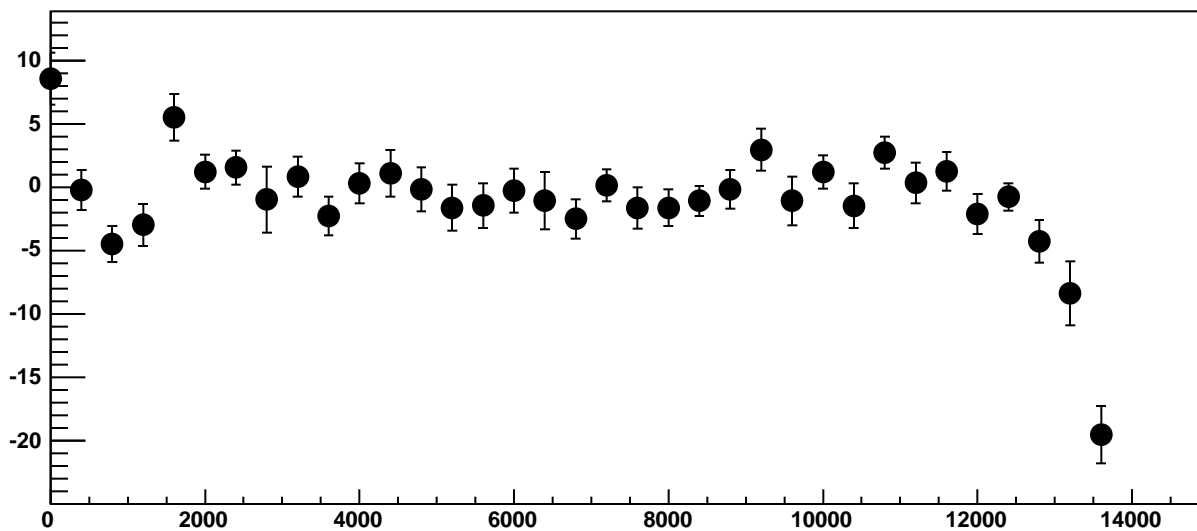


$\chi^2 / \text{ndf}$  34.22 / 23  
p0  $-354.8 \pm 0.737$   
p1  $0.02338 \pm 0.0001065$

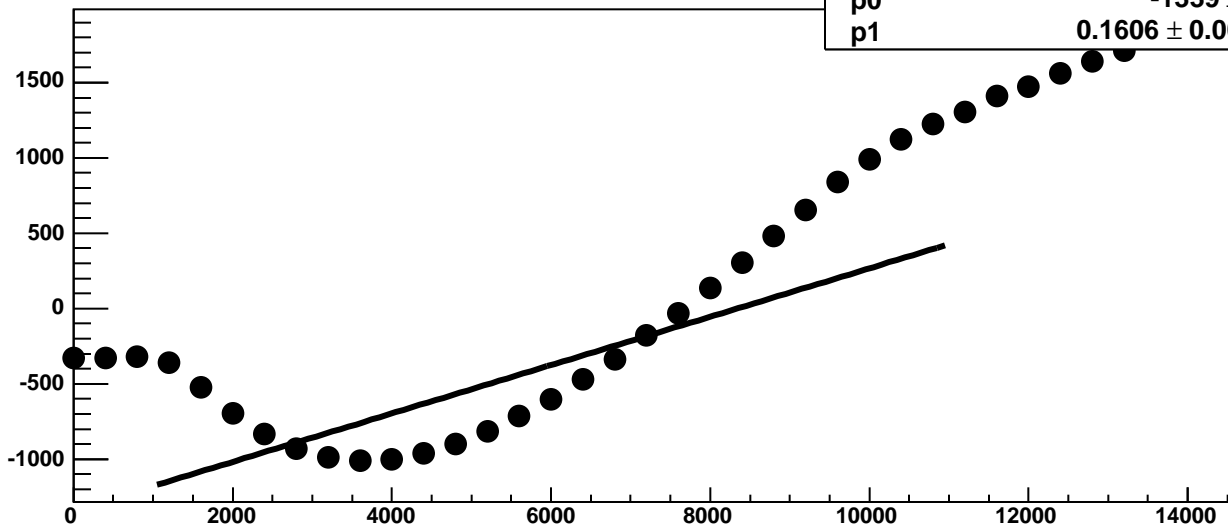
Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



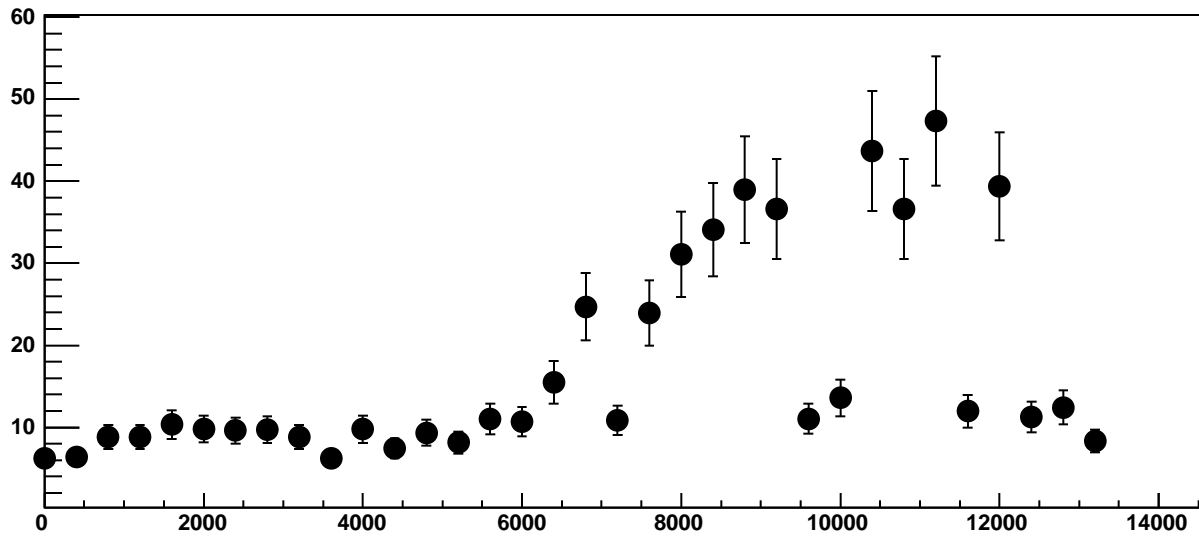
Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



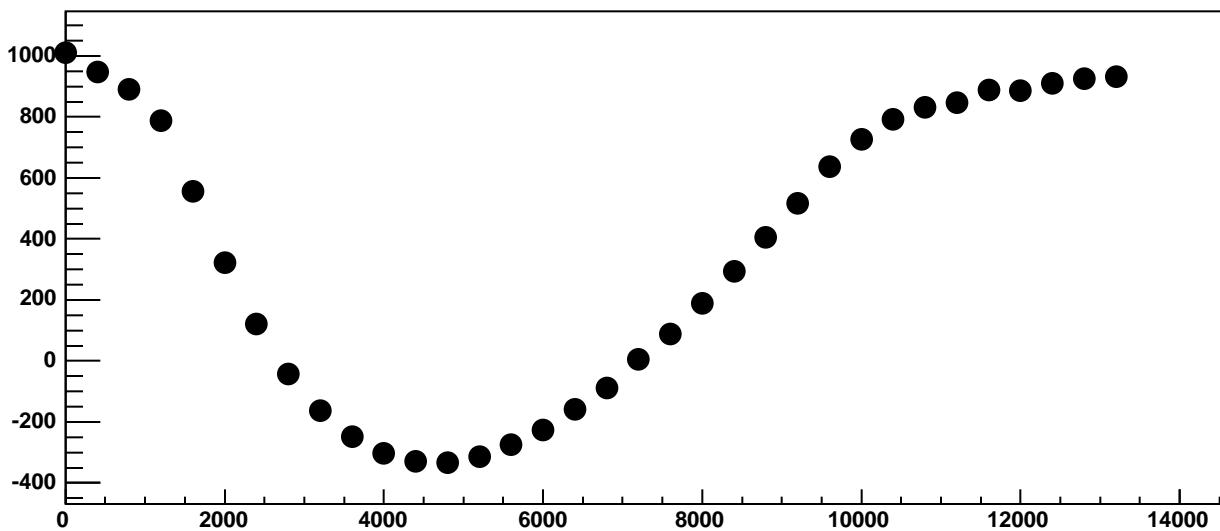
Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



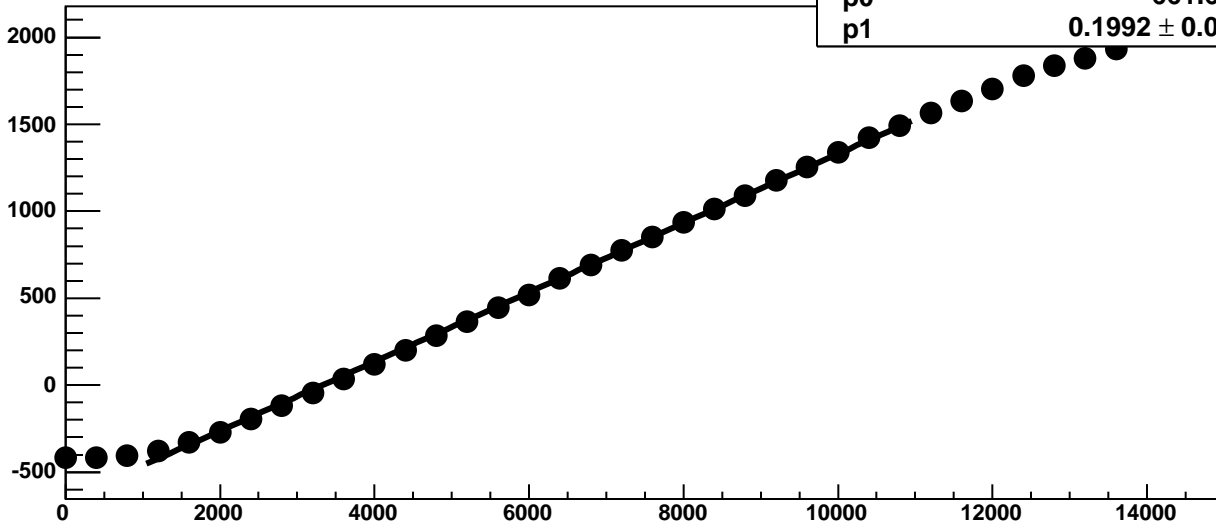
Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

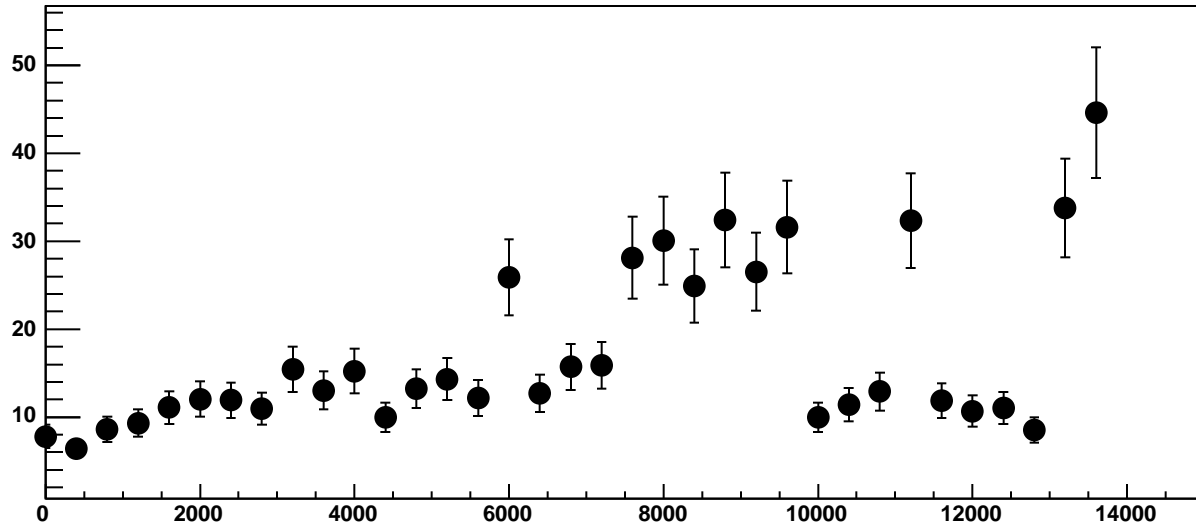


Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

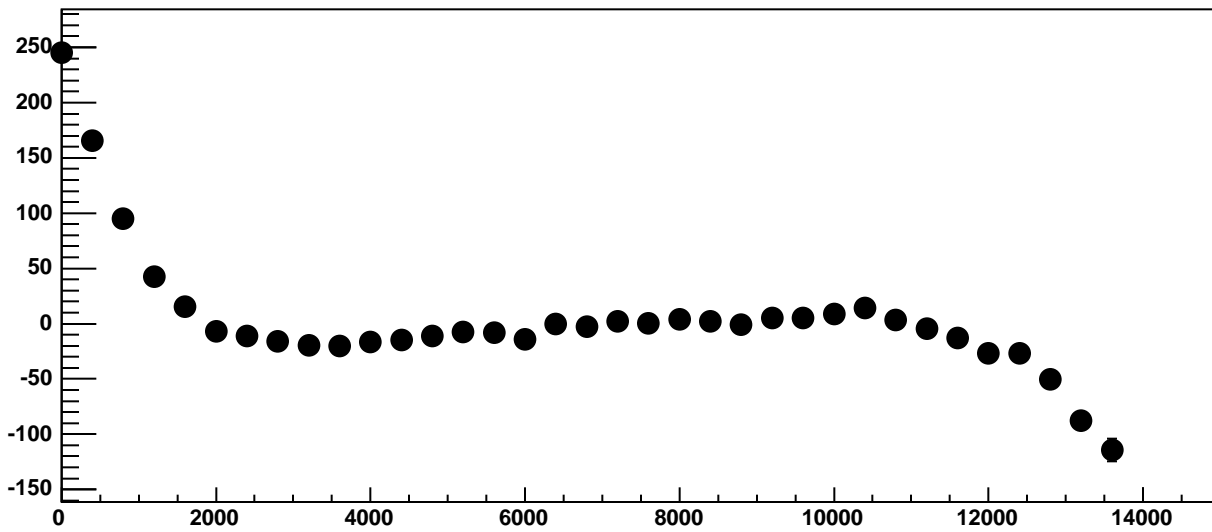


$\chi^2 / \text{ndf}$  718.2 / 23  
p0  $-661.6 \pm 1.24$   
p1  $0.1992 \pm 0.0002044$

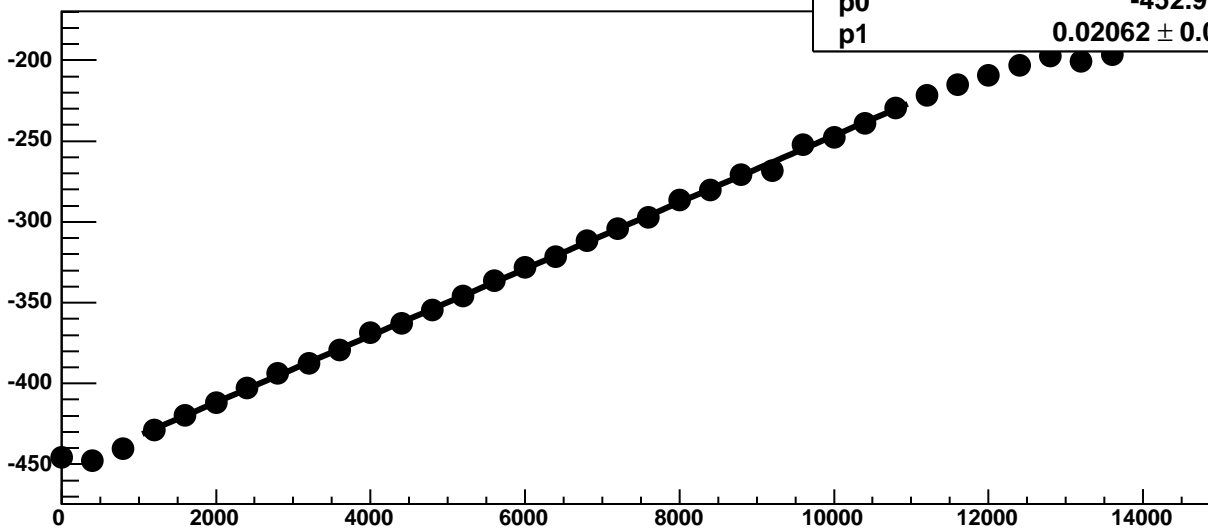
Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



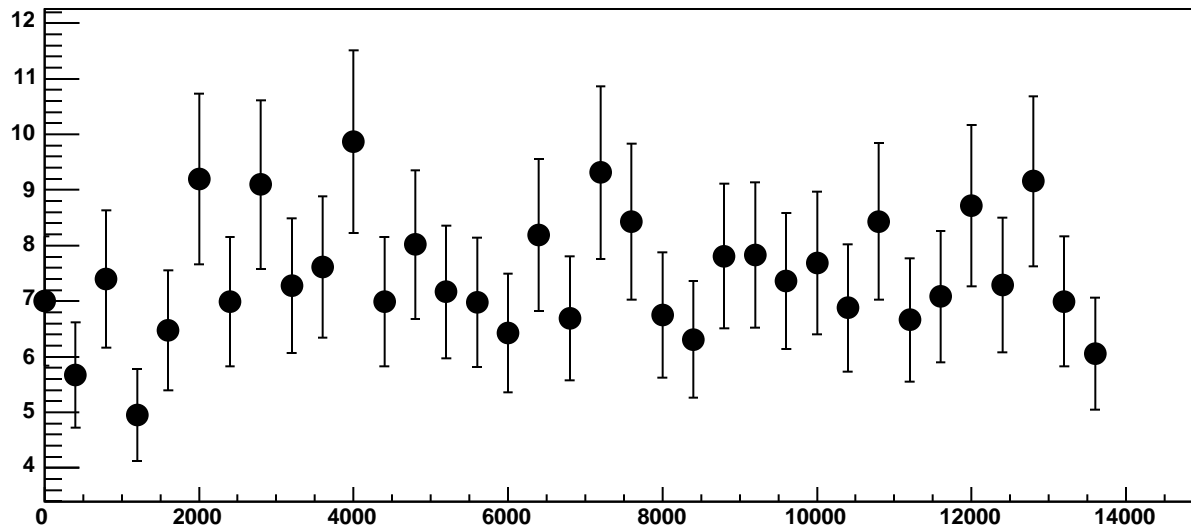
Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



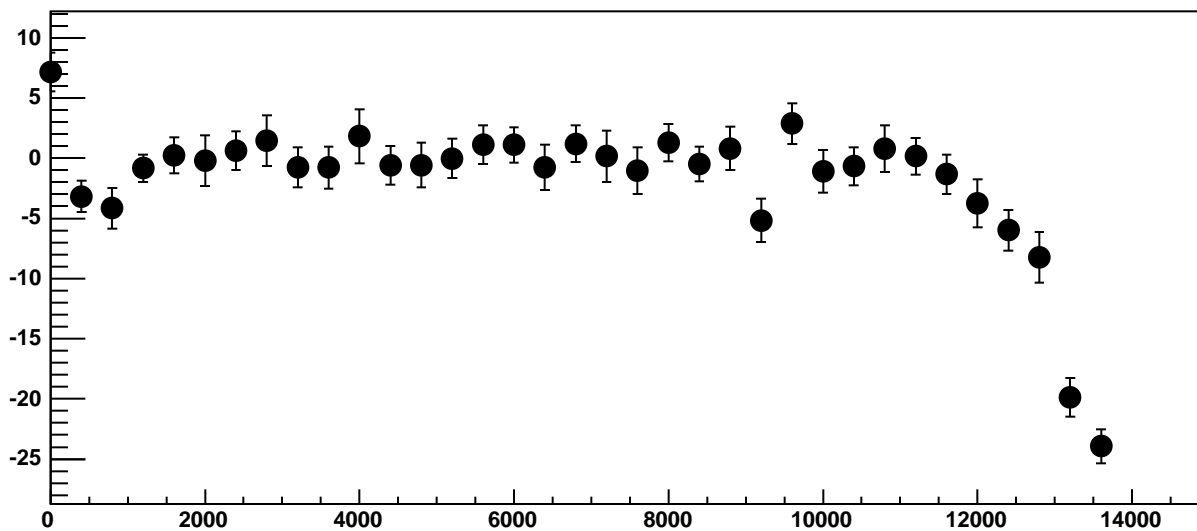
Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



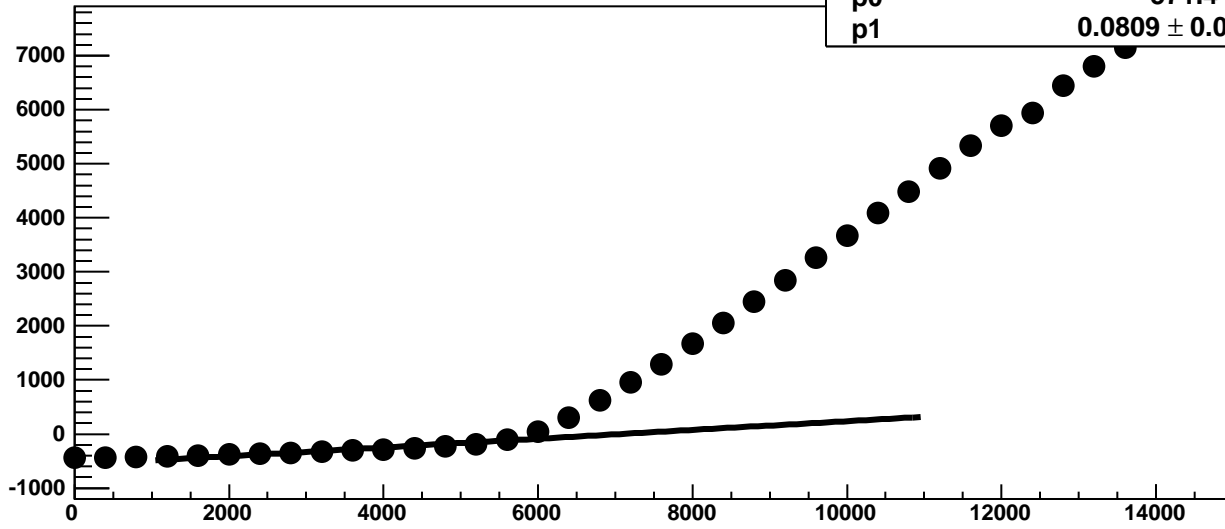
Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



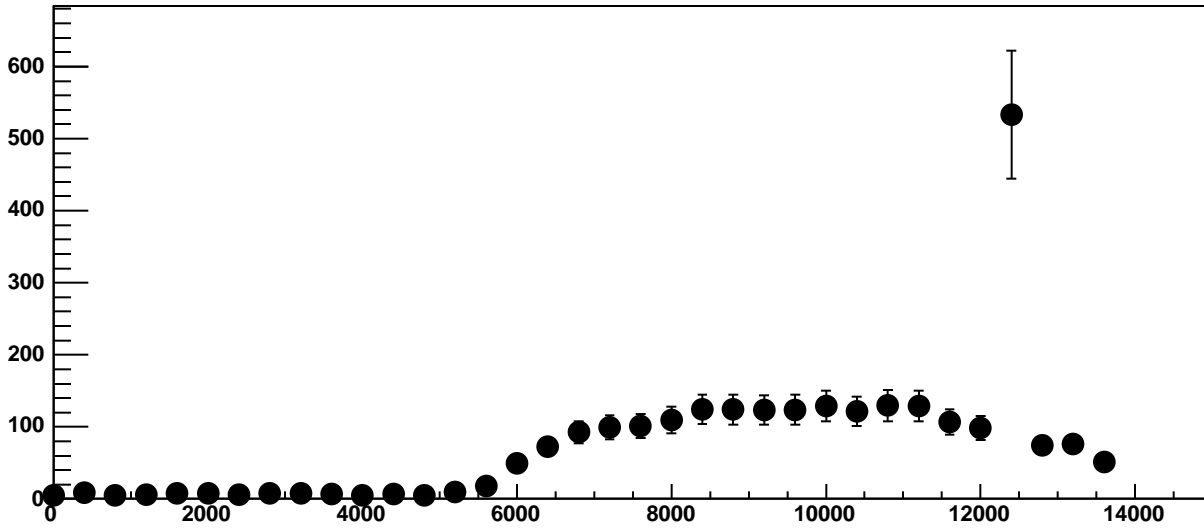
Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



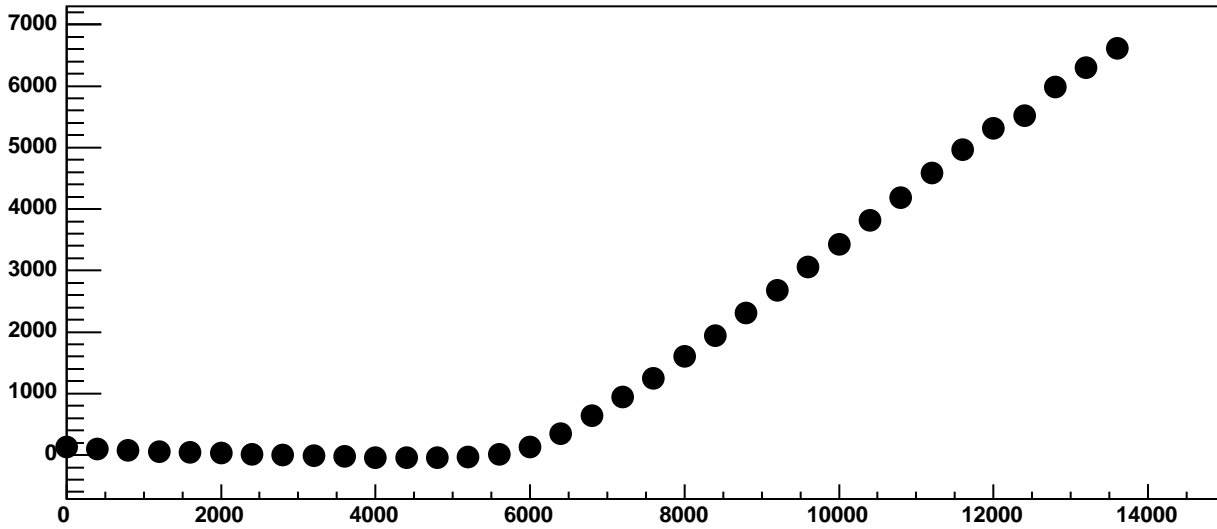
Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



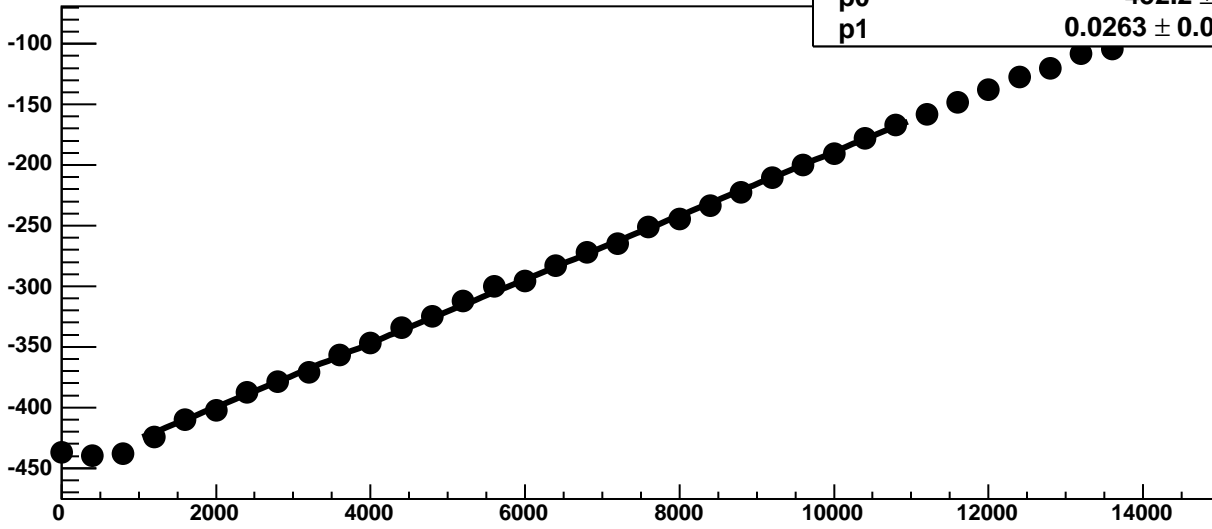
Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

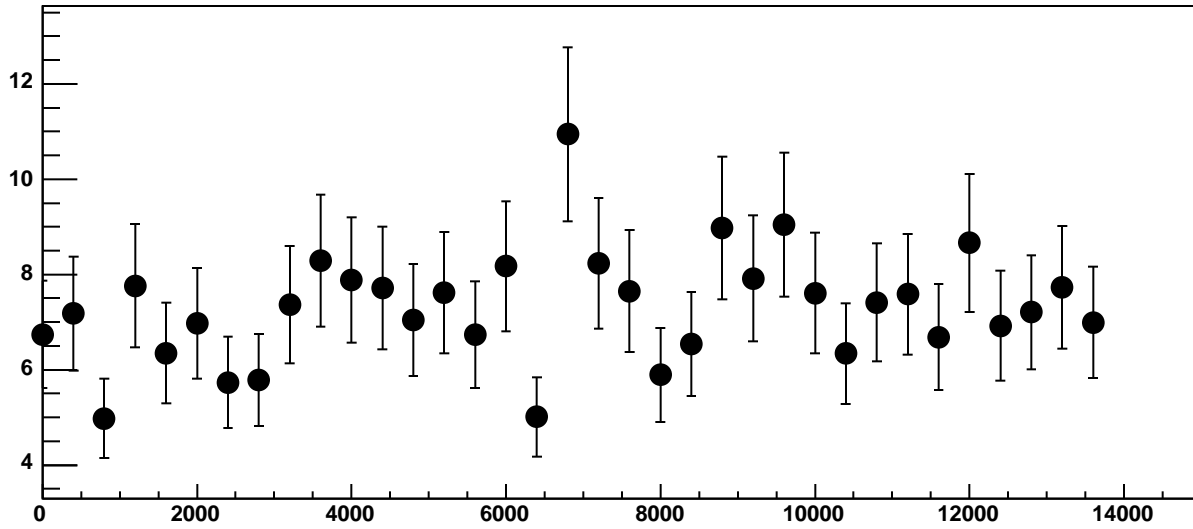


Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

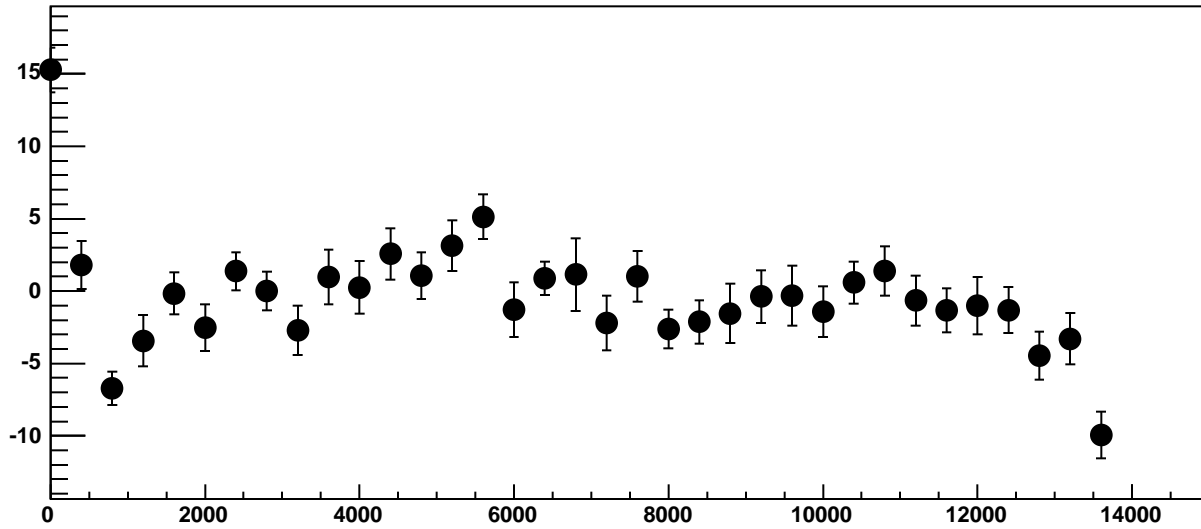


$\chi^2 / \text{ndf}$  37.76 / 23  
p0  $-452.2 \pm 0.7297$   
p1  $0.0263 \pm 0.0001123$

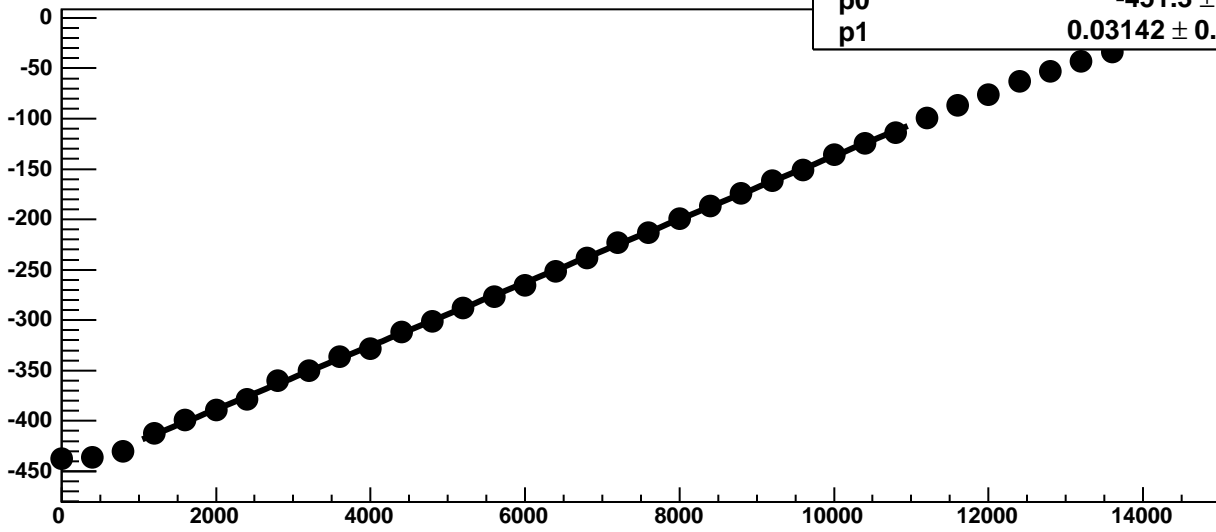
Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



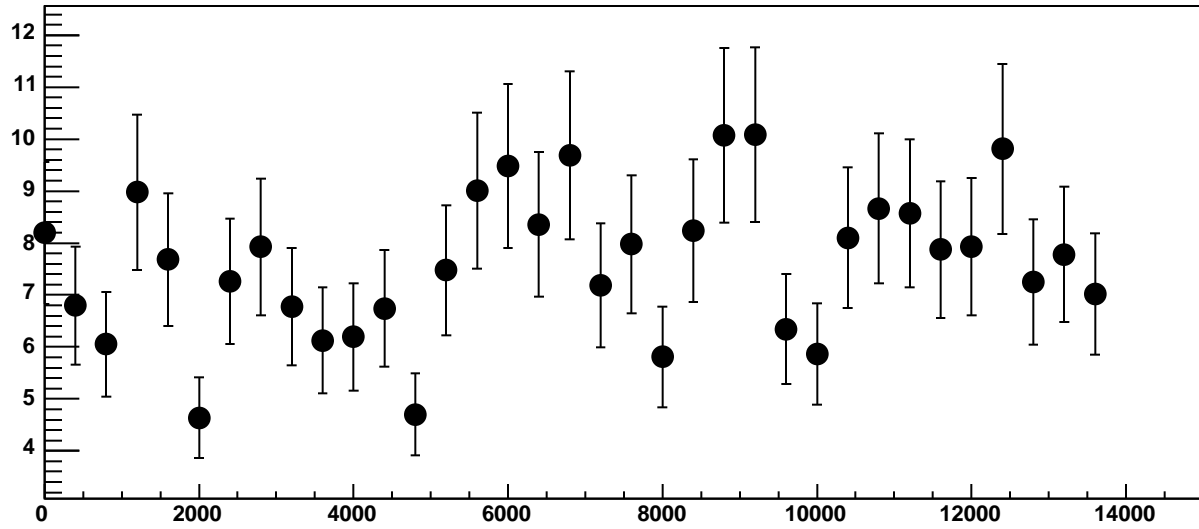
Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



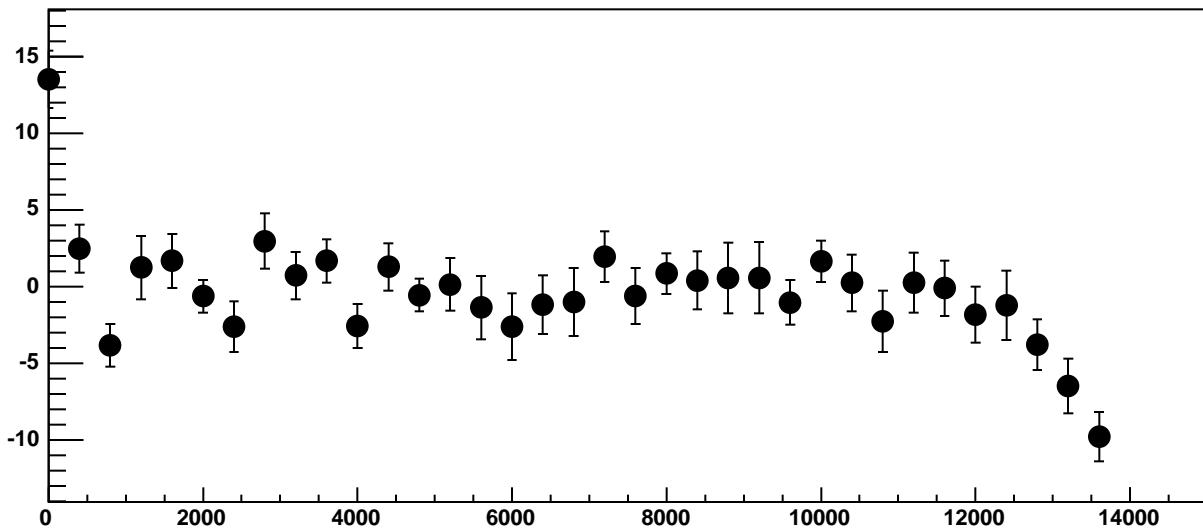
Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

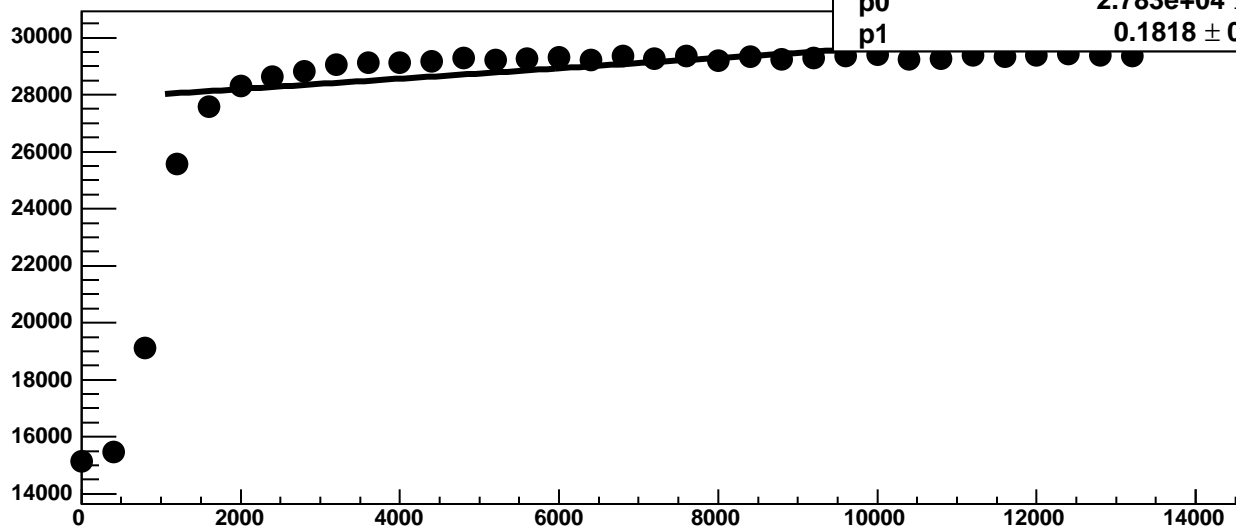


Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

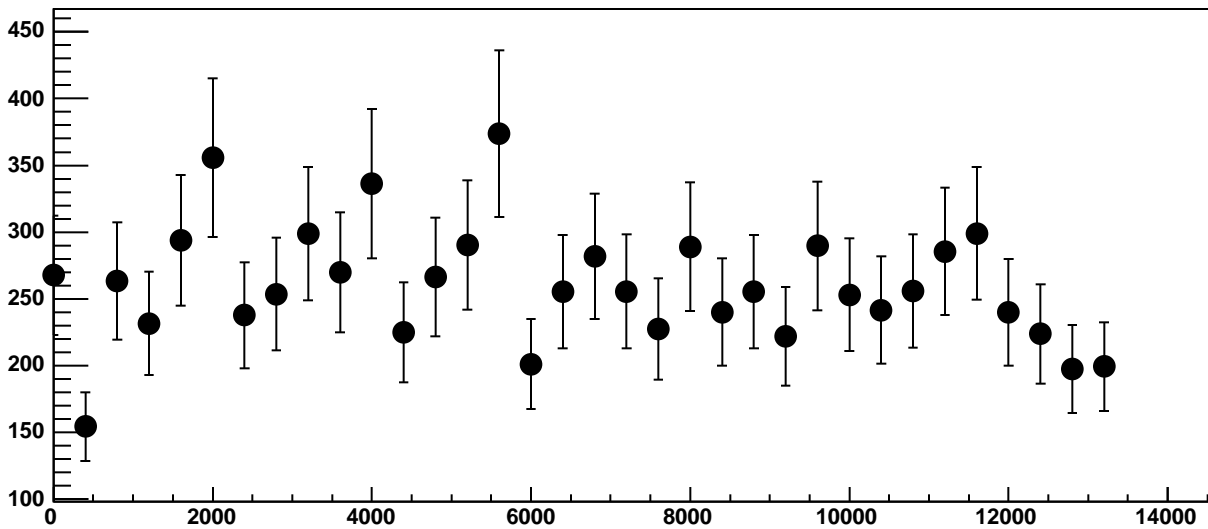




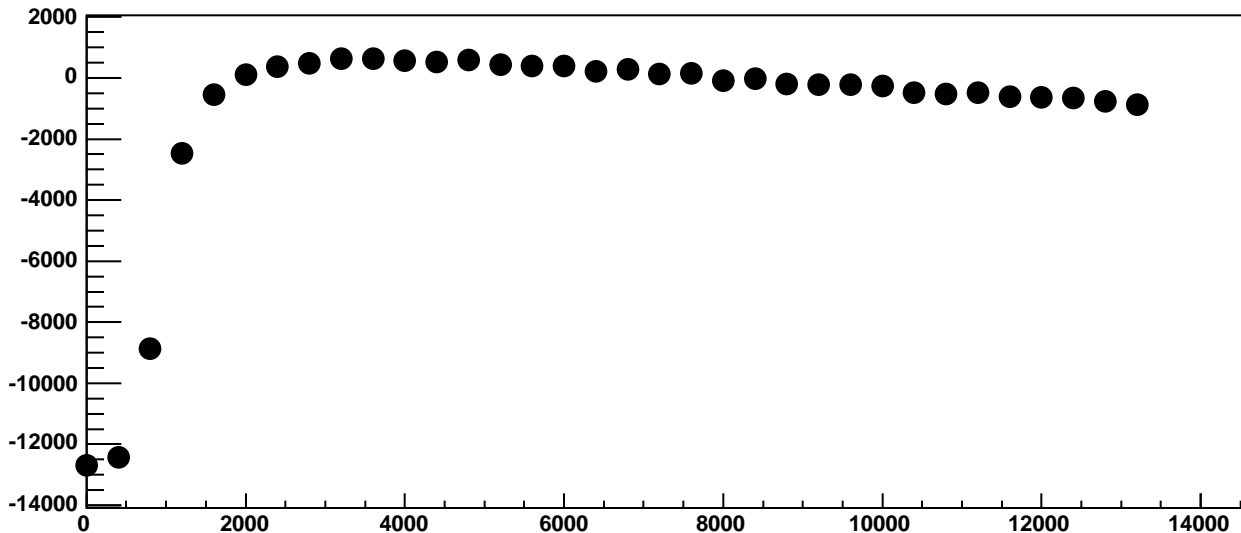
Chip 5, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 5, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

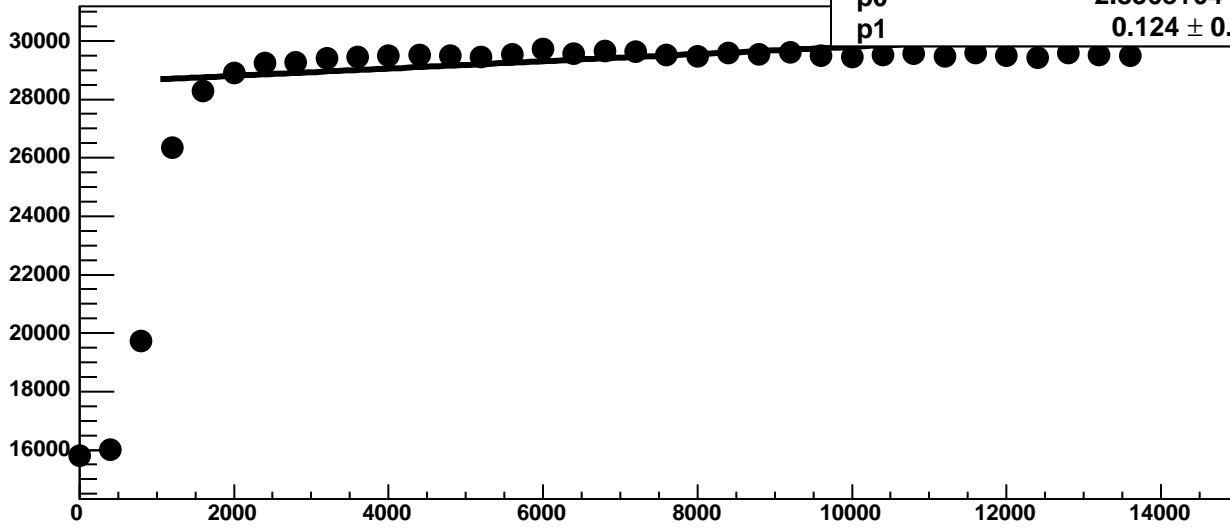
1394 / 23

p0

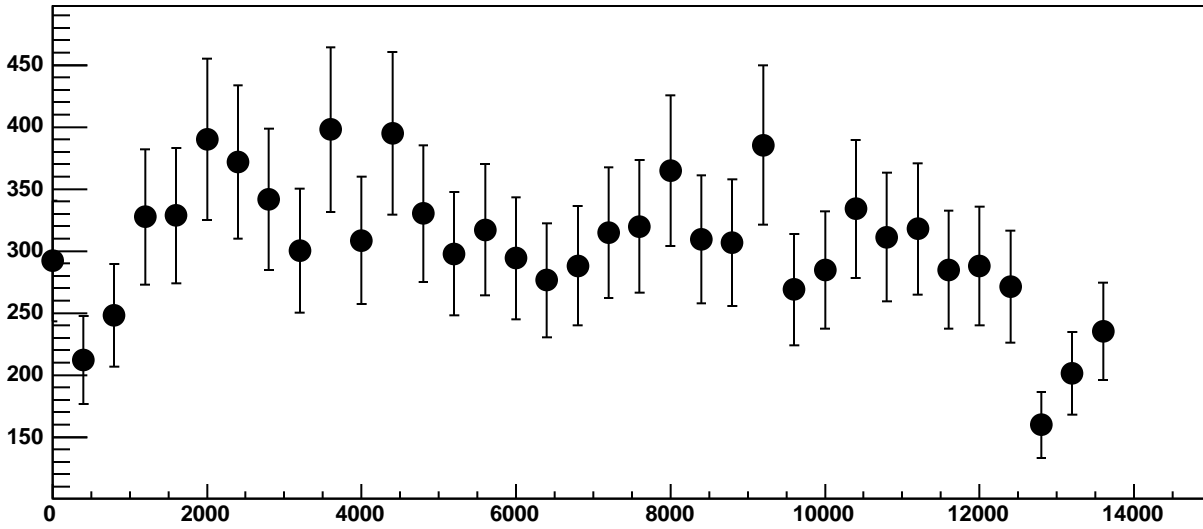
$2.856e+04 \pm 35.53$

p1

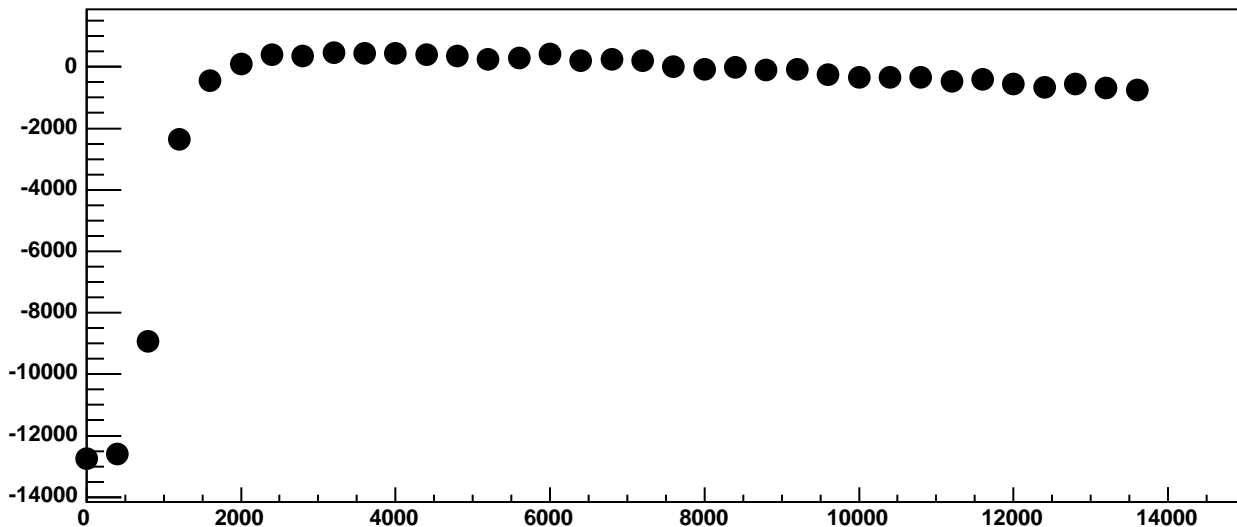
$0.124 \pm 0.005192$



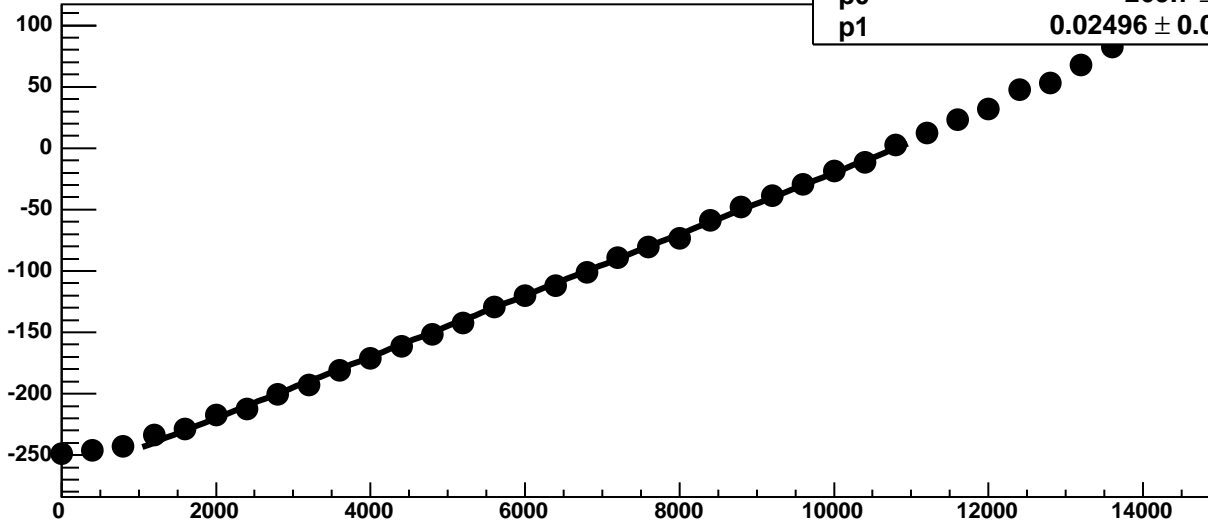
Chip 6, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

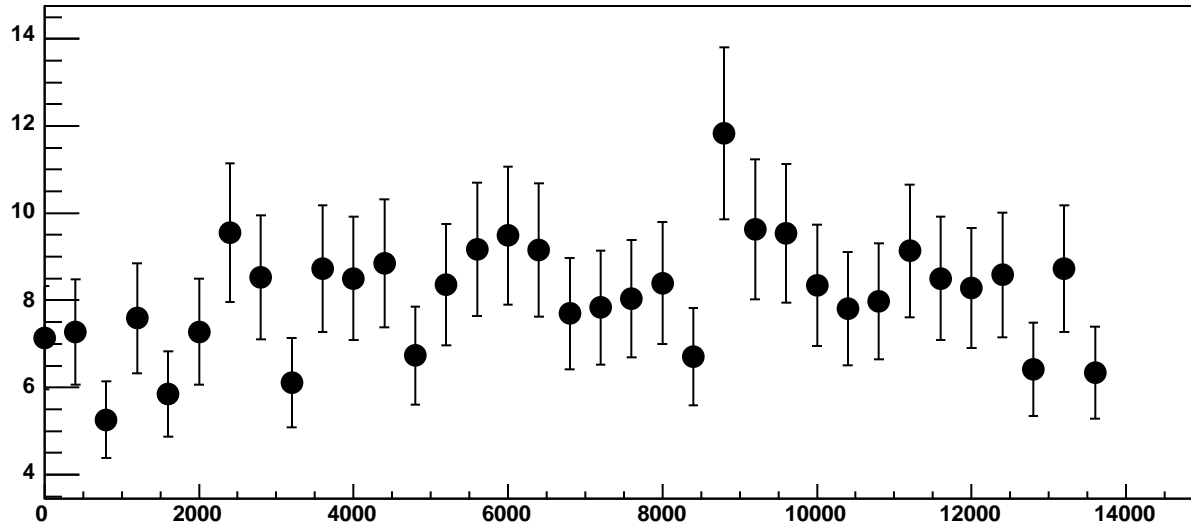


Chip 6, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

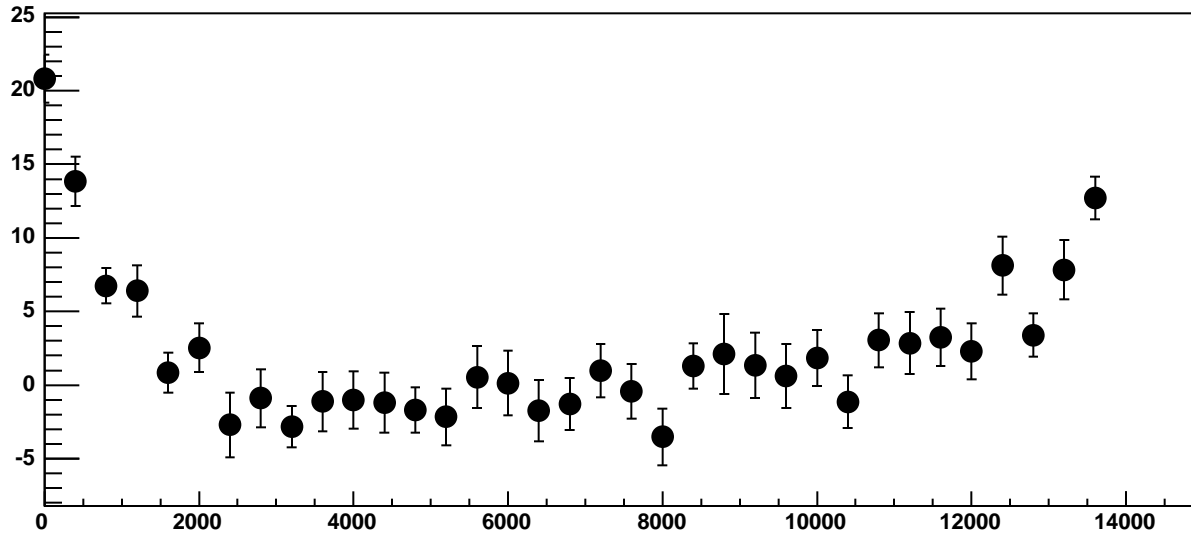


$\chi^2 / \text{ndf}$  36.25 / 23  
p0 -269.7 ± 0.7962  
p1 0.02496 ± 0.0001243

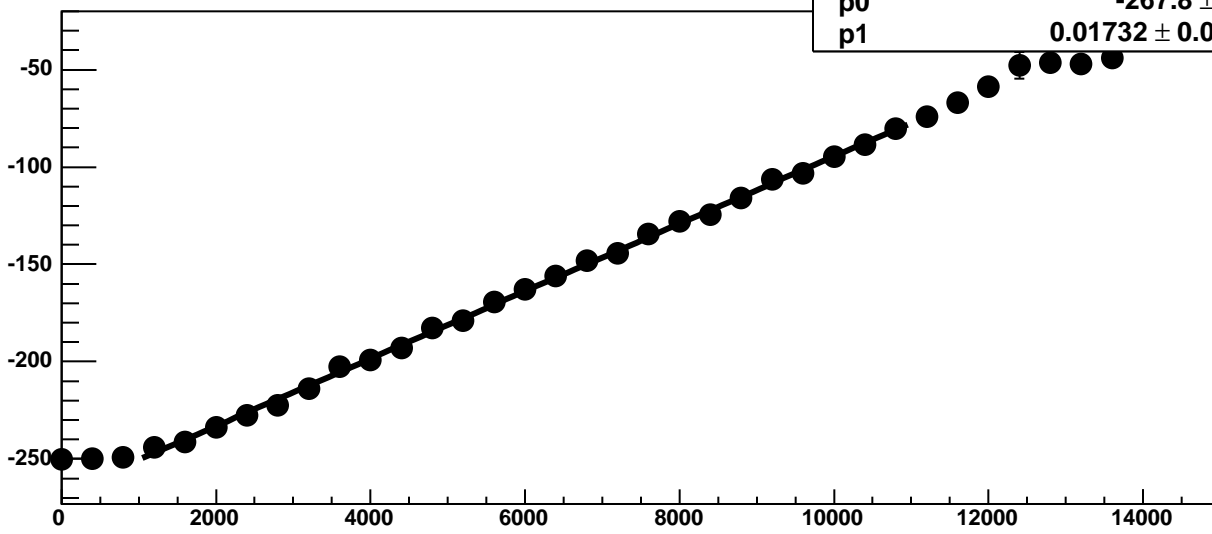
Chip 6, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



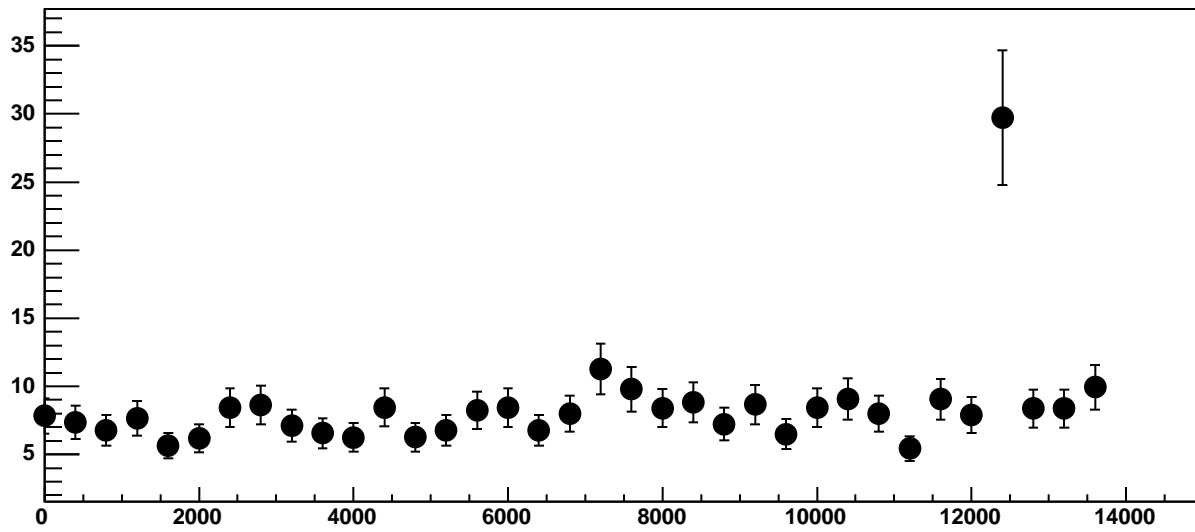
Chip 6, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



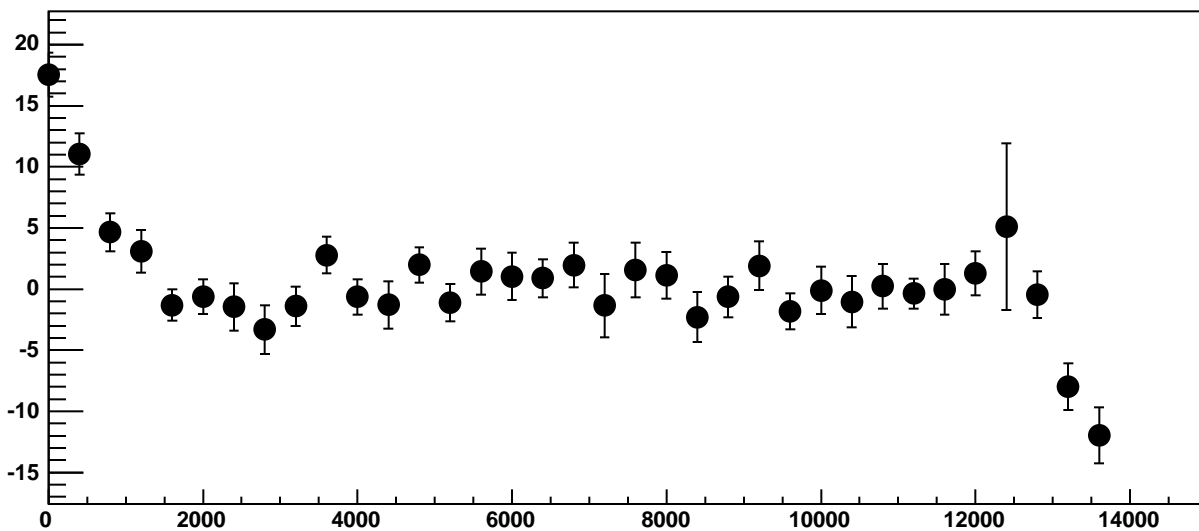
Chip 6, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



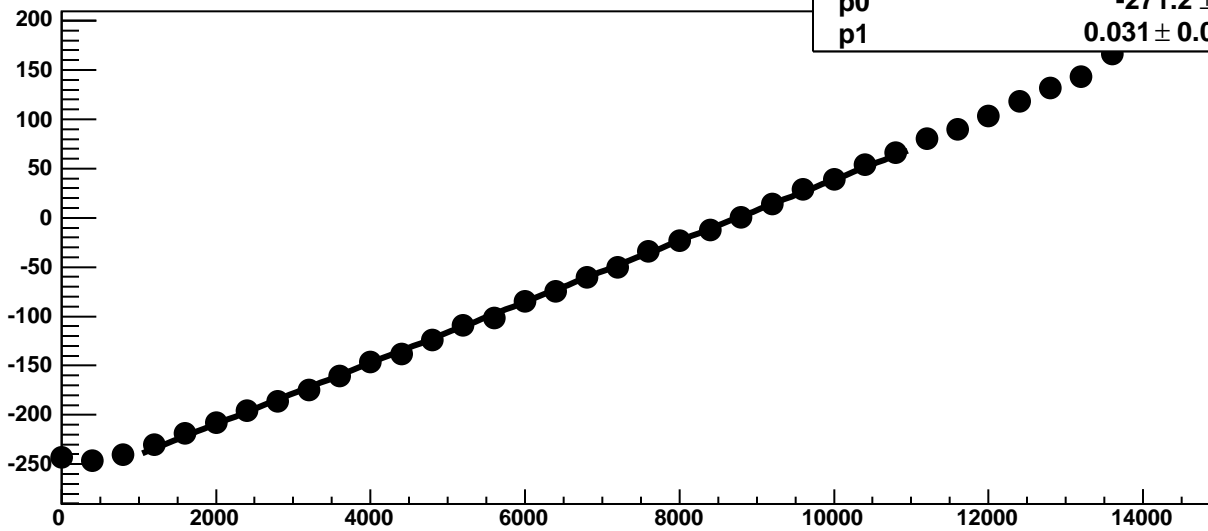
Chip 6, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

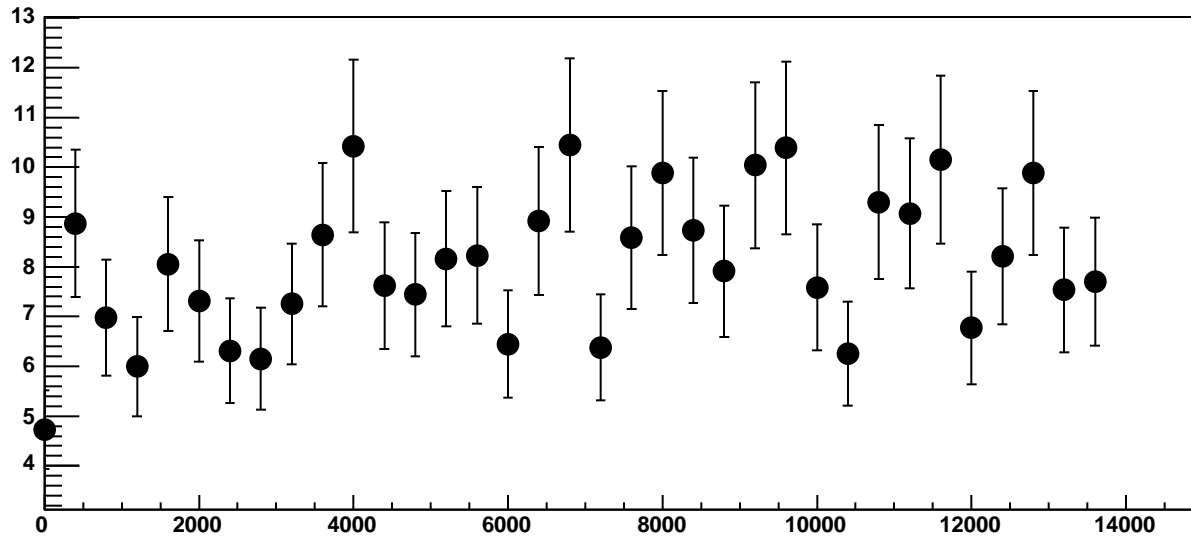


Chip 6, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

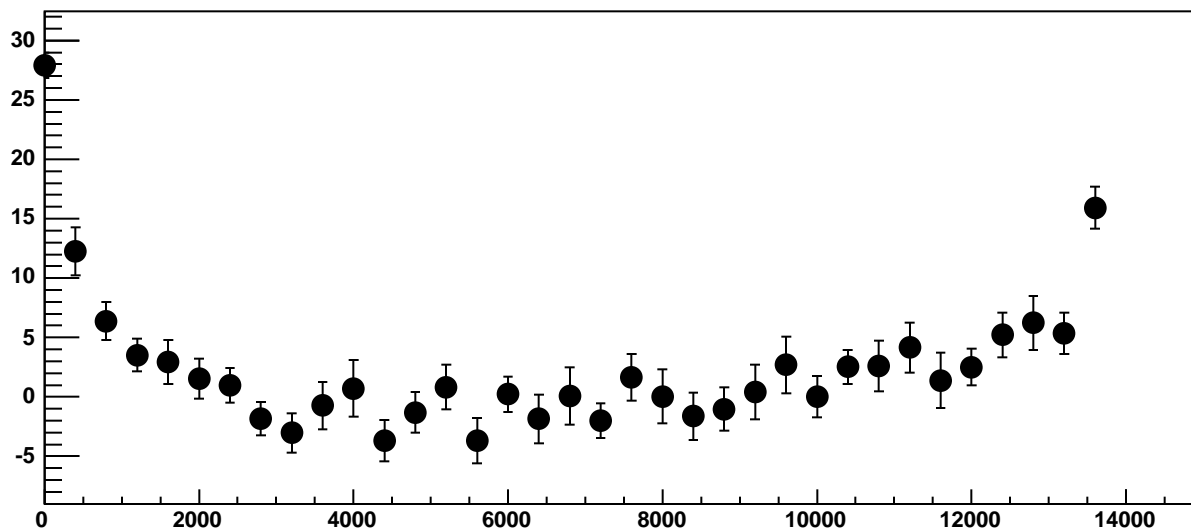


$\chi^2 / \text{ndf}$  34.89 / 23  
p0 -271.2 ± 0.7585  
p1 0.031 ± 0.0001193

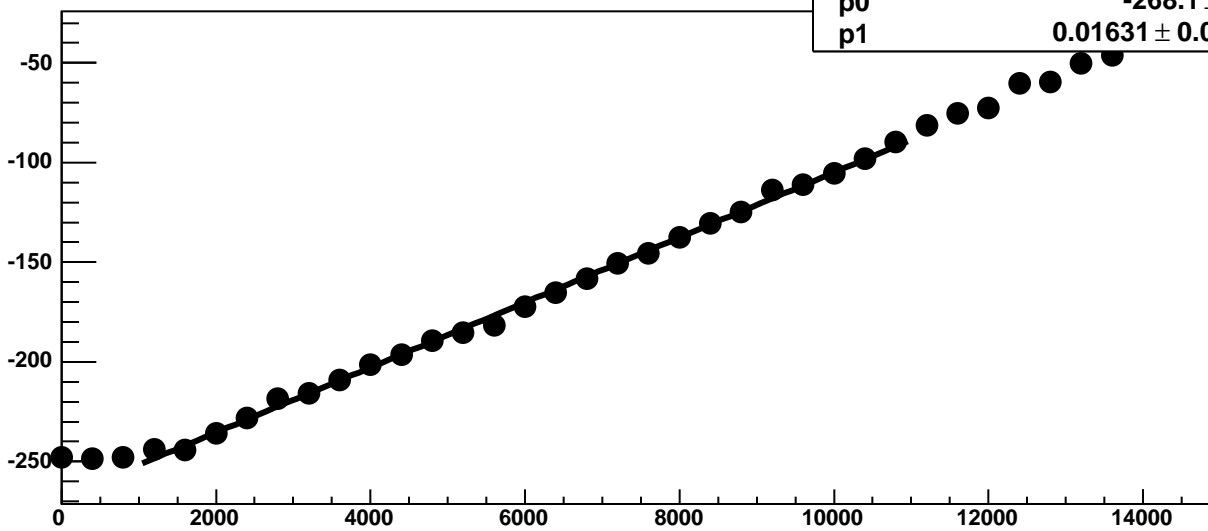
Chip 6, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

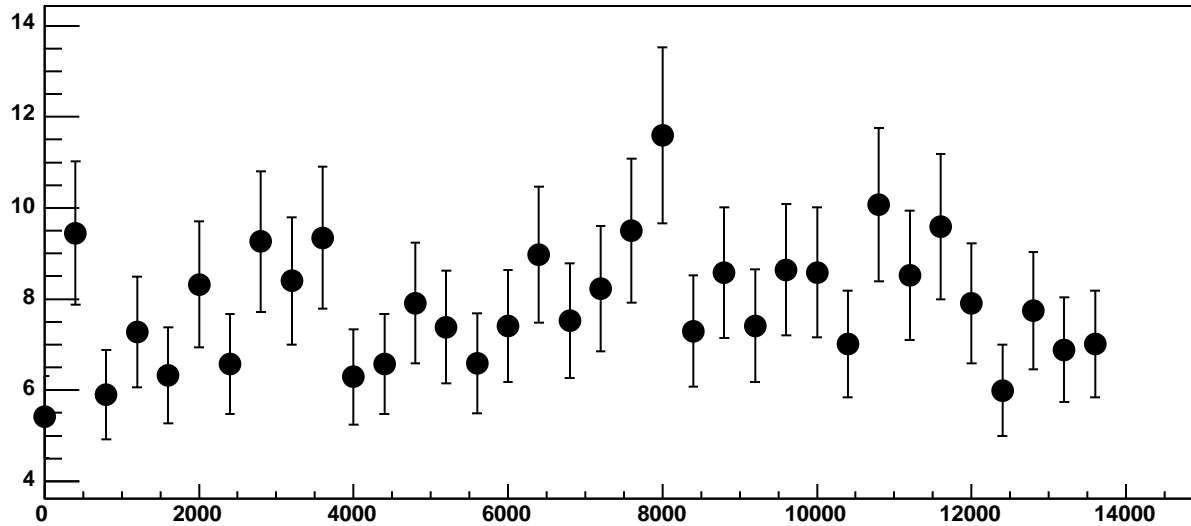


Chip 6, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

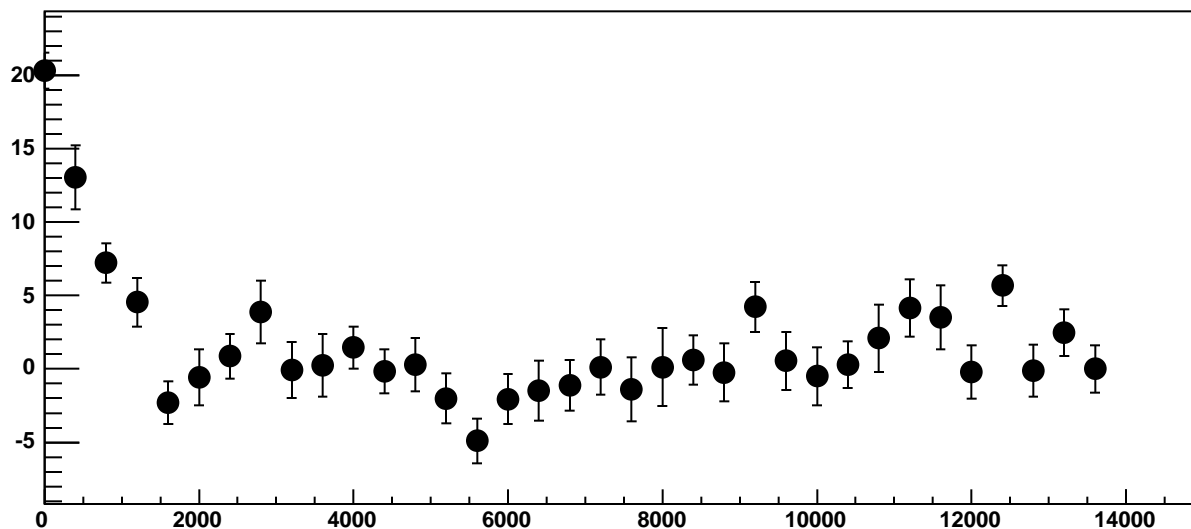


$\chi^2 / \text{ndf}$  36.74 / 23  
p0  $-268.1 \pm 0.7921$   
p1  $0.01631 \pm 0.0001242$

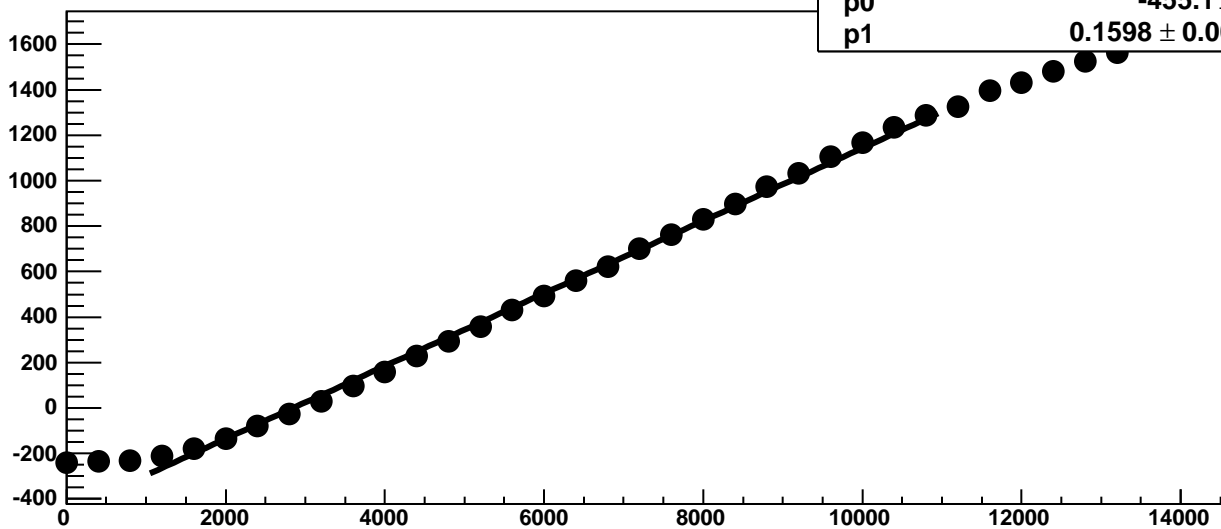
Chip 6, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 6, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

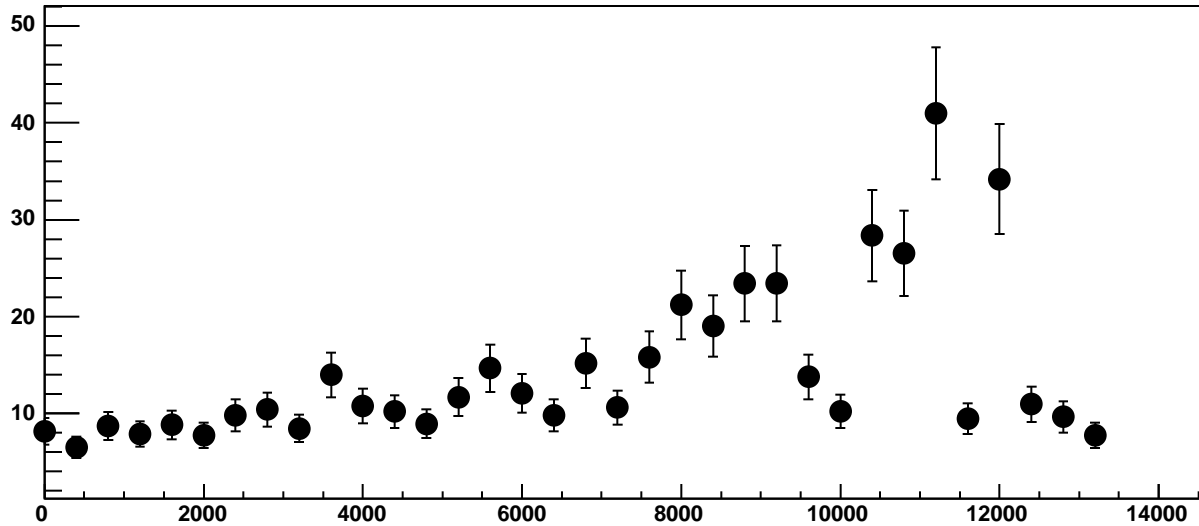


Chip 6, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

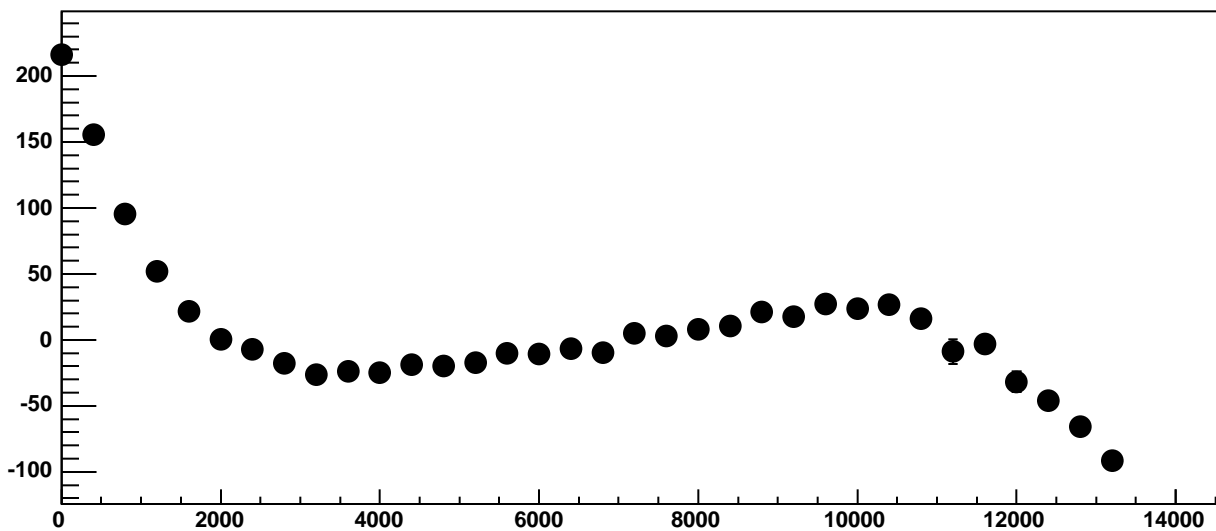


$\chi^2 / \text{ndf}$  1829 / 23  
p0  $-455.1 \pm 1.057$   
p1  $0.1598 \pm 0.0001977$

Chip 6, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

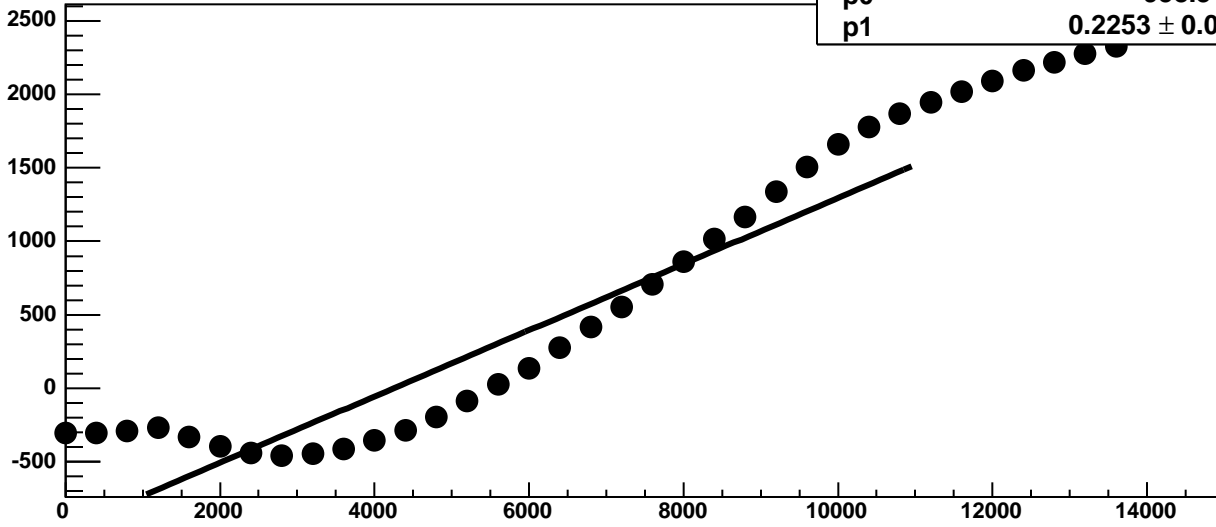


Chip 6, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

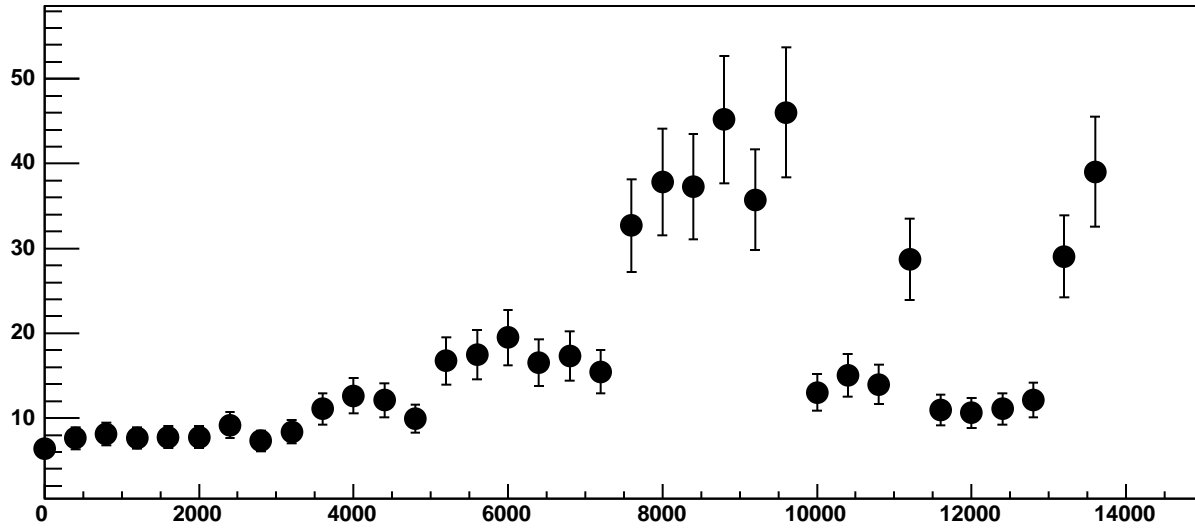


Chip 6, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

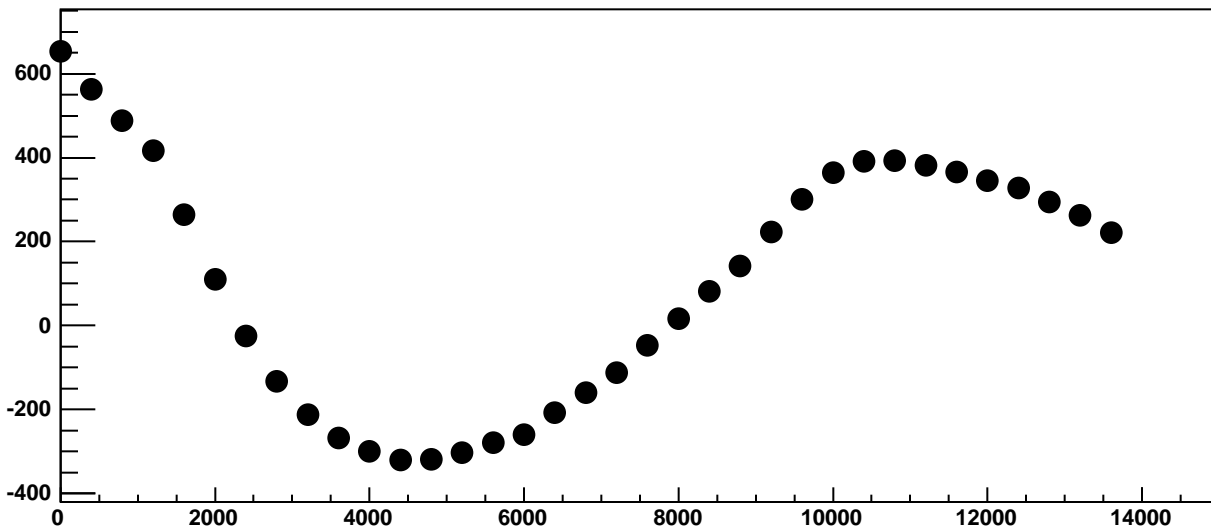
$\chi^2 / \text{ndf}$  2.202e+05 / 23  
p0 -958.3 ± 1.005  
p1 0.2253 ± 0.0002093



Chip 6, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

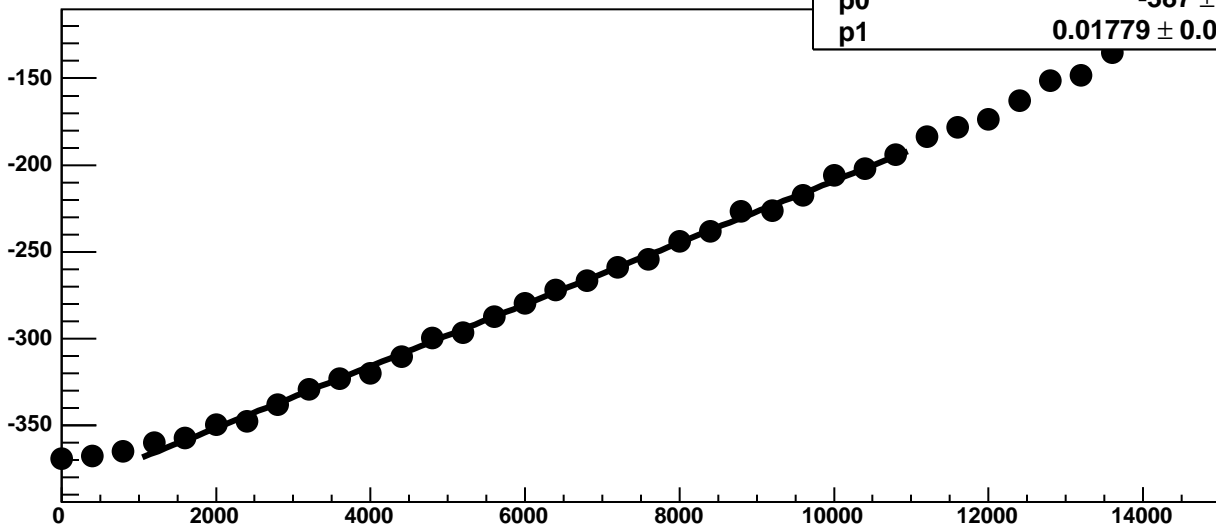


Chip 6, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



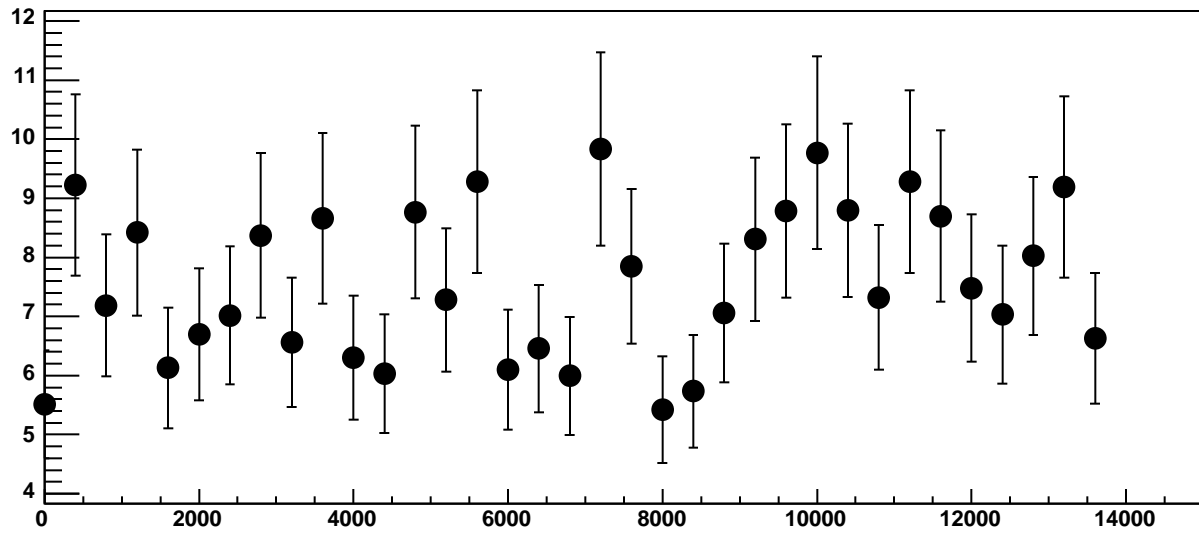


Chip 6, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

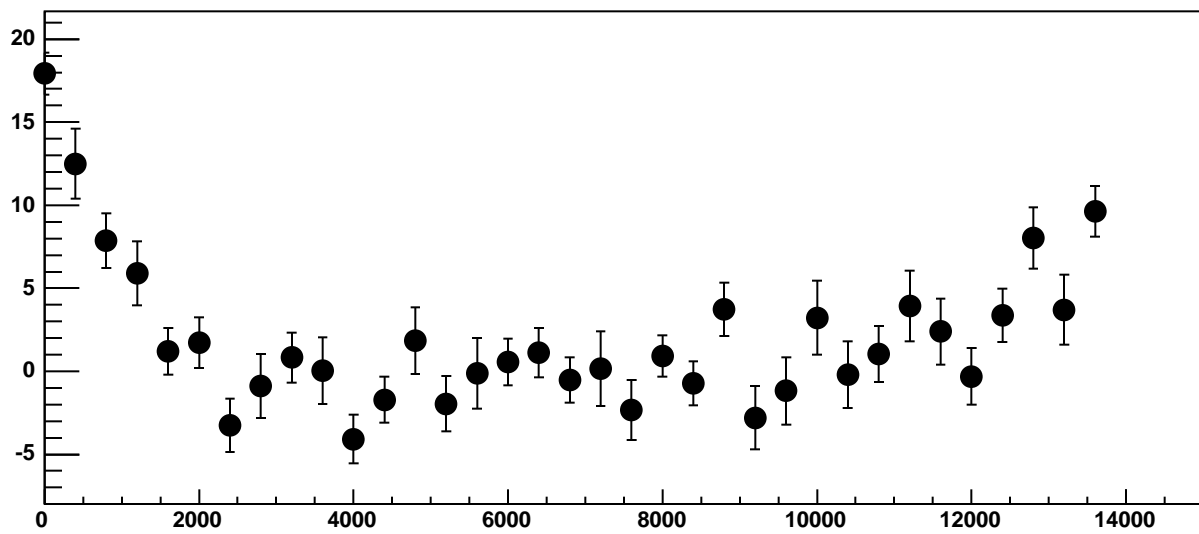


$\chi^2 / \text{ndf}$  41.29 / 23  
p0  $-387 \pm 0.7658$   
p1  $0.01779 \pm 0.0001182$

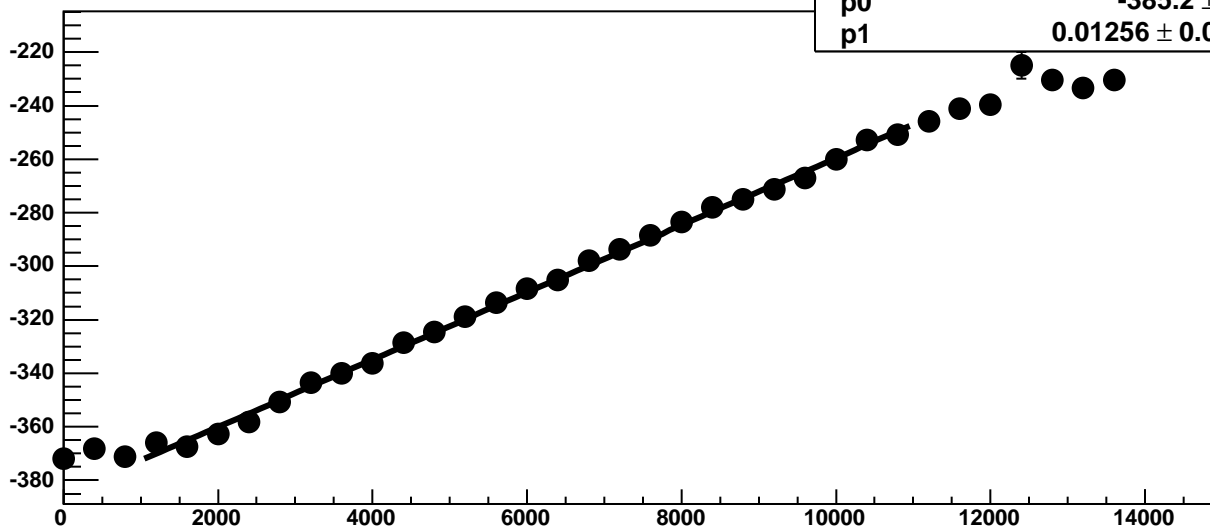
Chip 6, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



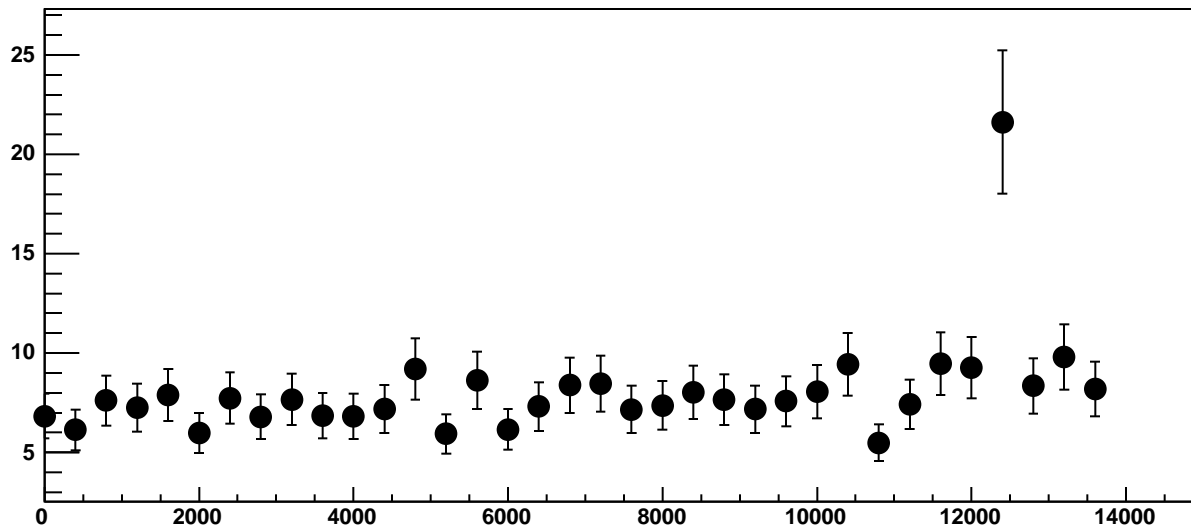
Chip 6, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



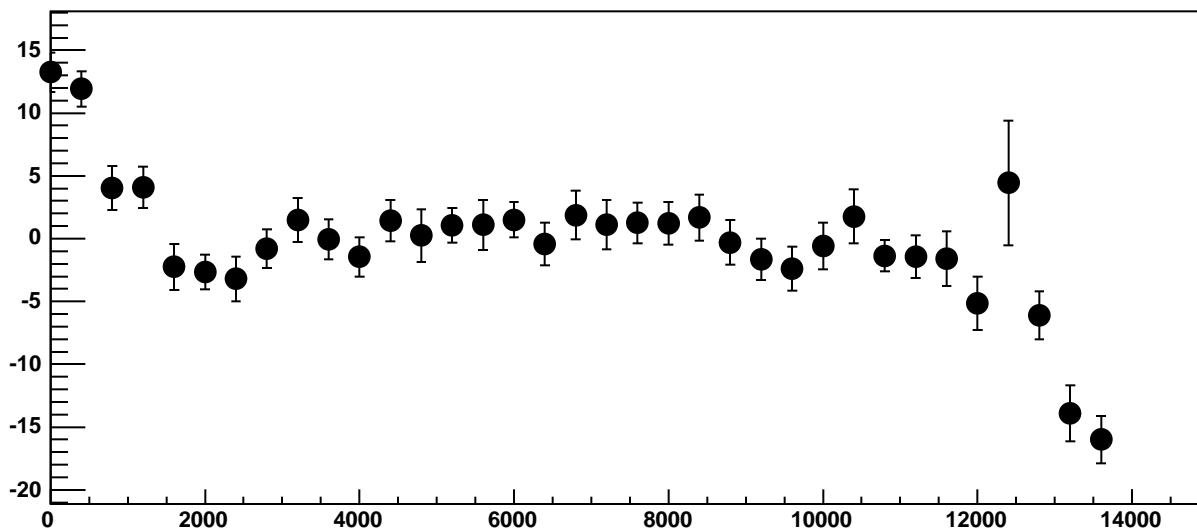
Chip 6, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



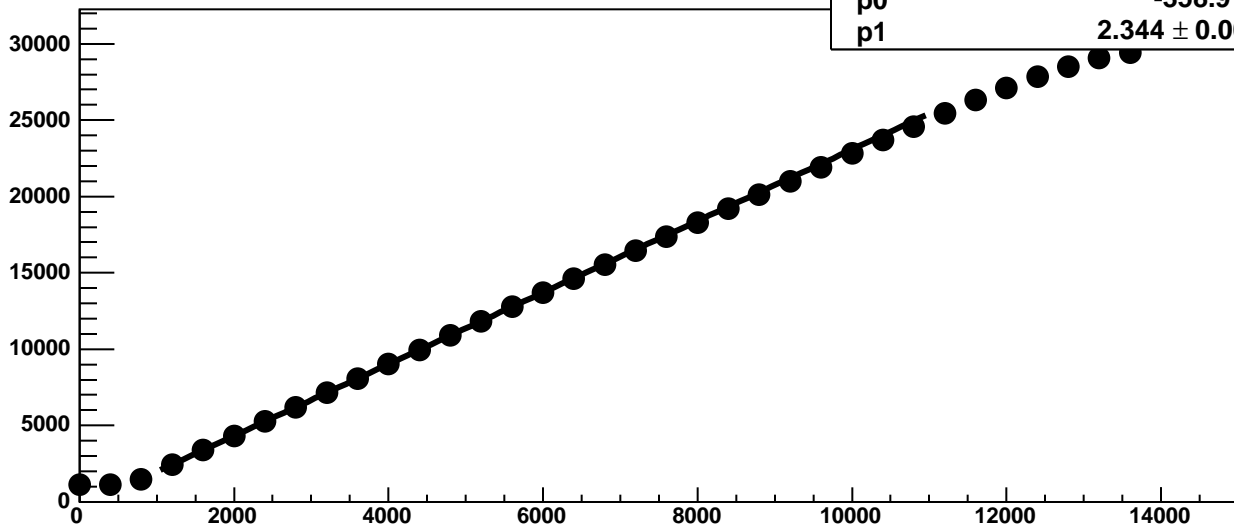
Chip 6, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



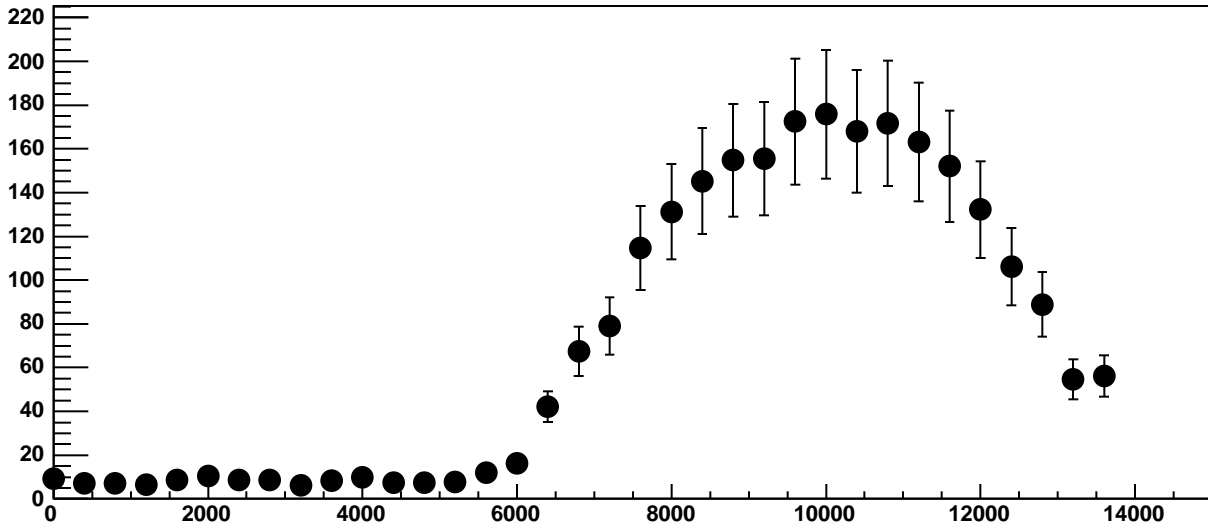
Chip 6, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



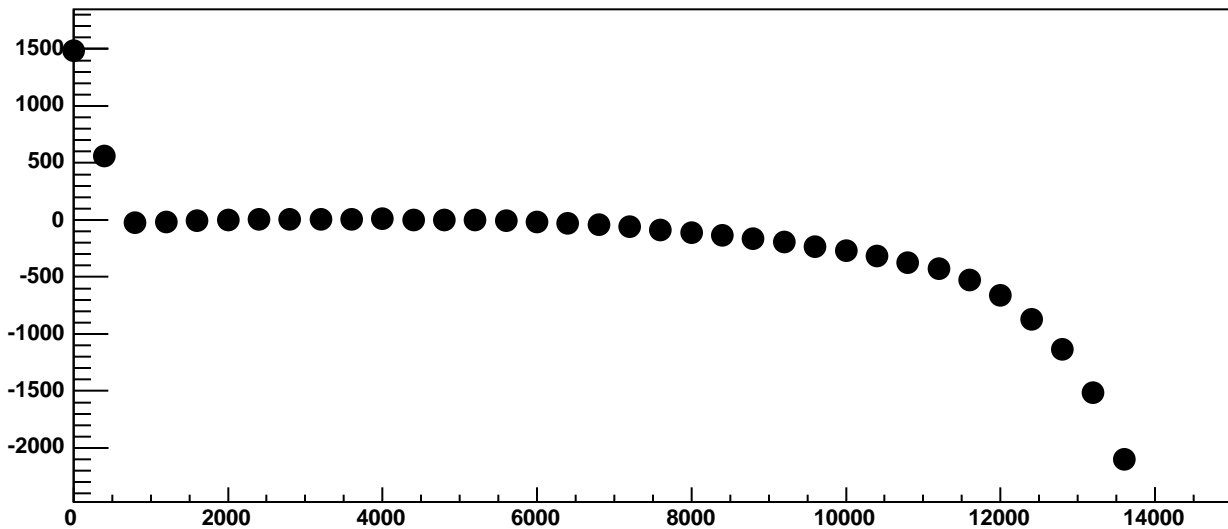
Chip 6, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



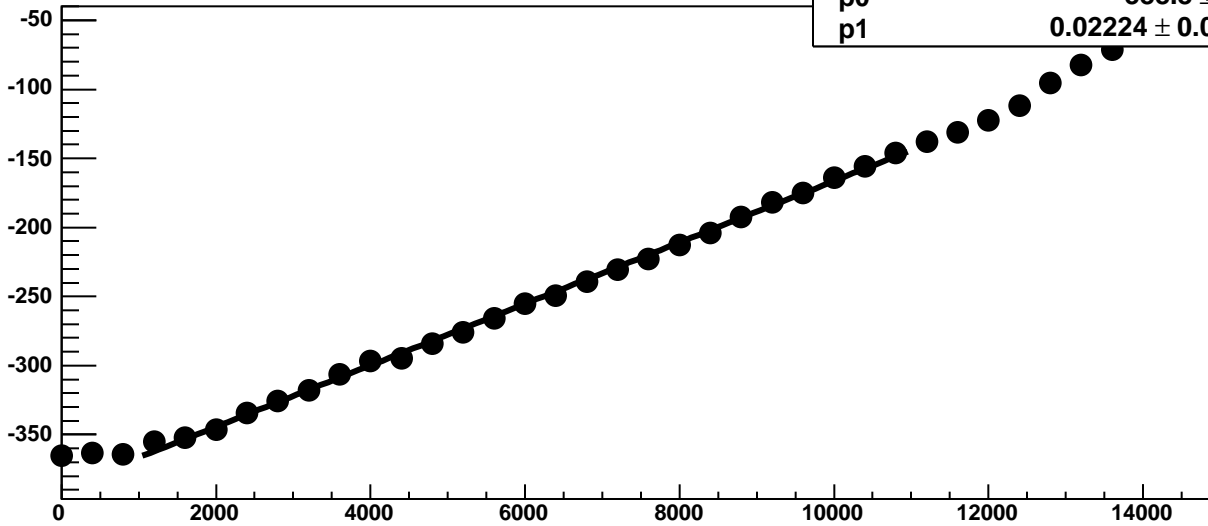
Chip 6, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

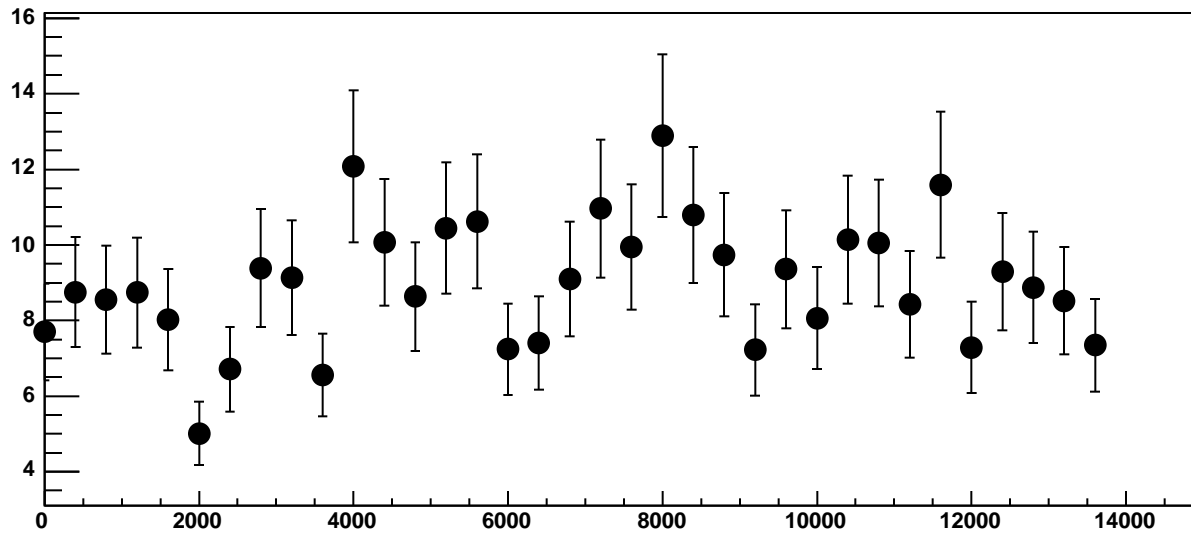


Chip 6, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

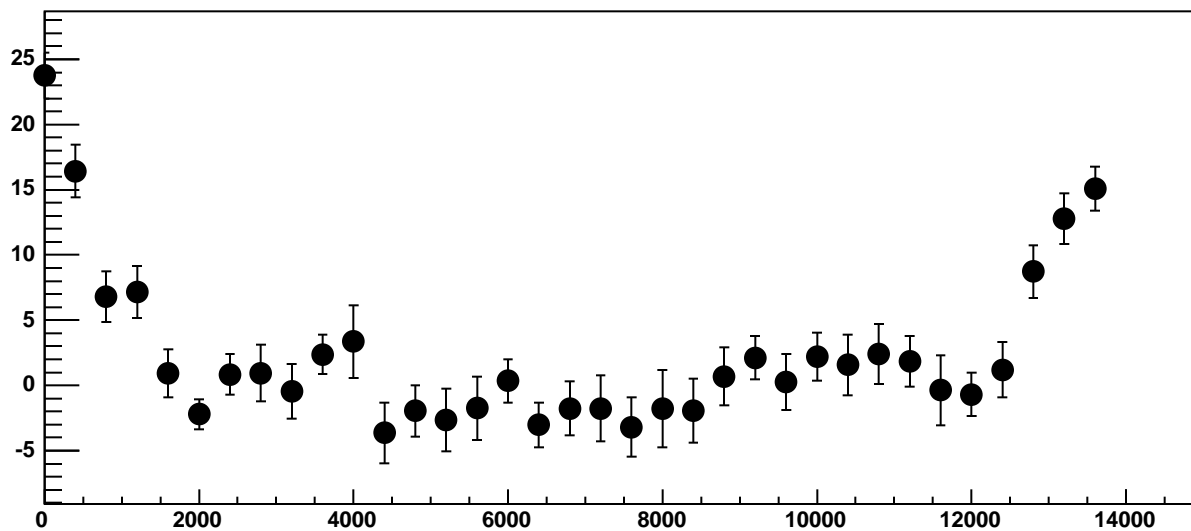


$\chi^2 / \text{ndf}$  38.46 / 23  
p0  $-388.8 \pm 0.8143$   
p1  $0.02224 \pm 0.0001313$

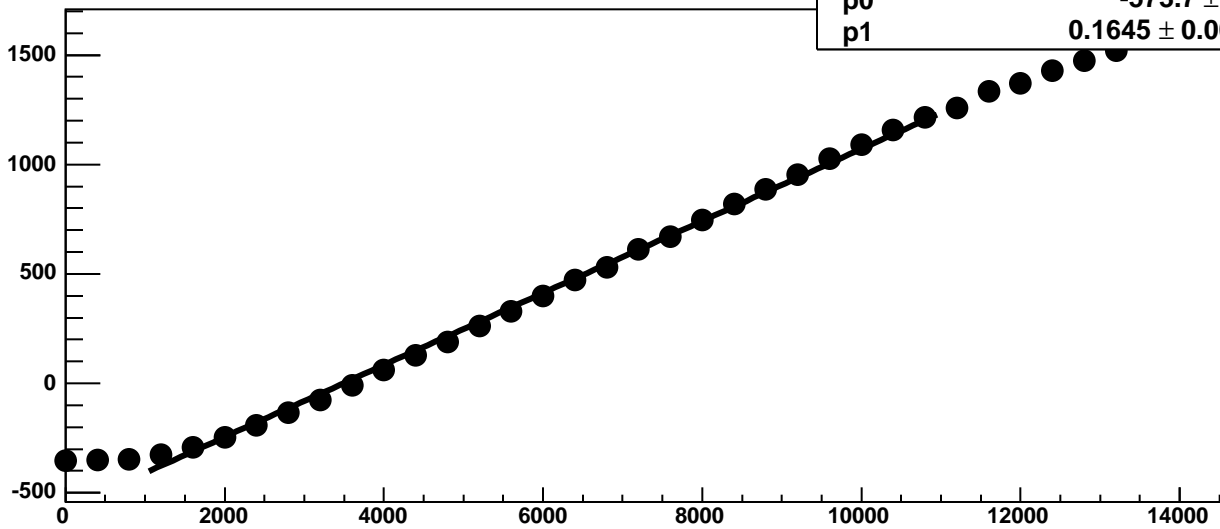
Chip 6, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



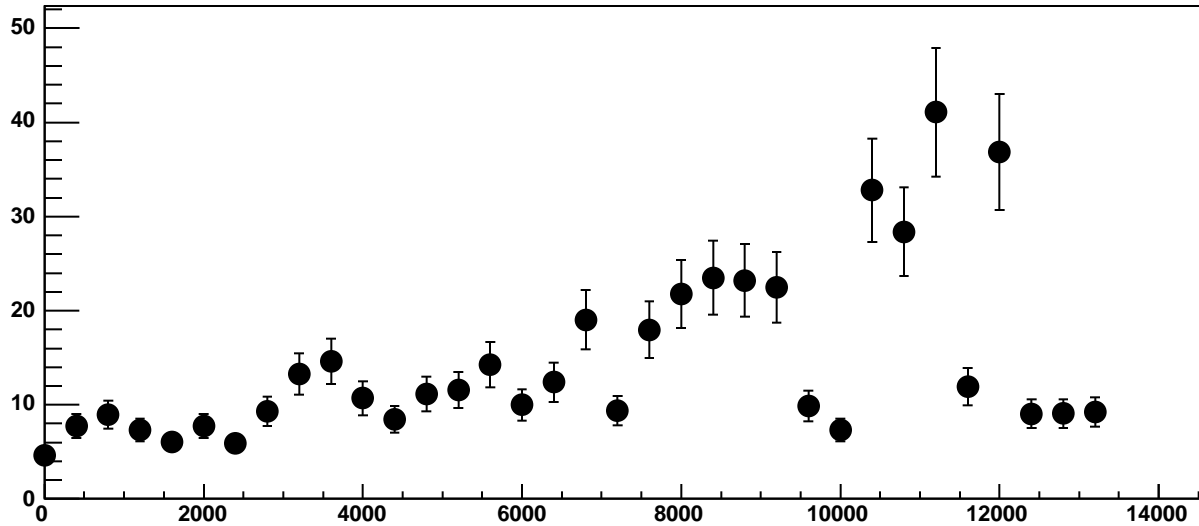
Chip 6, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



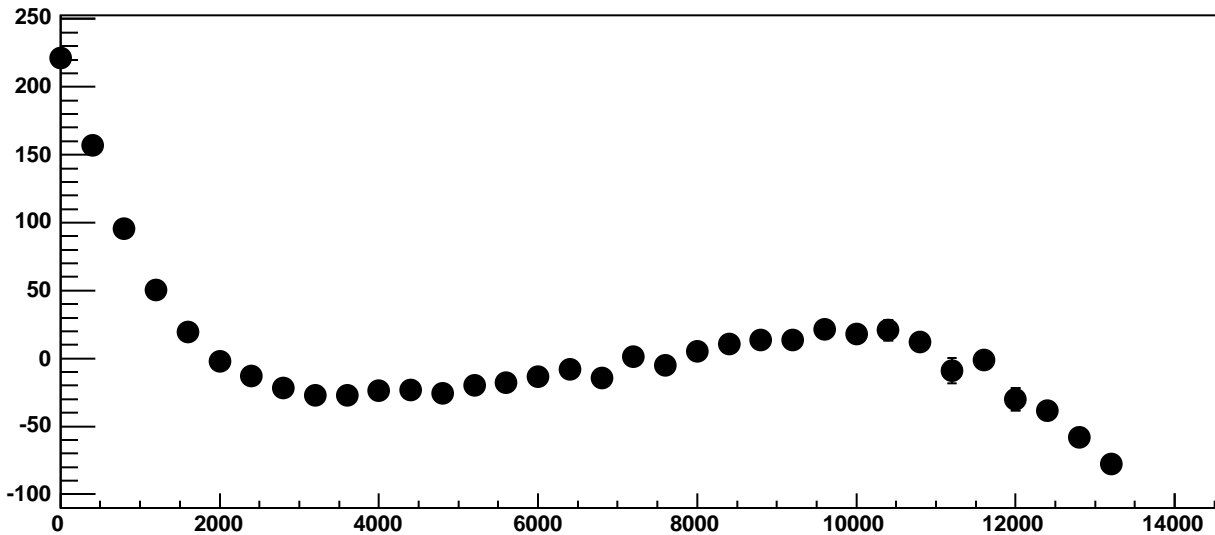
Chip 6, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



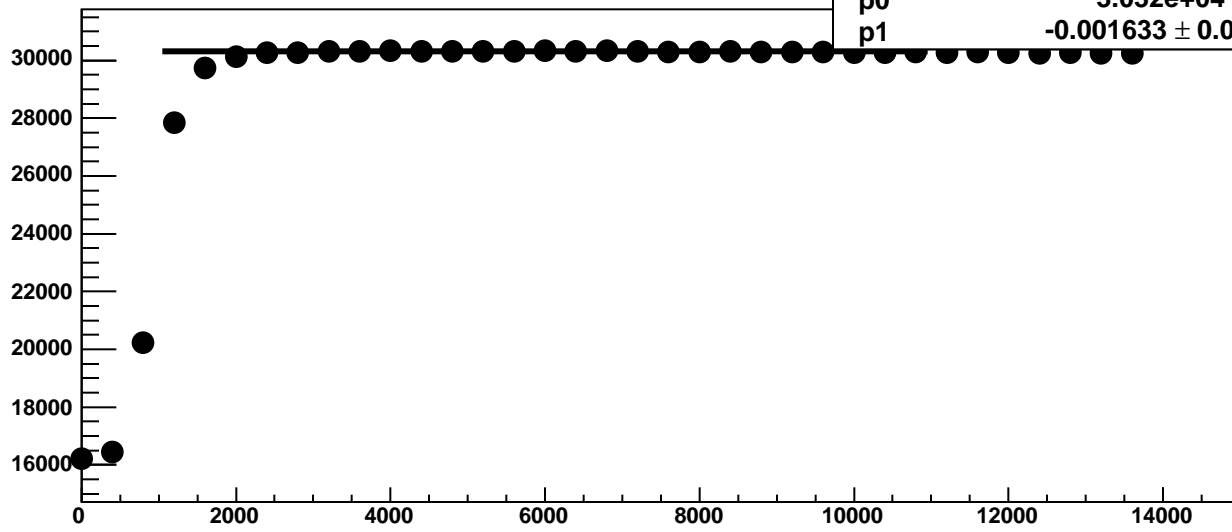
Chip 6, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



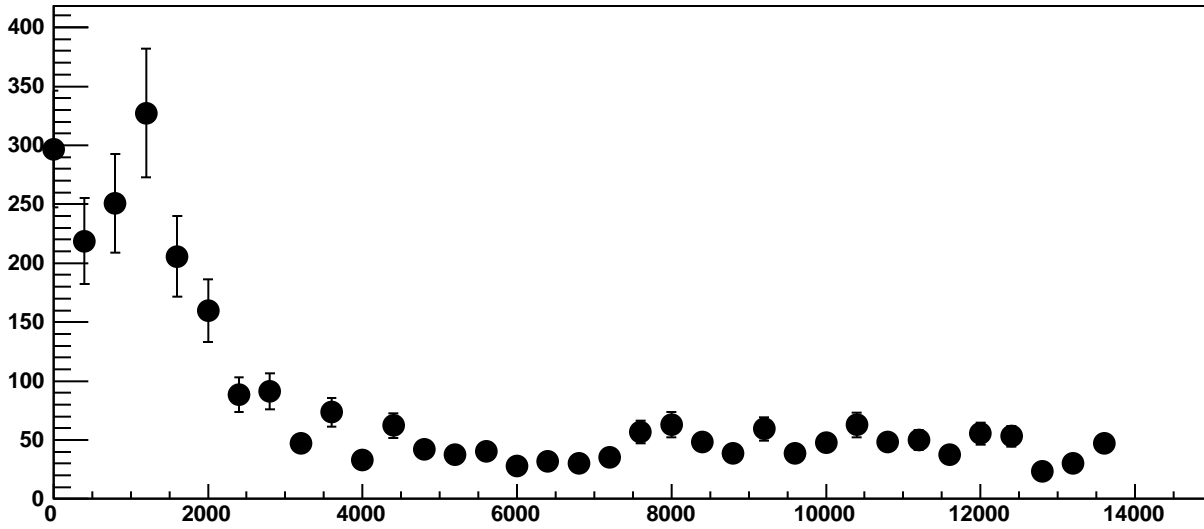
Chip 6, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



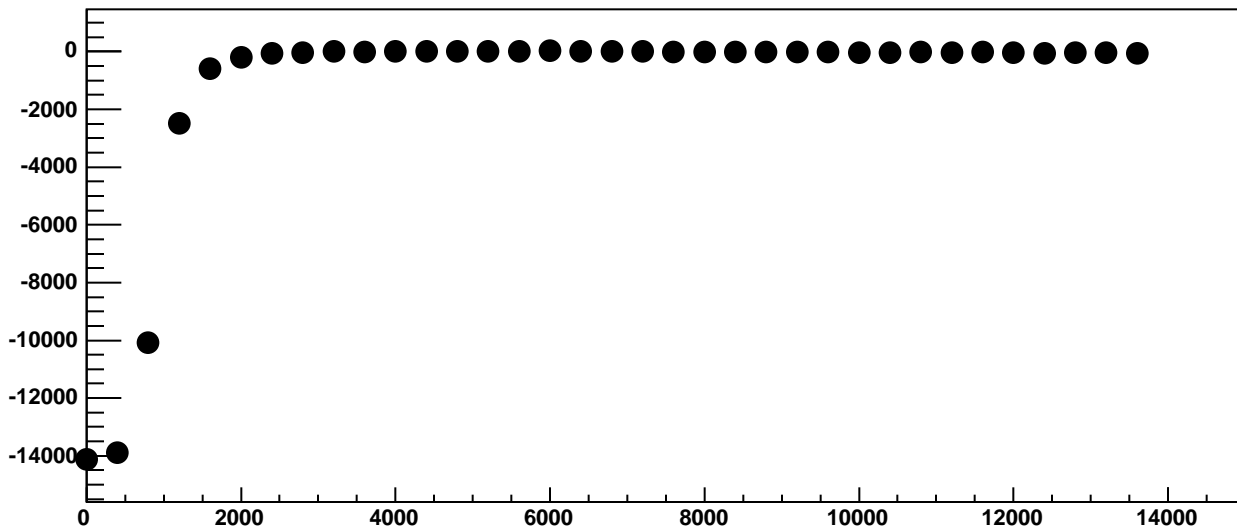
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



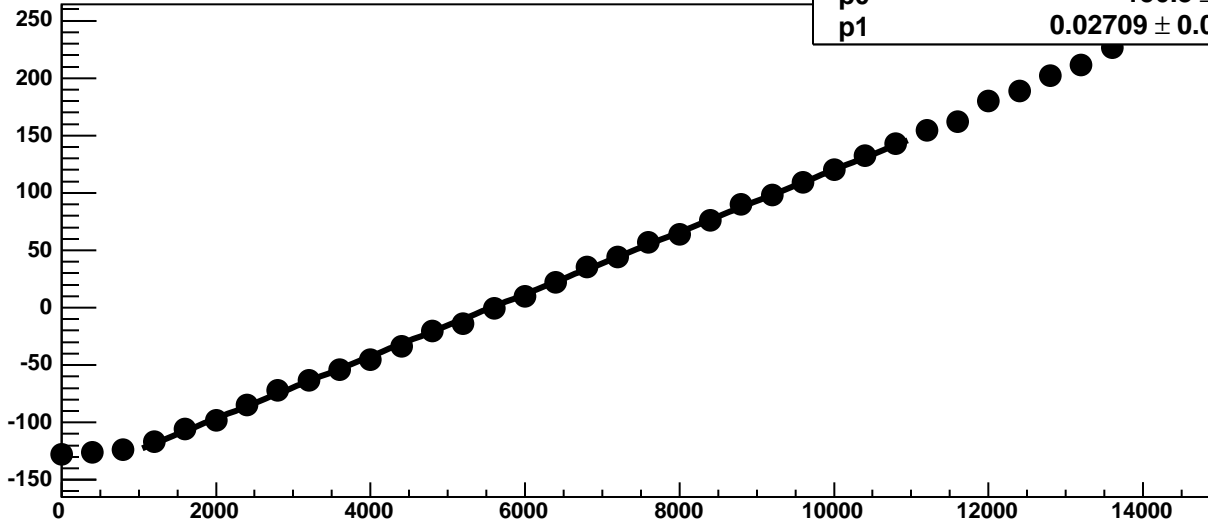
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

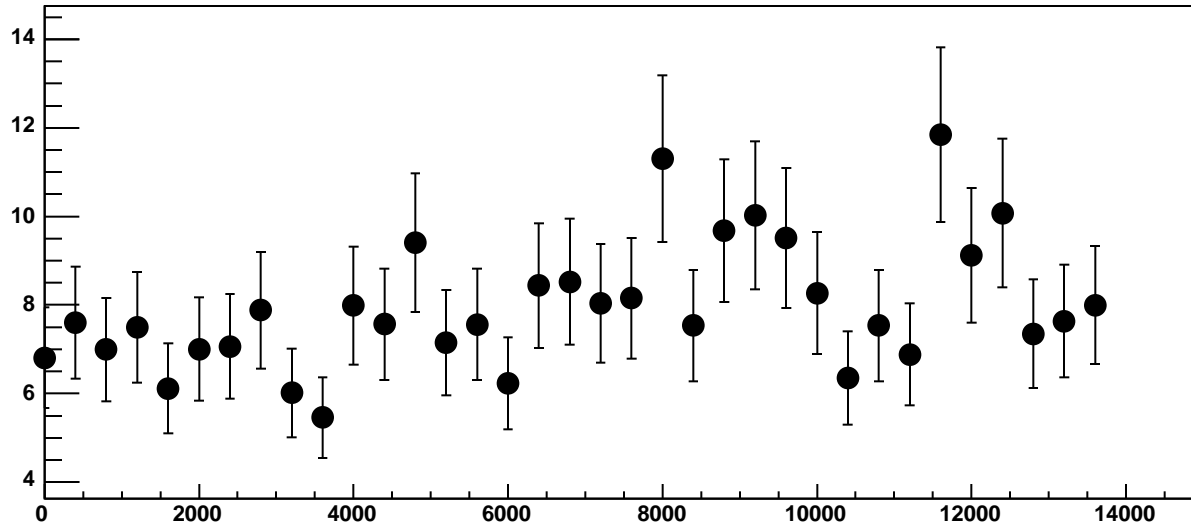


Chip 6, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

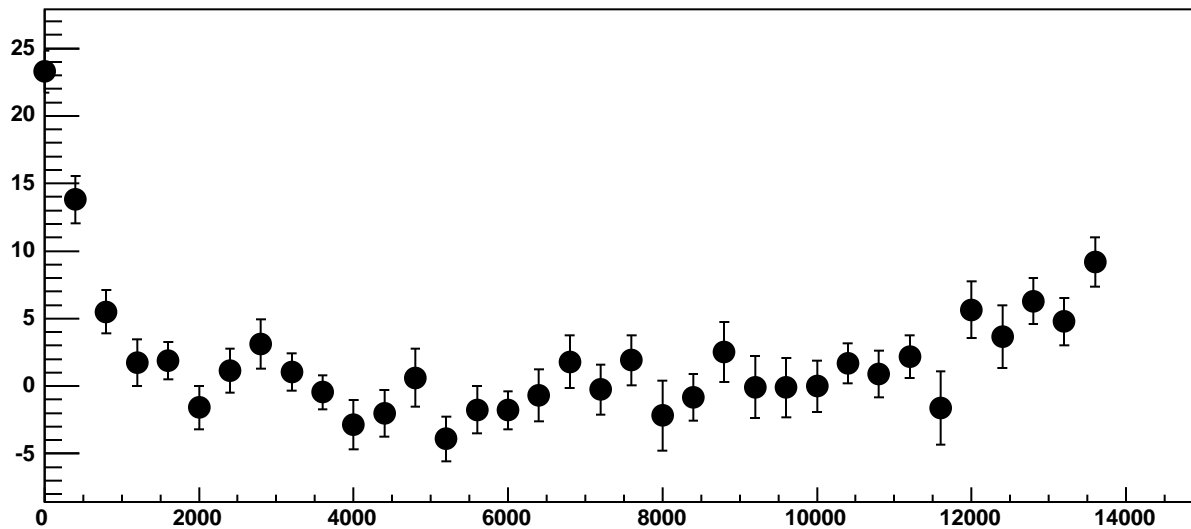


$\chi^2 / \text{ndf}$  26.01 / 23  
p0  $-150.8 \pm 0.7407$   
p1  $0.02709 \pm 0.0001177$

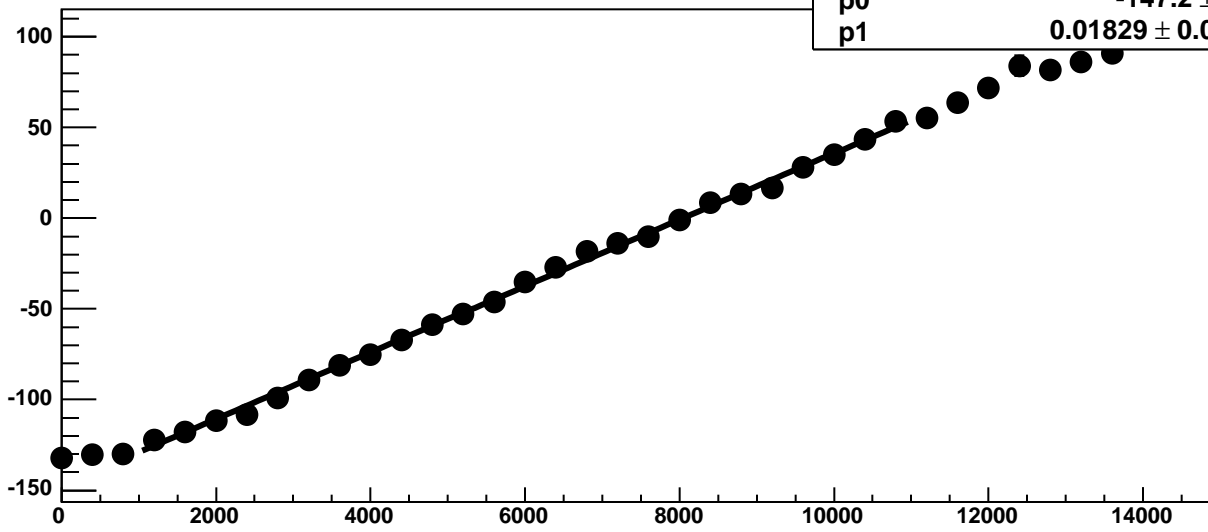
Chip 6, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



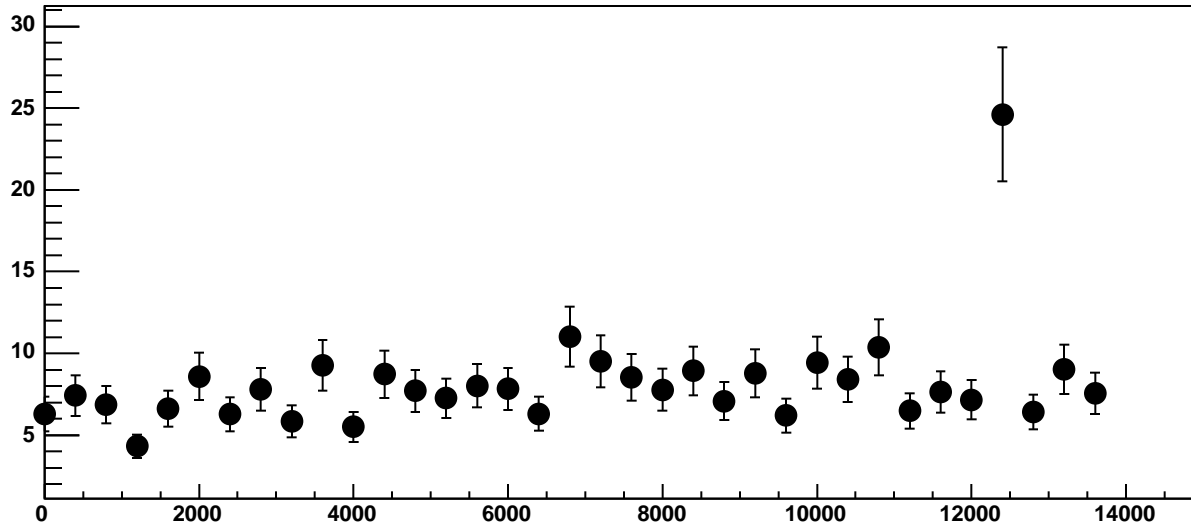
Chip 6, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



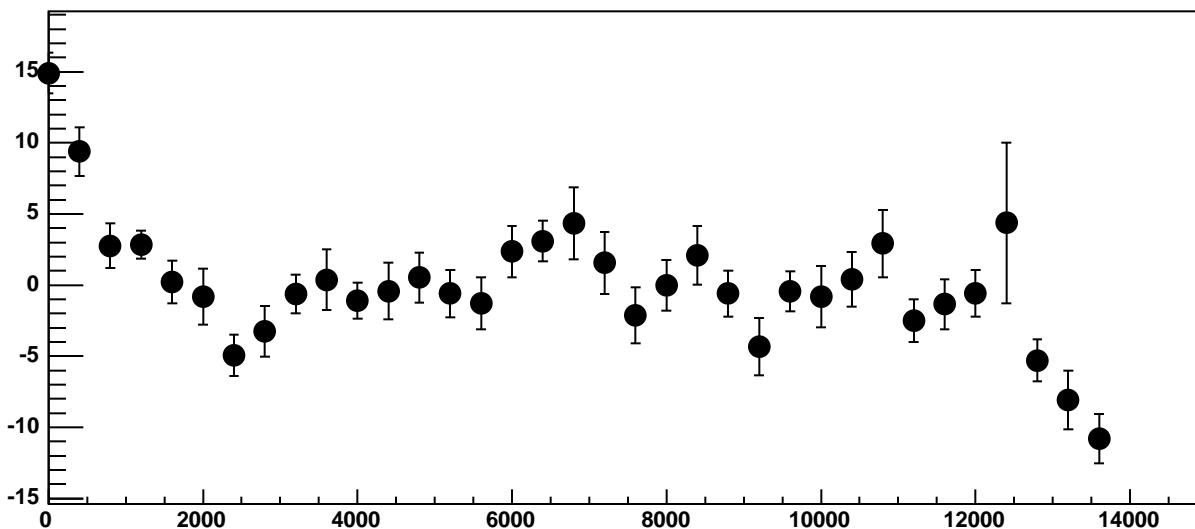
Chip 6, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



Chip 6, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

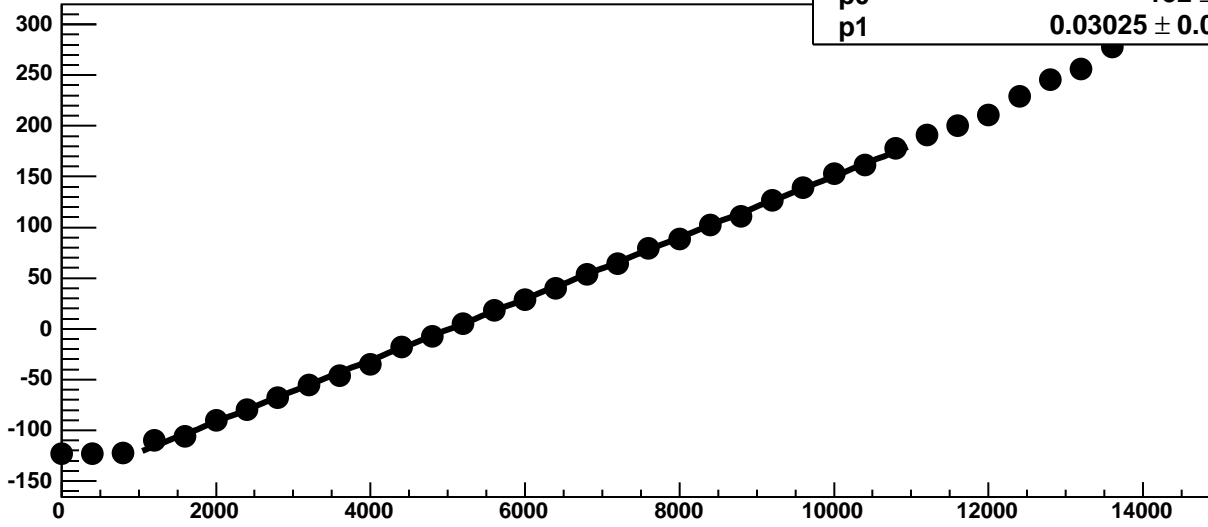


Chip 6, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



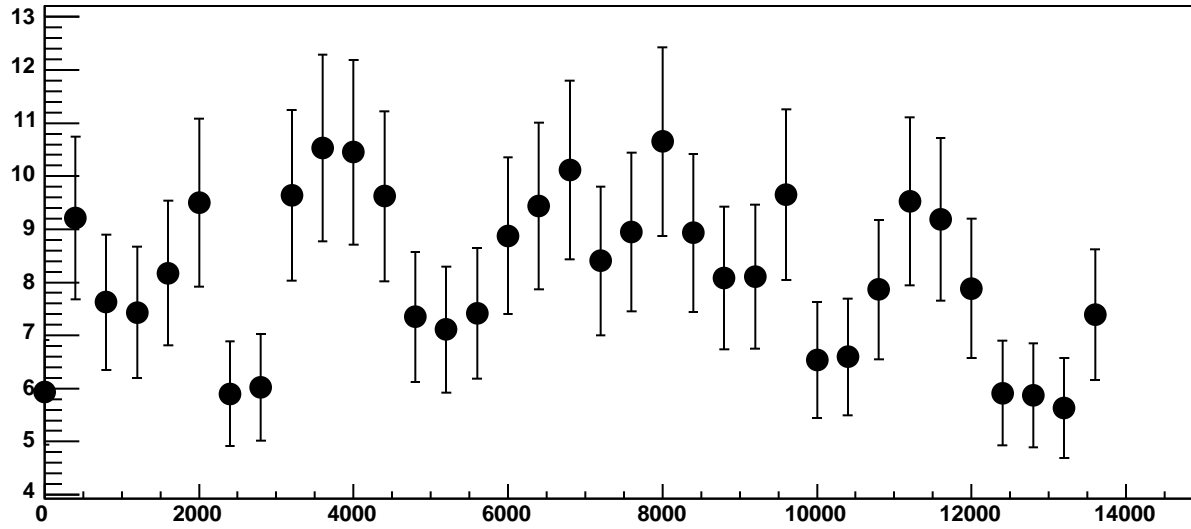


Chip 6, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

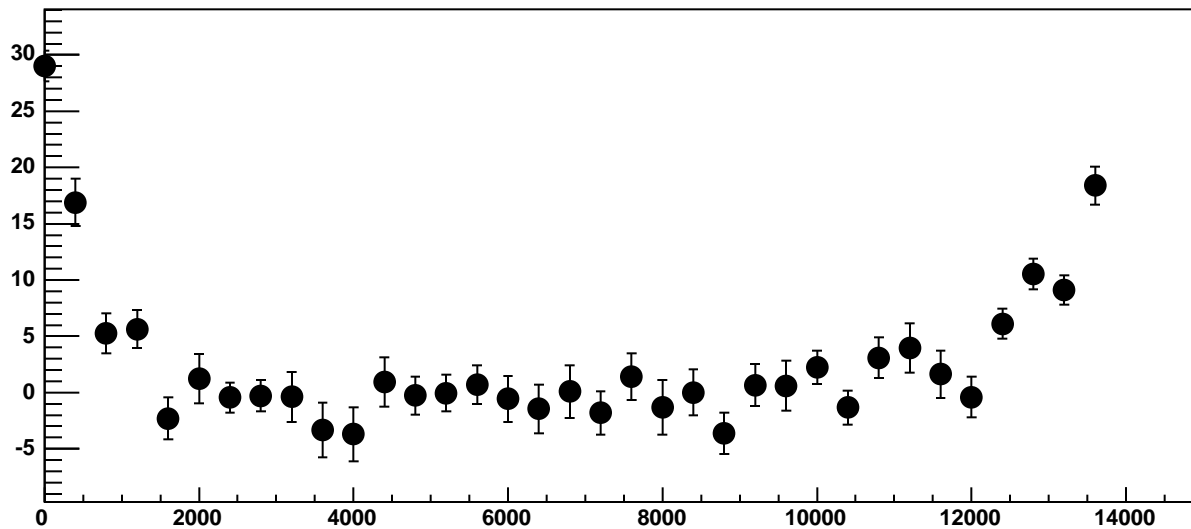


$\chi^2 / \text{ndf}$  29.69 / 23  
p0  $-152 \pm 0.8128$   
p1  $0.03025 \pm 0.0001217$

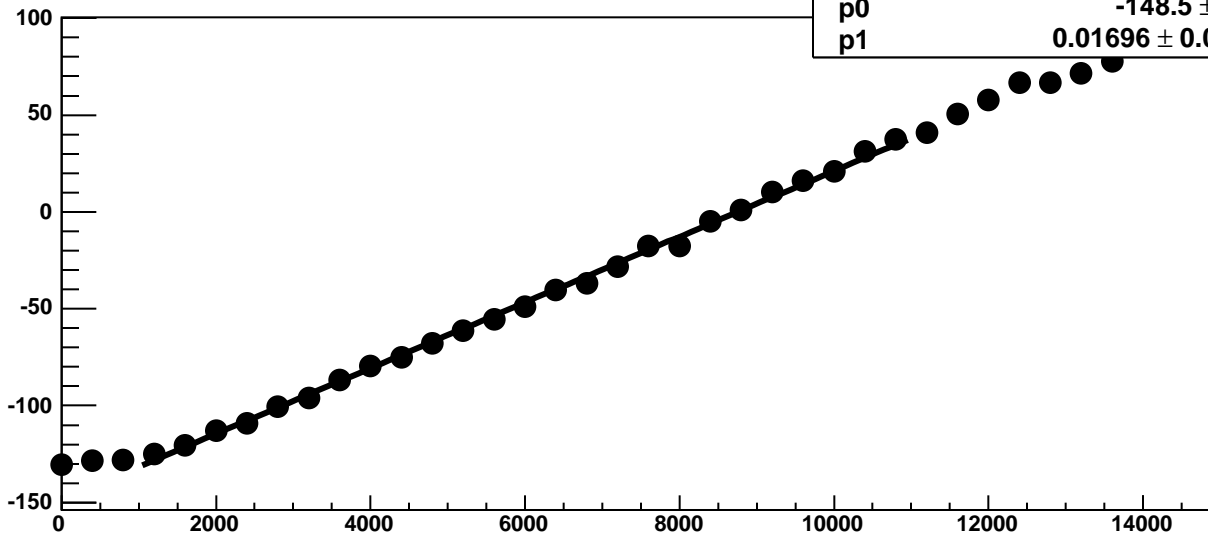
Chip 6, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

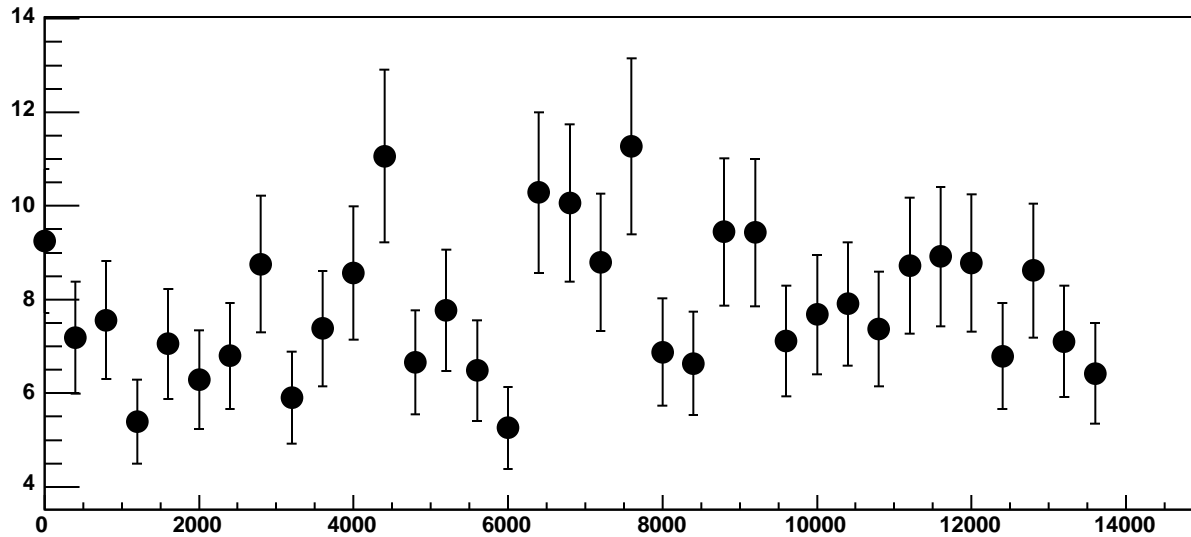


Chip 6, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

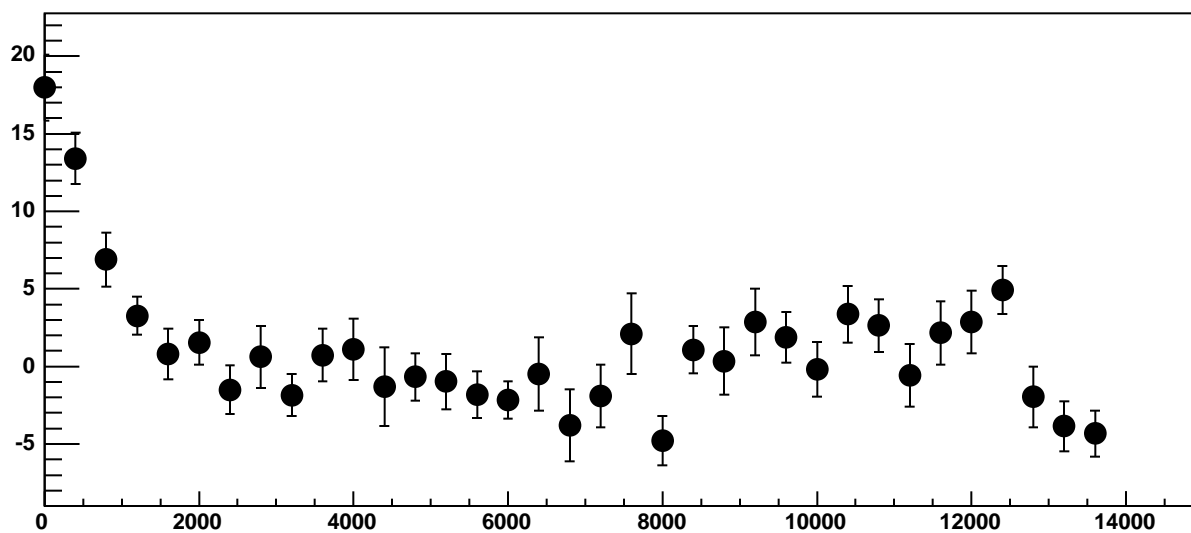


$\chi^2 / \text{ndf}$  40.27 / 23  
p0  $-148.5 \pm 0.7179$   
p1  $0.01696 \pm 0.0001132$

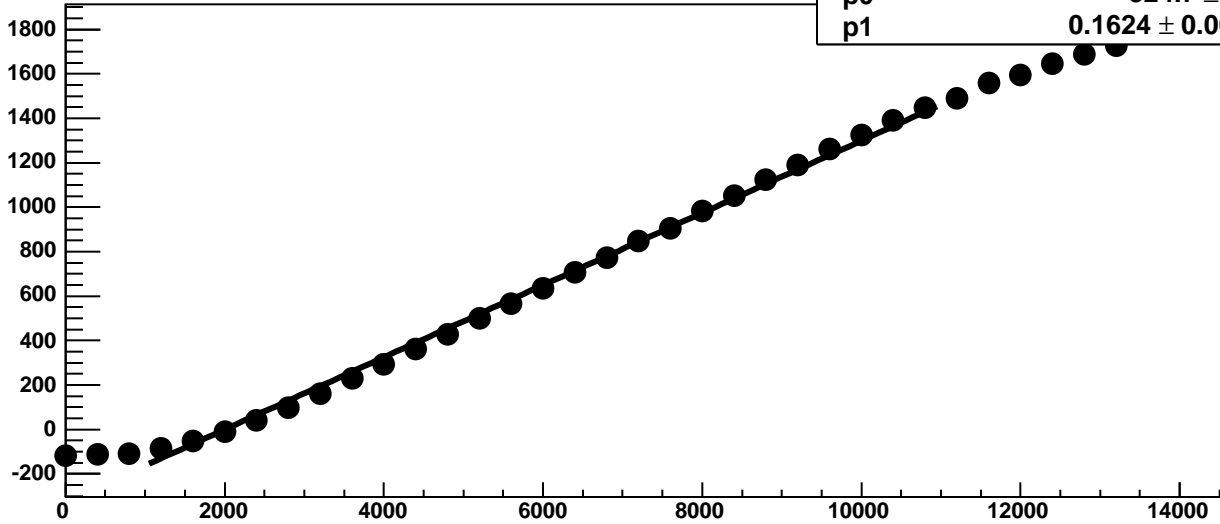
Chip 6, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



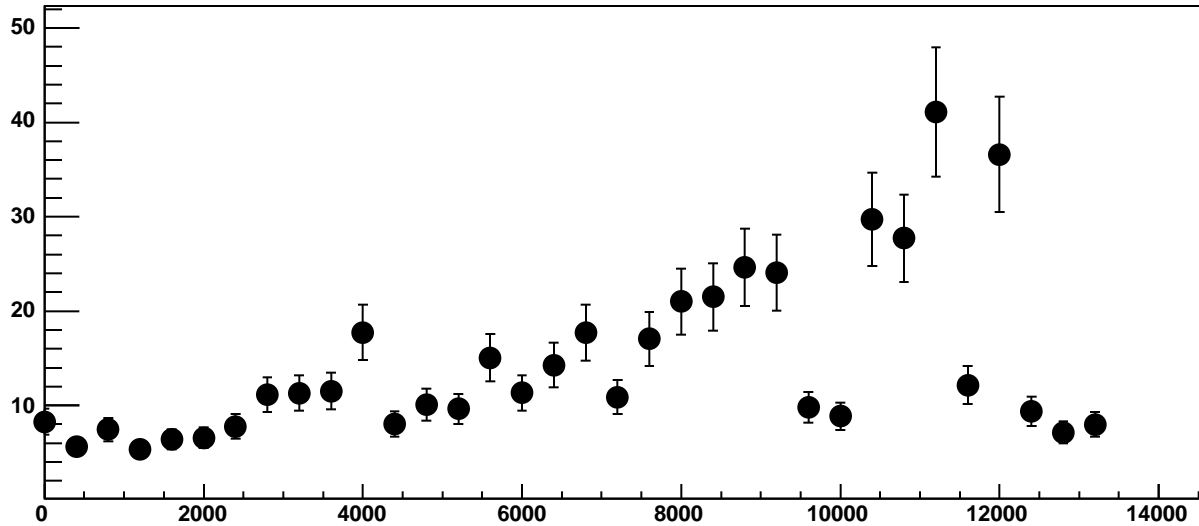
Chip 6, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



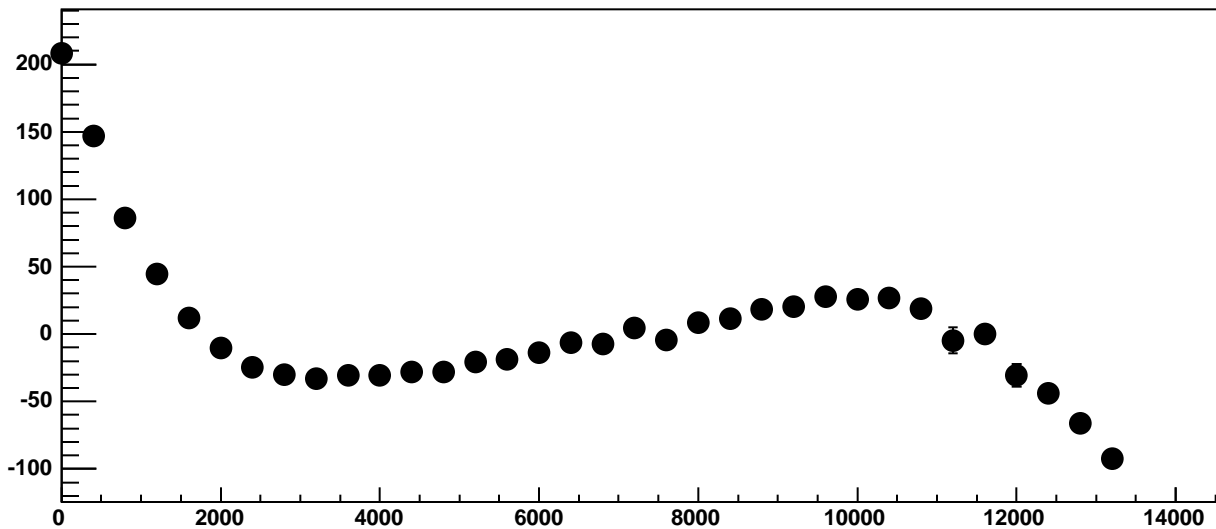
Chip 6, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

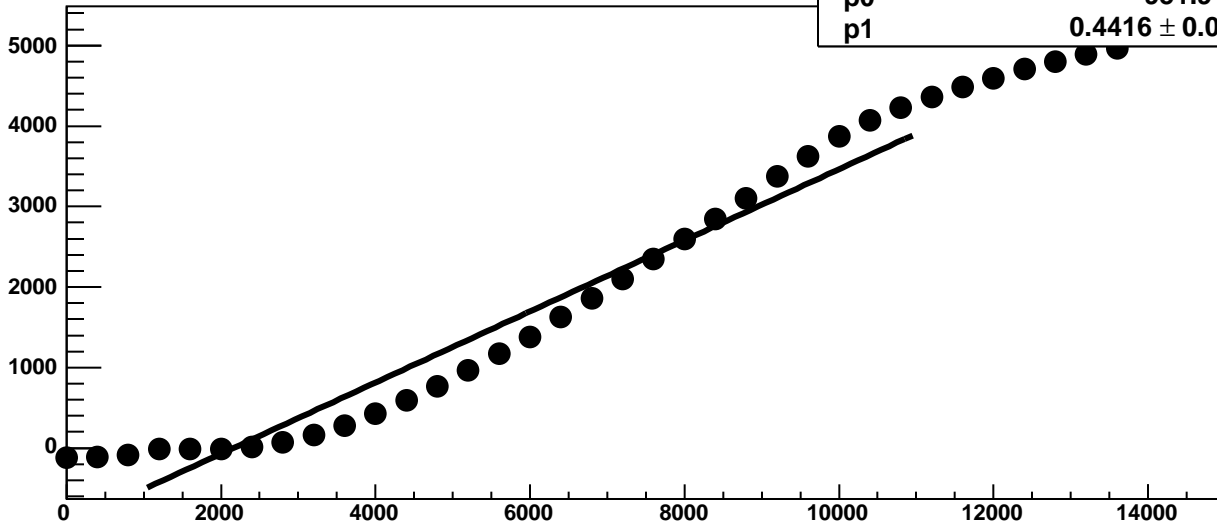


Chip 6, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

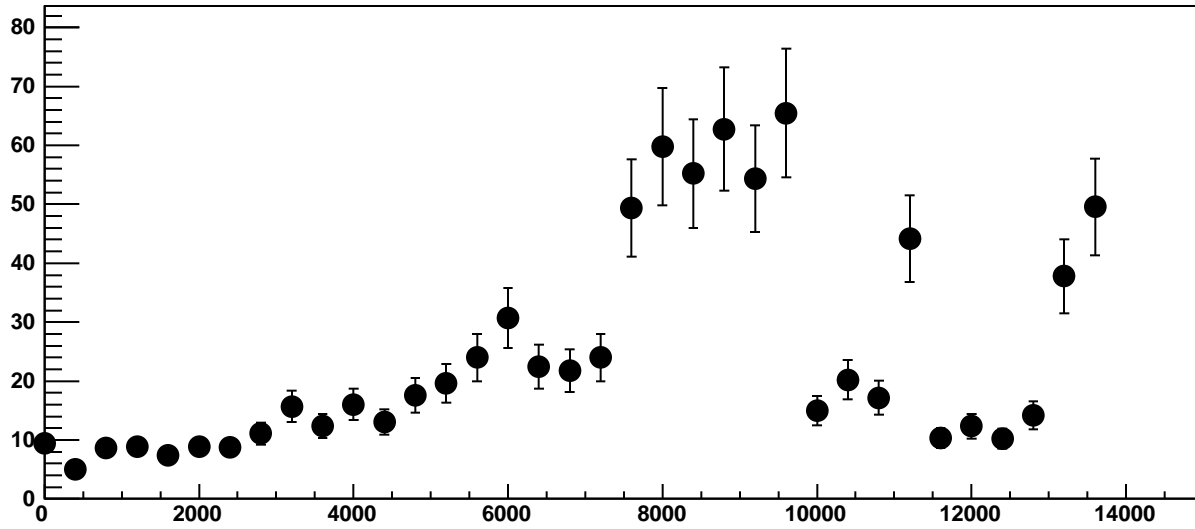


Chip 6, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

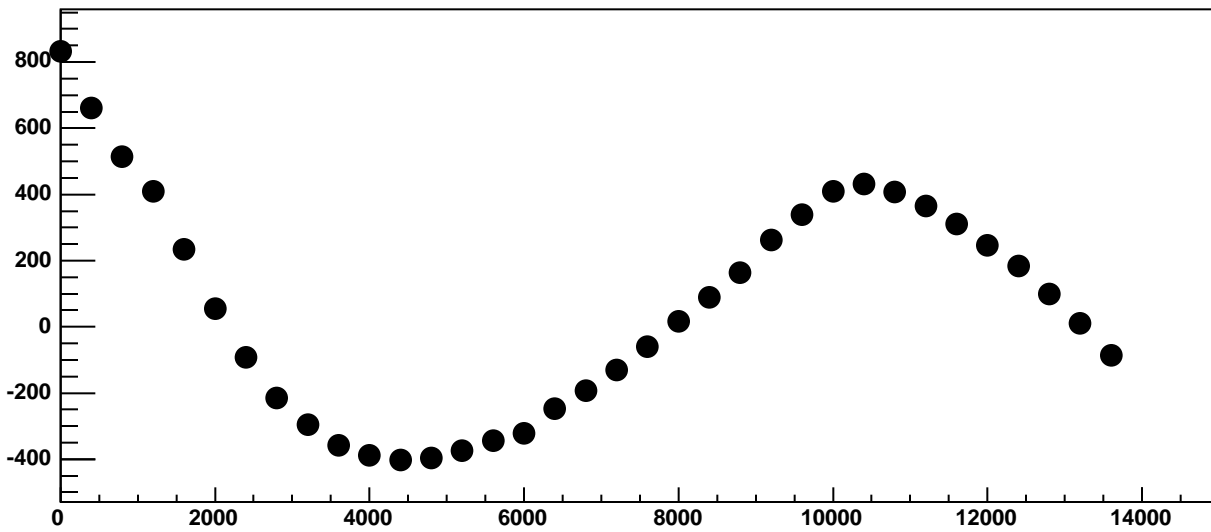
$\chi^2 / \text{ndf}$  1.829e+05 / 23  
p0 -951.9 ± 1.157  
p1 0.4416 ± 0.0002552



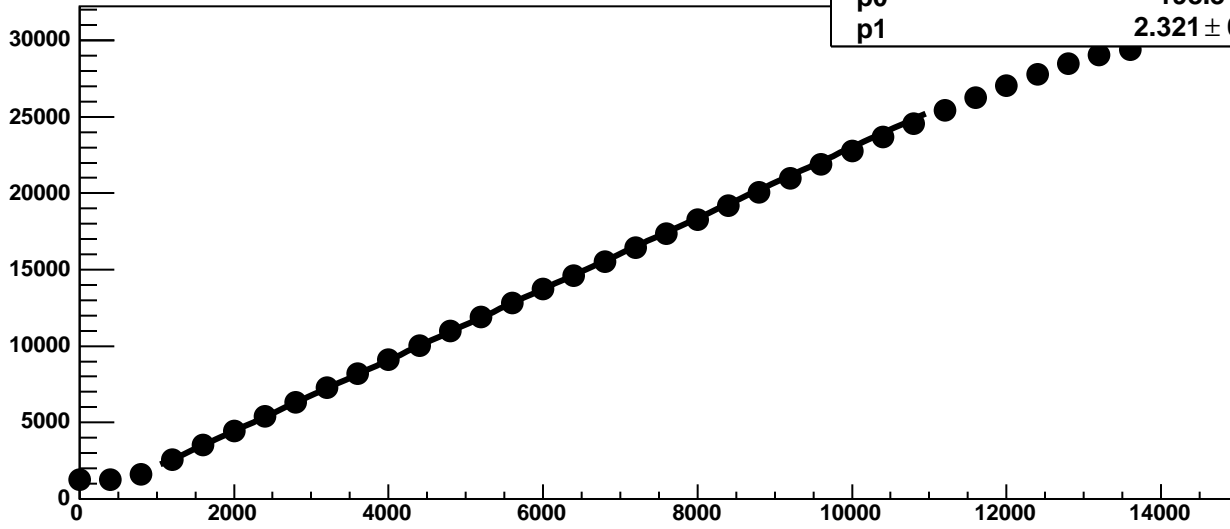
Chip 6, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

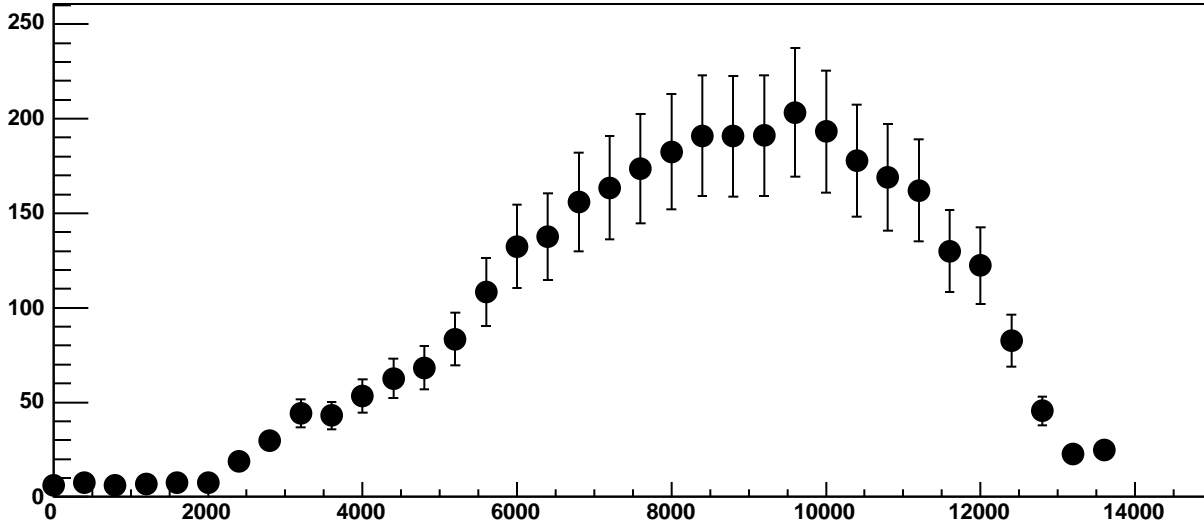


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

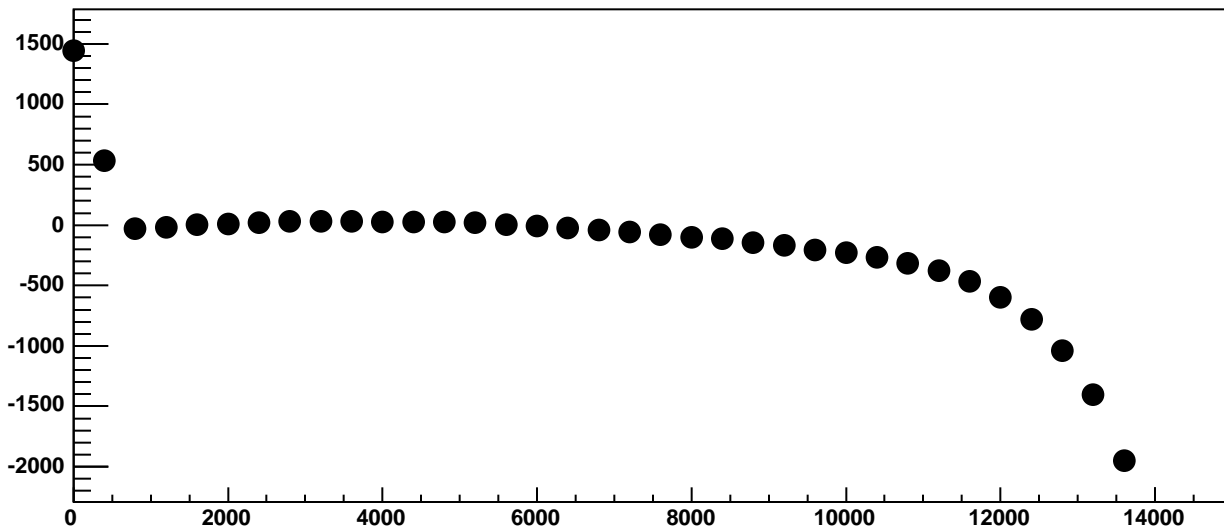


$\chi^2 / \text{ndf}$  422 / 23  
p0  $-198.3 \pm 2.186$   
p1  $2.321 \pm 0.00113$

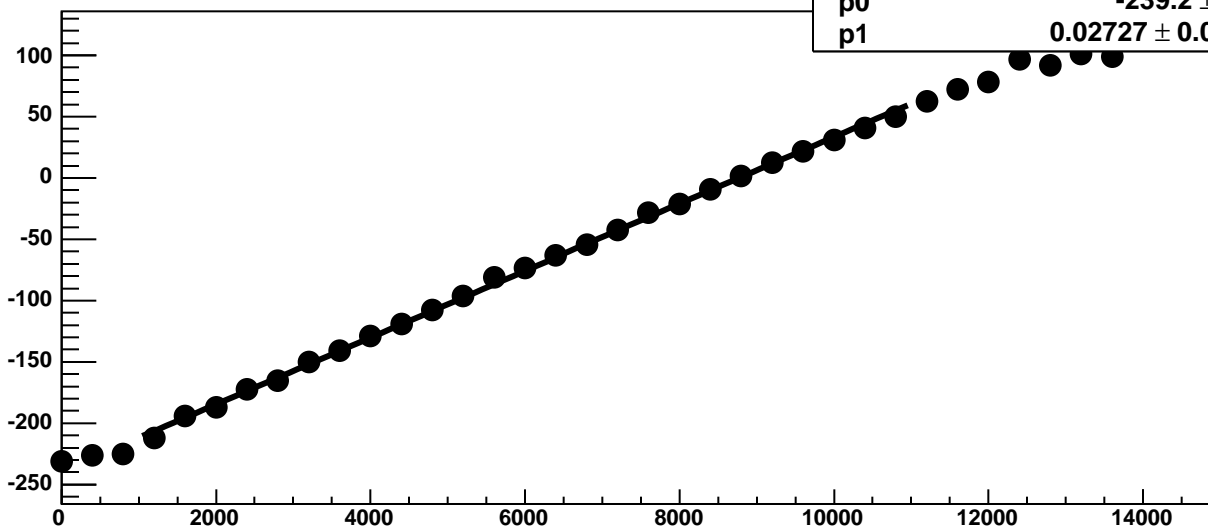
Chip 6, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



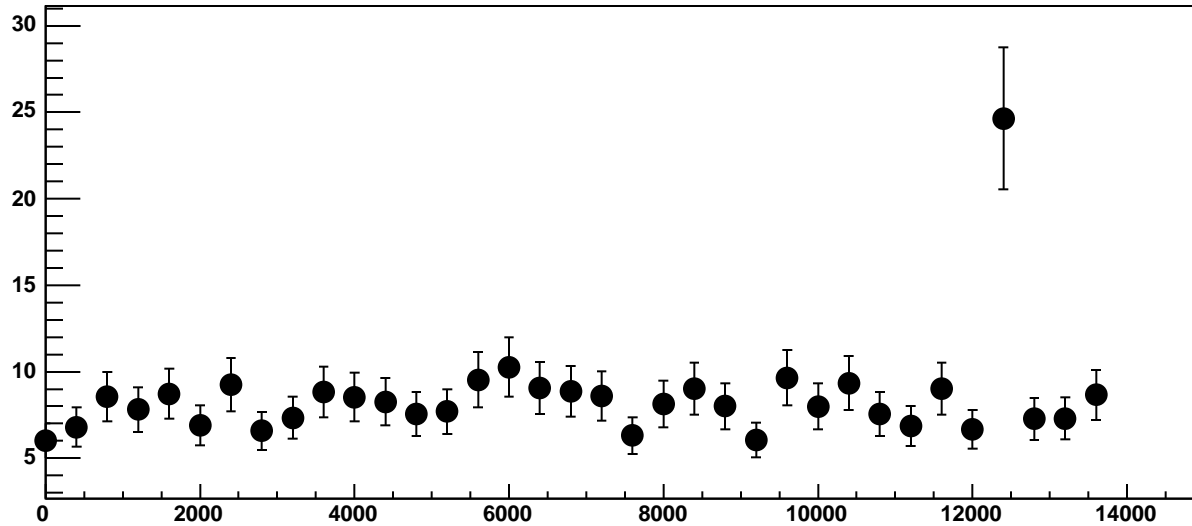
Chip 6, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



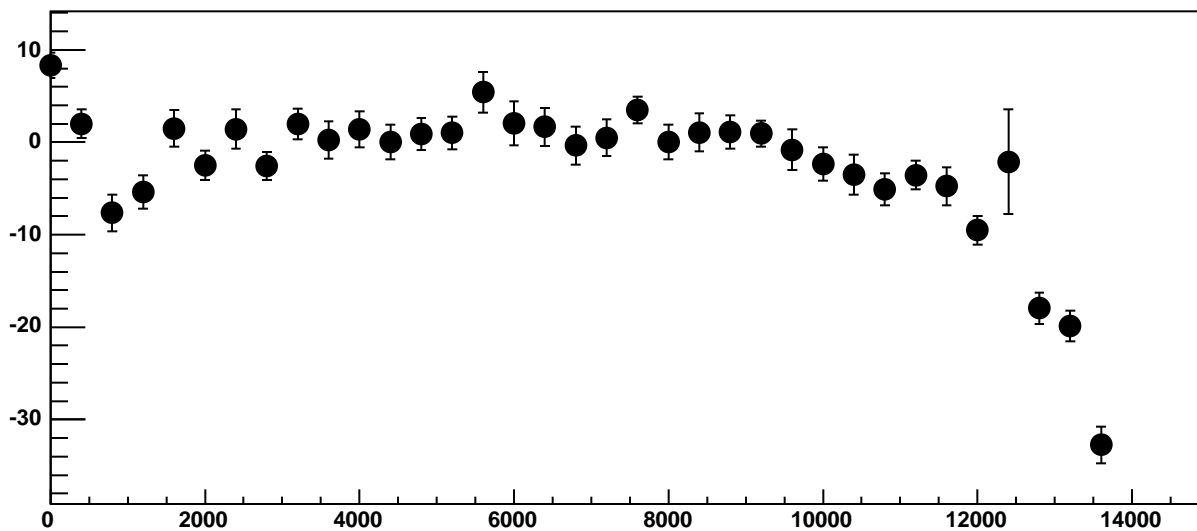
Chip 6, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



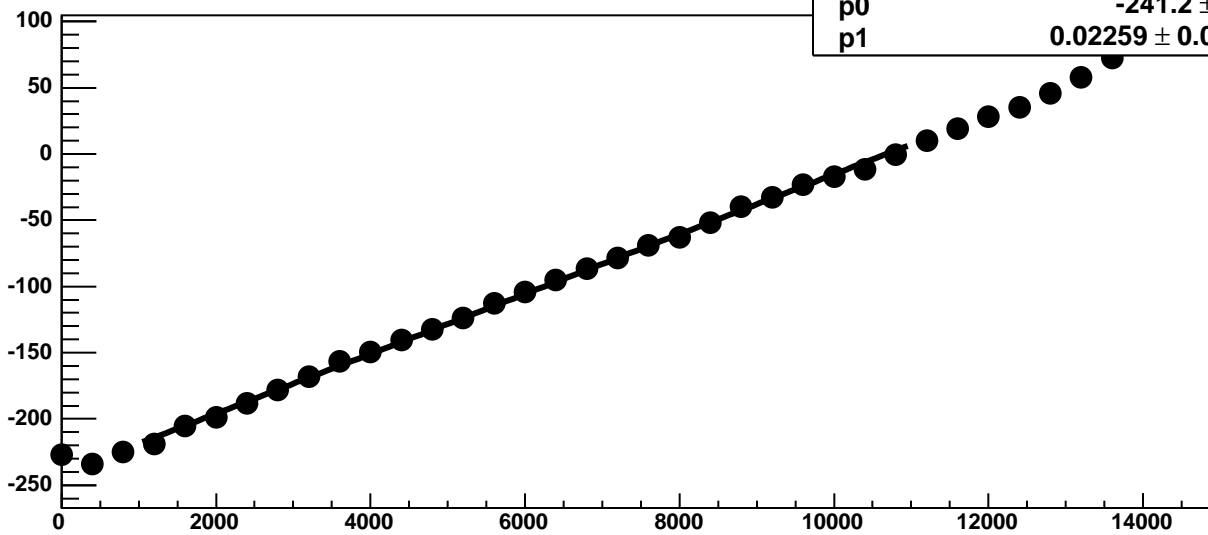
Chip 6, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

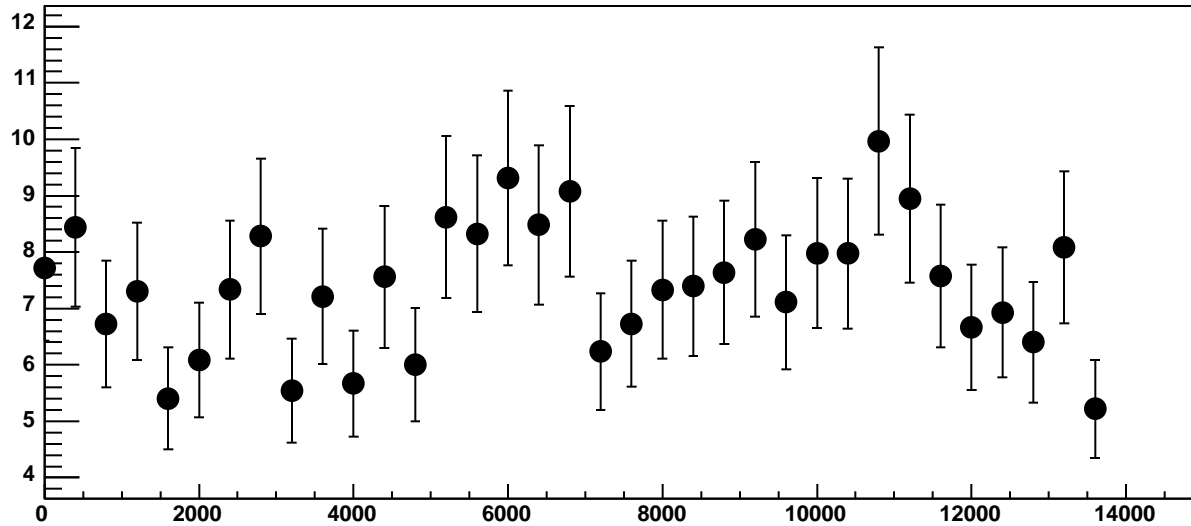


Chip 6, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

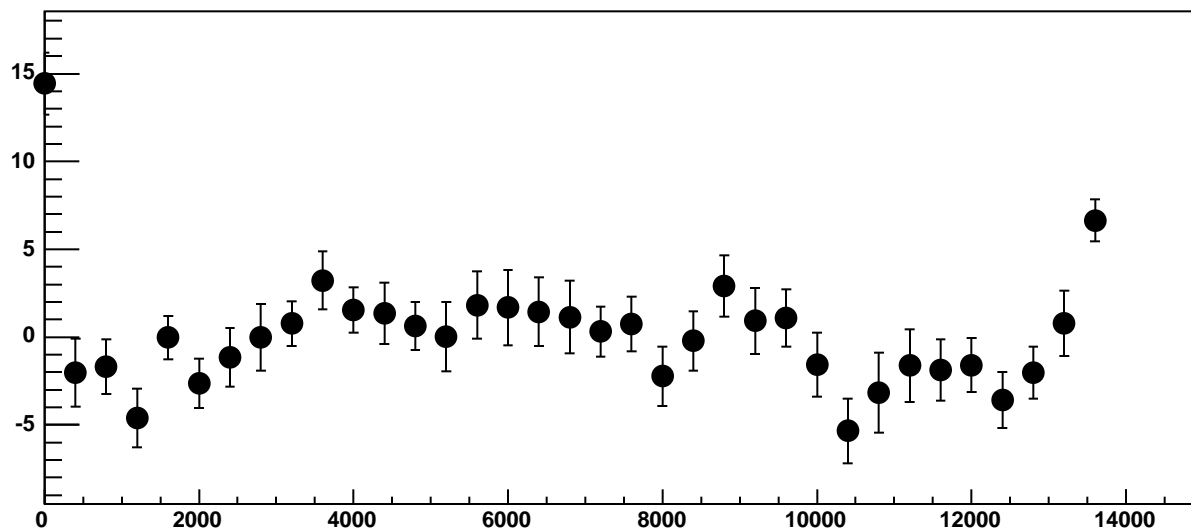


$\chi^2 / \text{ndf}$  37.12 / 23  
p0 -241.2  $\pm$  0.7138  
p1 0.02259  $\pm$  0.0001146

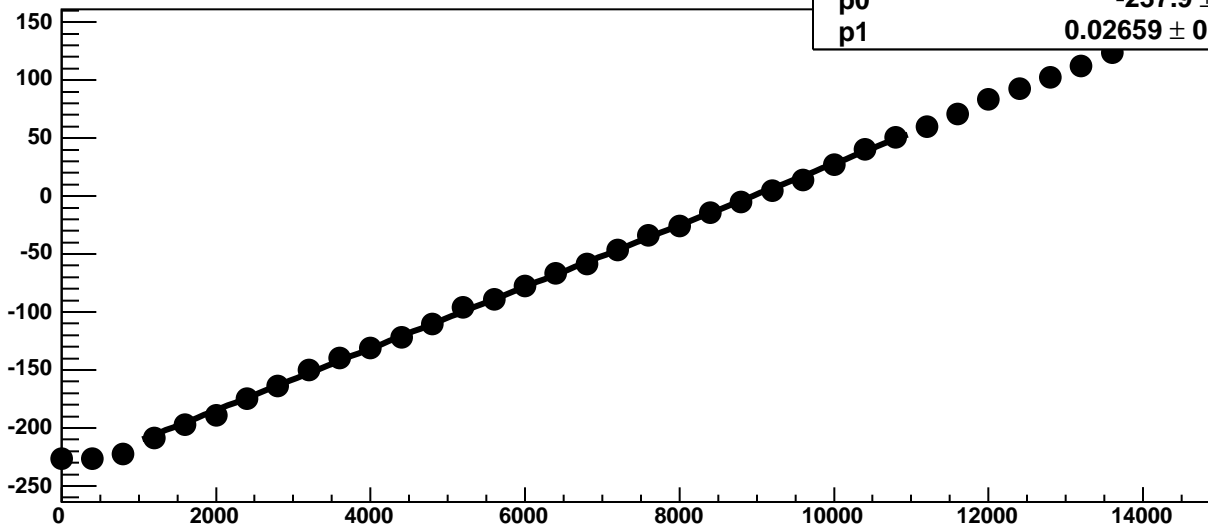
Chip 6, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

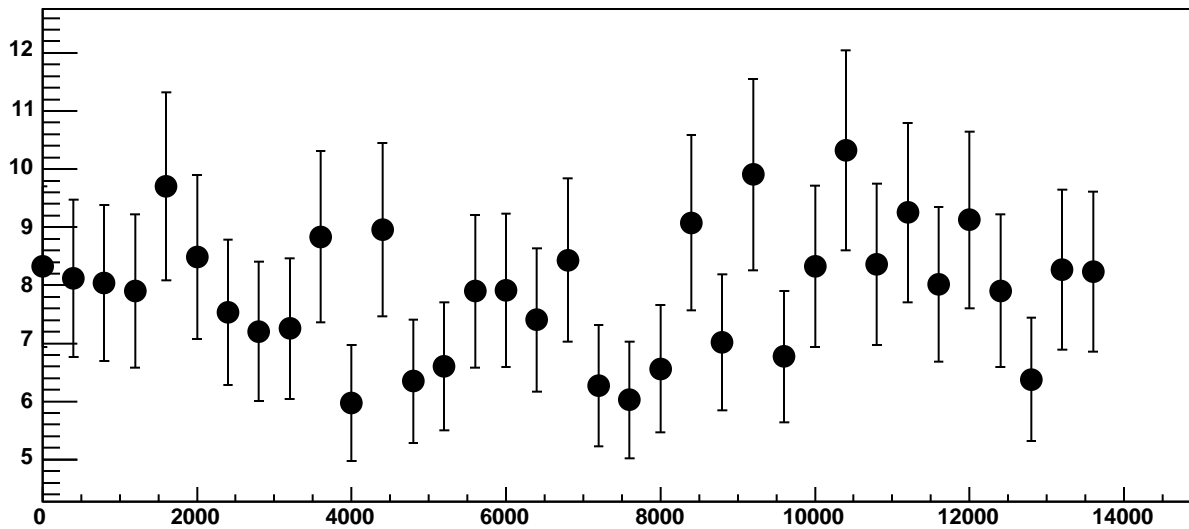


Chip 6, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

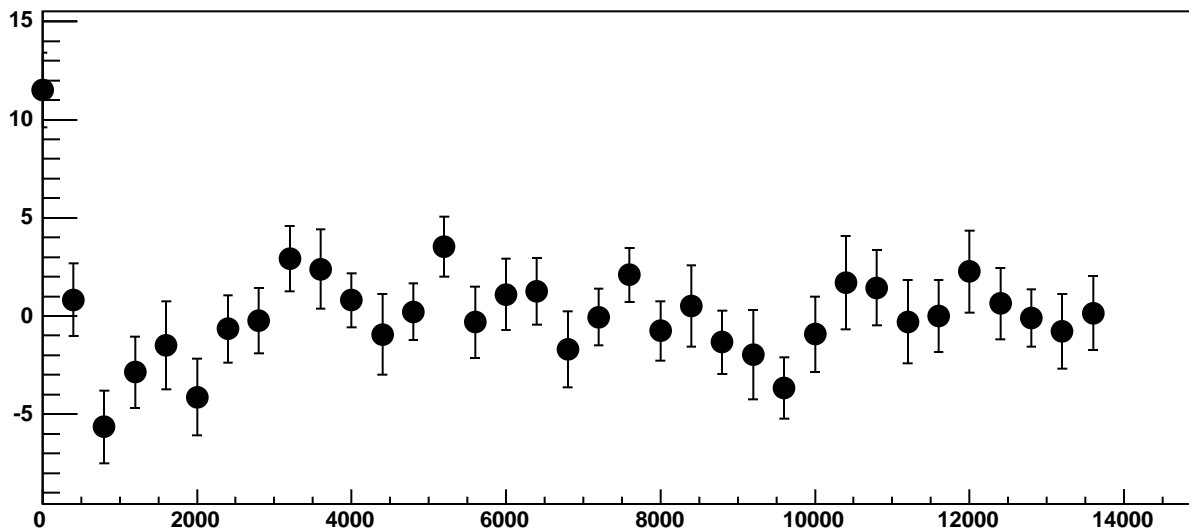


$\chi^2 / \text{ndf}$  30.67 / 23  
p0  $-237.9 \pm 0.8385$   
p1  $0.02659 \pm 0.000128$

Chip 6, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

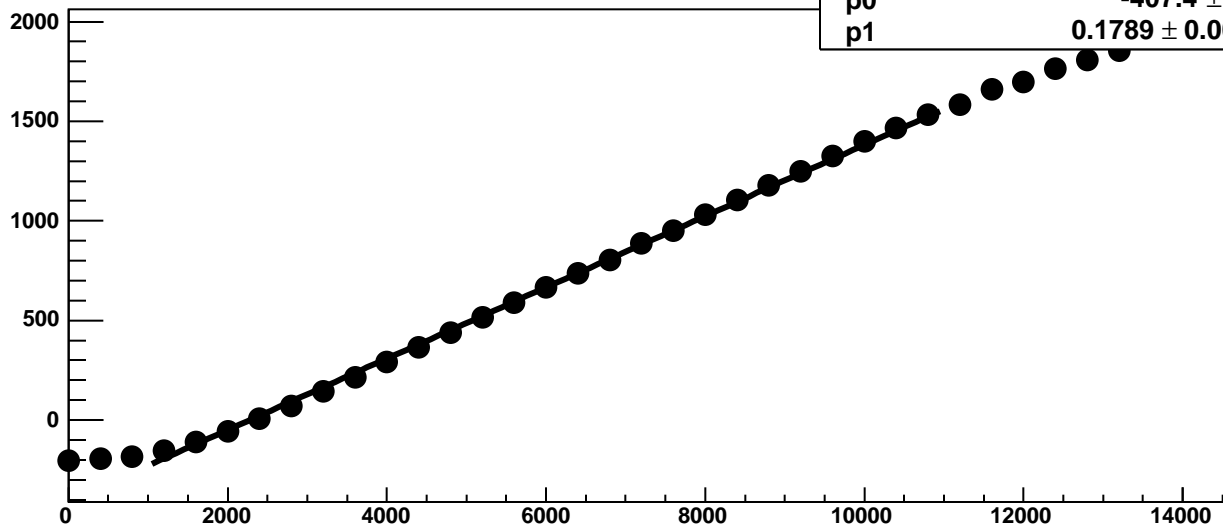


Chip 6, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

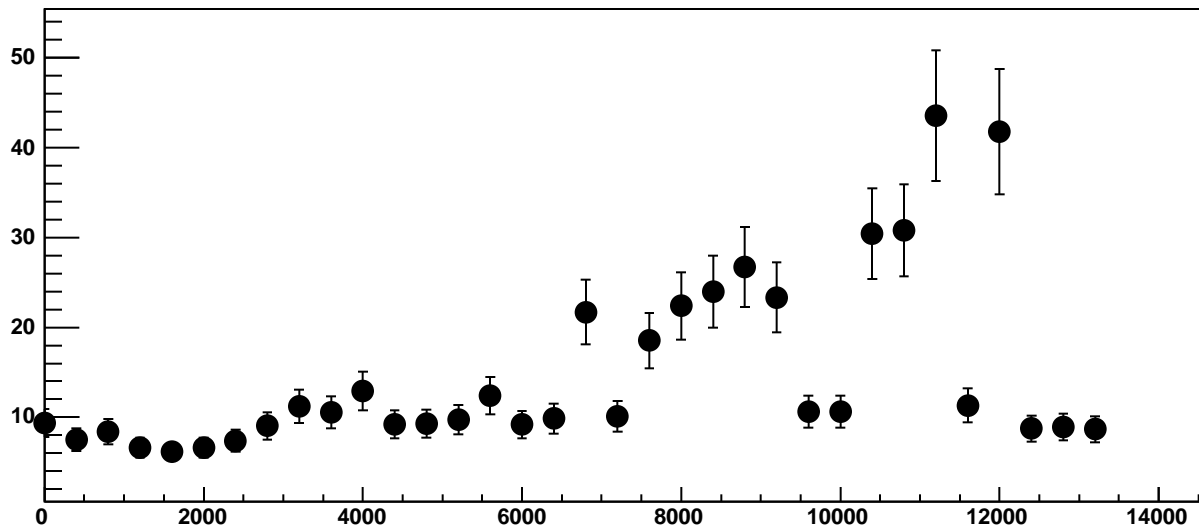




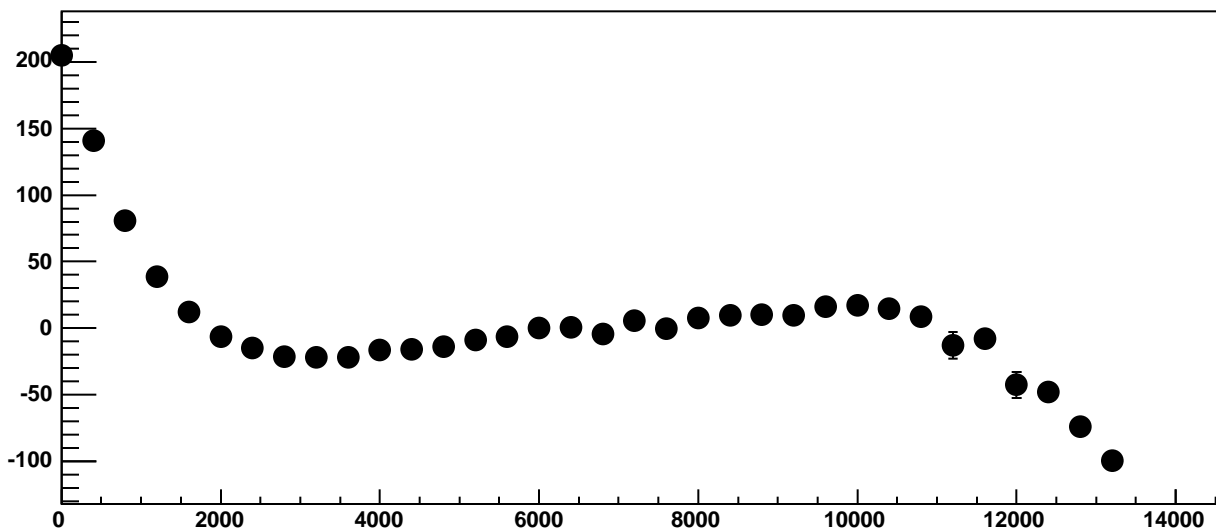
Chip 6, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



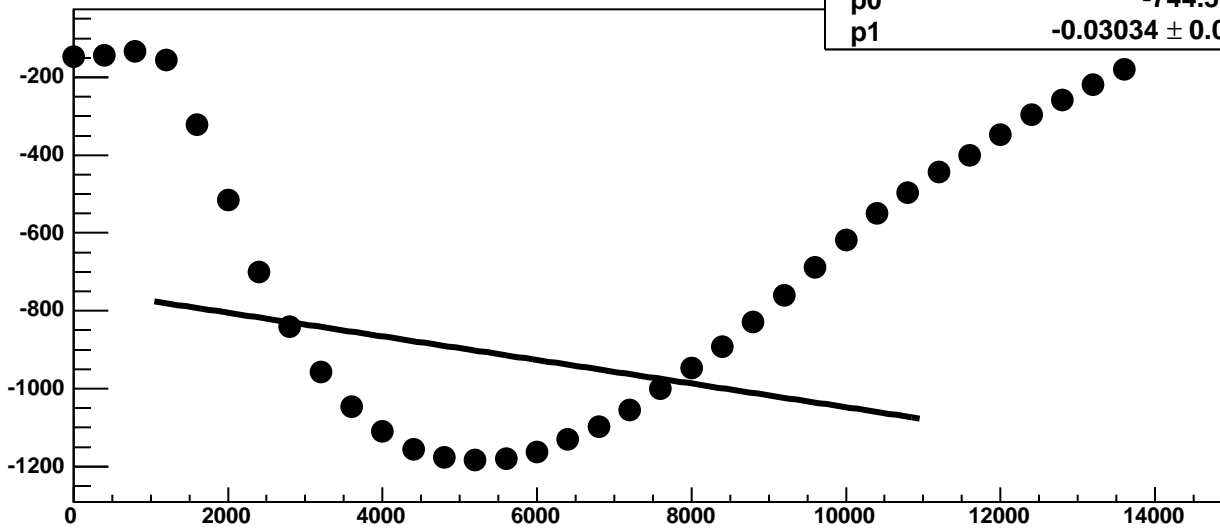
Chip 6, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

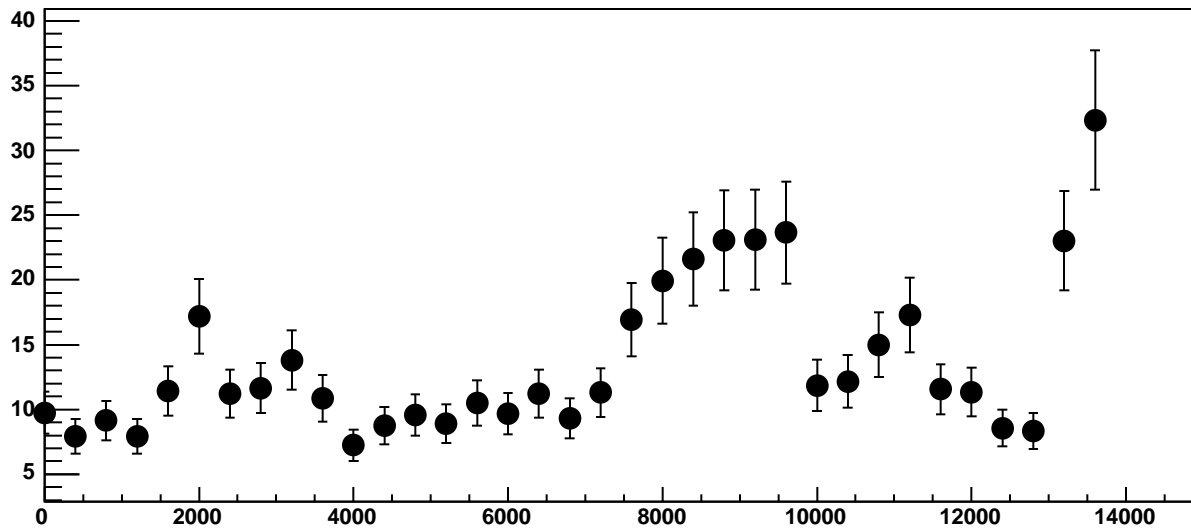


Chip 6, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

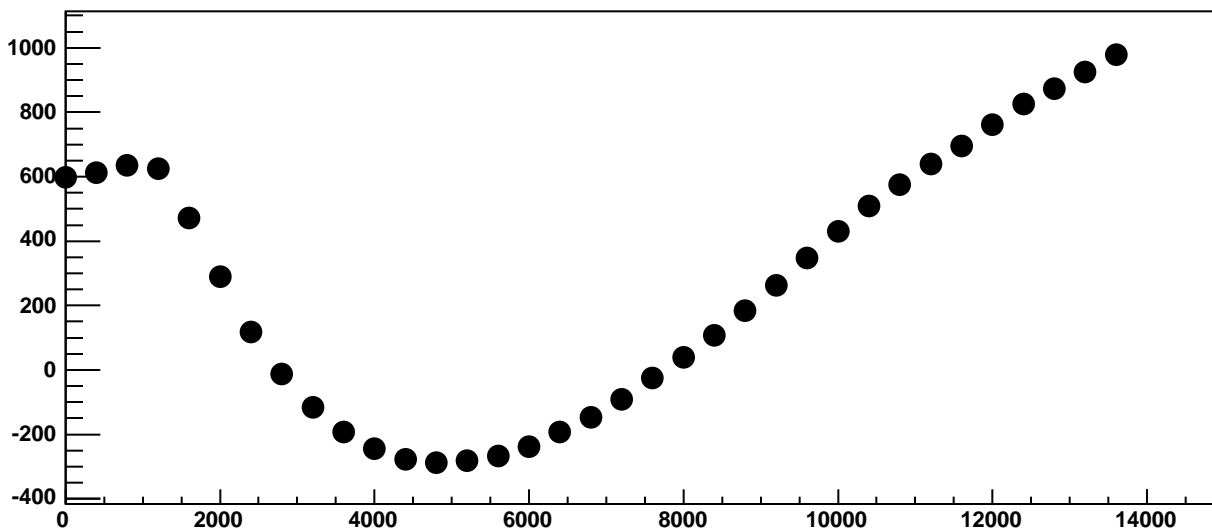


$\chi^2 / \text{ndf}$  3.722e+05 / 23  
p0 -744.3 ± 1.165  
p1 -0.03034 ± 0.0002019

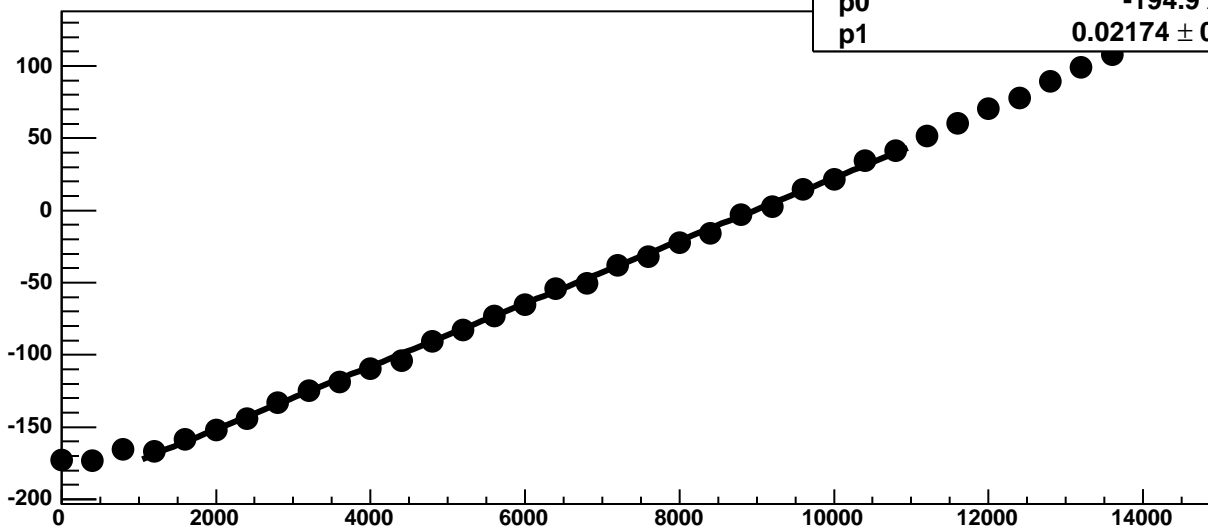
Chip 6, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



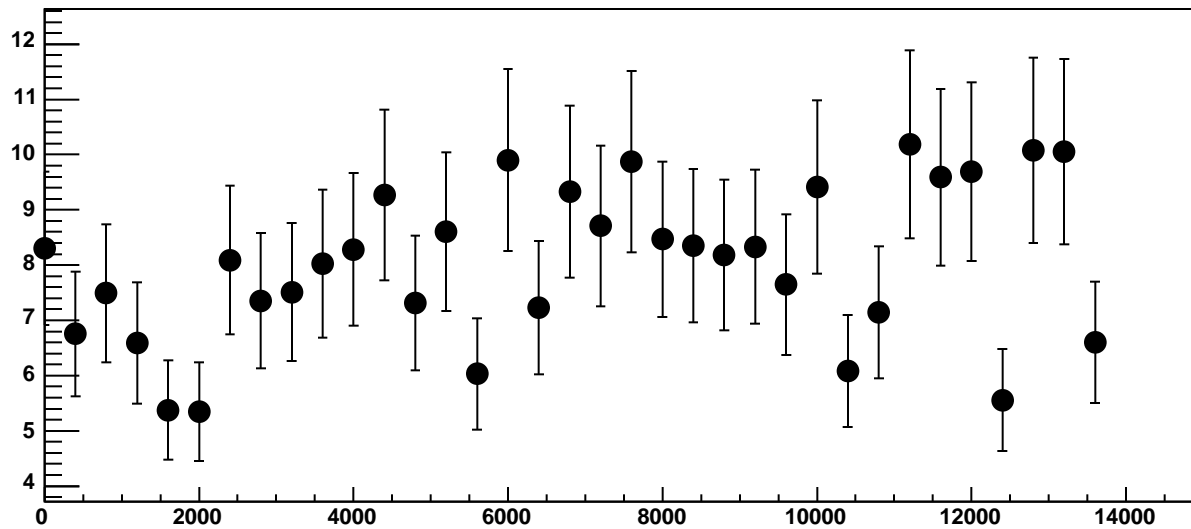
Chip 6, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



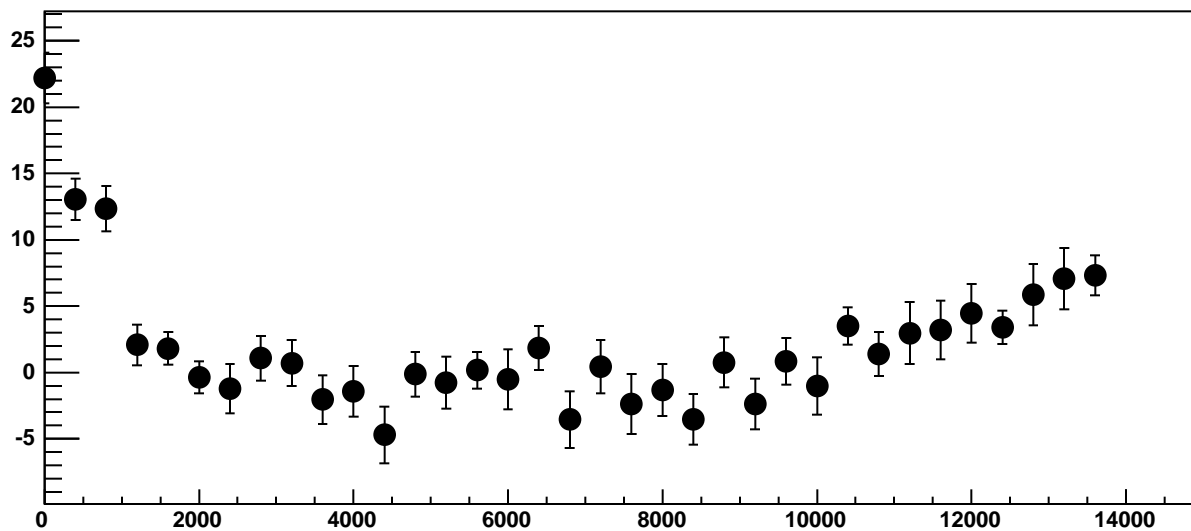
Chip 6, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



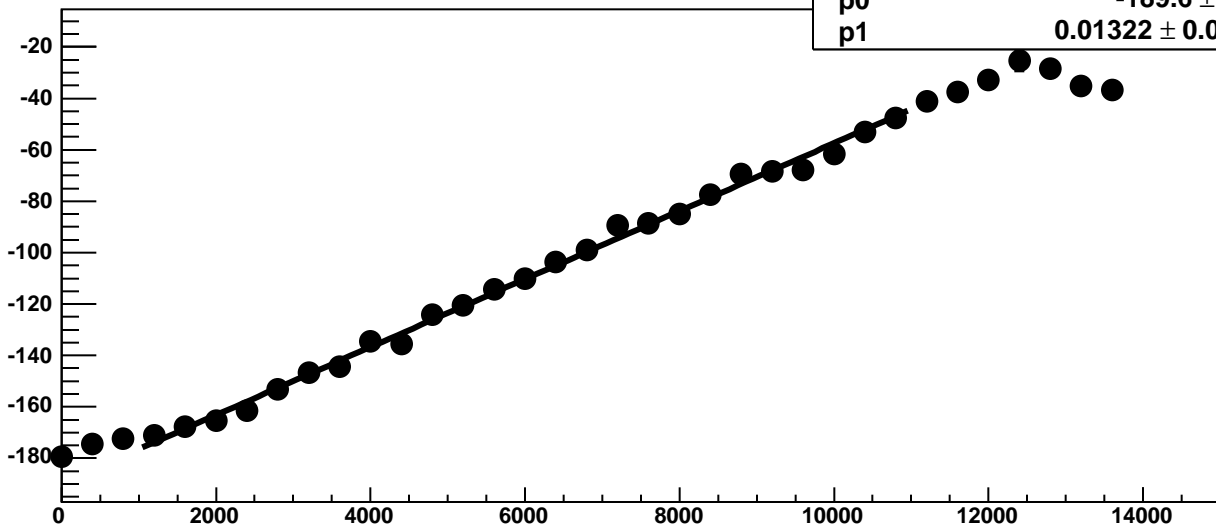
Chip 6, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



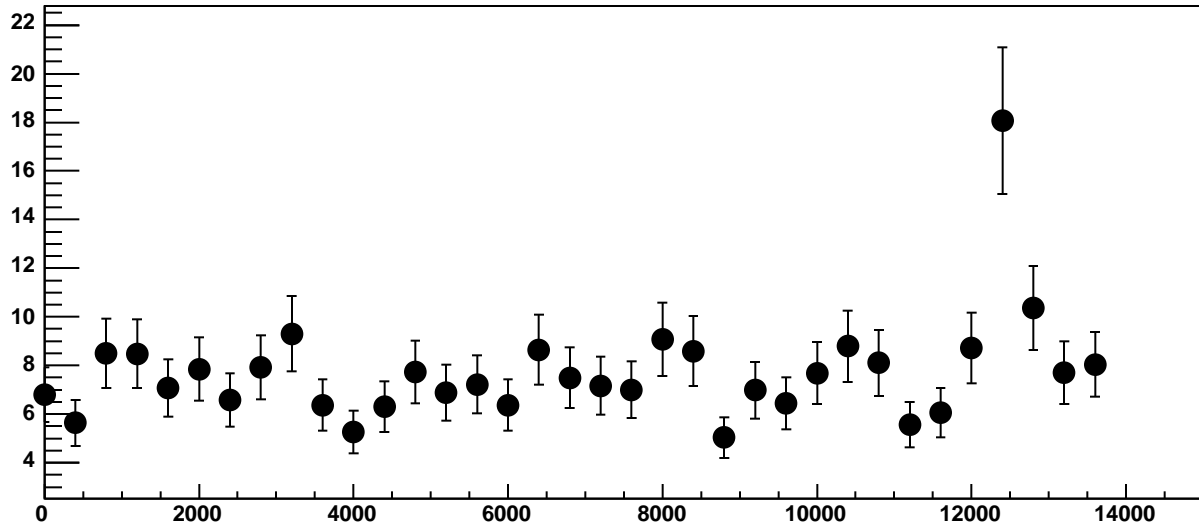
Chip 6, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



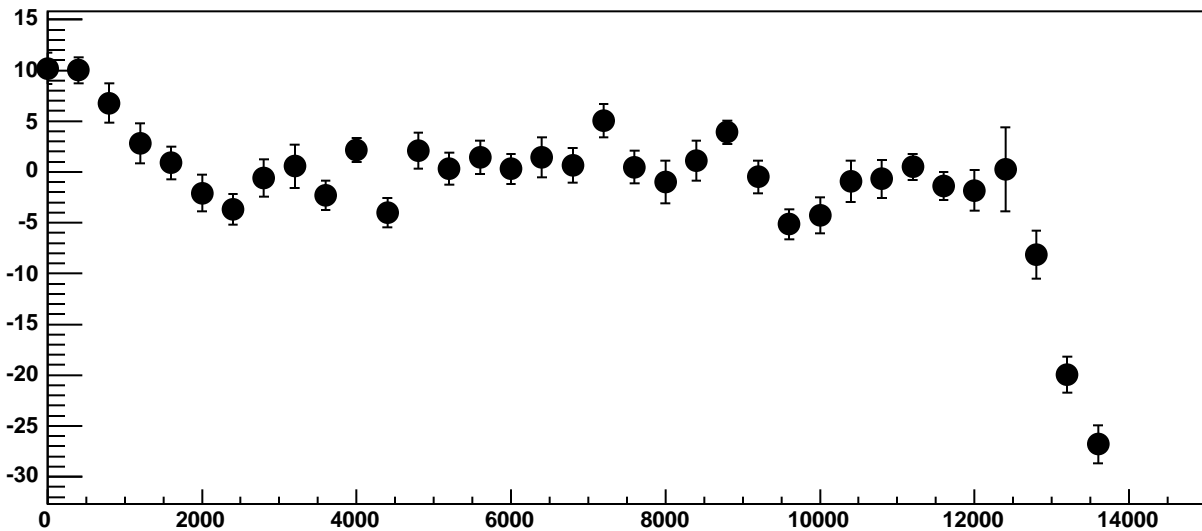
Chip 6, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



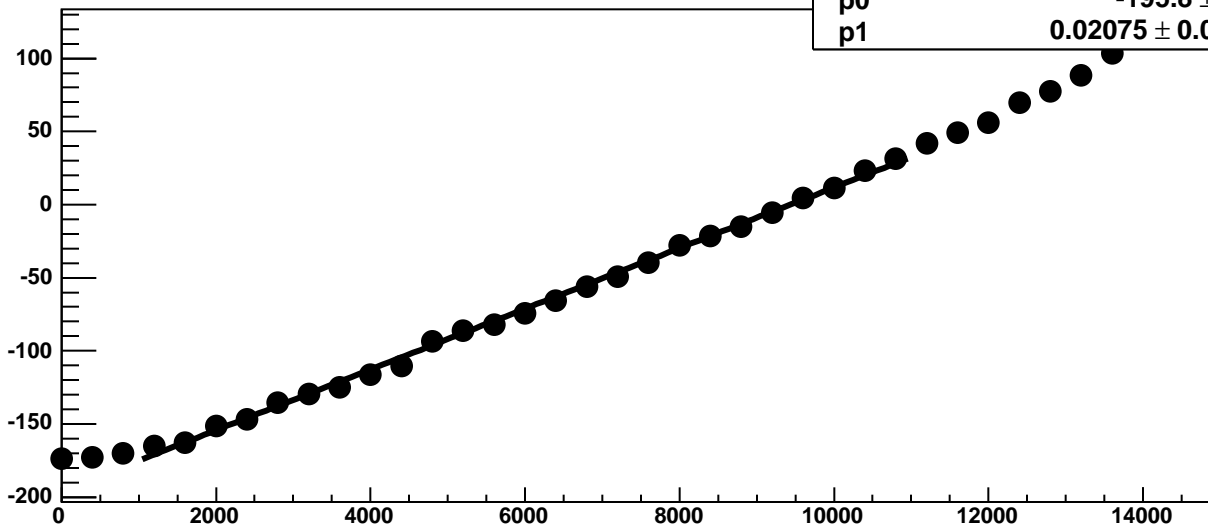
Chip 6, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

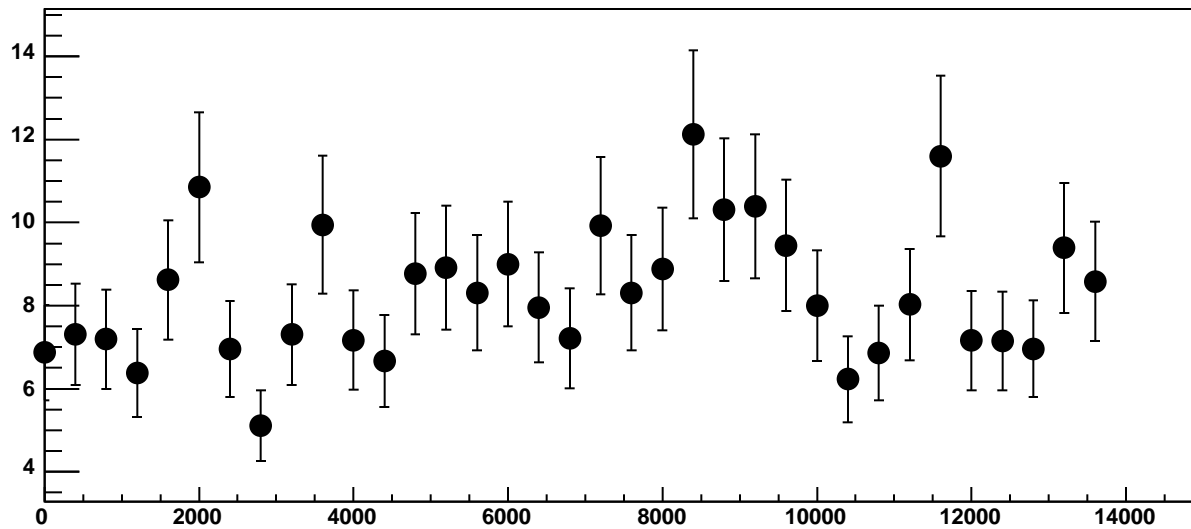


Chip 6, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

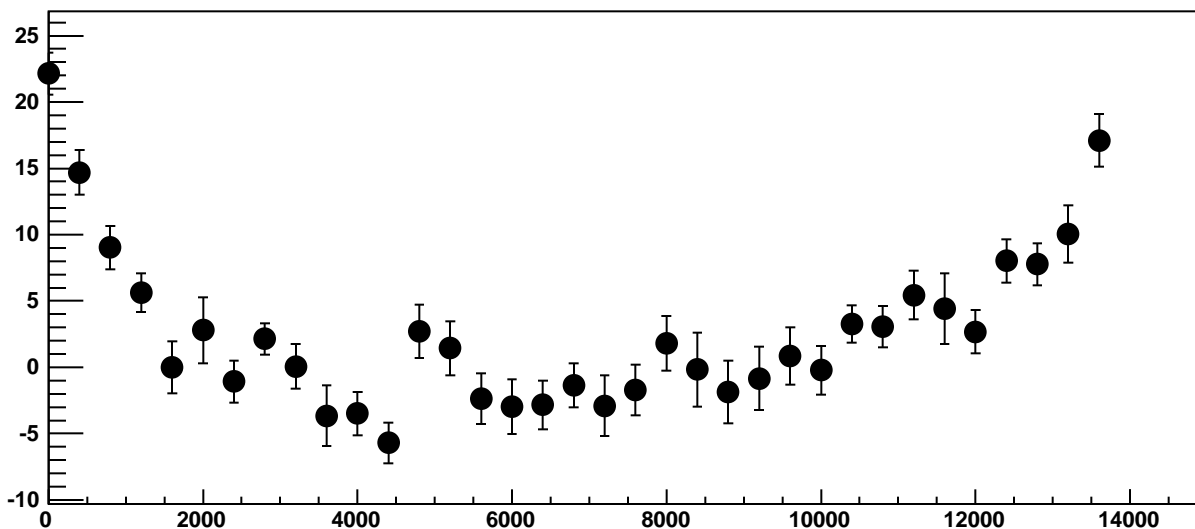


$\chi^2 / \text{ndf}$  62.85 / 23  
p0 -195.8 ± 0.7738  
p1 0.02075 ± 0.0001198

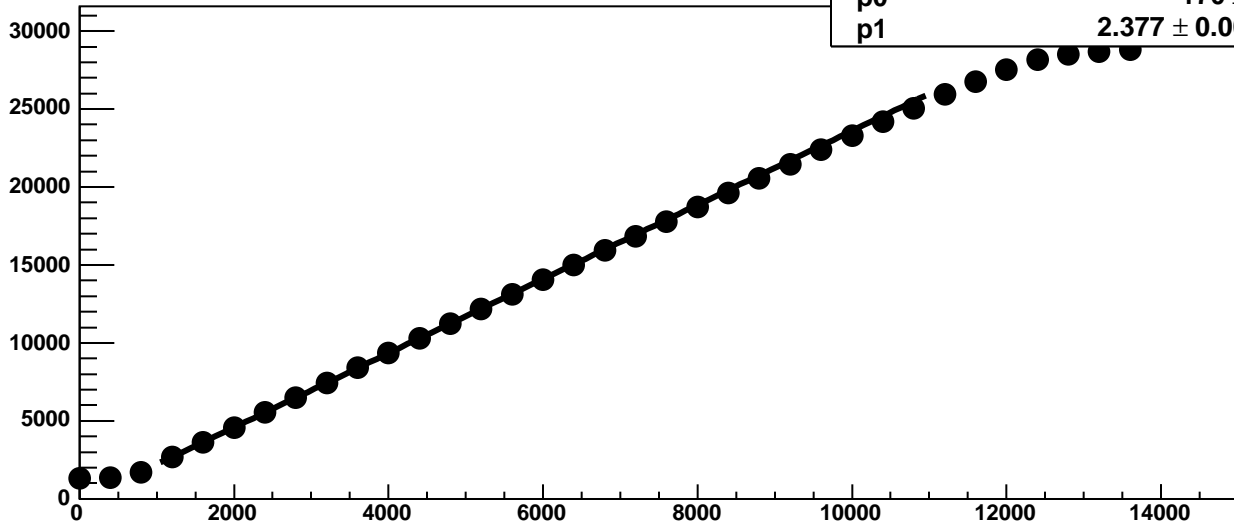
Chip 6, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



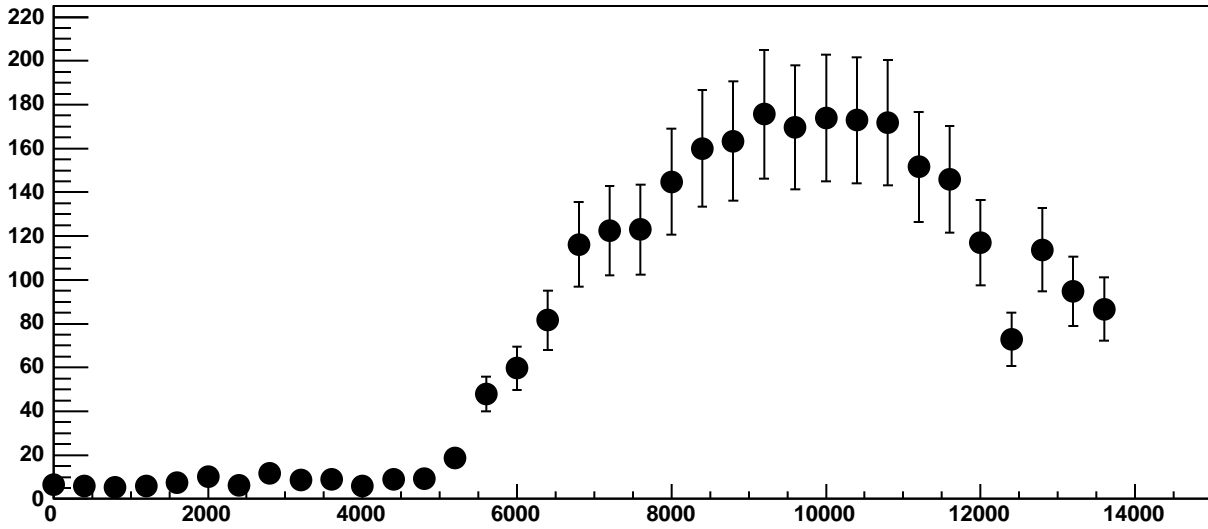
Chip 6, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



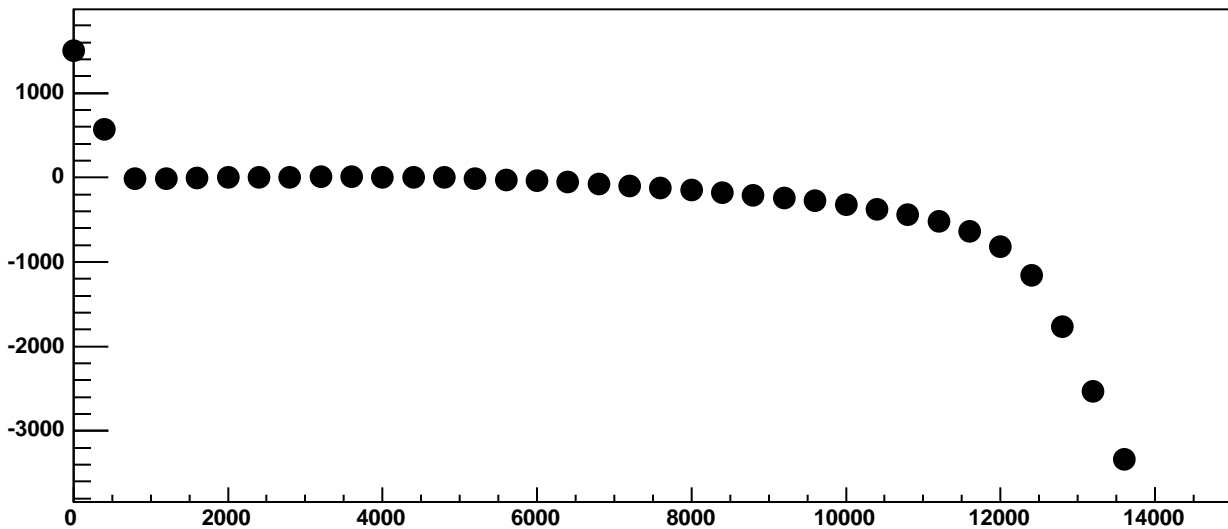
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC



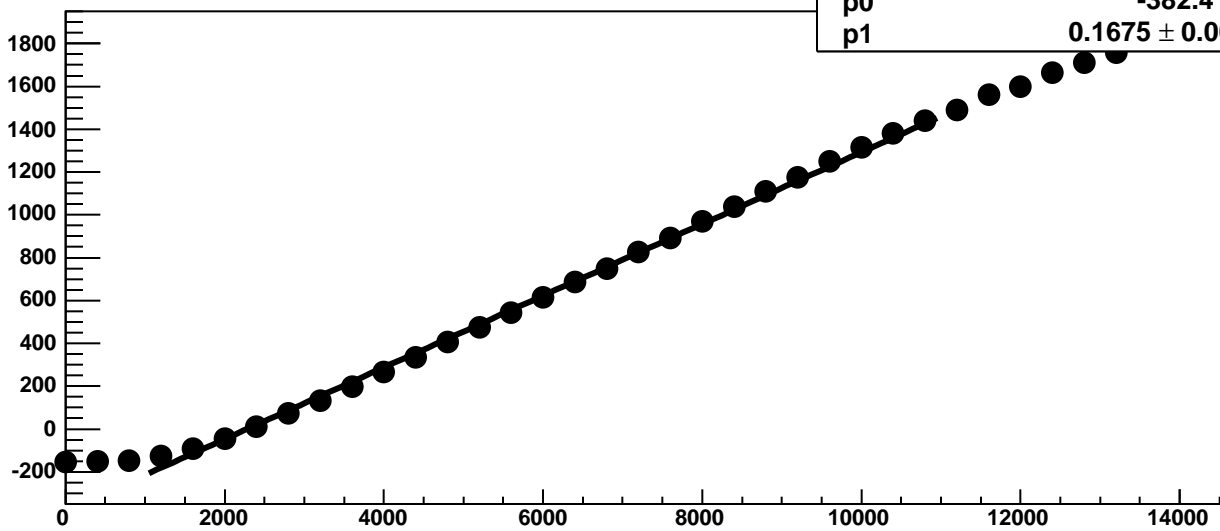
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



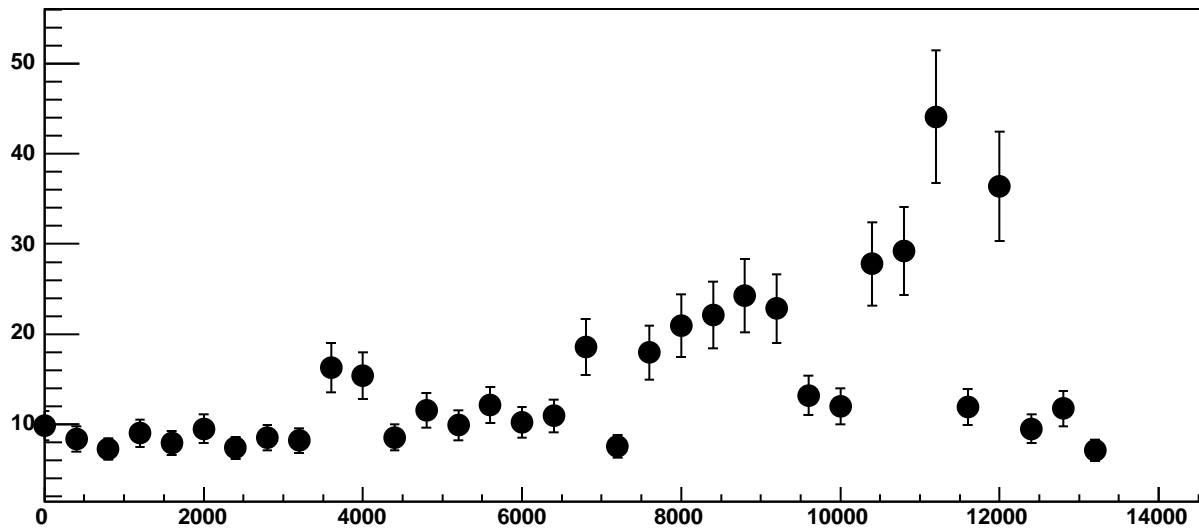
Chip 6, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



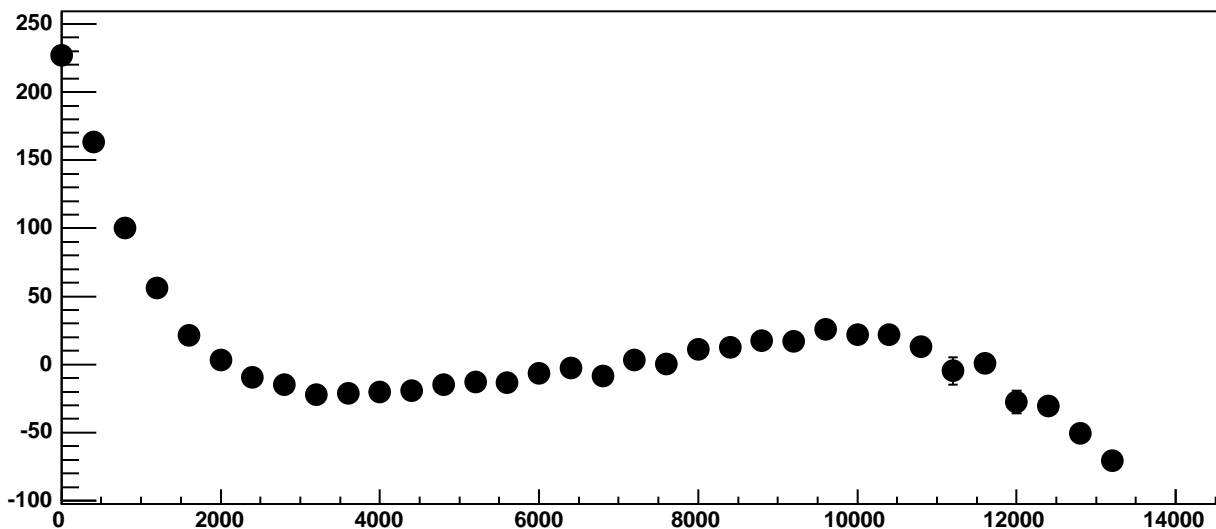
Chip 6, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

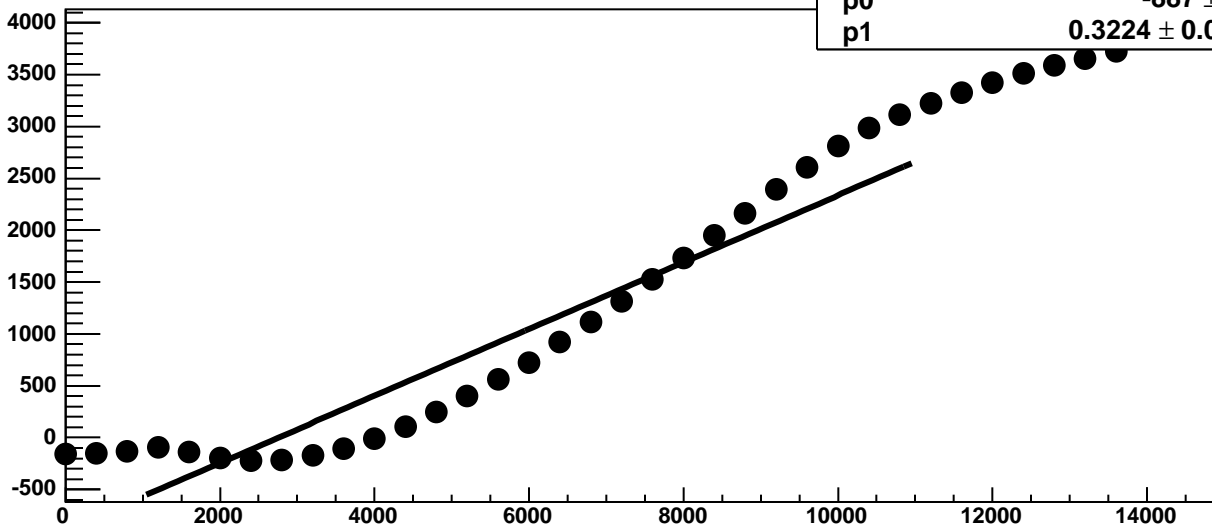


Chip 6, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

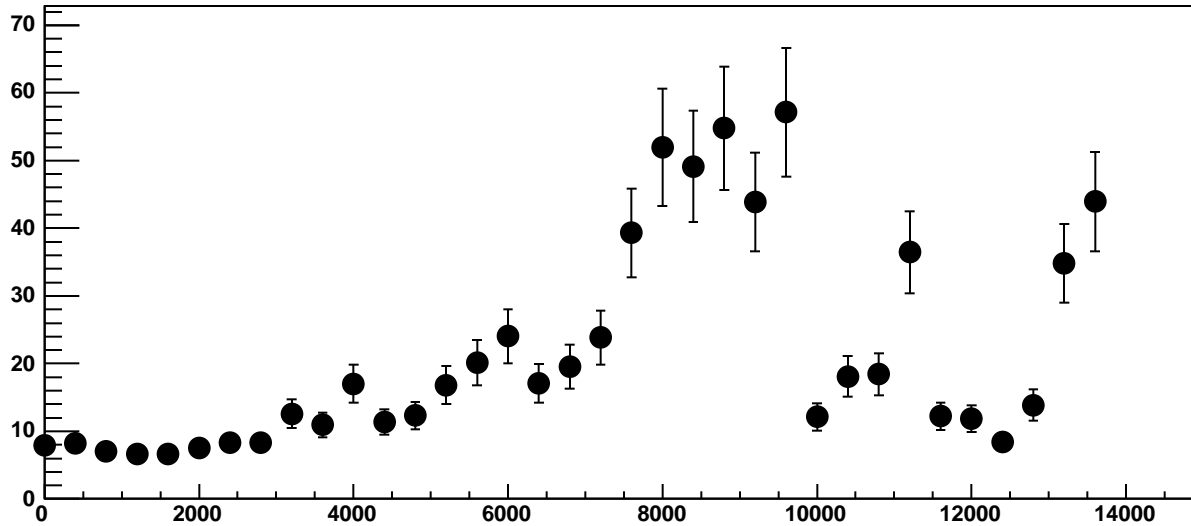


Chip 6, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

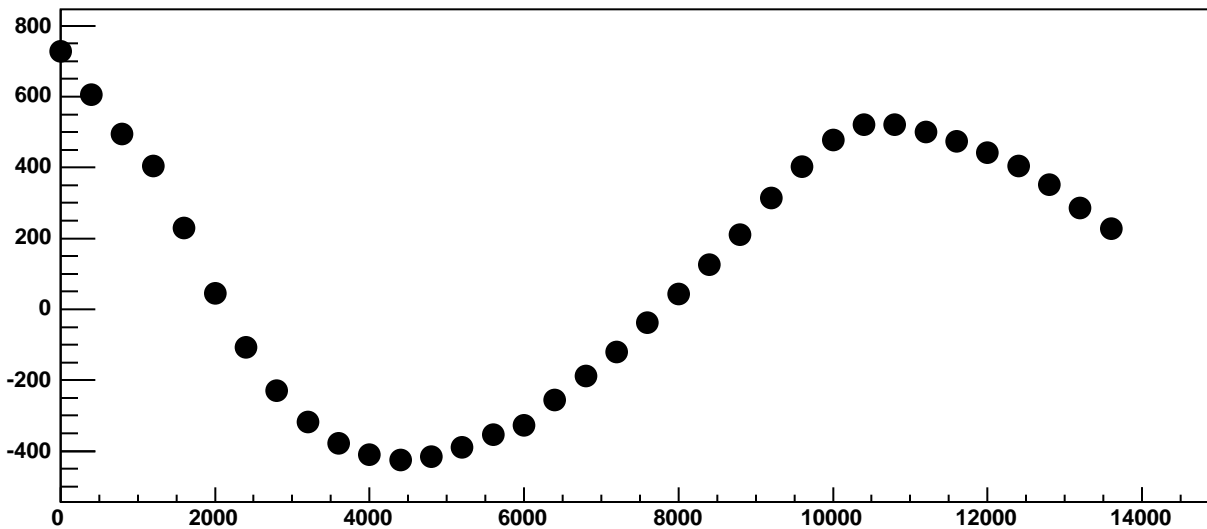
$\chi^2 / \text{ndf}$  2.938e+05 / 23  
p0 -887 ± 0.9857  
p1 0.3224 ± 0.0002237



Chip 6, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

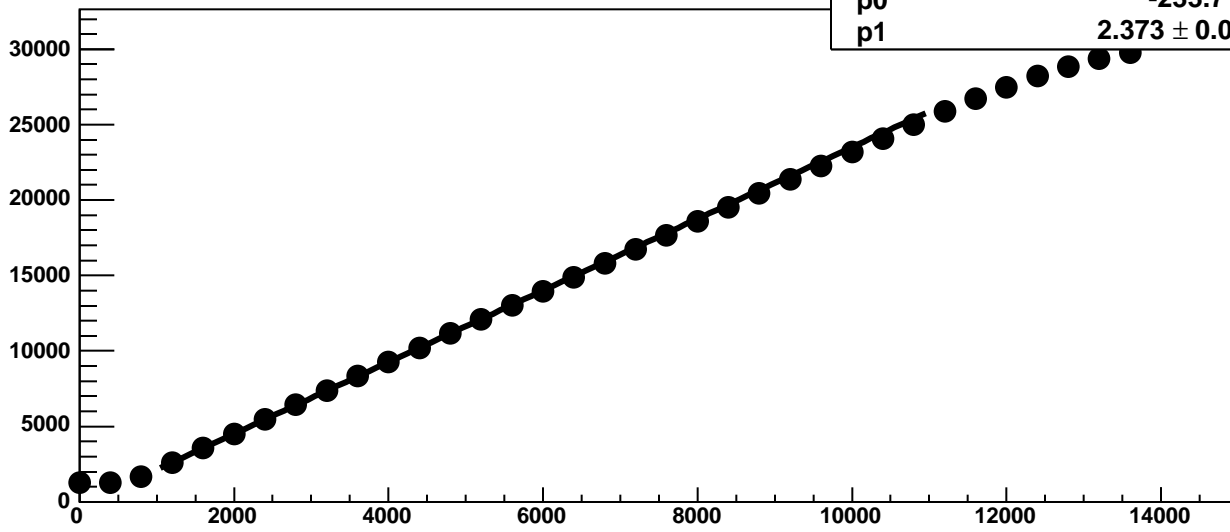


Chip 6, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC





Chip 6, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

528.7 / 23

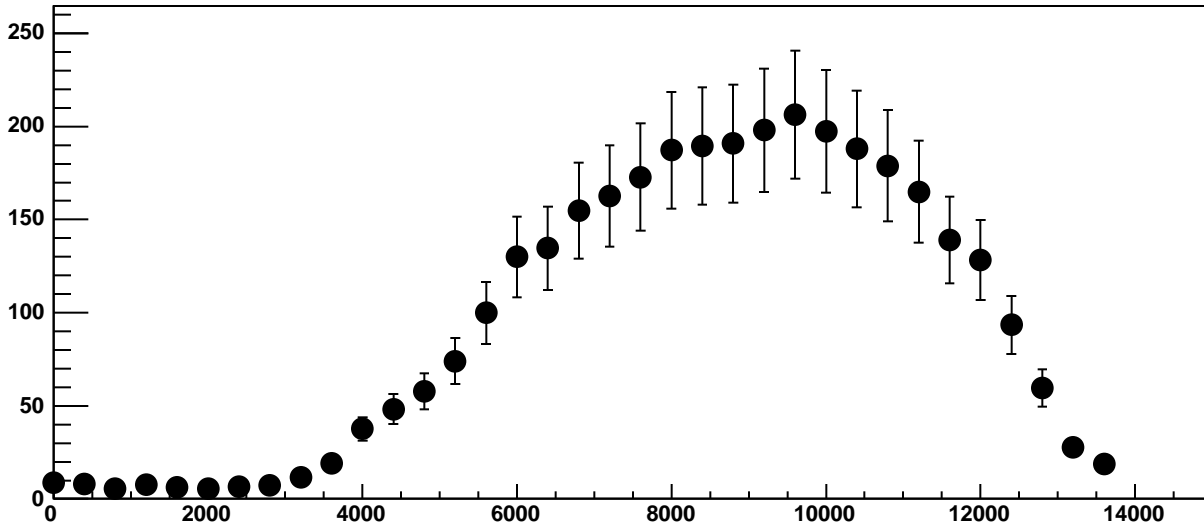
p0

$-233.7 \pm 1.923$

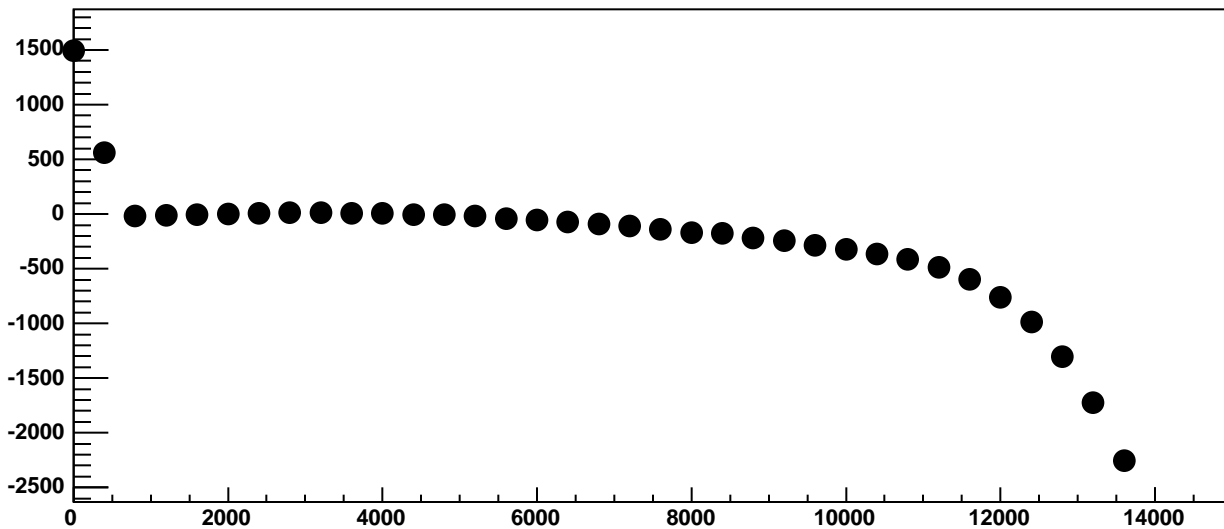
p1

$2.373 \pm 0.0008436$

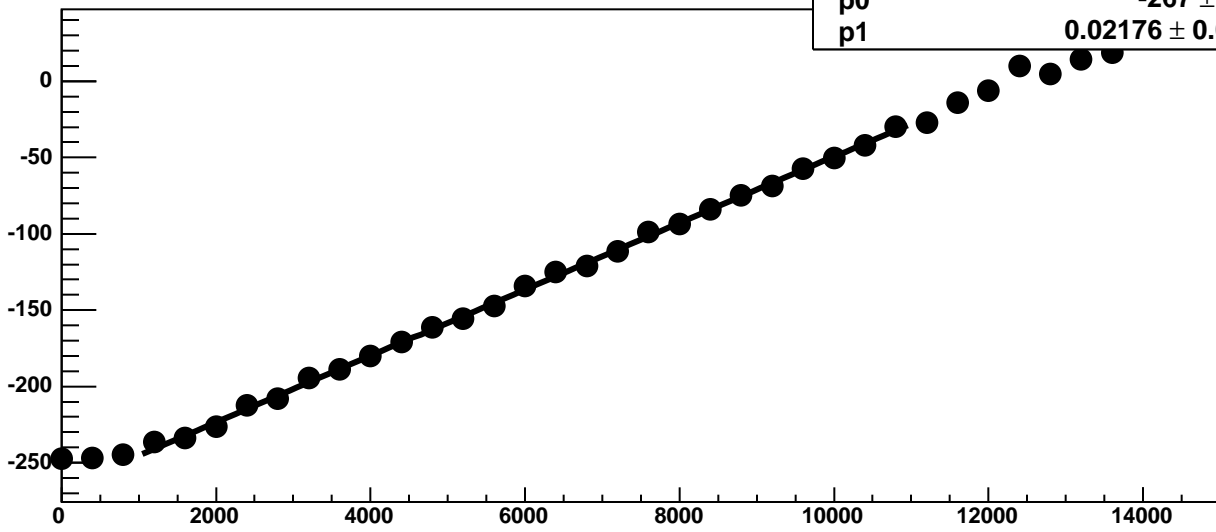
Chip 6, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



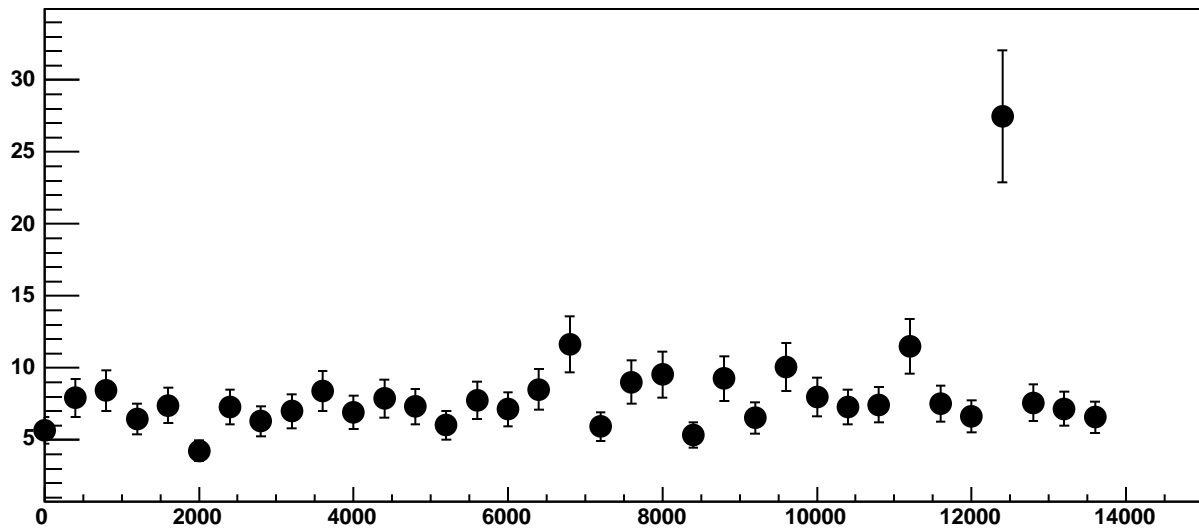
Chip 6, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



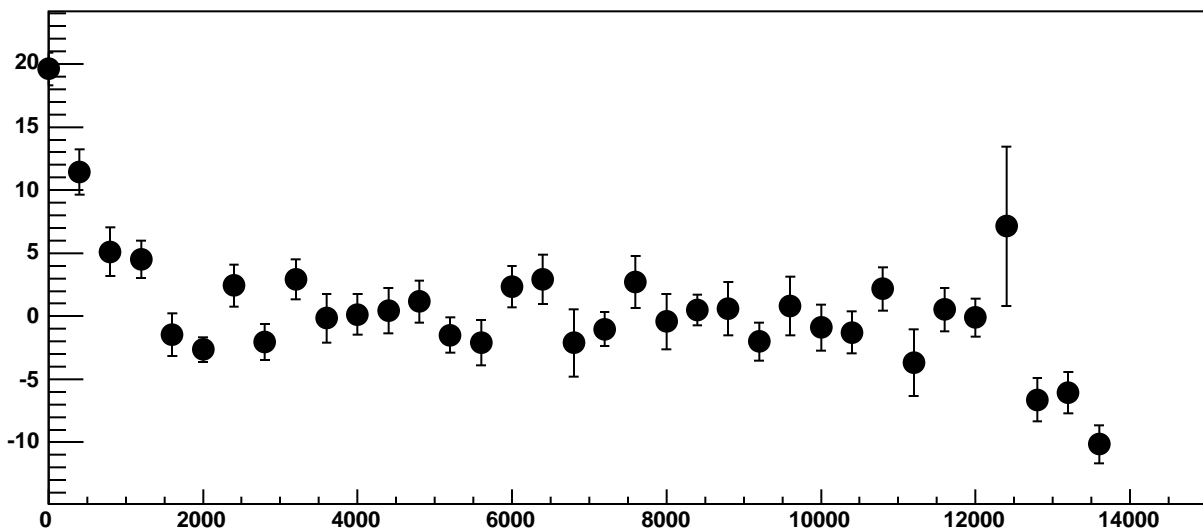
Chip 6, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



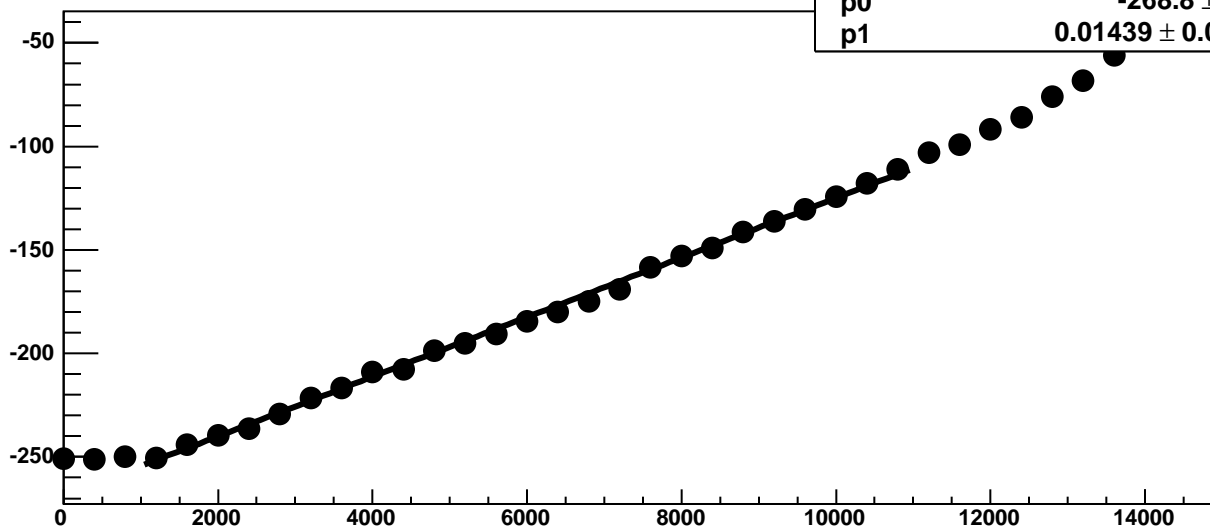
Chip 6, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

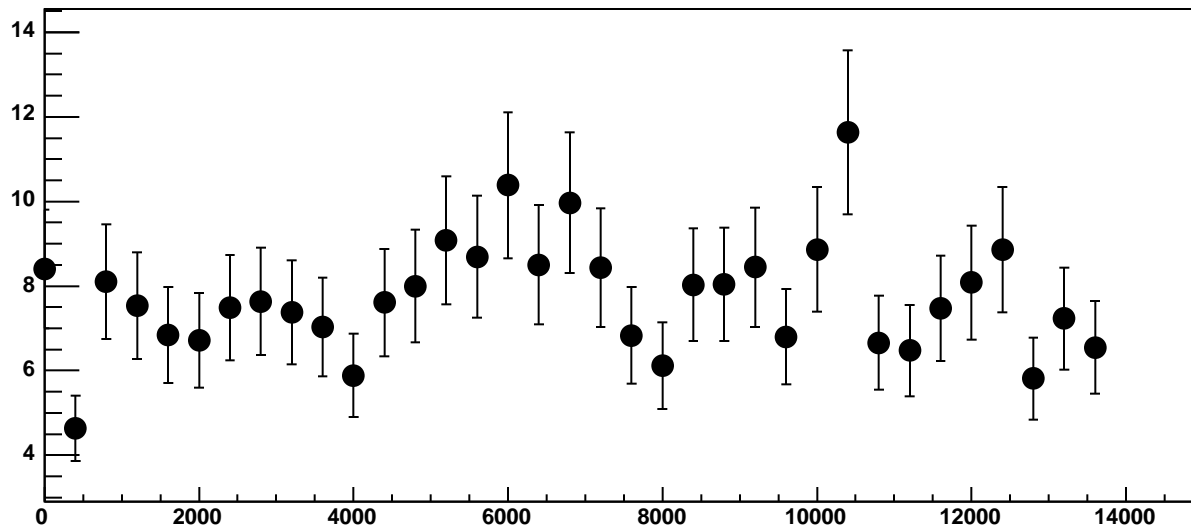


Chip 6, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

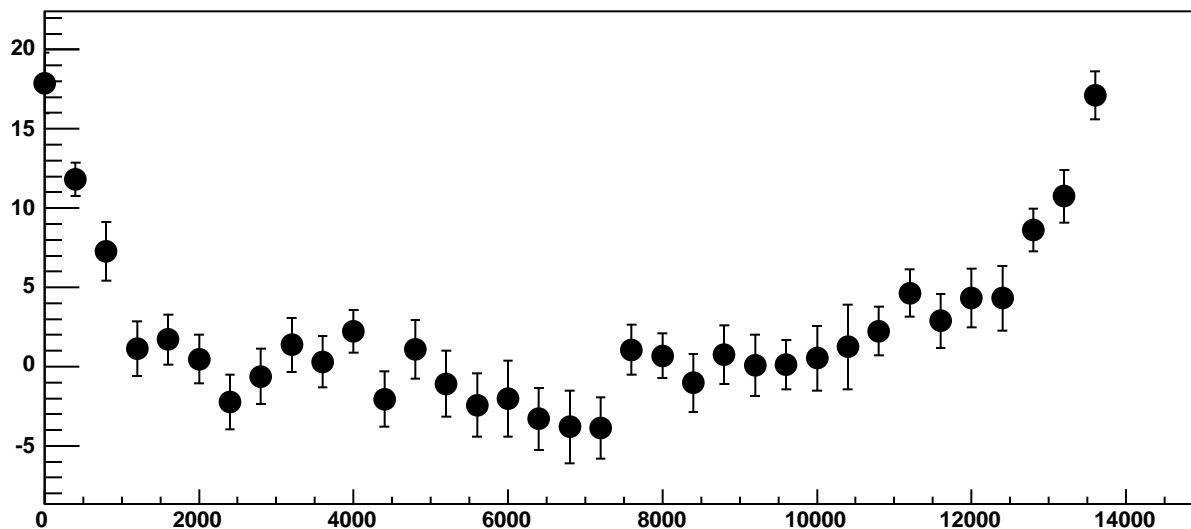


$\chi^2 / \text{ndf}$  24.34 / 23  
p0 -268.8 ± 0.7738  
p1 0.01439 ± 0.0001192

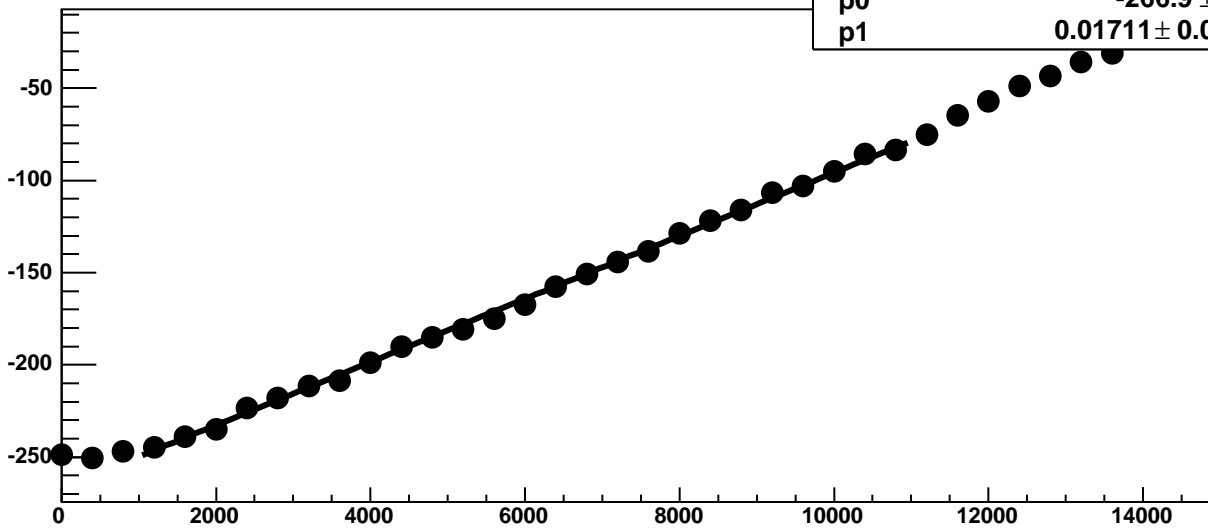
Chip 6, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

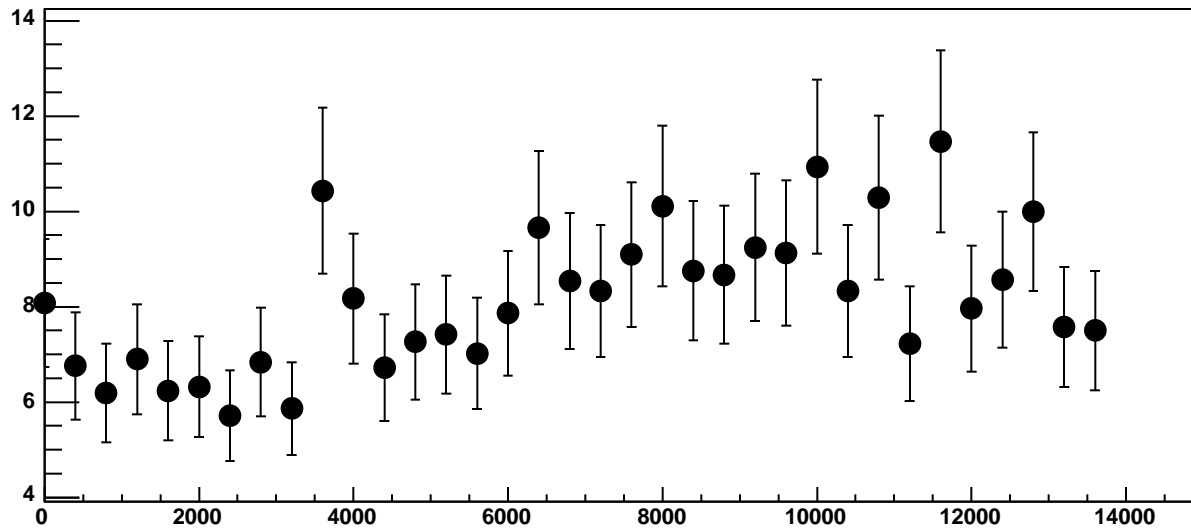


Chip 6, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

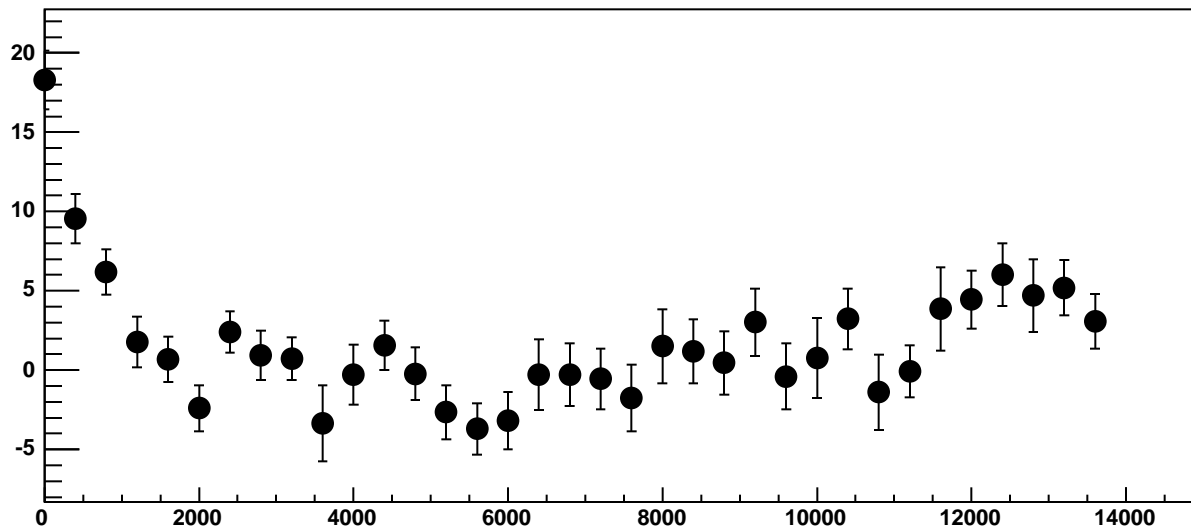


$\chi^2 / \text{ndf}$  29.04 / 23  
p0  $-266.9 \pm 0.7405$   
p1  $0.01711 \pm 0.0001256$

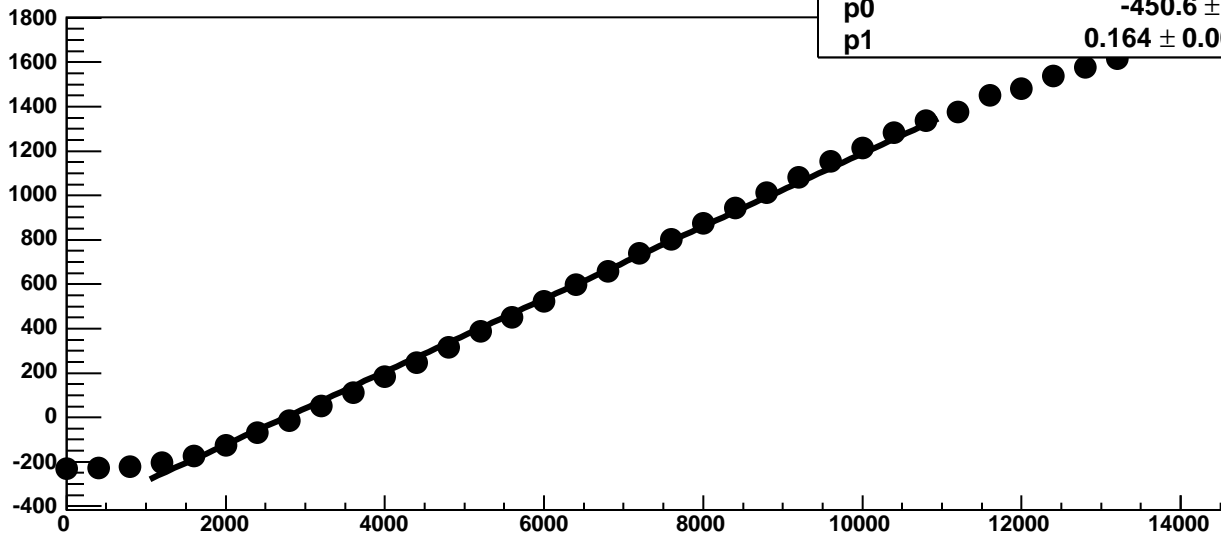
Chip 6, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



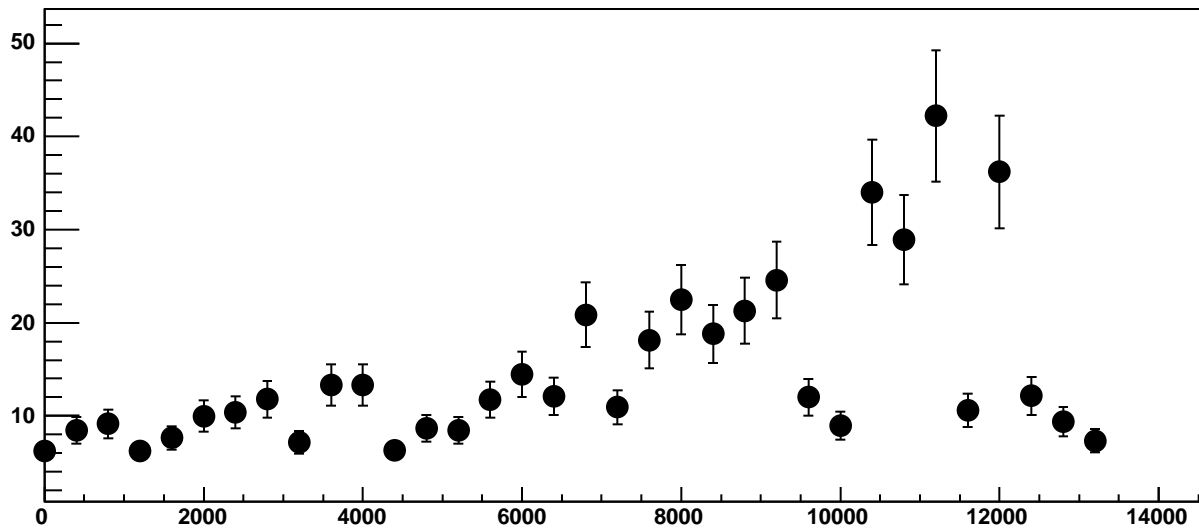
Chip 6, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



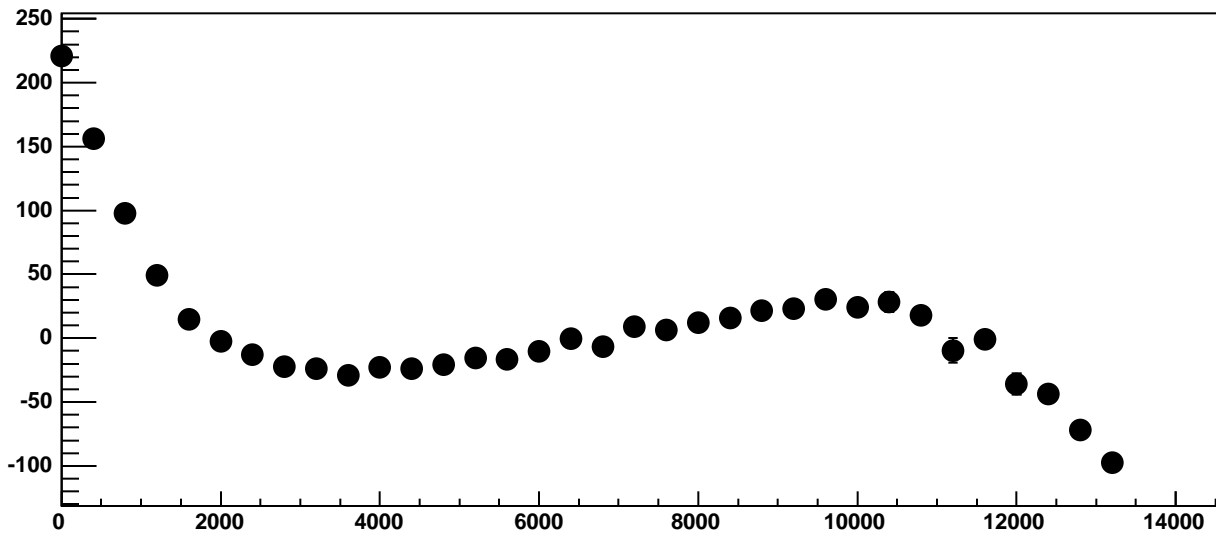
Chip 6, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



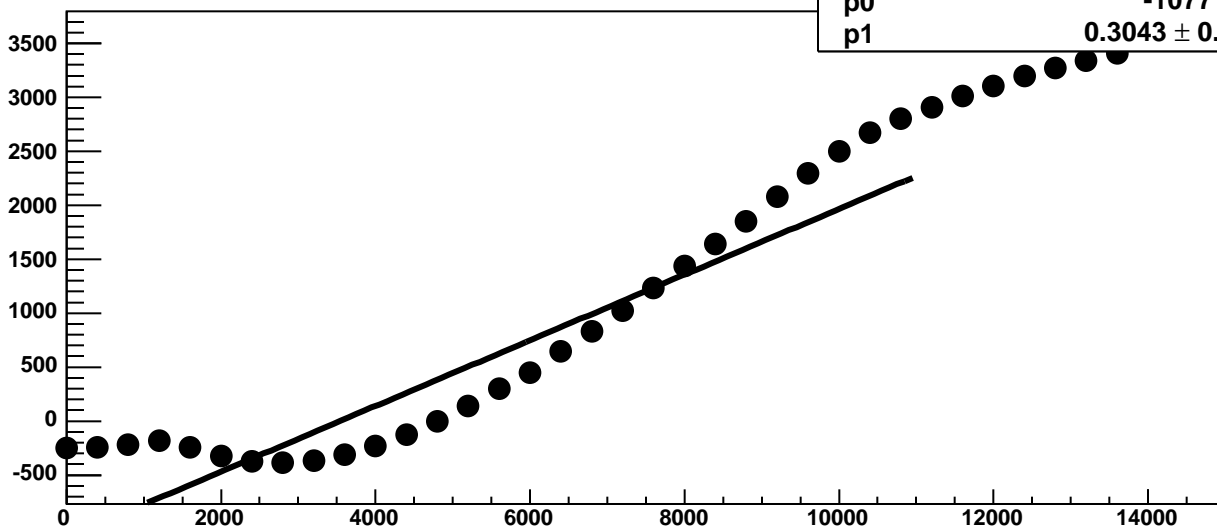
Chip 6, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

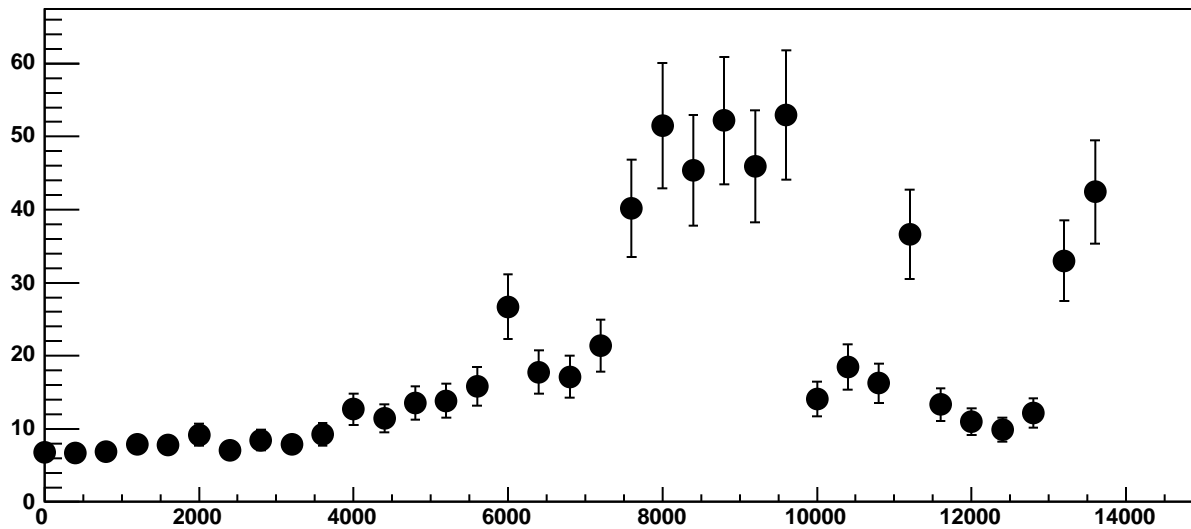


Chip 6, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

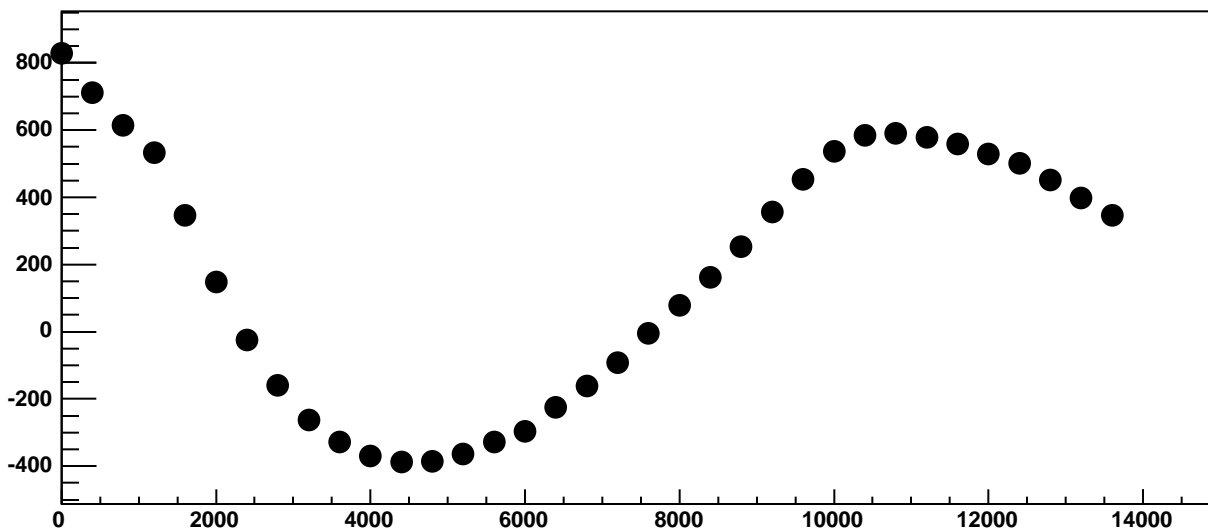


$\chi^2 / \text{ndf}$  3.391e+05 / 23  
p0 -1077 ± 1.044  
p1 0.3043 ± 0.000233

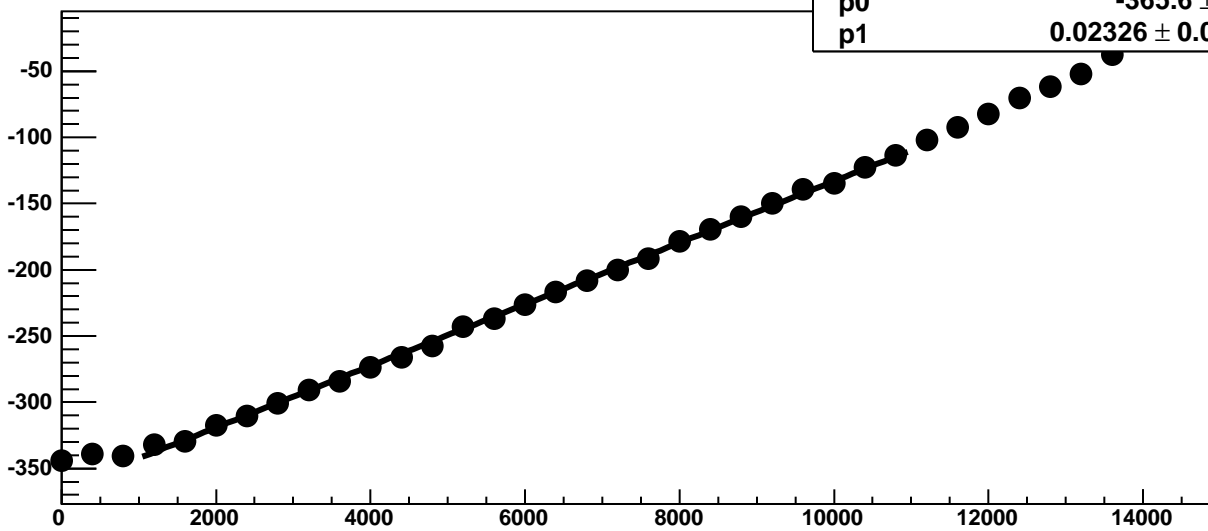
Chip 6, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

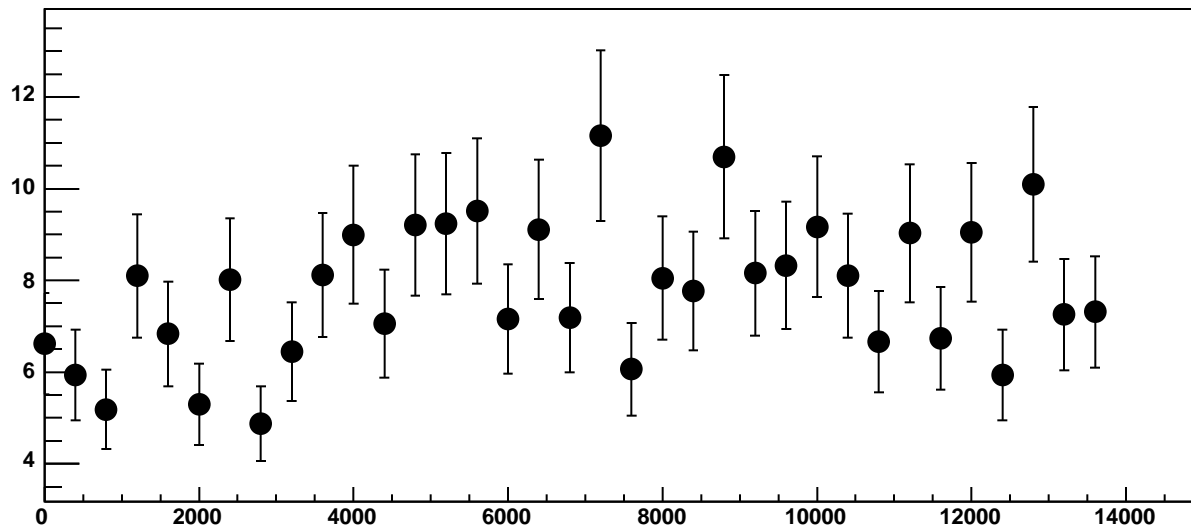


Chip 6, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

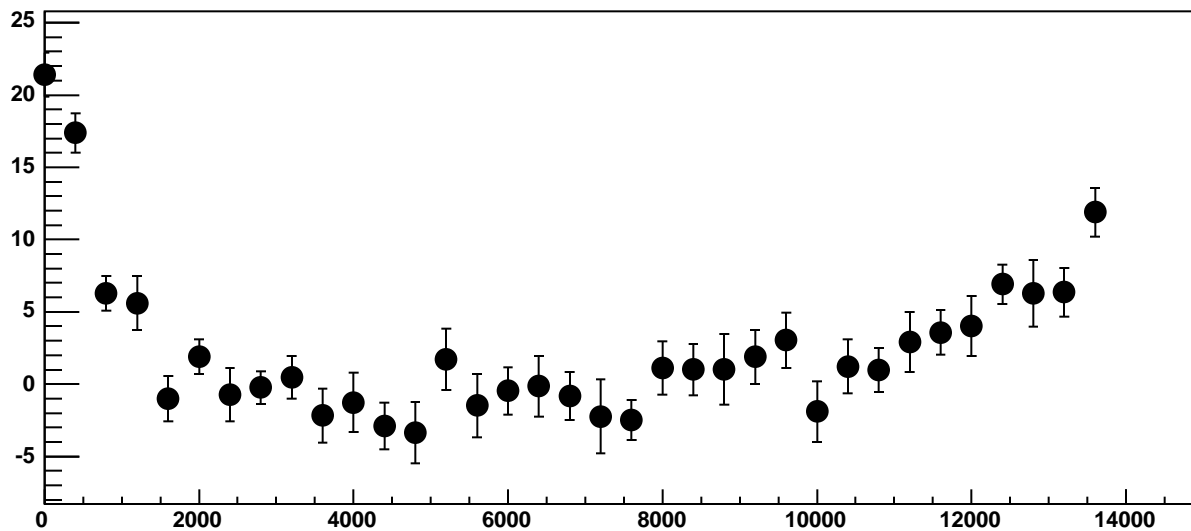


$\chi^2 / \text{ndf}$  31.12 / 23  
p0  $-365.6 \pm 0.7279$   
p1  $0.02326 \pm 0.0001154$

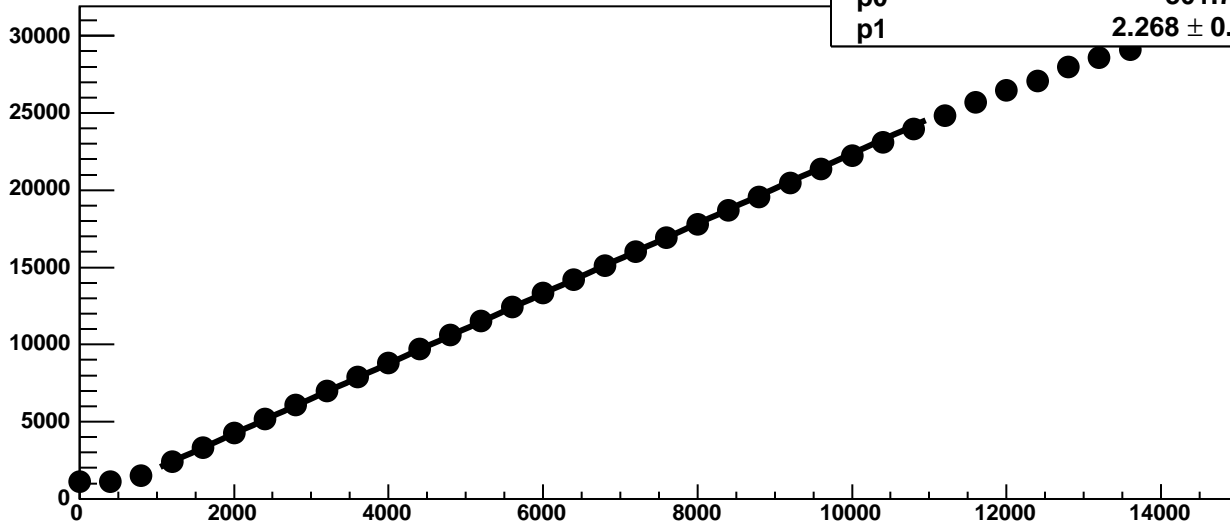
Chip 6, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

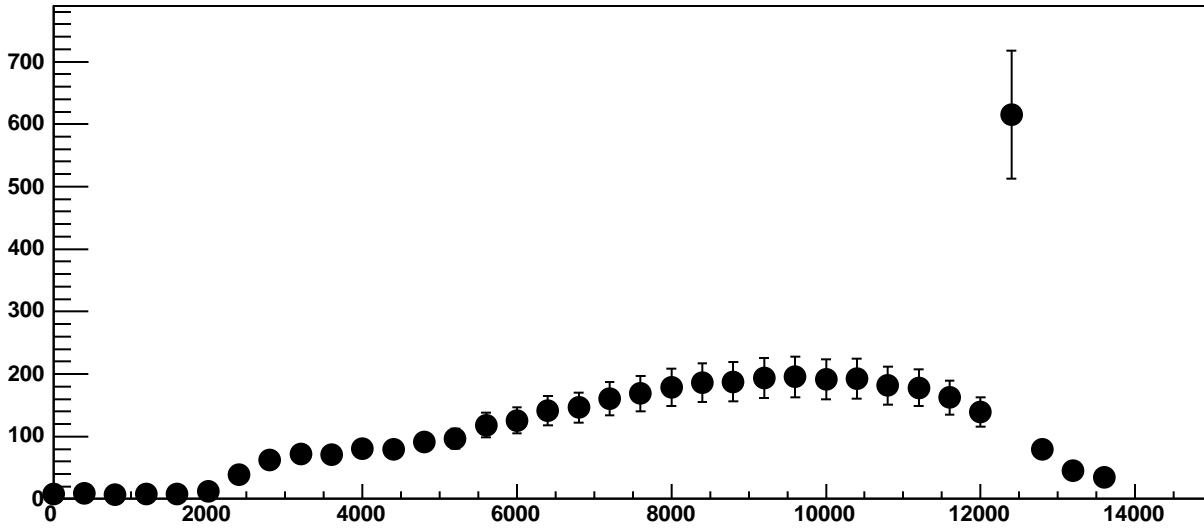


Chip 6, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

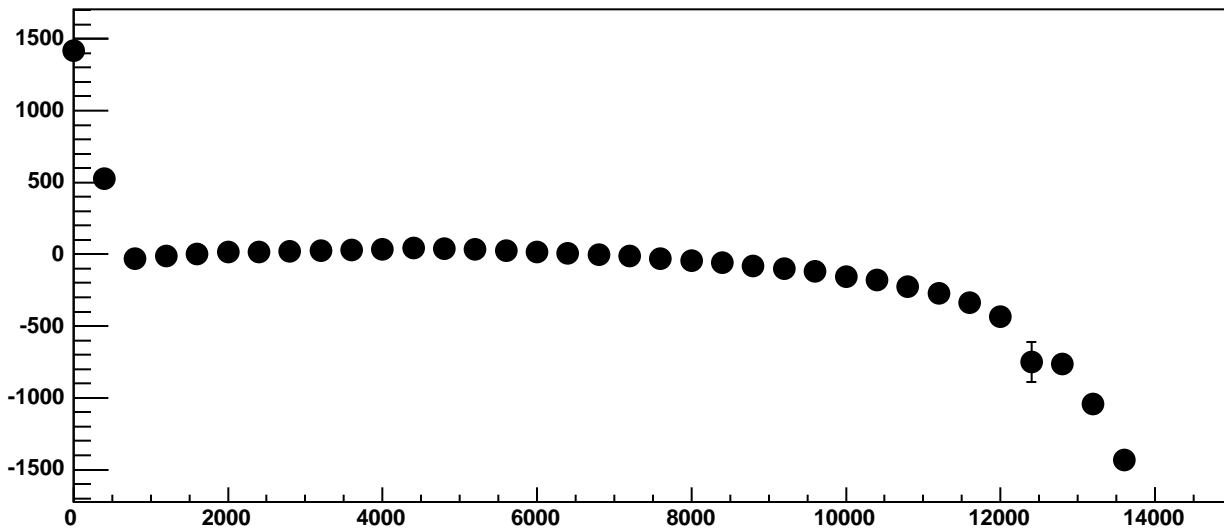


$\chi^2 / \text{ndf}$  194.8 / 23  
p0  $-301.7 \pm 2.44$   
p1  $2.268 \pm 0.001295$

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

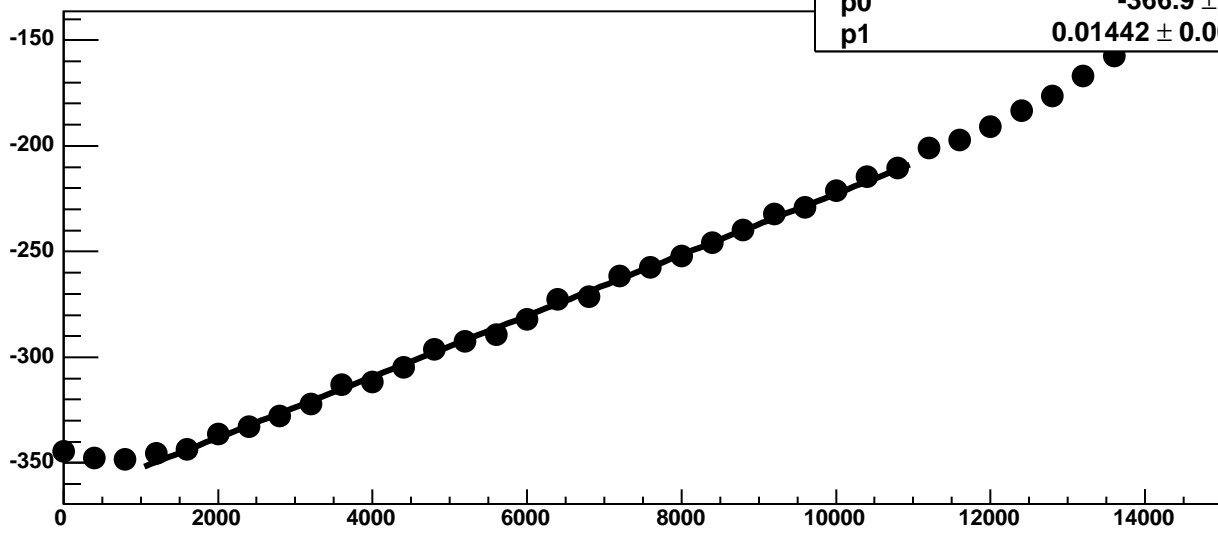


Chip 6, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

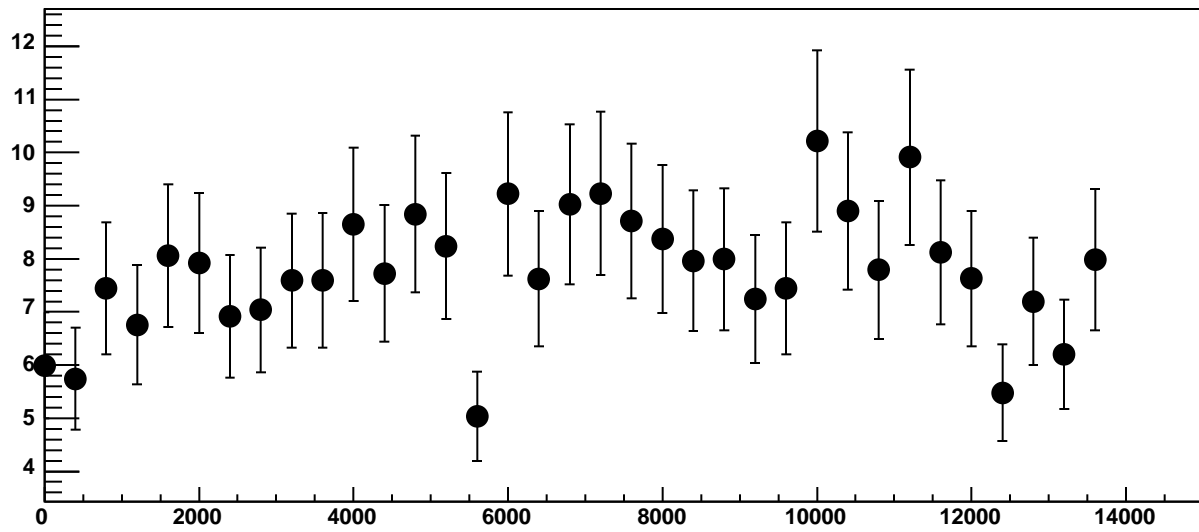




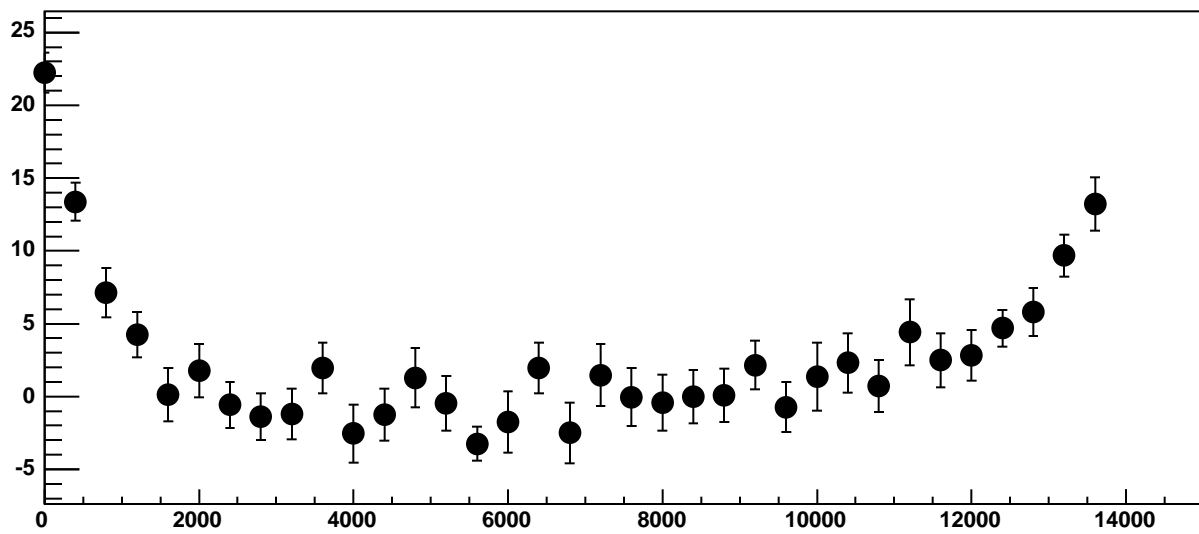
Chip 6, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



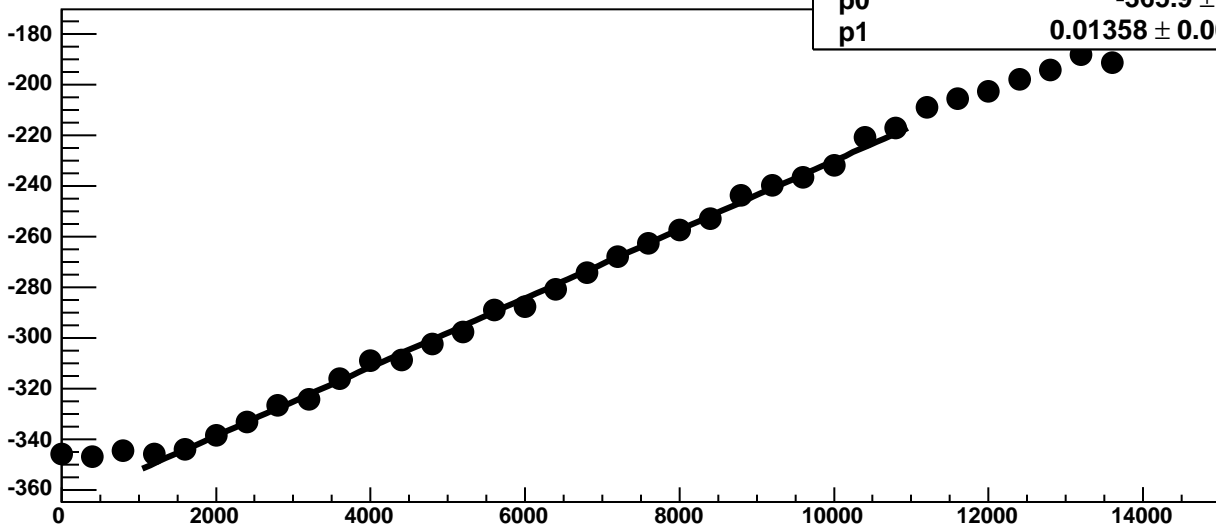
Chip 6, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

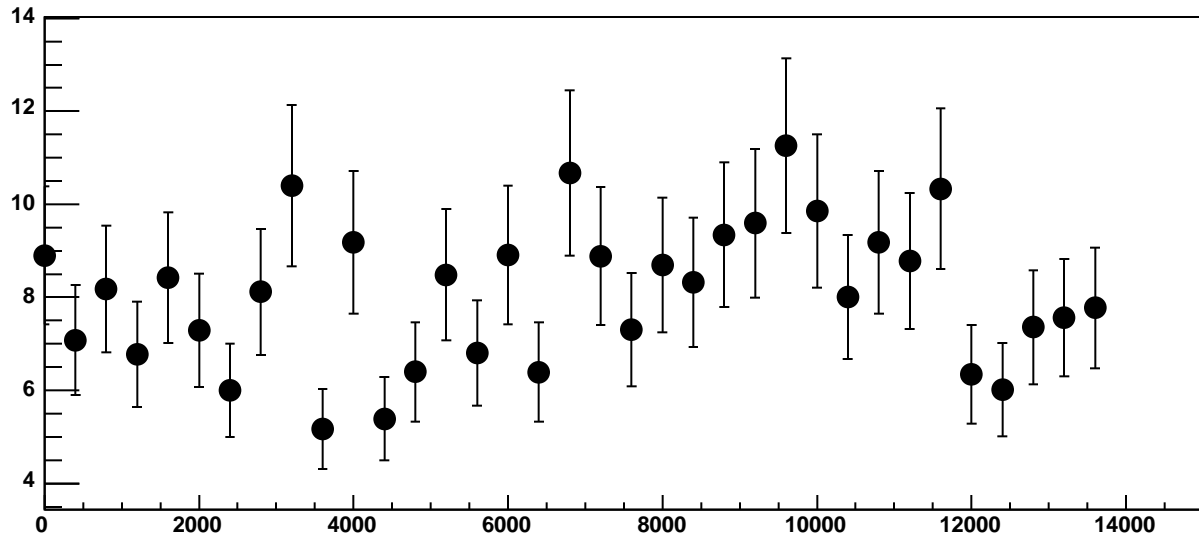


Chip 6, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

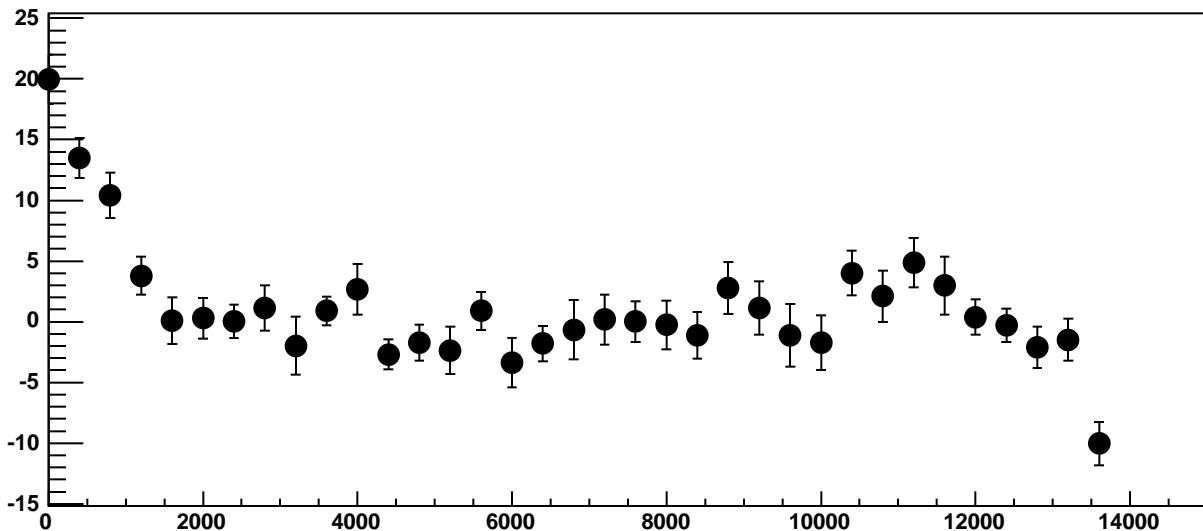


$\chi^2 / \text{ndf}$  30.27 / 23  
p0  $-365.9 \pm 0.7814$   
p1  $0.01358 \pm 0.0001288$

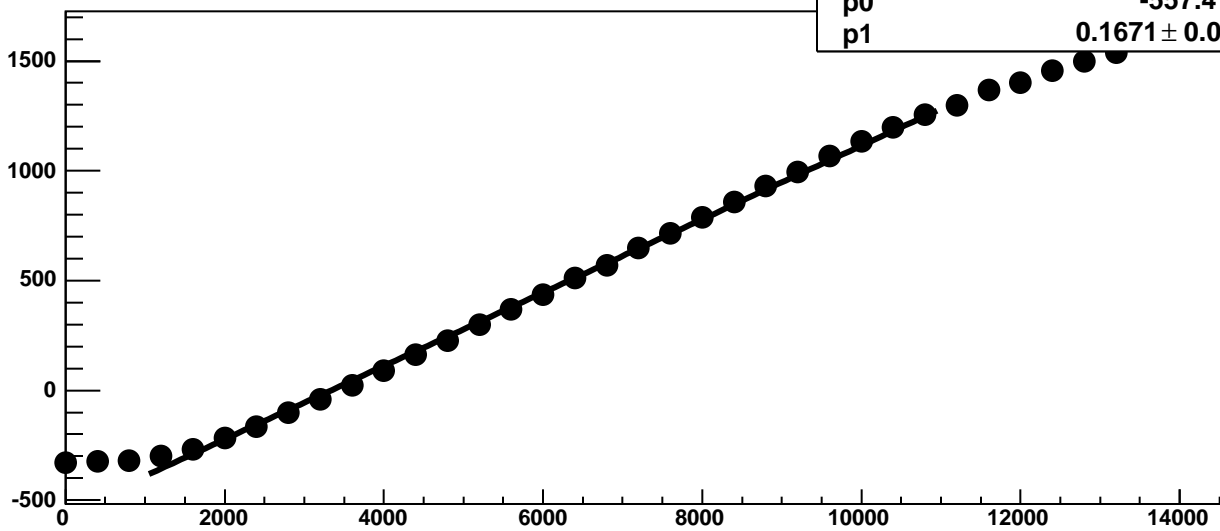
Chip 6, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



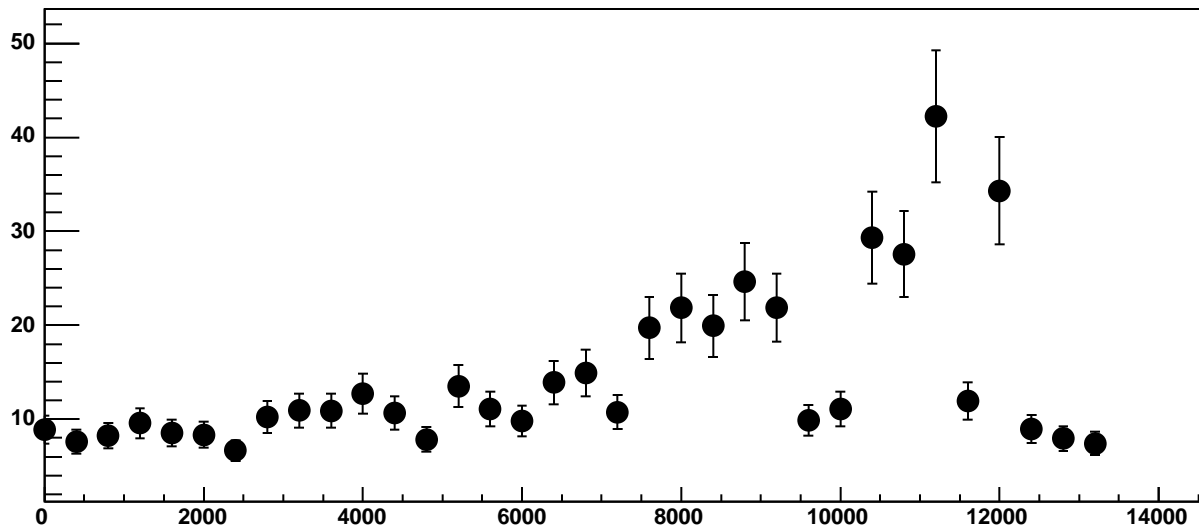
Chip 6, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



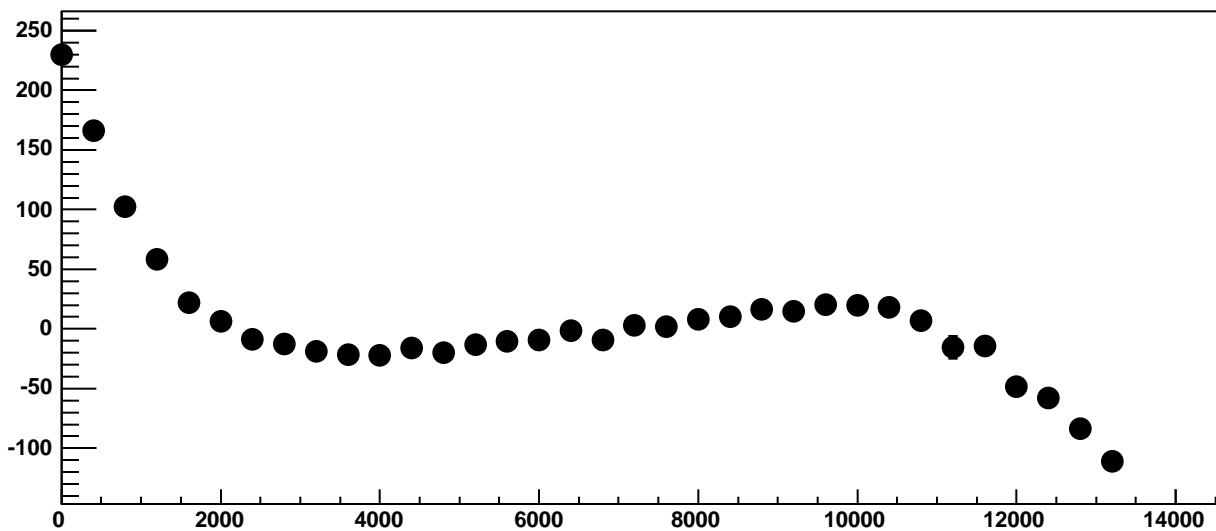
Chip 6, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



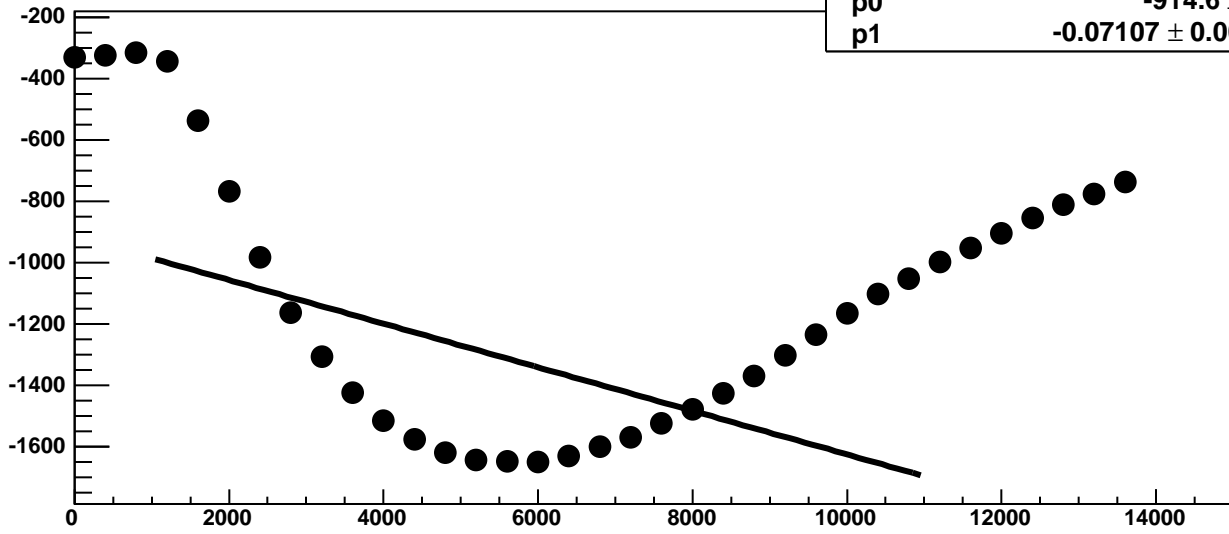
Chip 6, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

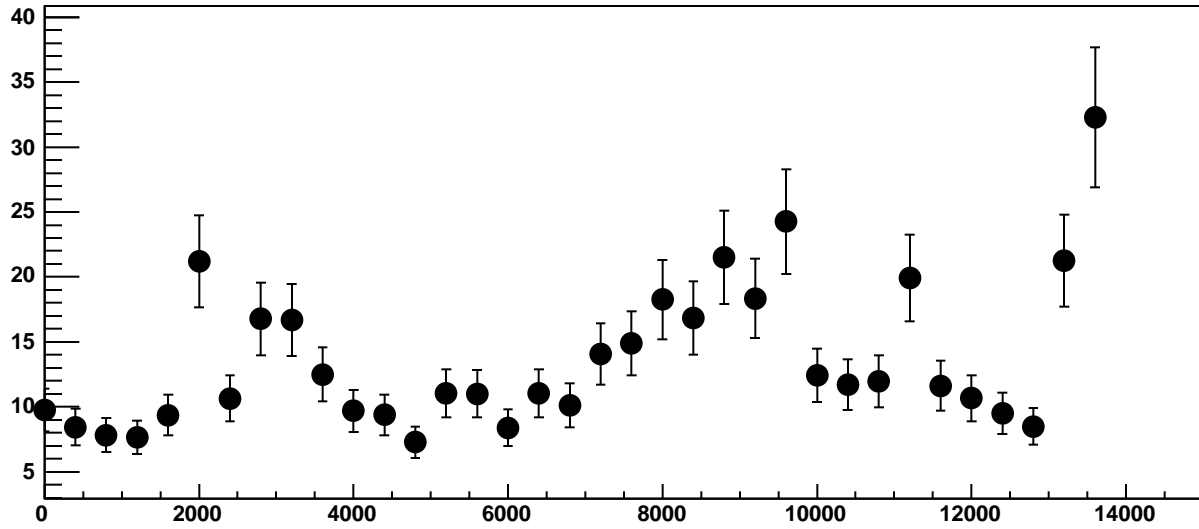


Chip 6, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

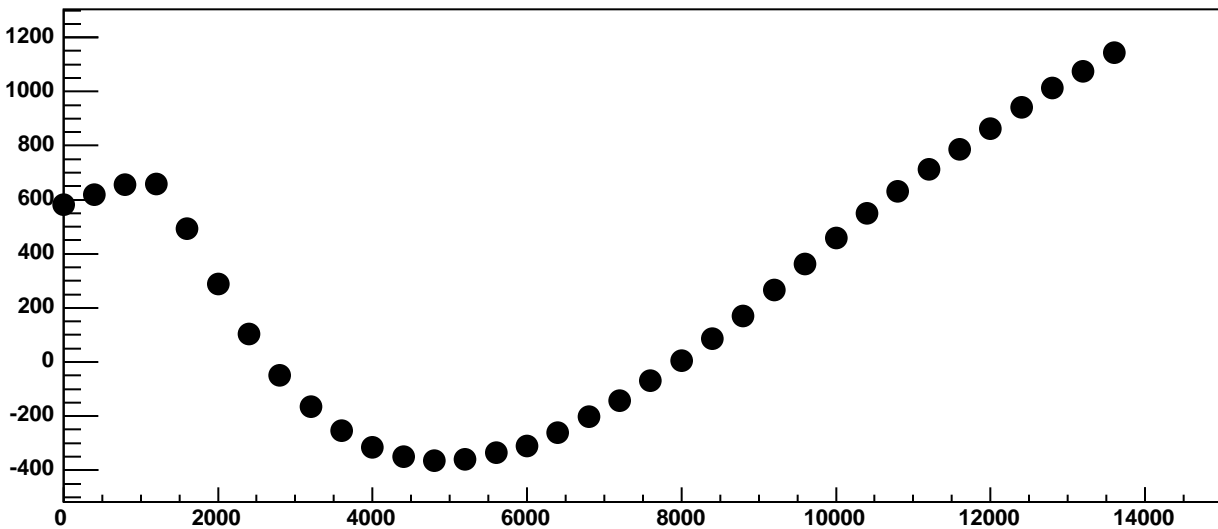


$\chi^2 / \text{ndf}$  5.164e+05 / 23  
p0 -914.6 ± 1.153  
p1 -0.07107 ± 0.0001929

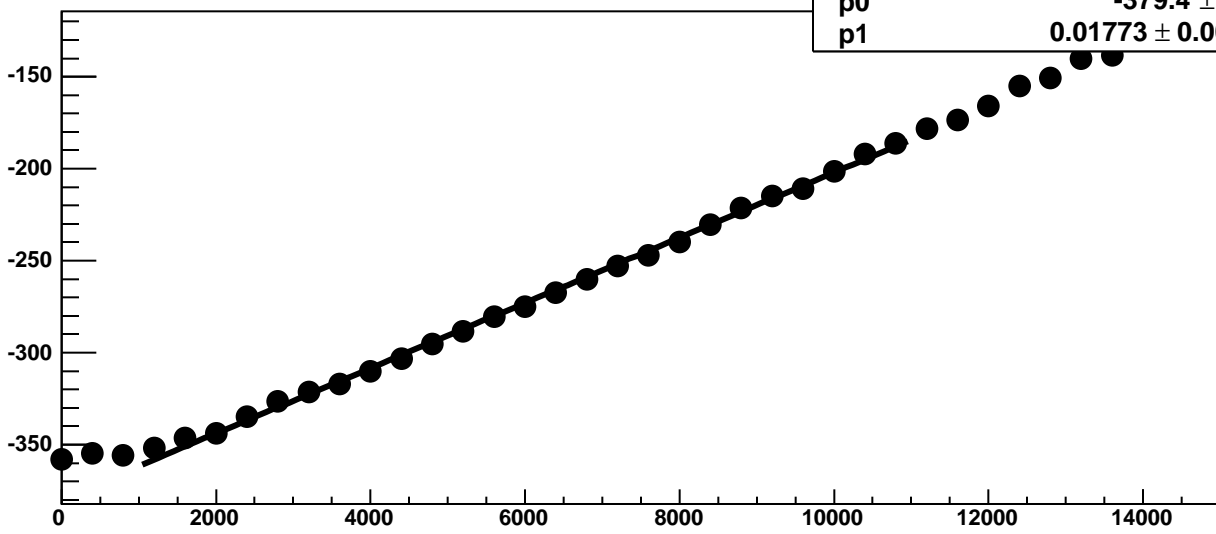
Chip 6, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

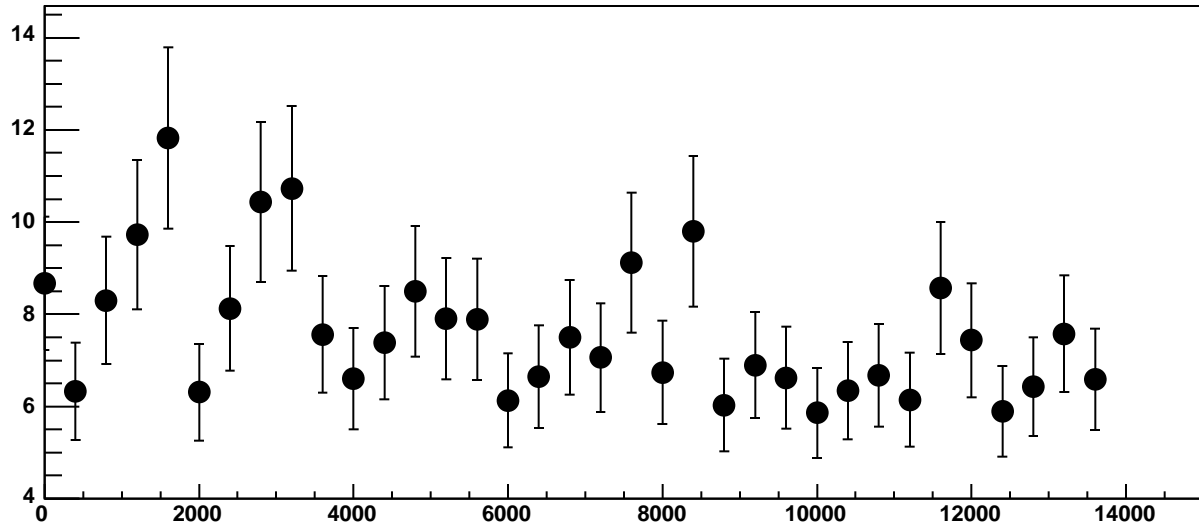


Chip 6, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

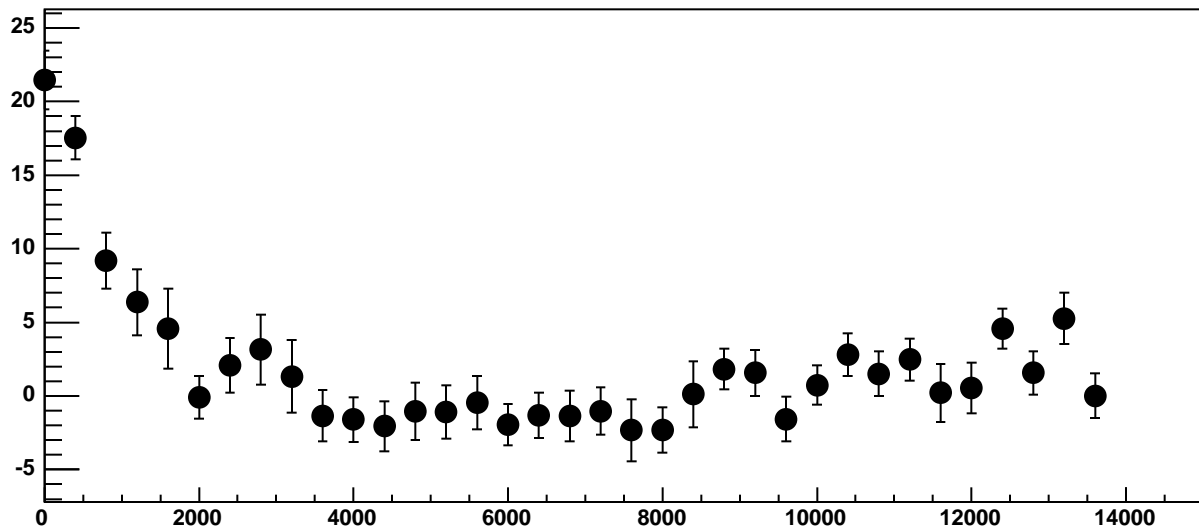


$\chi^2 / \text{ndf}$  34.18 / 23  
p0 -379.4 ± 0.8518  
p1 0.01773 ± 0.0001194

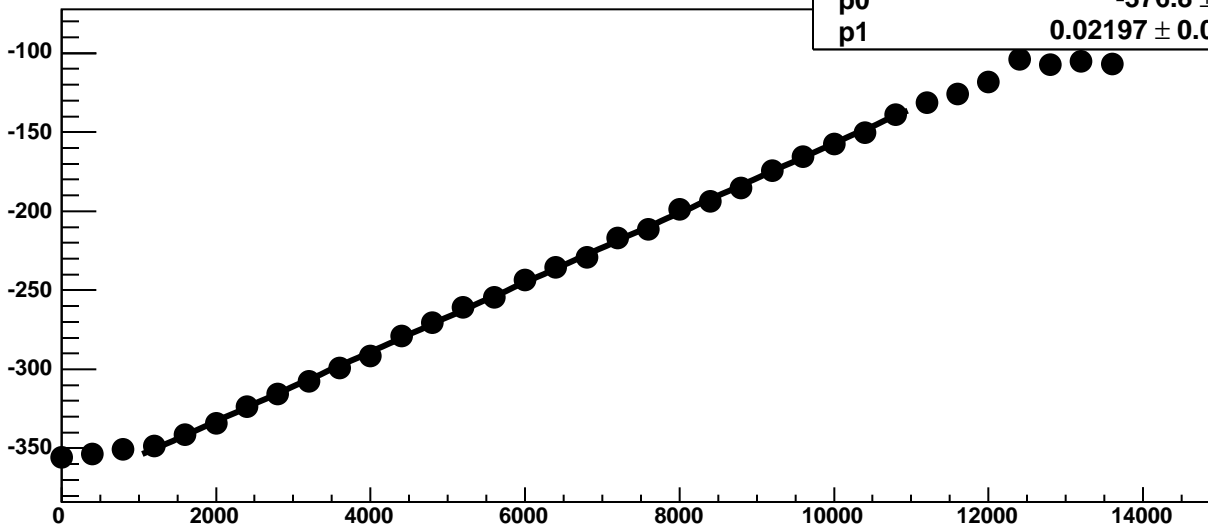
Chip 6, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



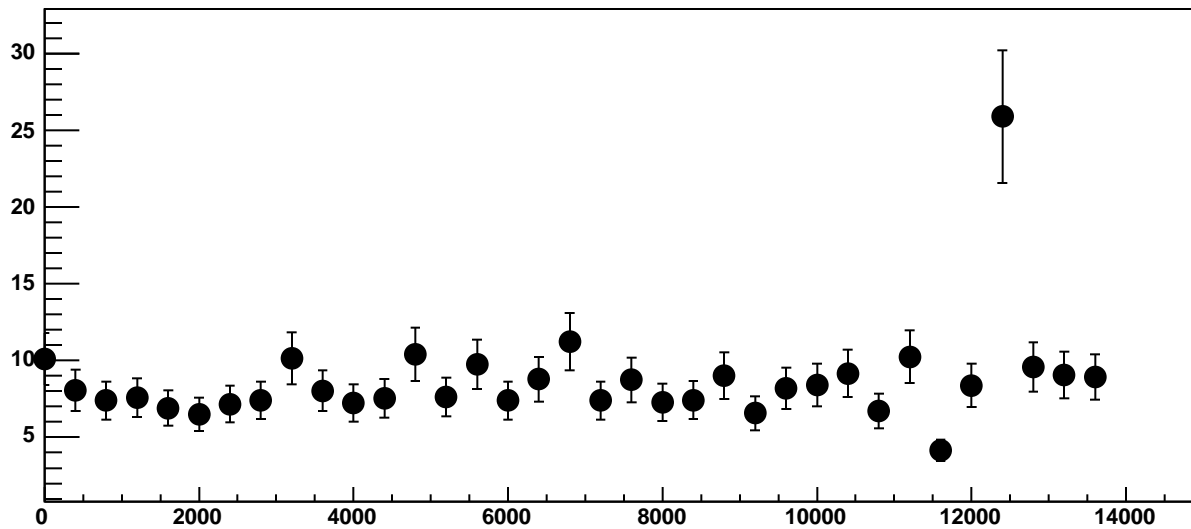
Chip 6, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



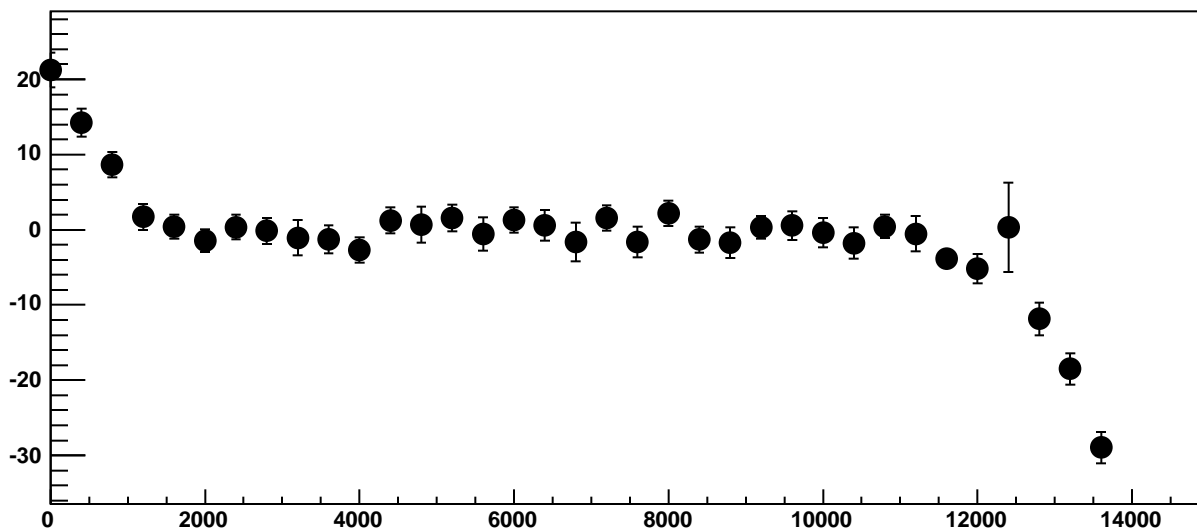
Chip 6, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



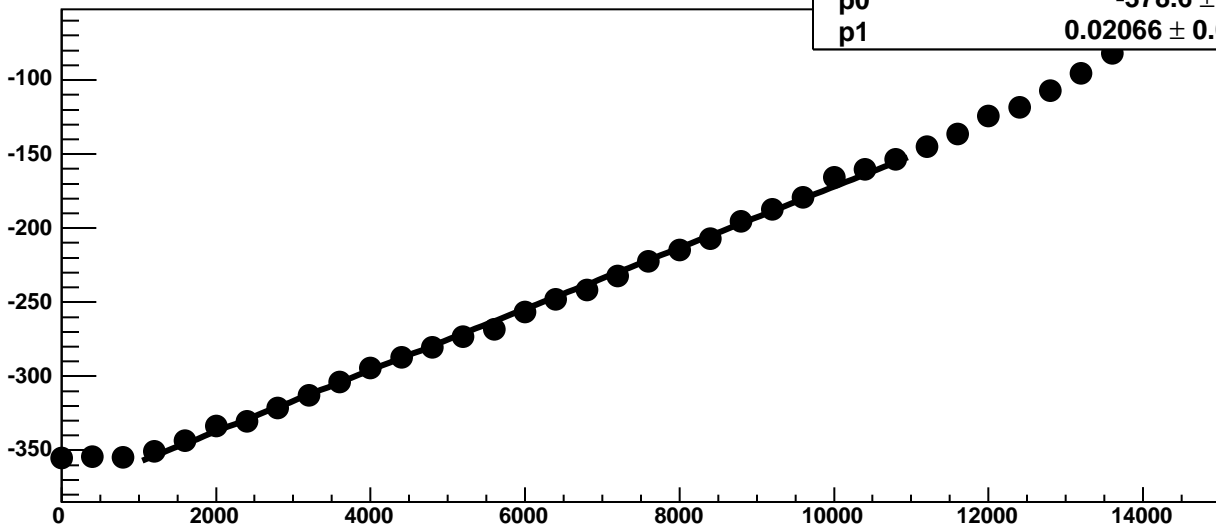
Chip 6, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

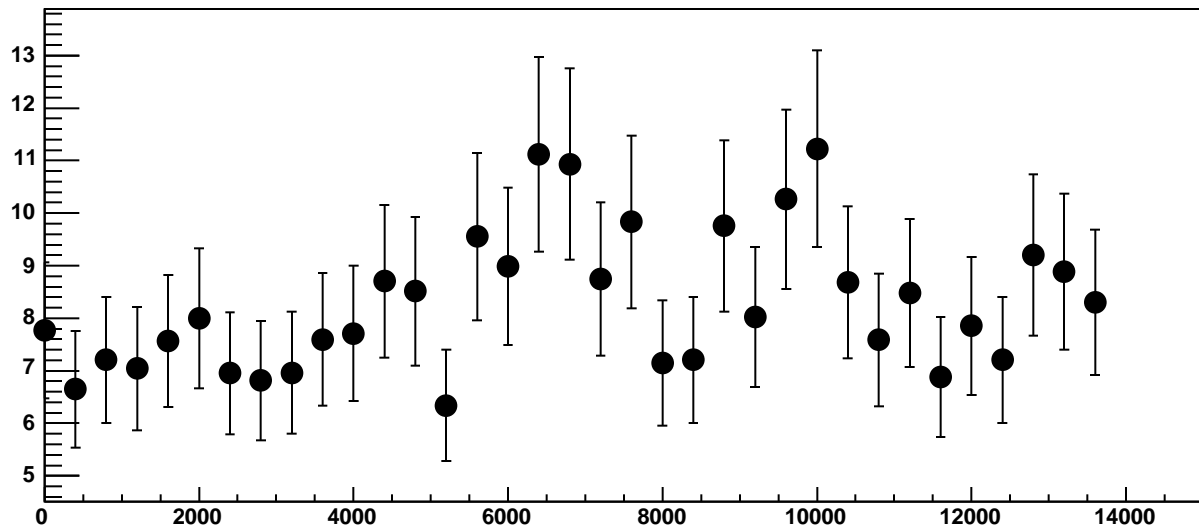


Chip 6, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

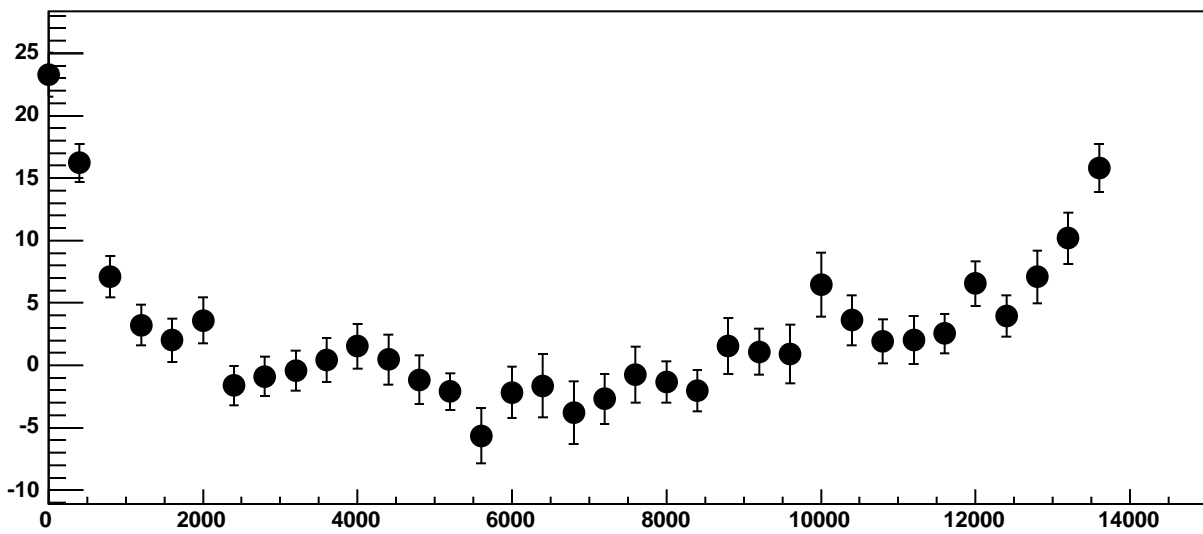


$\chi^2 / \text{ndf}$  40.14 / 23  
p0  $-378.6 \pm 0.8004$   
p1  $0.02066 \pm 0.000127$

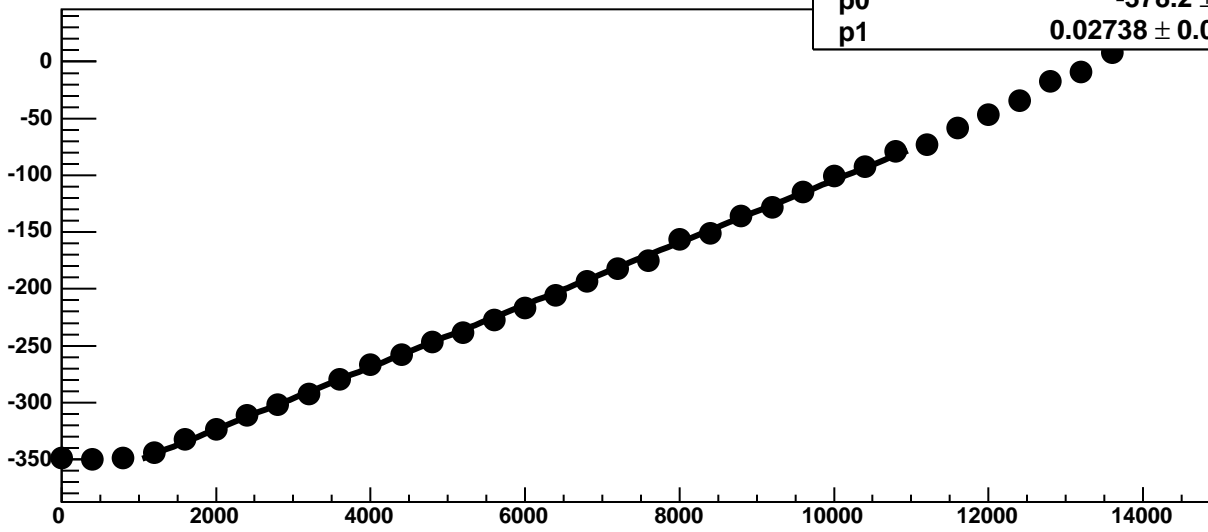
Chip 6, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



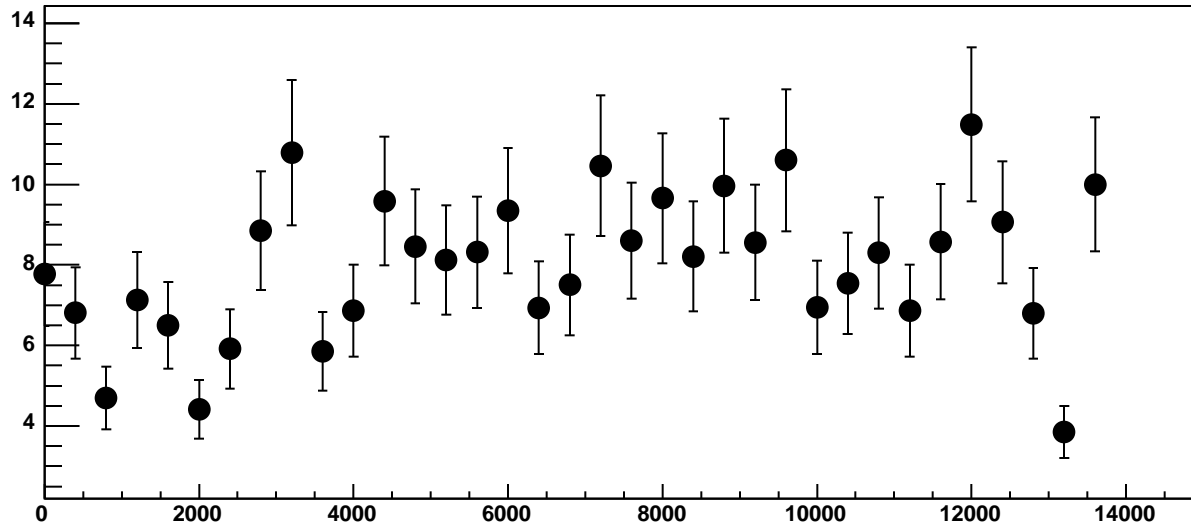
Chip 6, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



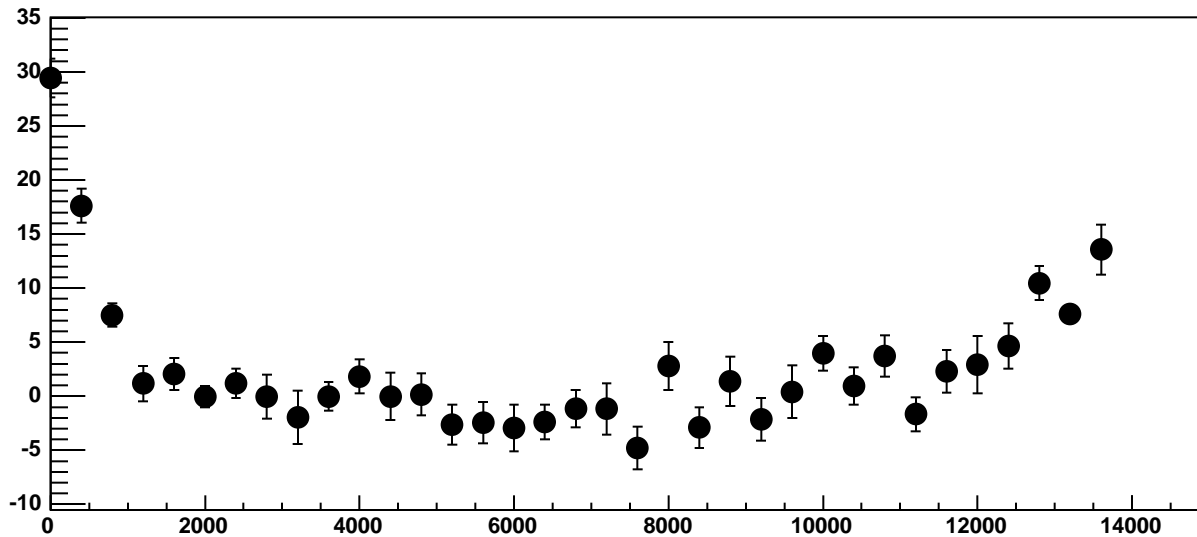
Chip 6, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



Chip 6, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

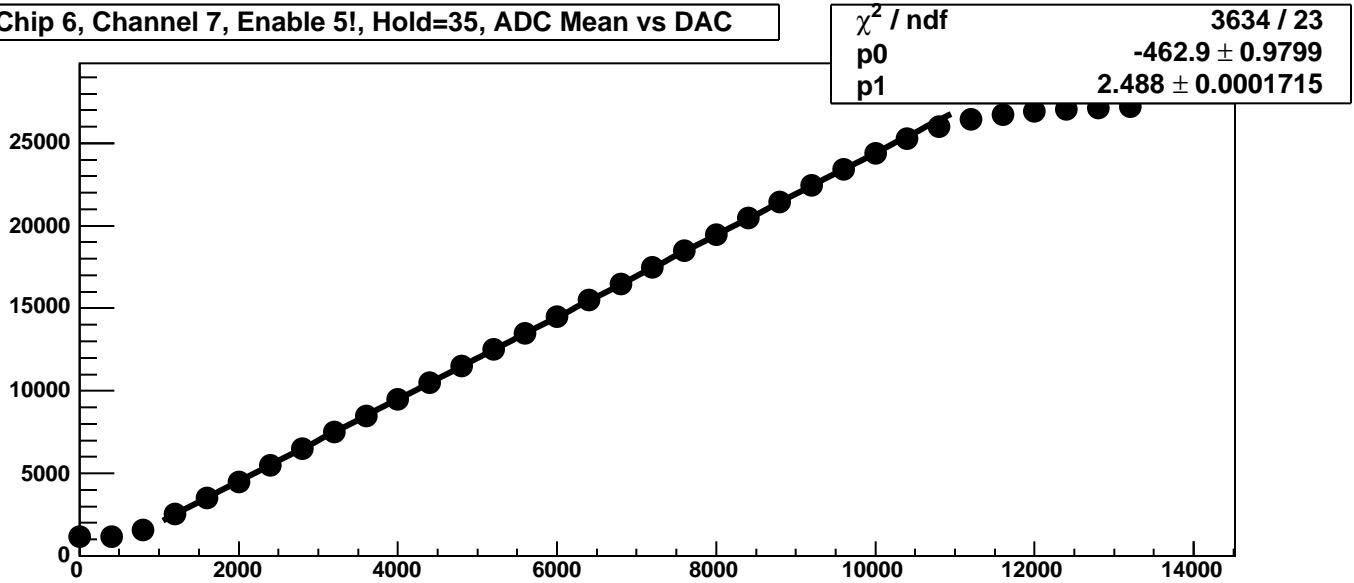


Chip 6, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

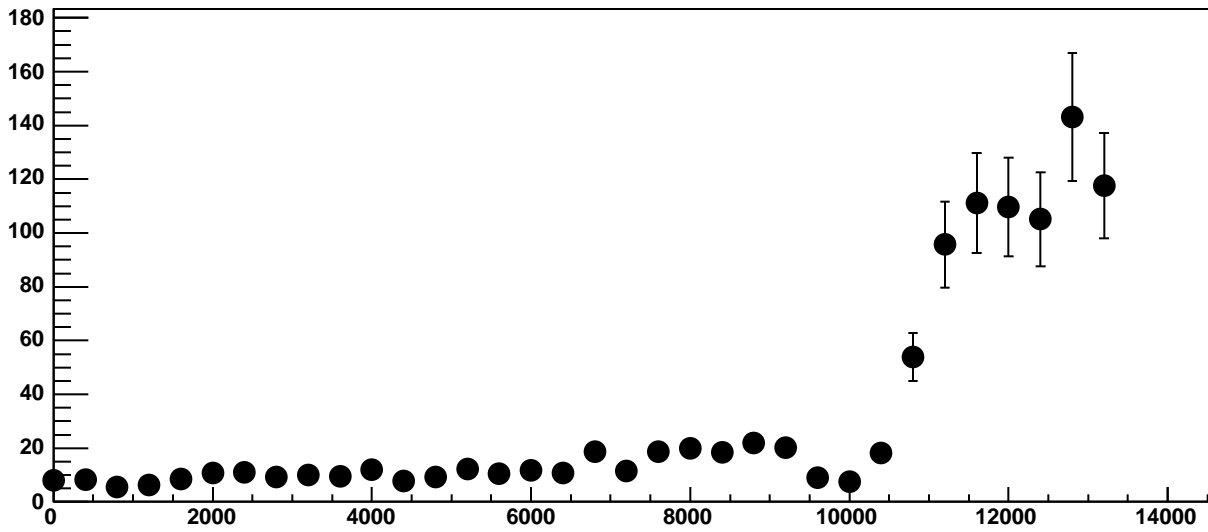




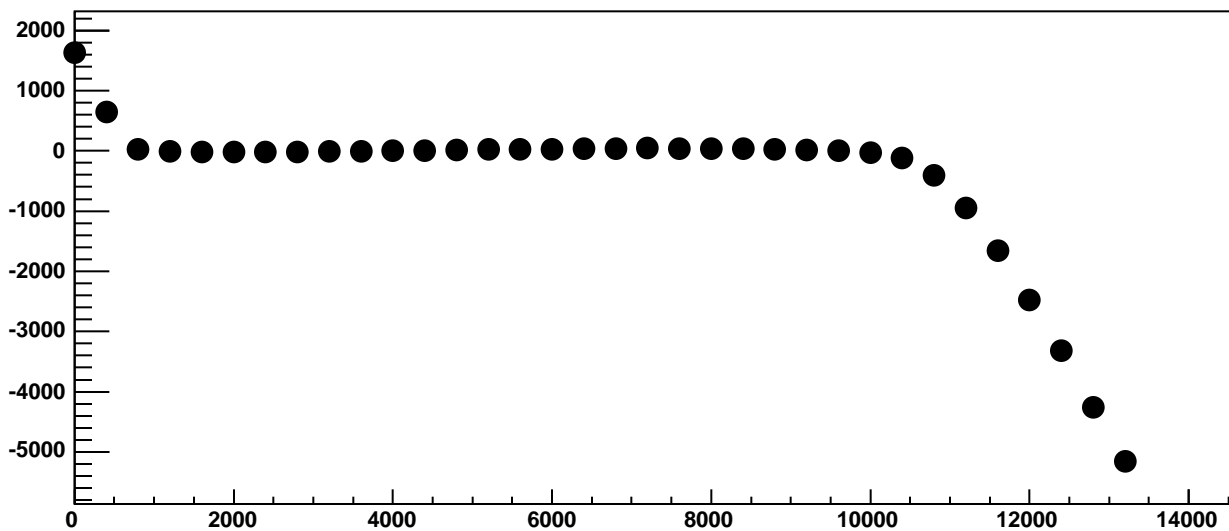
Chip 6, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



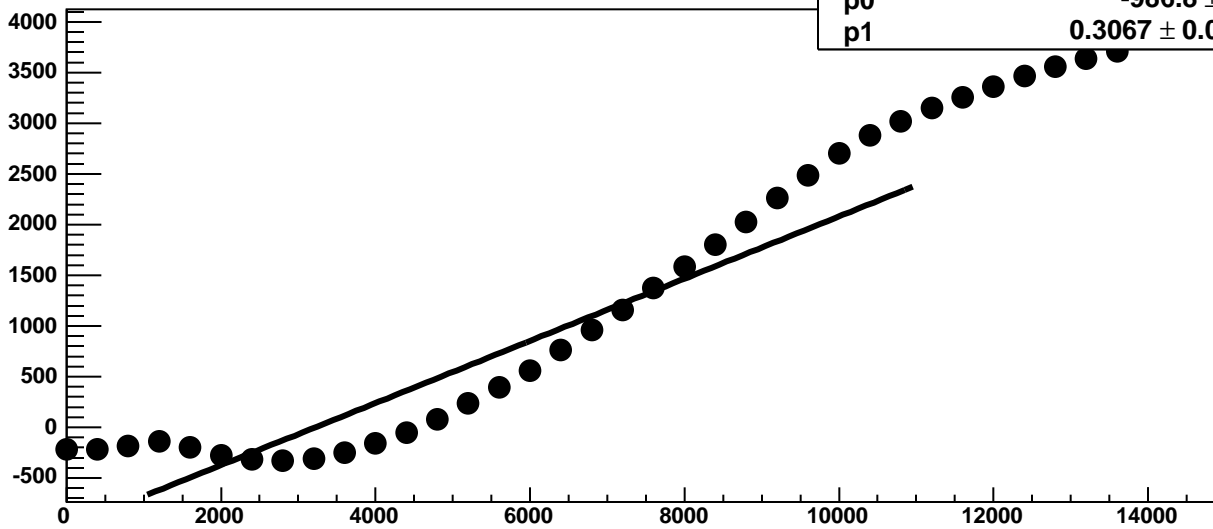
Chip 6, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

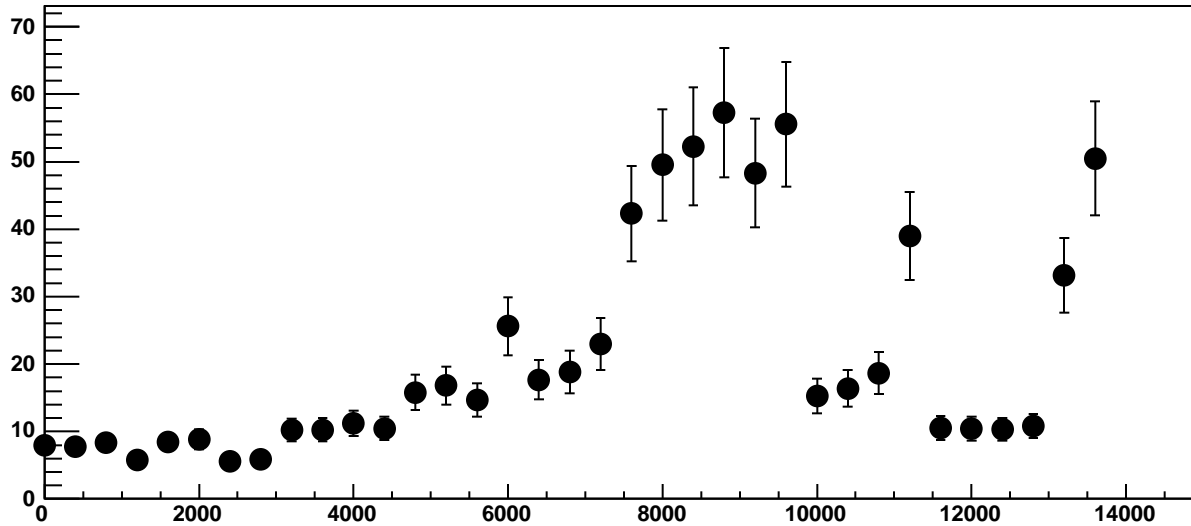


Chip 6, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

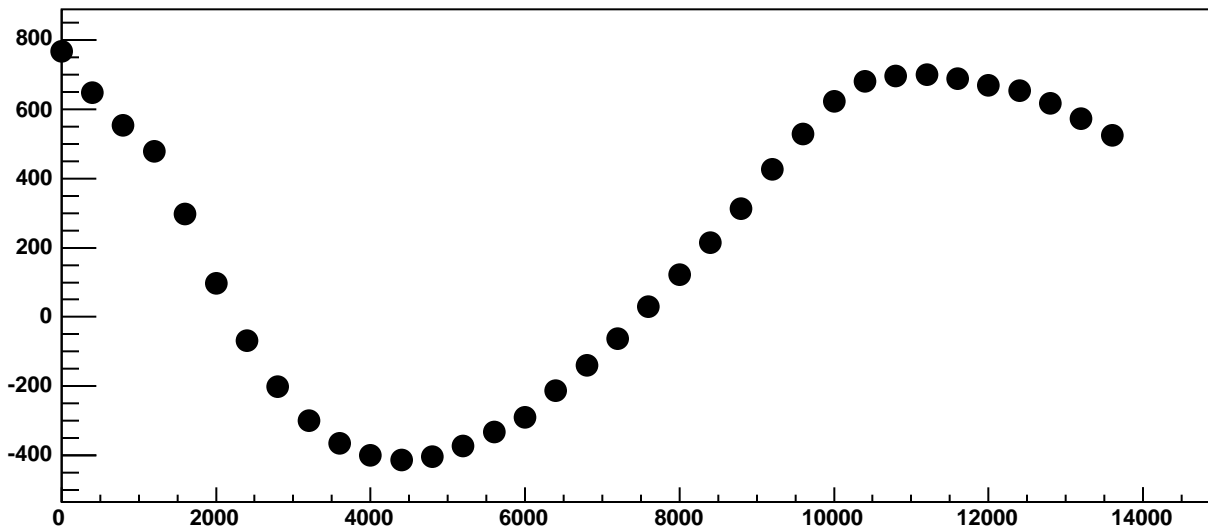


$\chi^2 / \text{ndf}$  4.069e+05 / 23  
p0 -986.8 ± 0.9388  
p1 0.3067 ± 0.0002294

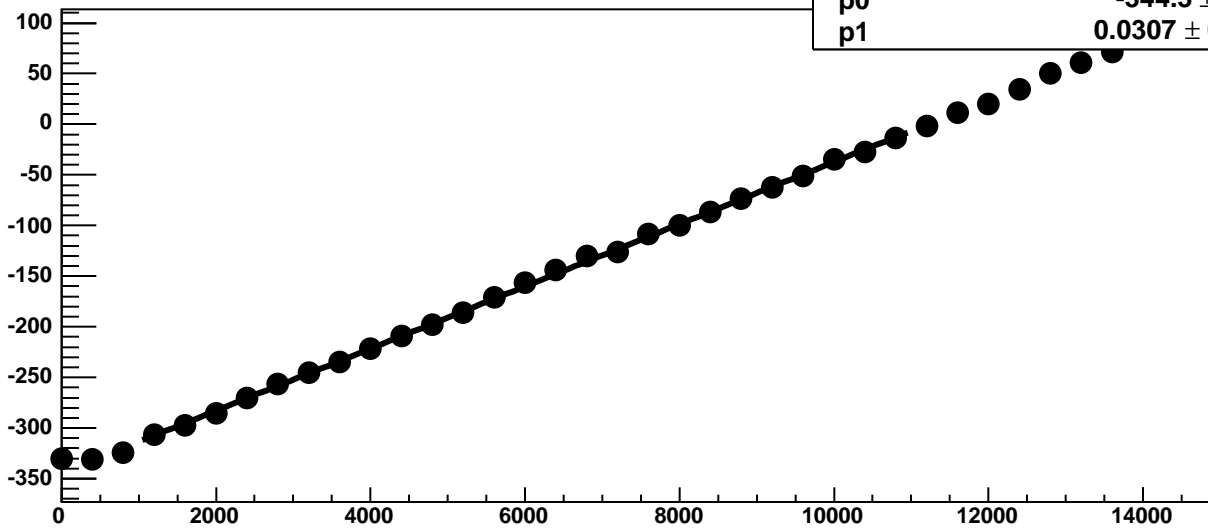
Chip 6, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

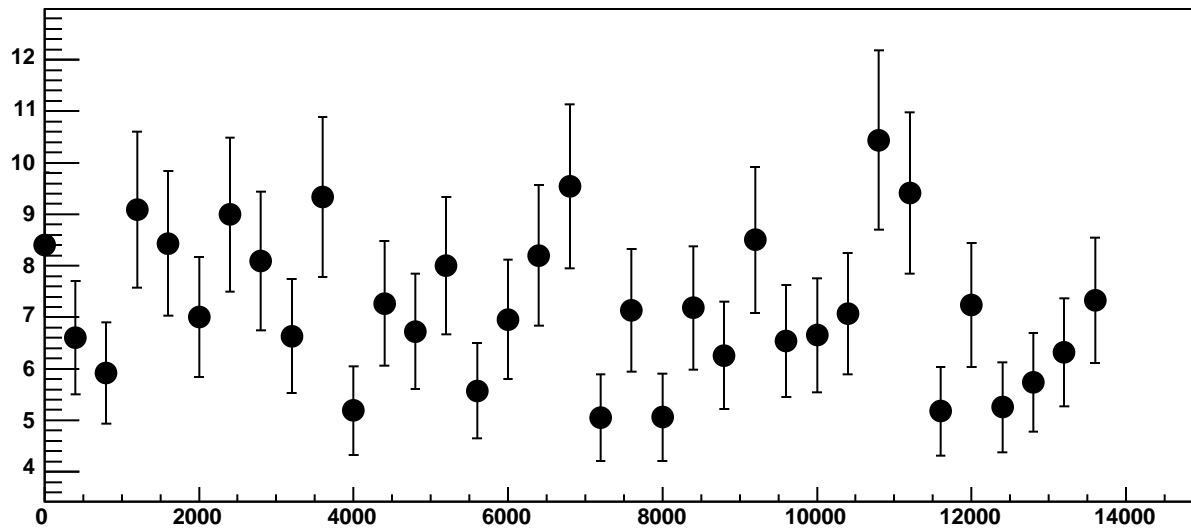


Chip 6, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

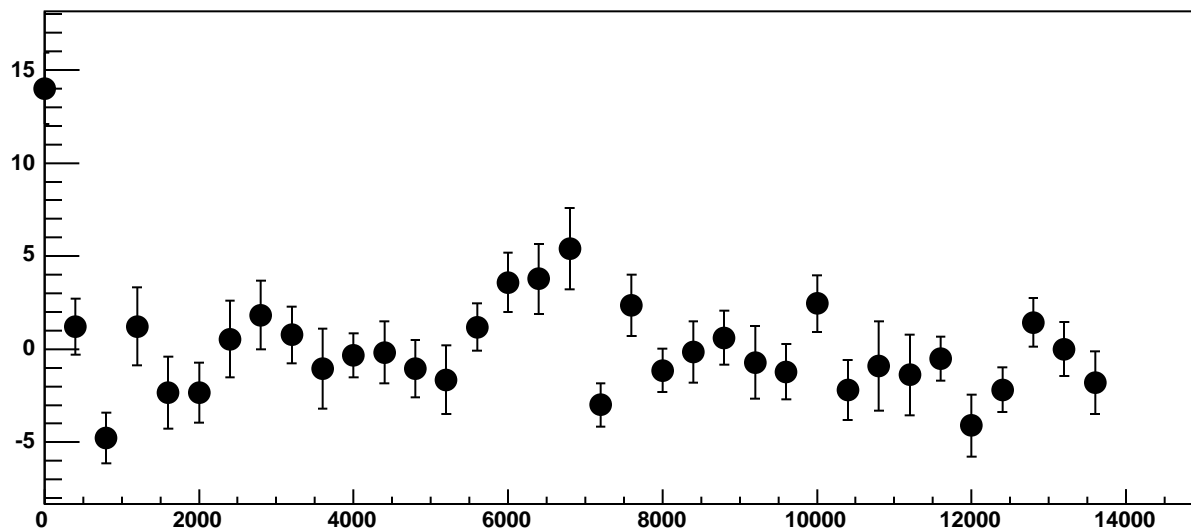


$\chi^2 / \text{ndf}$  38.02 / 23  
p0  $-344.3 \pm 0.8092$   
p1  $0.0307 \pm 0.00012$

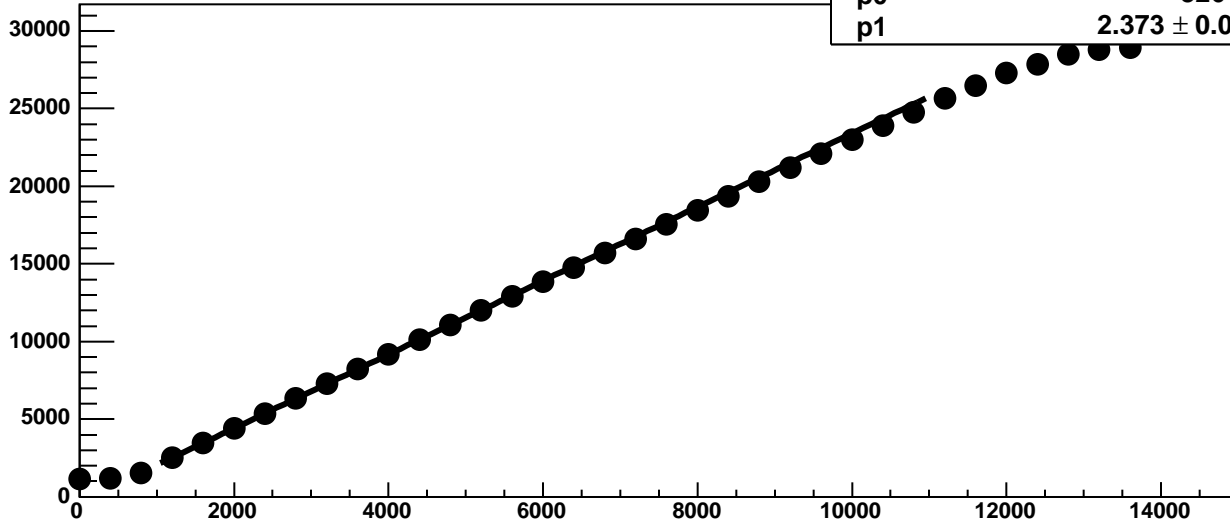
Chip 6, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

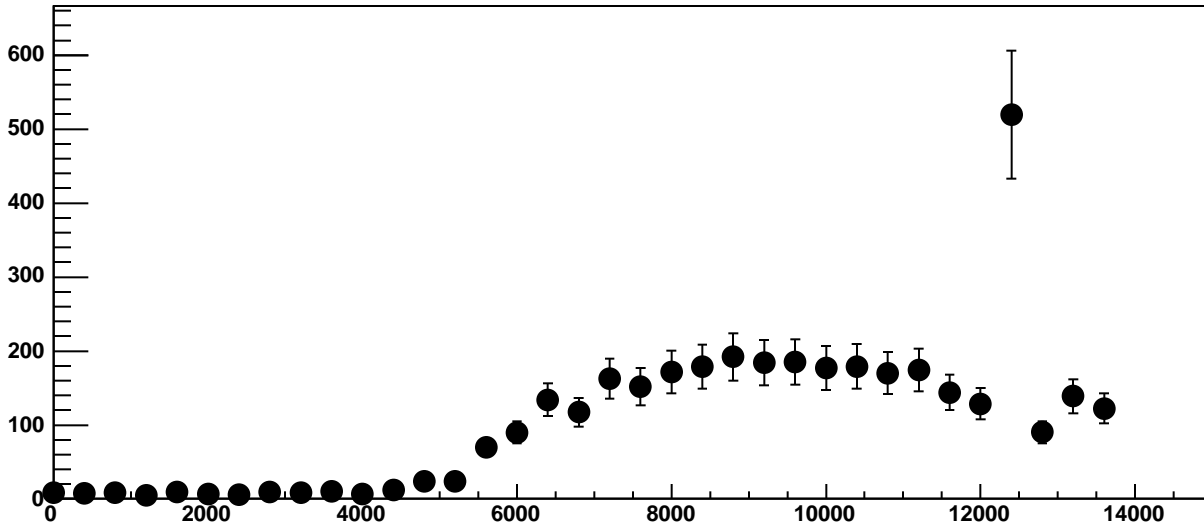


Chip 6, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

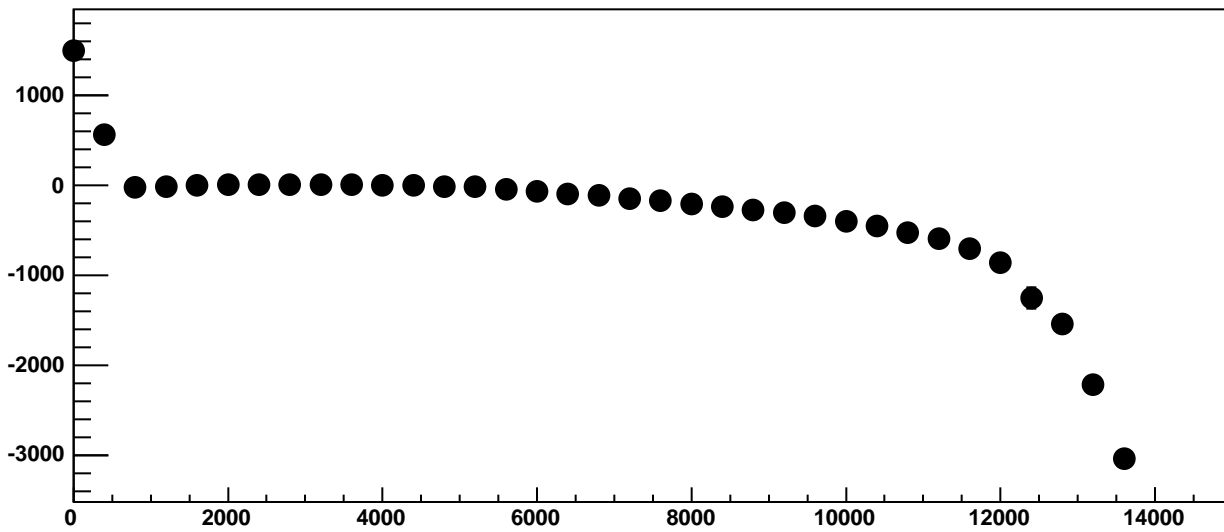


$\chi^2 / \text{ndf}$  950.1 / 23  
p0  $-326 \pm 1.306$   
p1  $2.373 \pm 0.0004749$

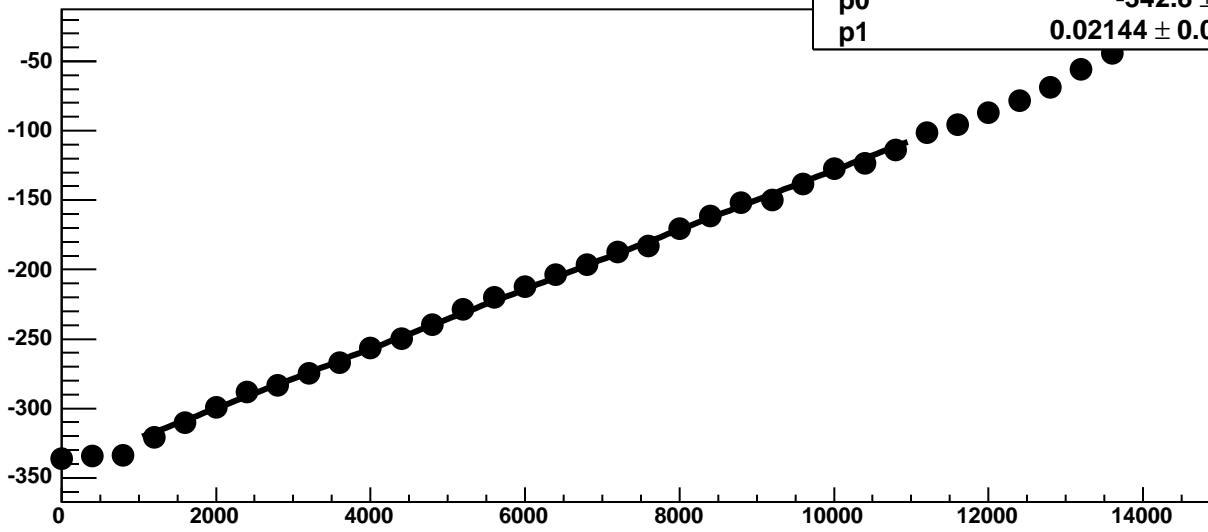
Chip 6, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

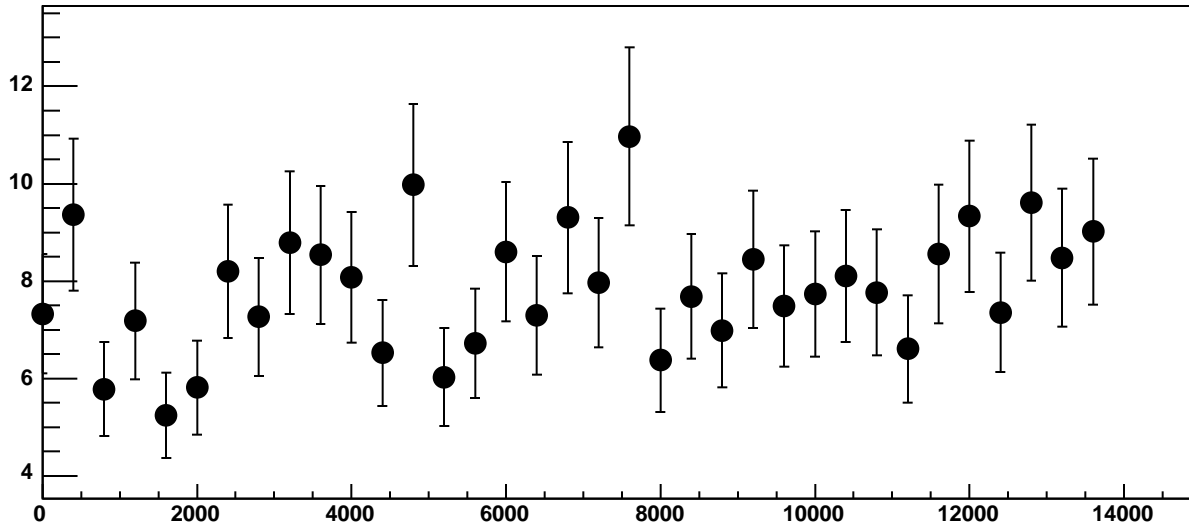


Chip 6, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

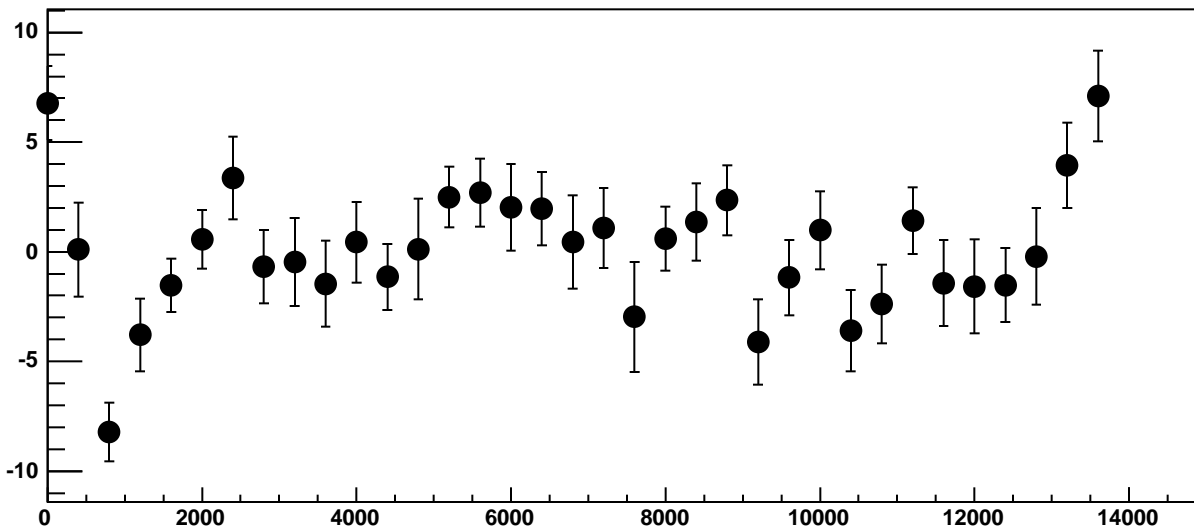


$\chi^2 / \text{ndf}$  35.96 / 23  
p0  $-342.8 \pm 0.7326$   
p1  $0.02144 \pm 0.0001143$

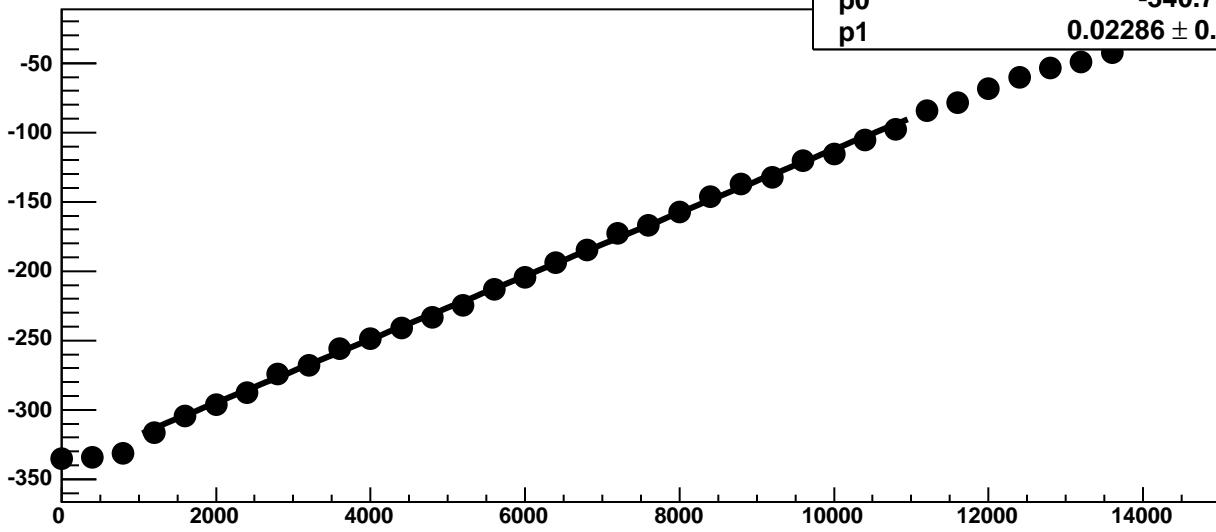
Chip 6, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

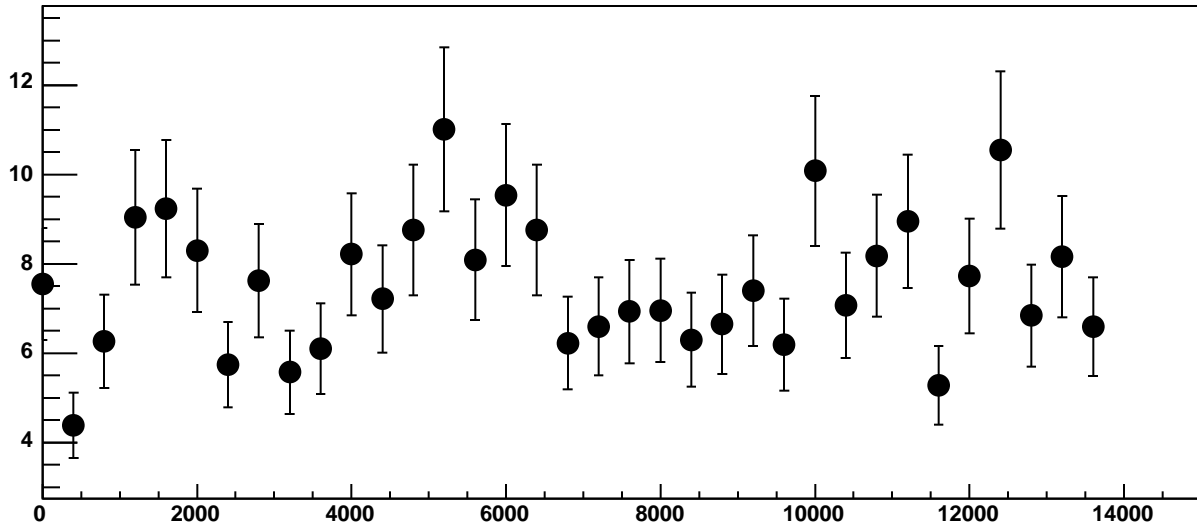


Chip 6, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

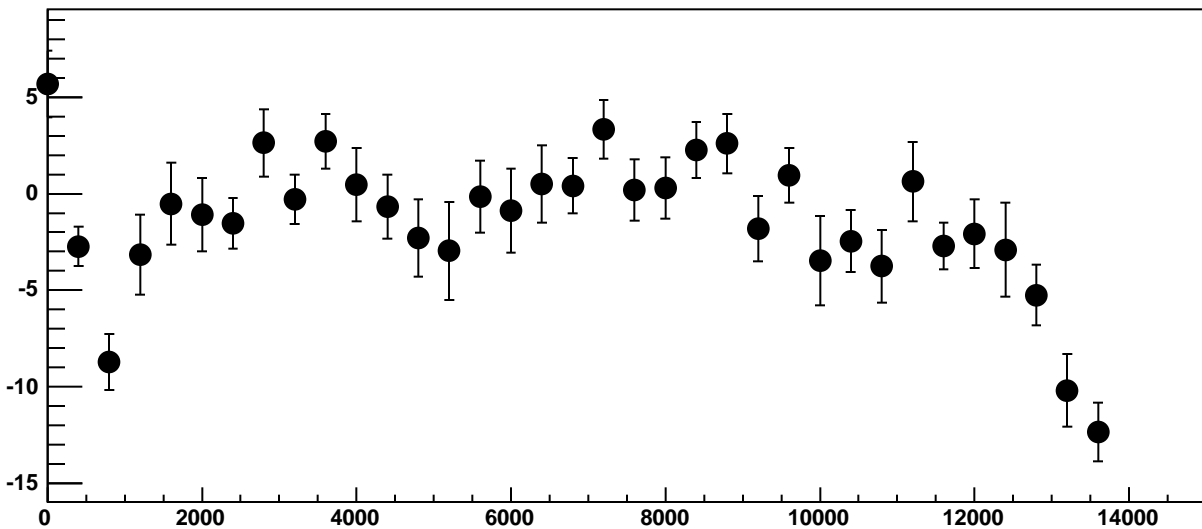


$\chi^2 / \text{ndf}$  33.71 / 23  
p0  $-340.7 \pm 0.79$   
p1  $0.02286 \pm 0.000118$

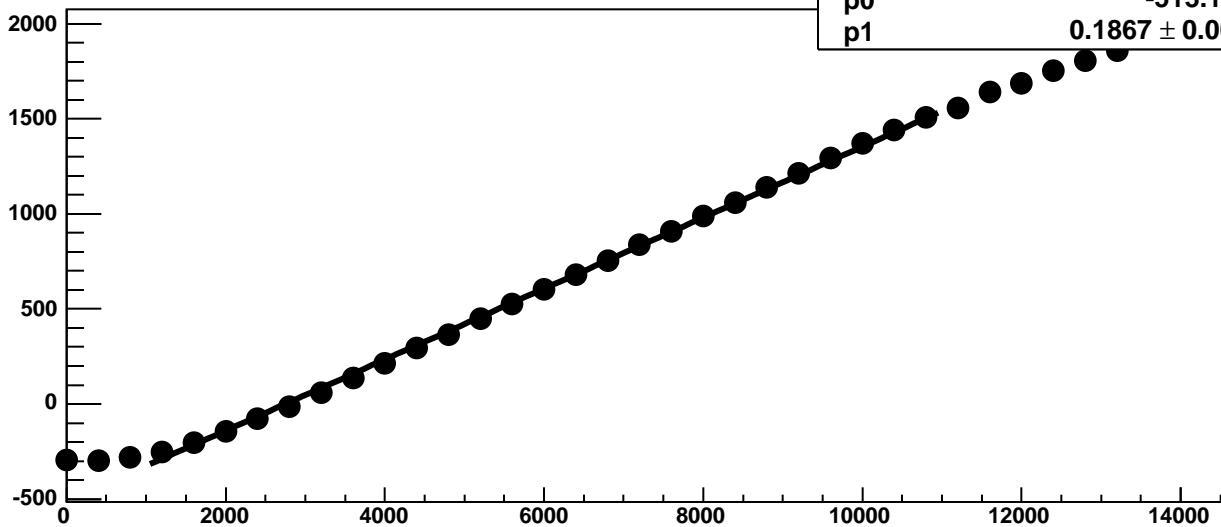
Chip 6, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



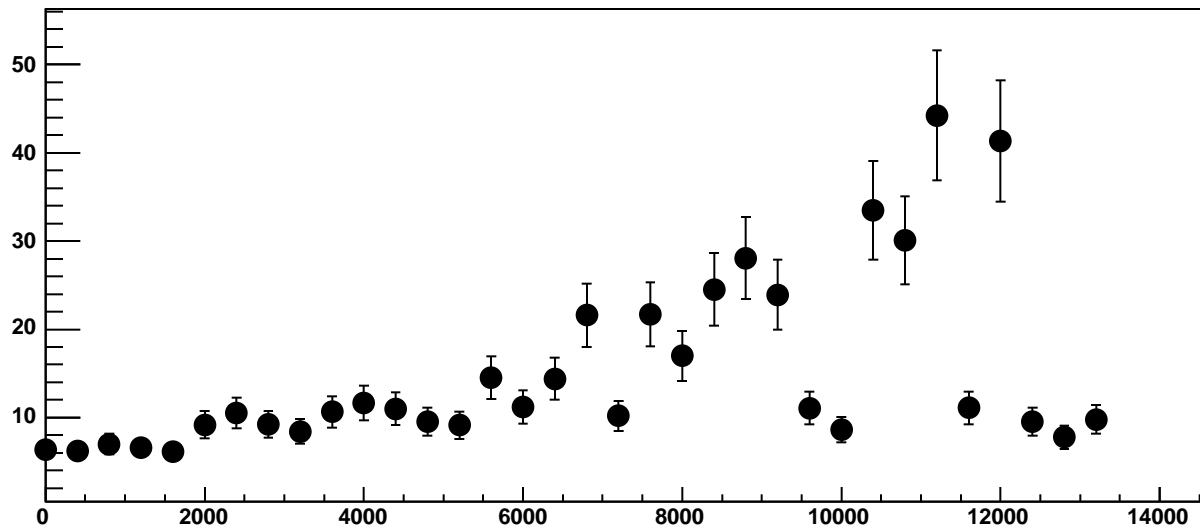
Chip 6, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



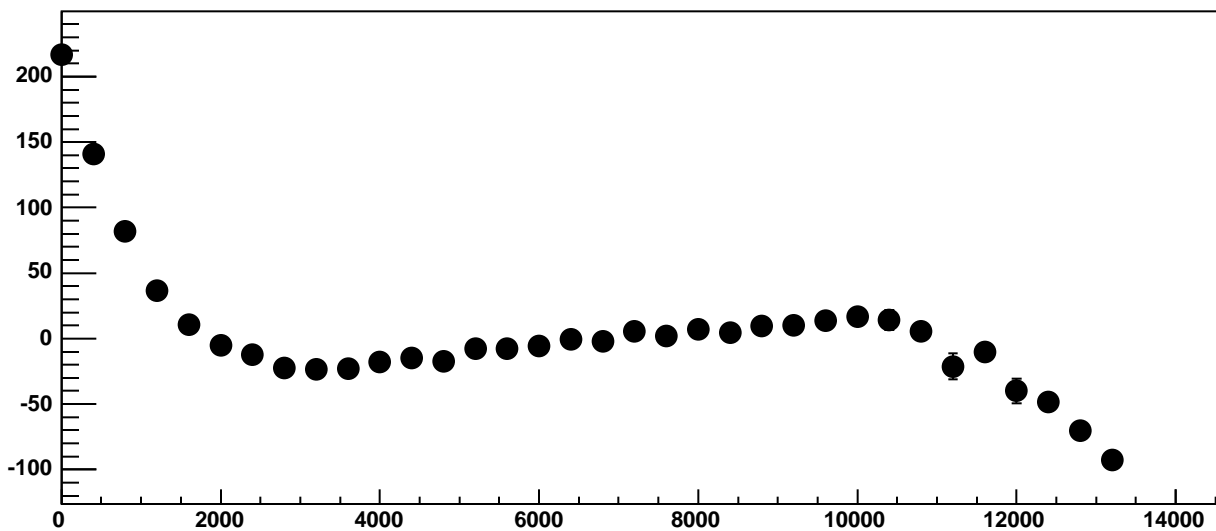
Chip 6, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

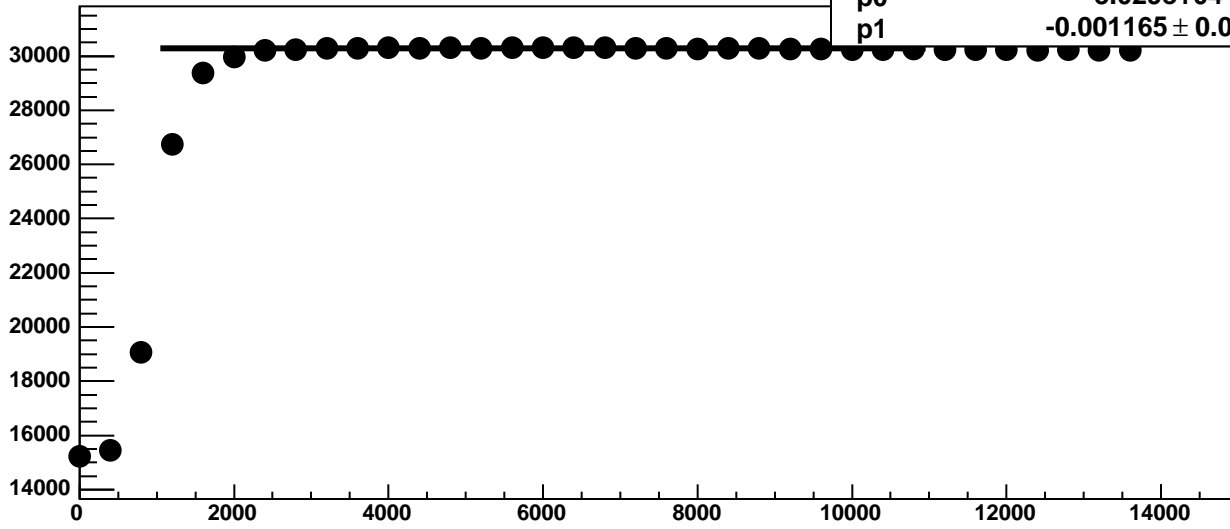
2285 / 23

p0

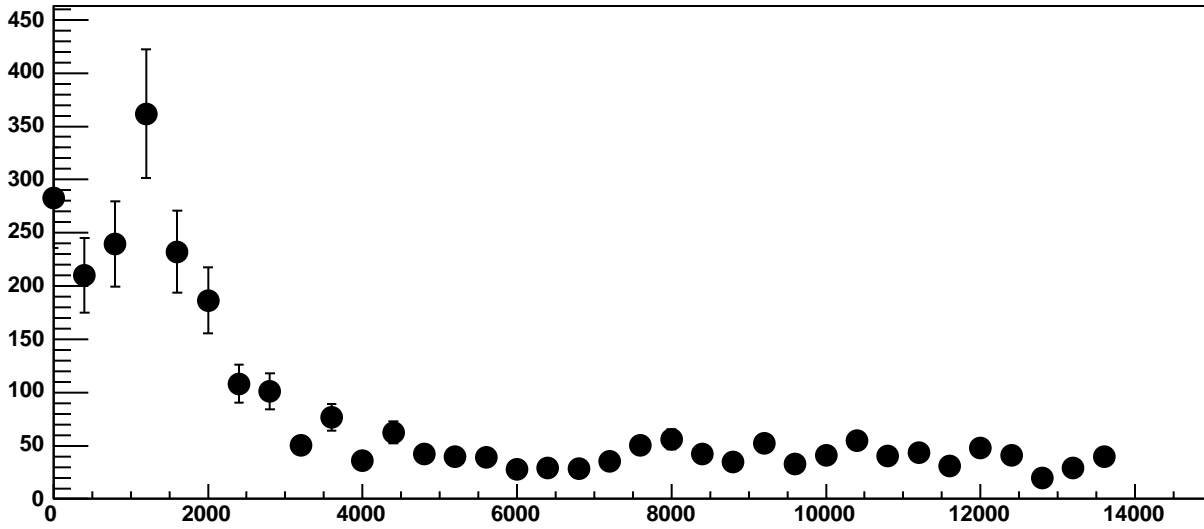
$3.029\text{e}+04 \pm 6.858$

p1

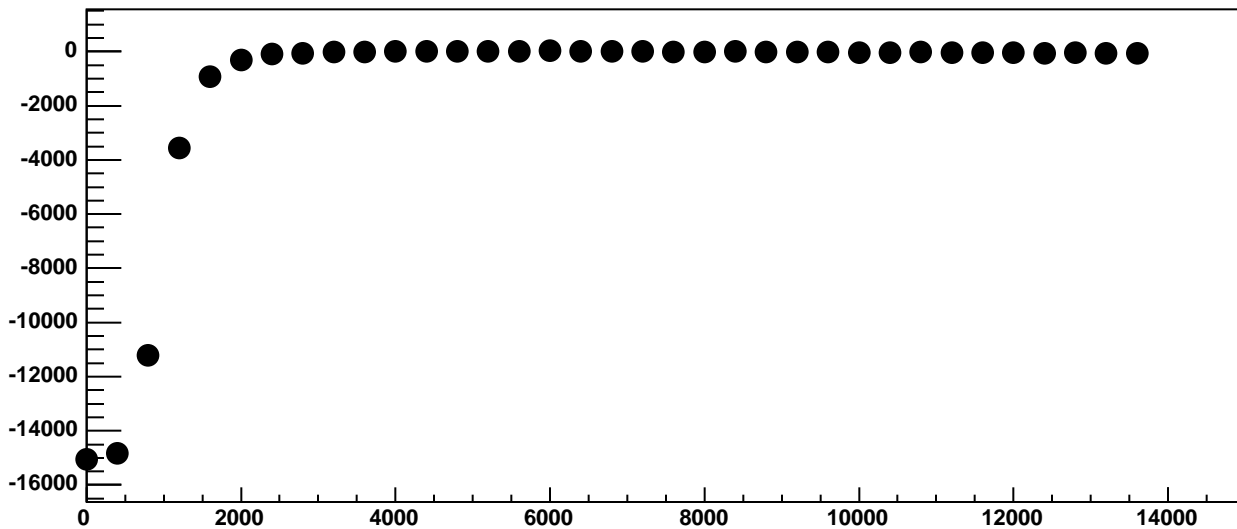
$-0.001165 \pm 0.0009396$



Chip 6, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

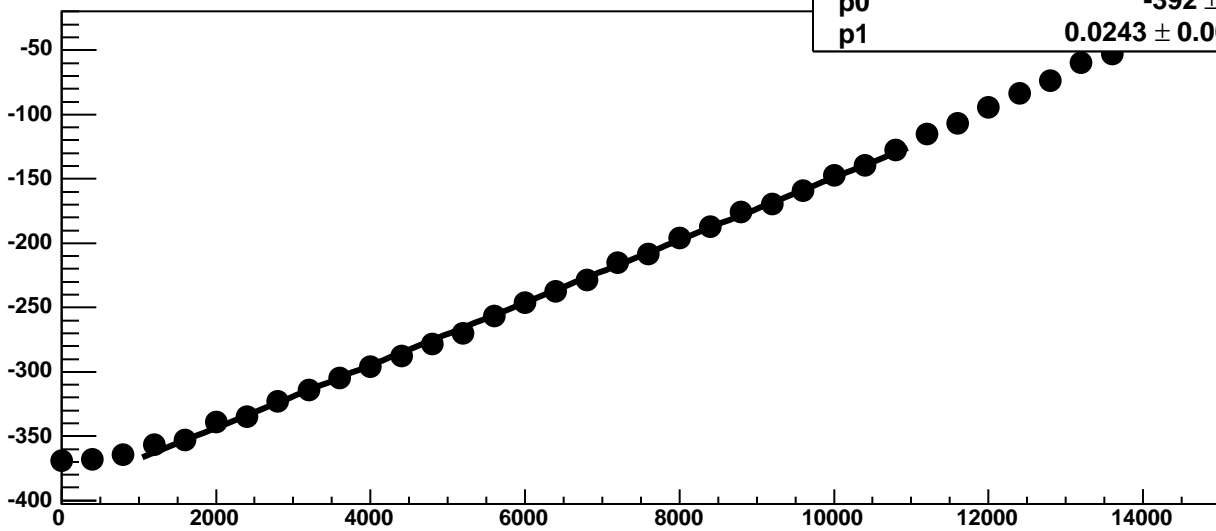


Chip 6, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



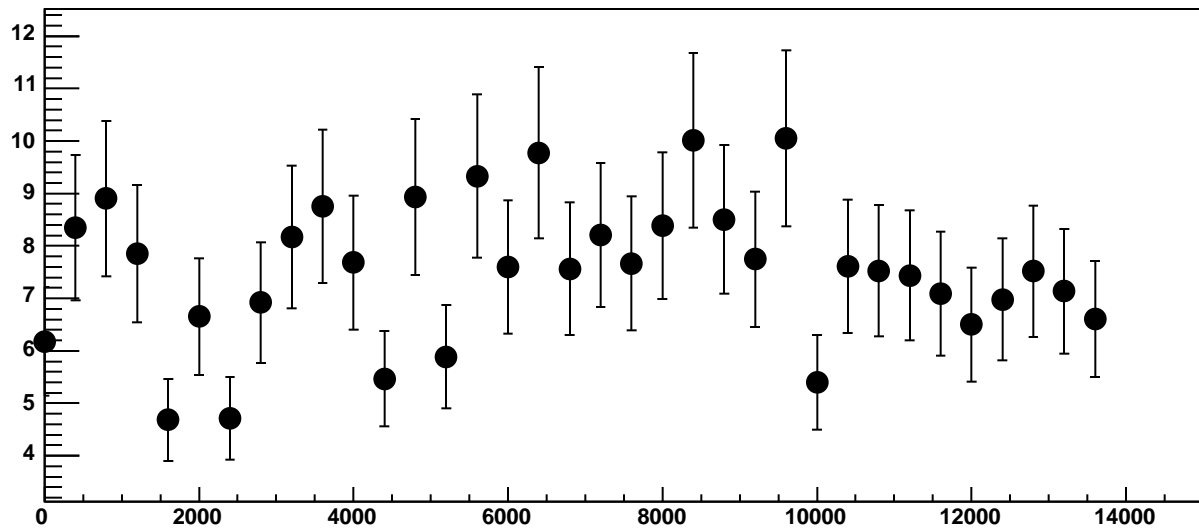


Chip 6, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

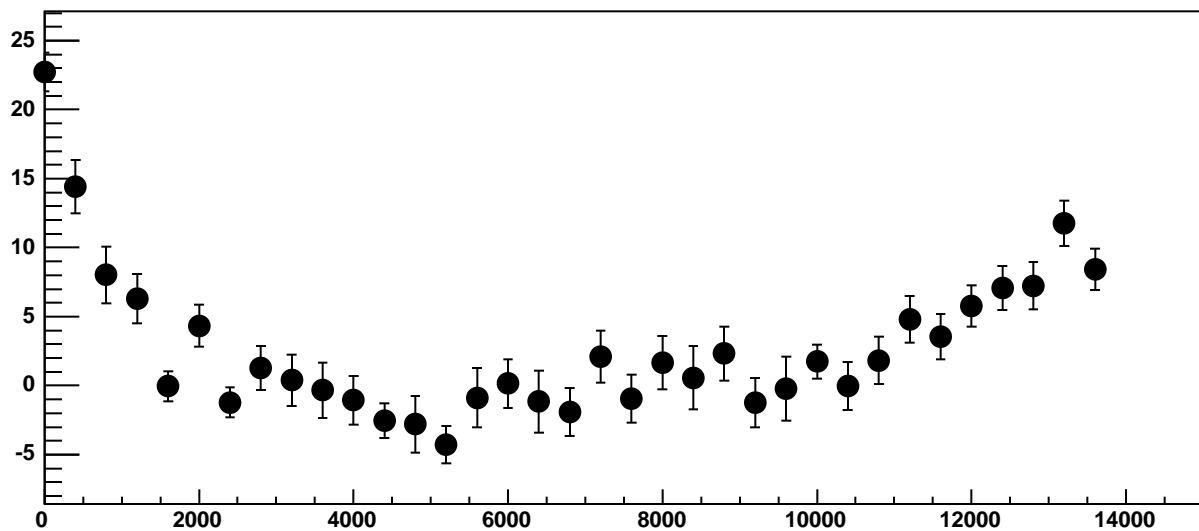


$\chi^2 / \text{ndf}$  47.63 / 23  
p0  $-392 \pm 0.6731$   
p1  $0.0243 \pm 0.0001075$

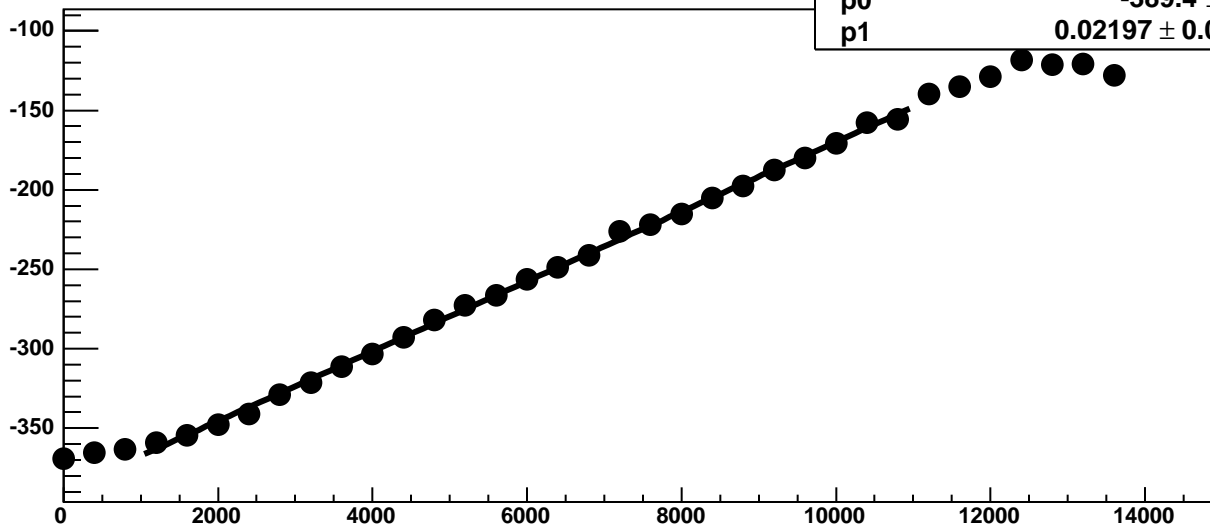
Chip 6, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



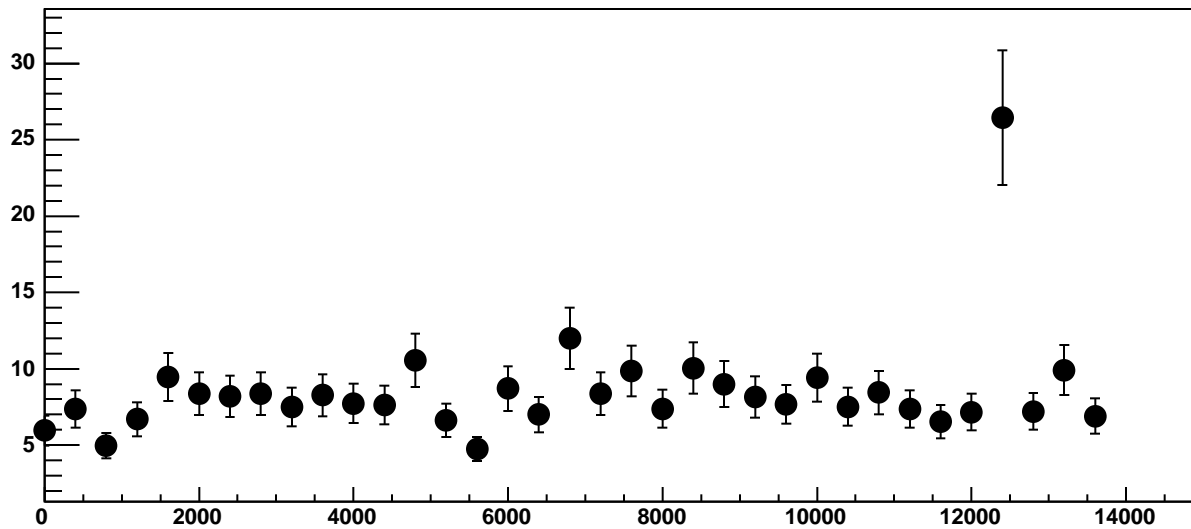
Chip 6, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



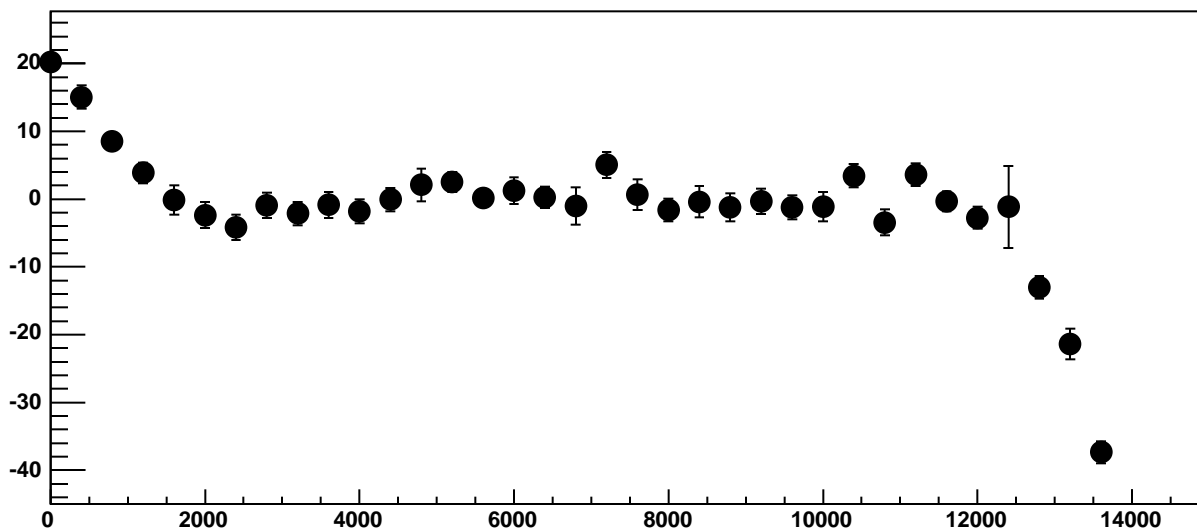
Chip 6, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



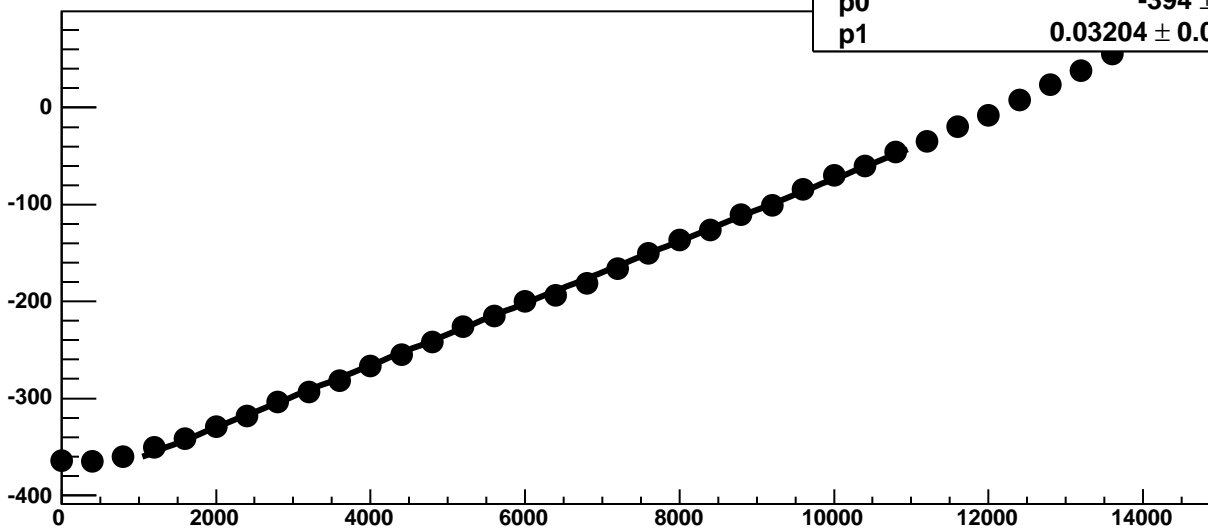
Chip 6, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

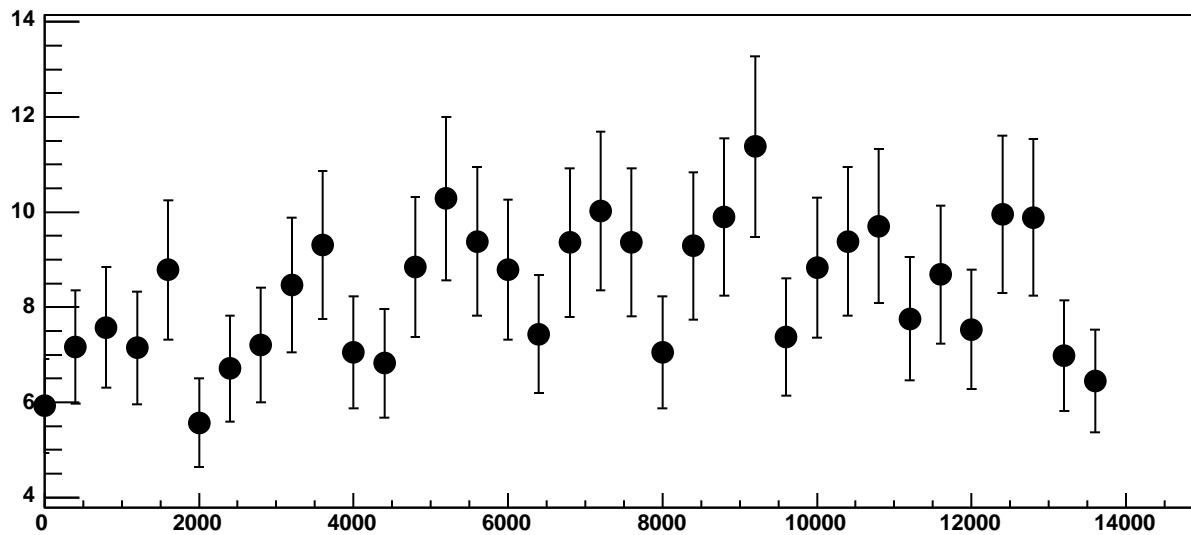


Chip 6, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

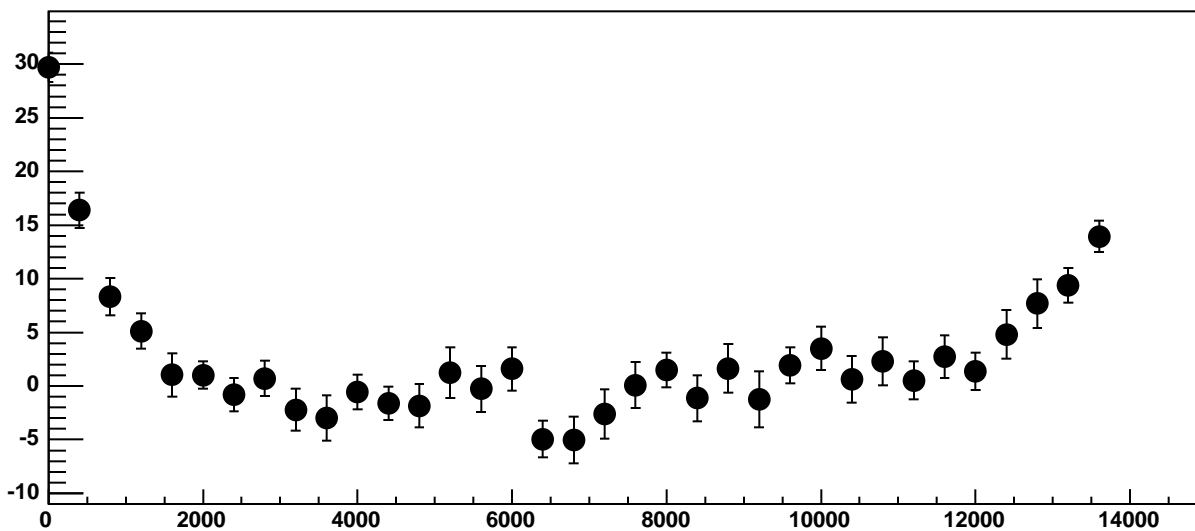


$\chi^2 / \text{ndf}$  39.81 / 23  
p0  $-394 \pm 0.7942$   
p1  $0.03204 \pm 0.0001283$

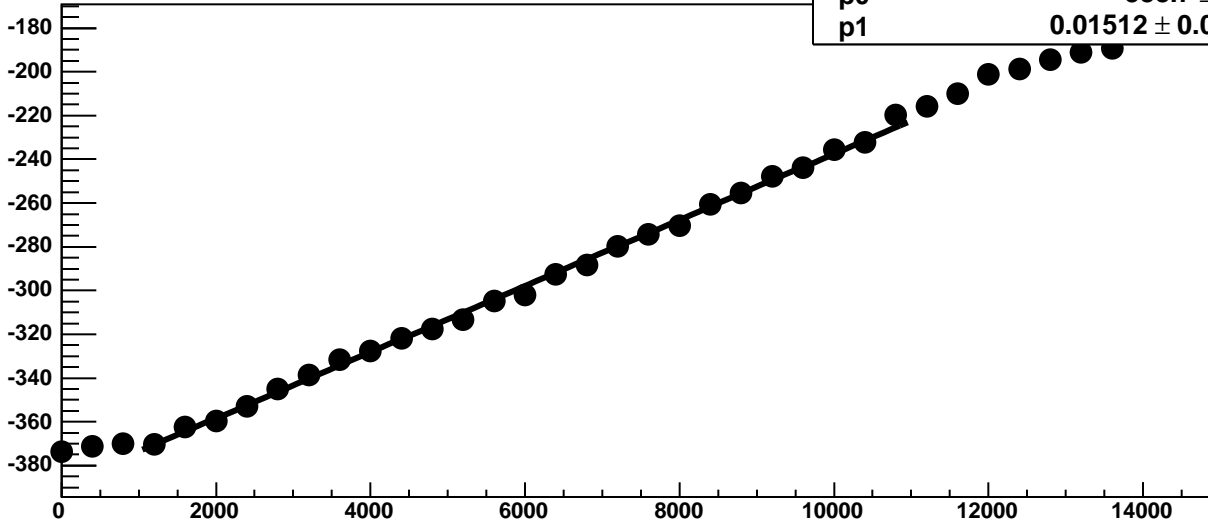
Chip 6, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

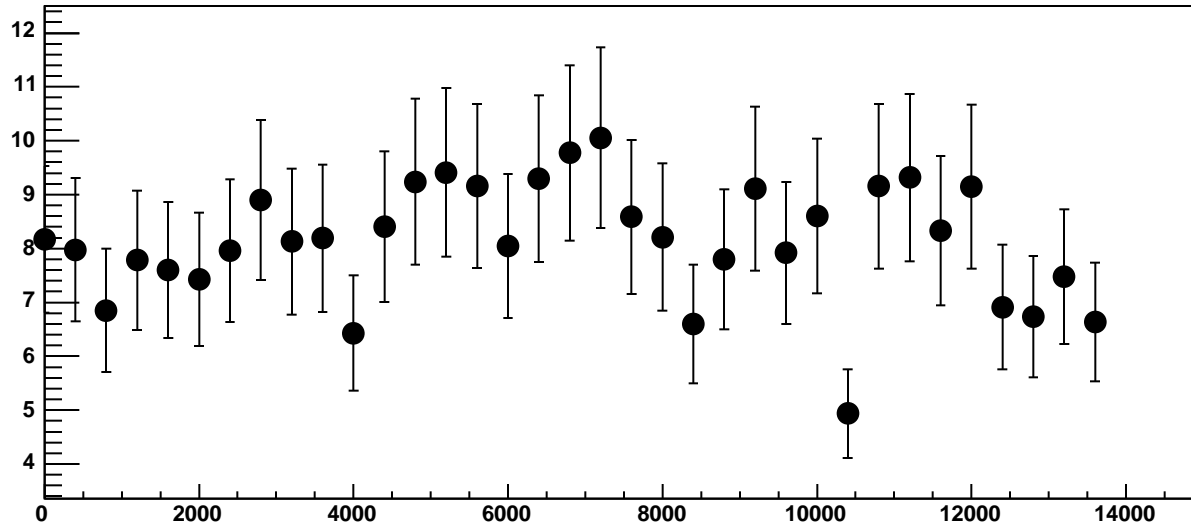


Chip 6, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

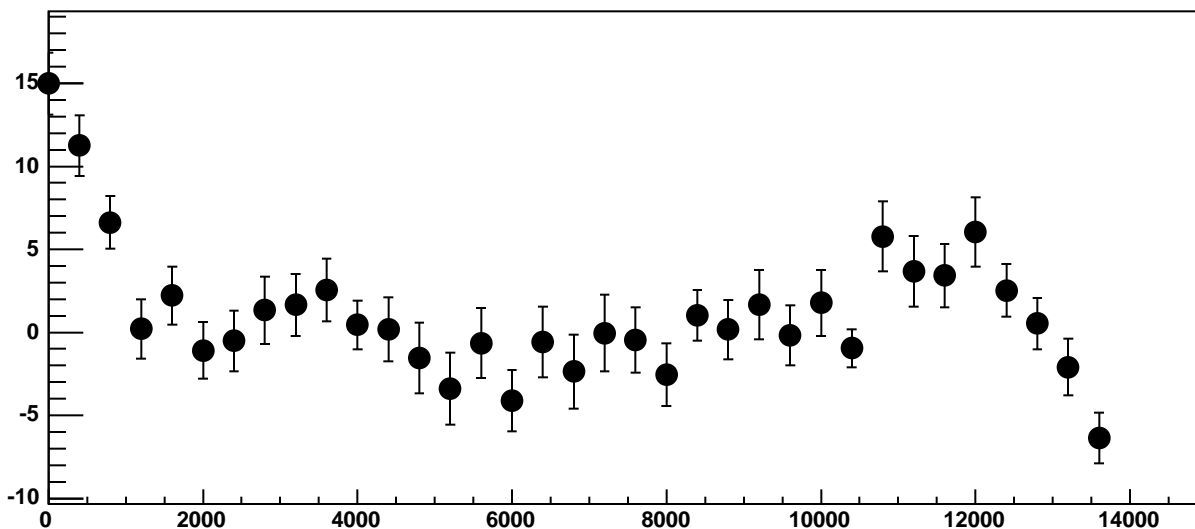


$\chi^2 / \text{ndf}$  26.6 / 23  
p0  $-388.7 \pm 0.8219$   
p1  $0.01512 \pm 0.0001195$

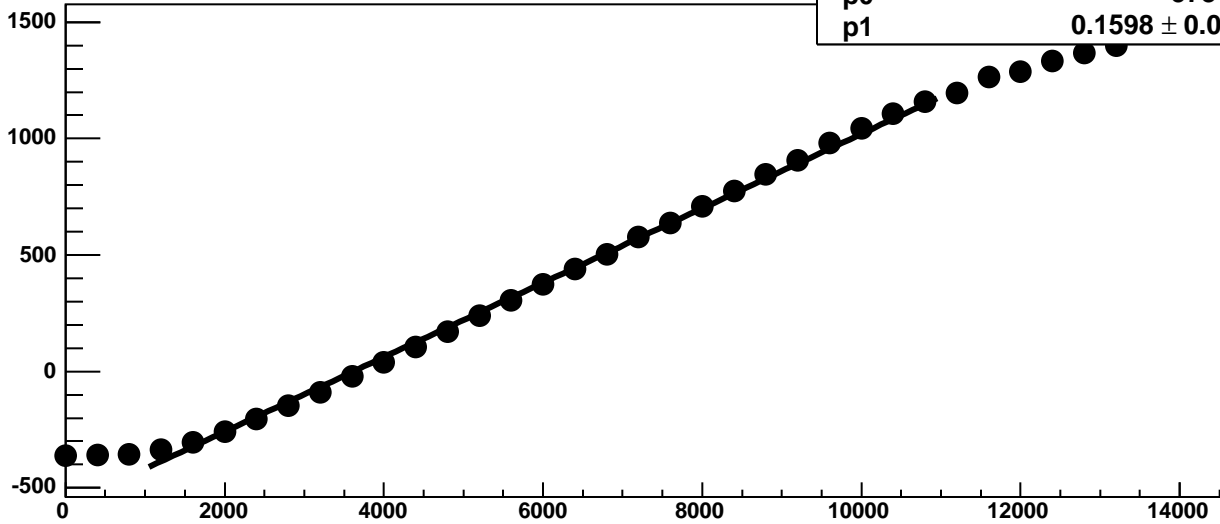
Chip 6, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



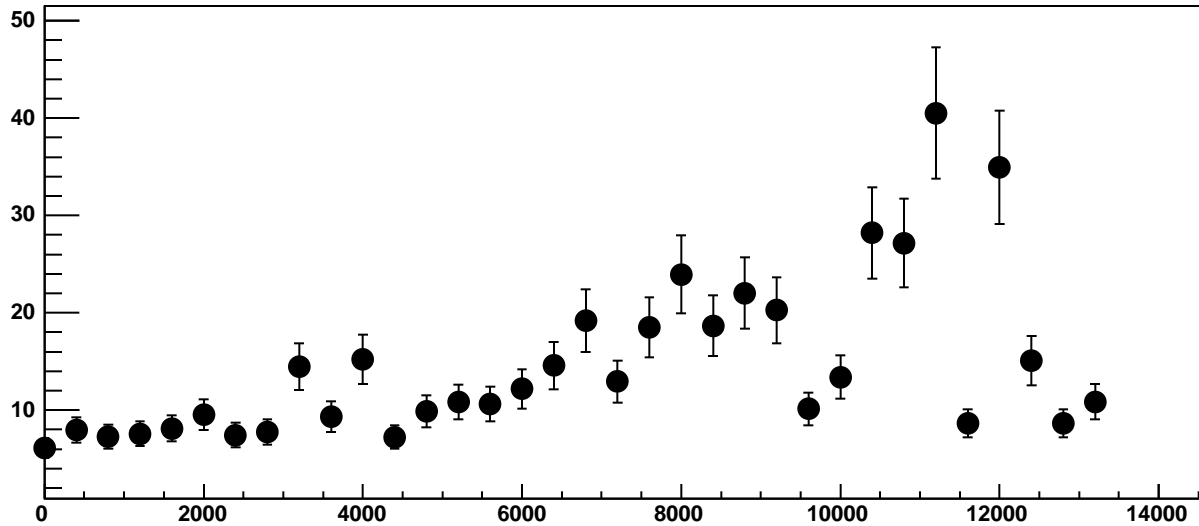
Chip 6, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



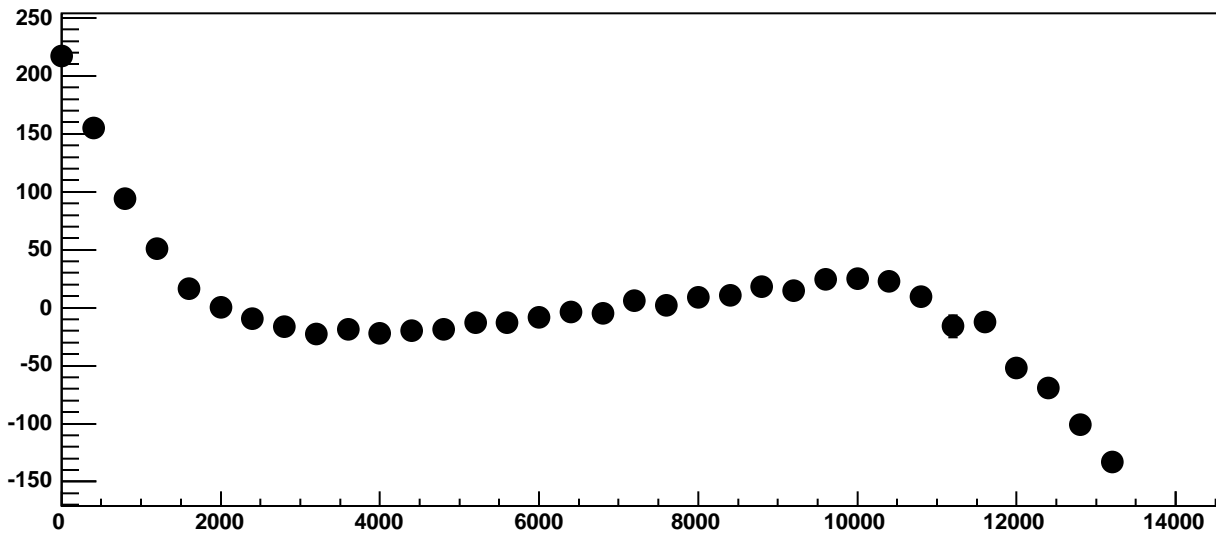
Chip 6, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

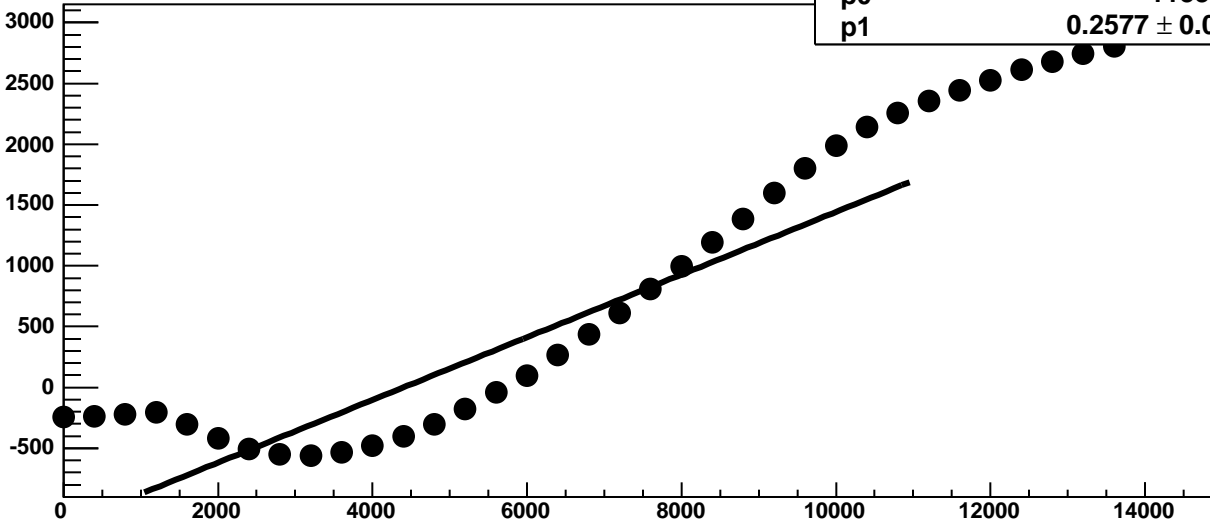


Chip 6, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

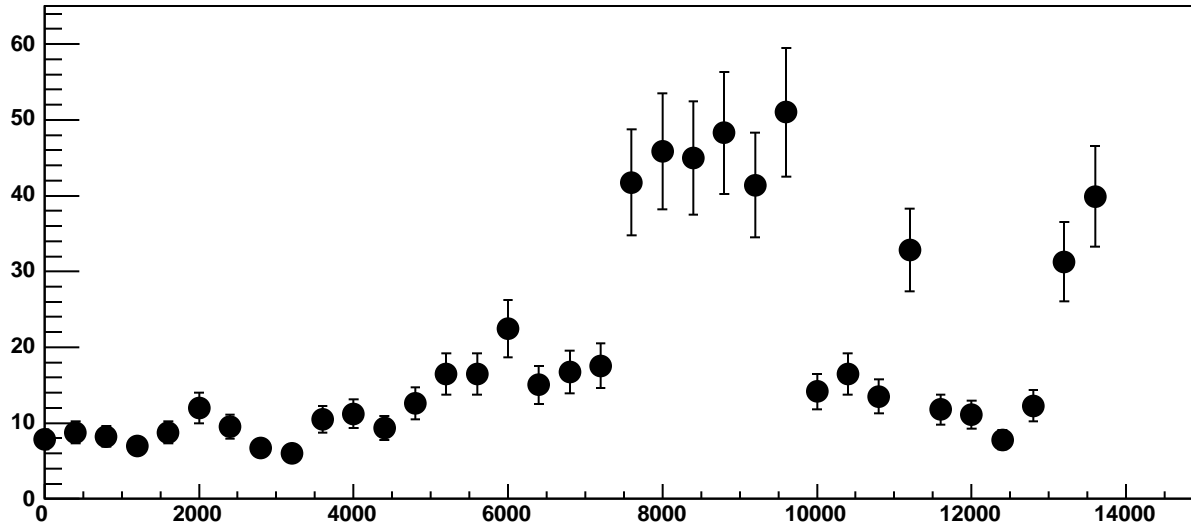


Chip 6, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

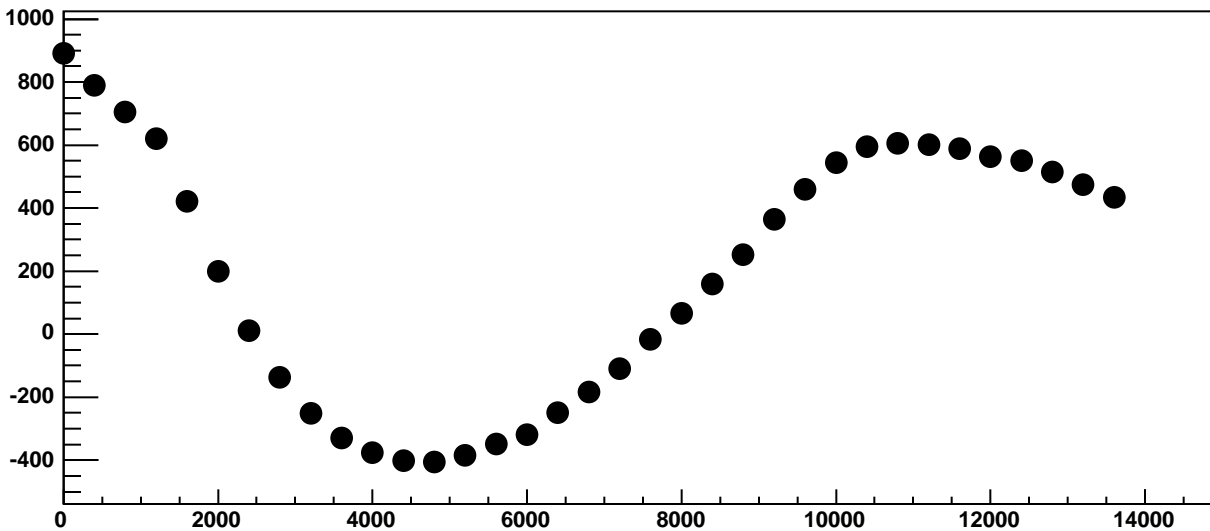
$\chi^2 / \text{ndf}$  4.613e+05 / 23  
p0 -1133 ± 1.018  
p1 0.2577 ± 0.0002184



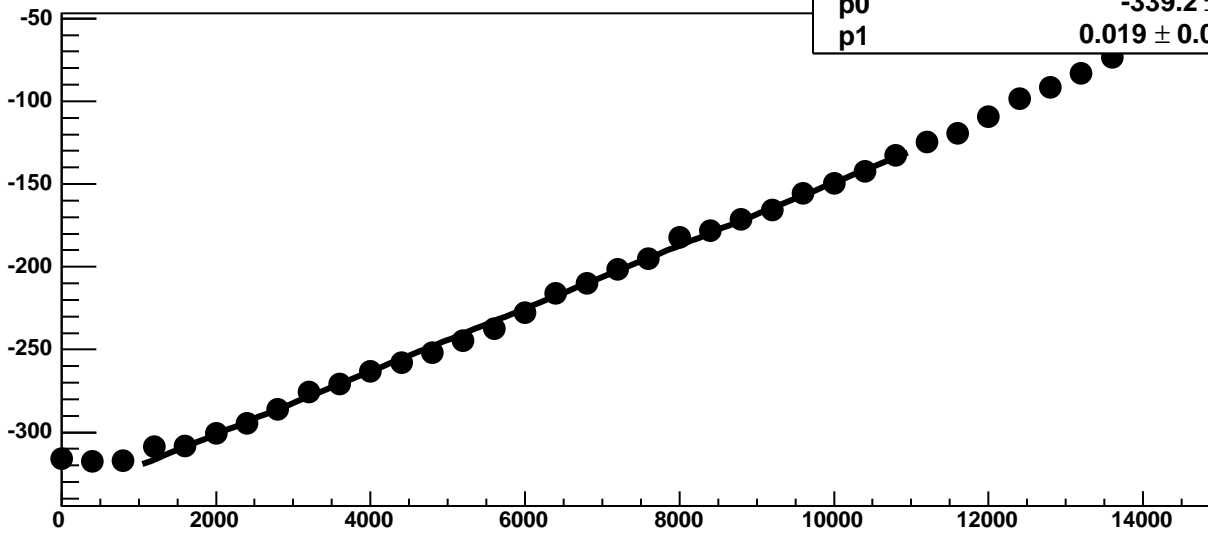
Chip 6, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

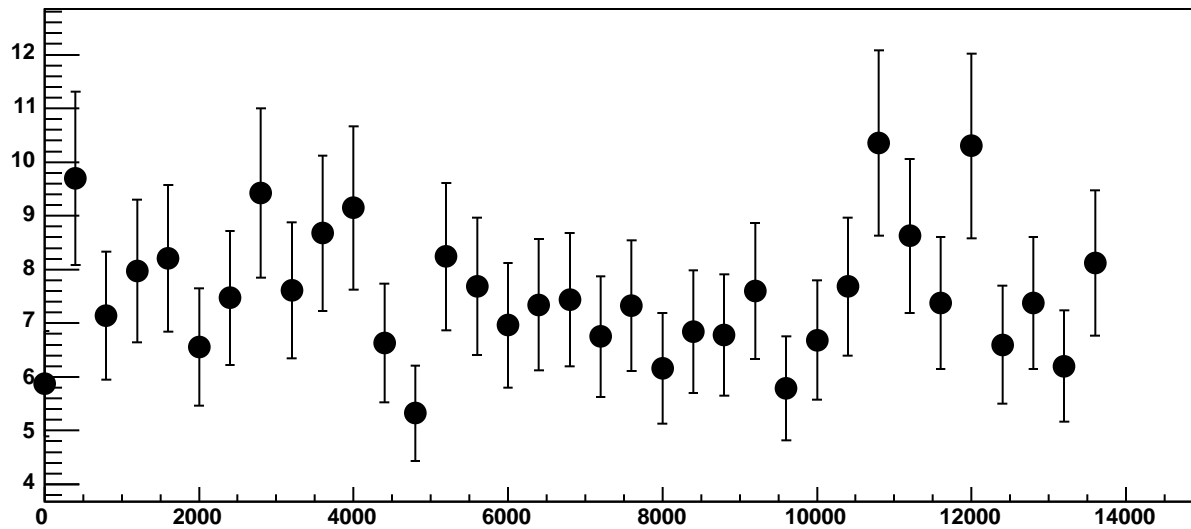


Chip 6, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

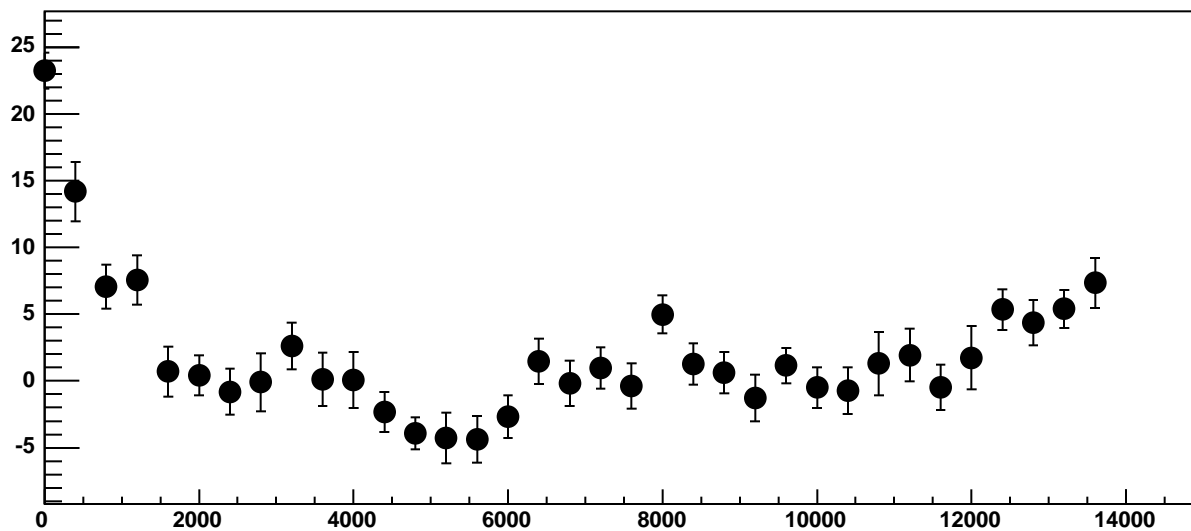


$\chi^2 / \text{ndf}$  62.82 / 23  
p0  $-339.2 \pm 0.8071$   
p1  $0.019 \pm 0.0001193$

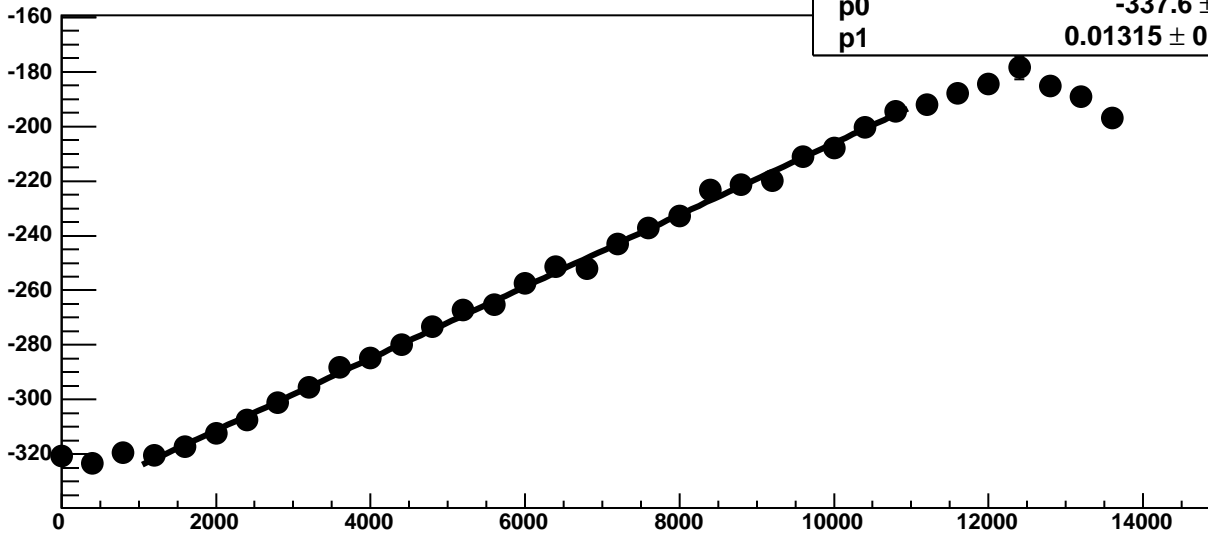
Chip 6, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



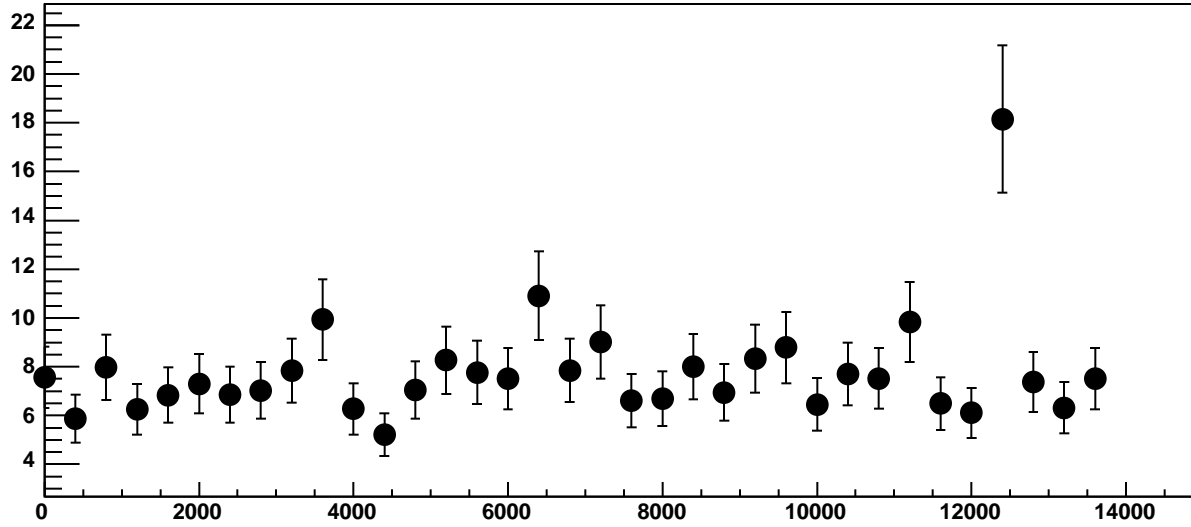
Chip 6, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



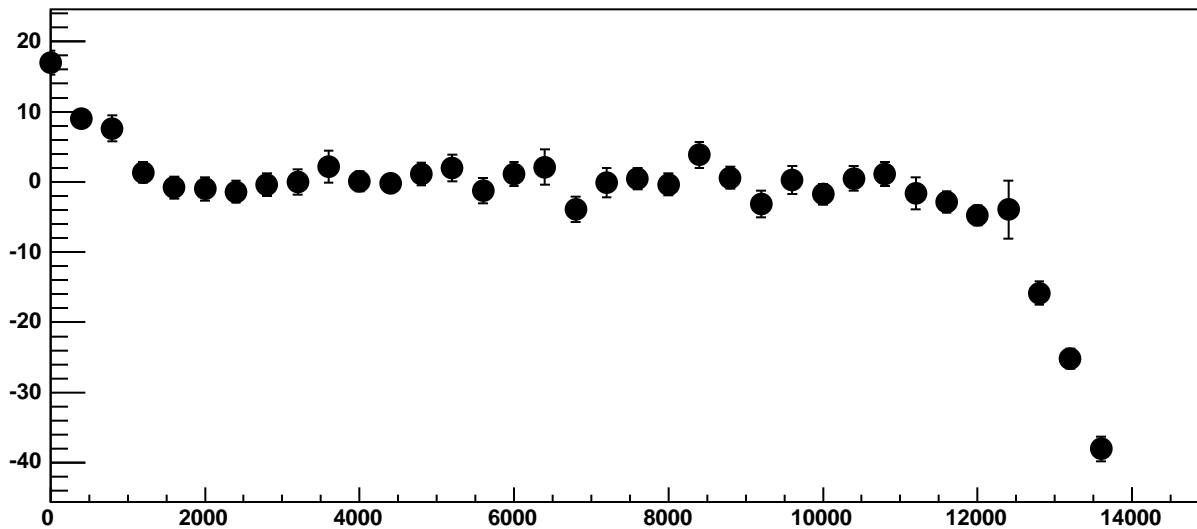
Chip 6, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 6, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

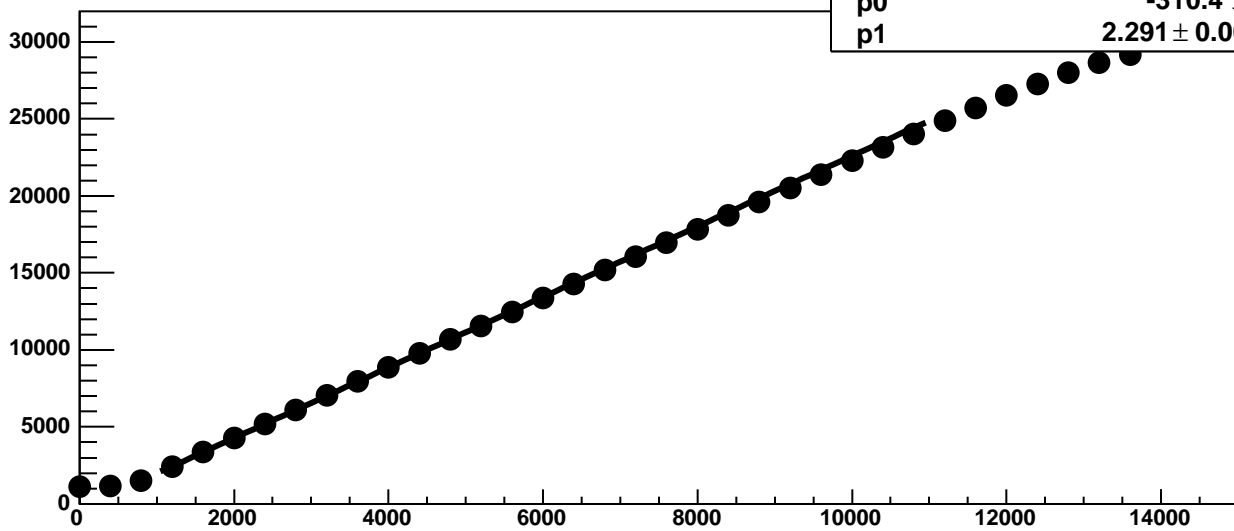


Chip 6, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC





Chip 6, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

604.1 / 23

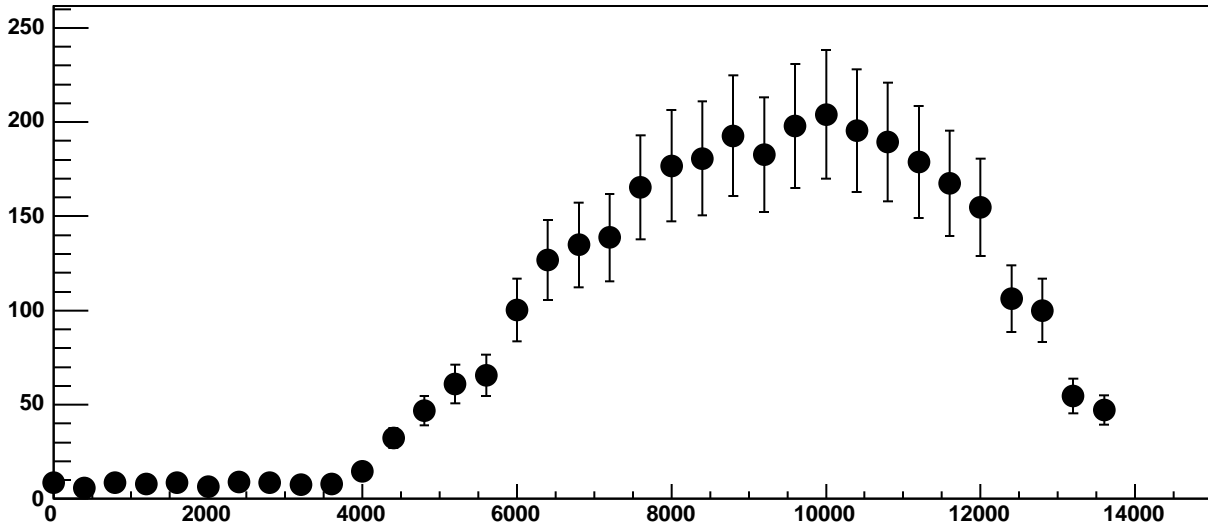
p0

$-310.4 \pm 1.879$

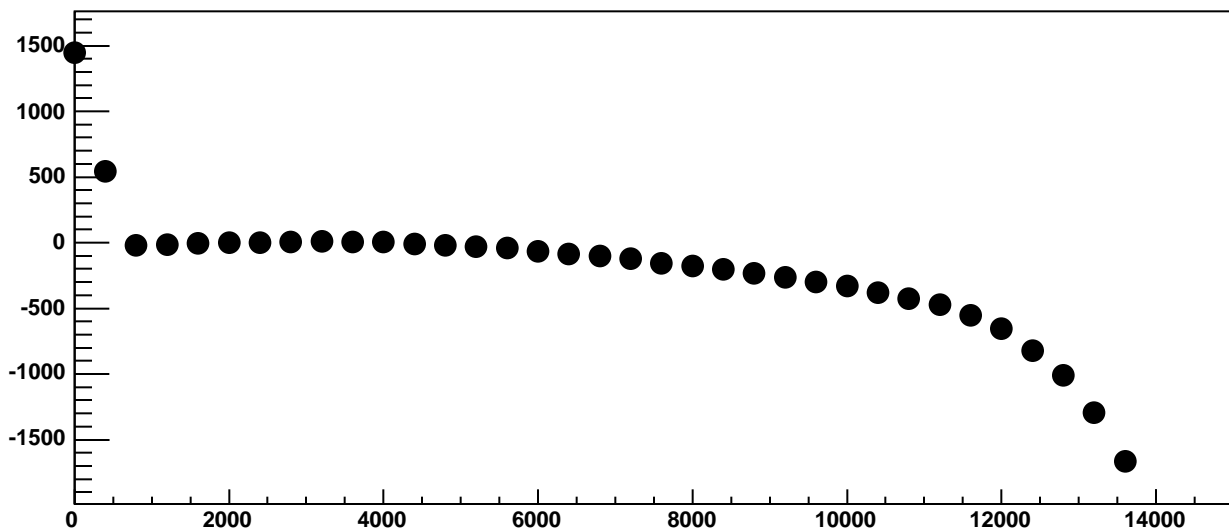
p1

$2.291 \pm 0.0006966$

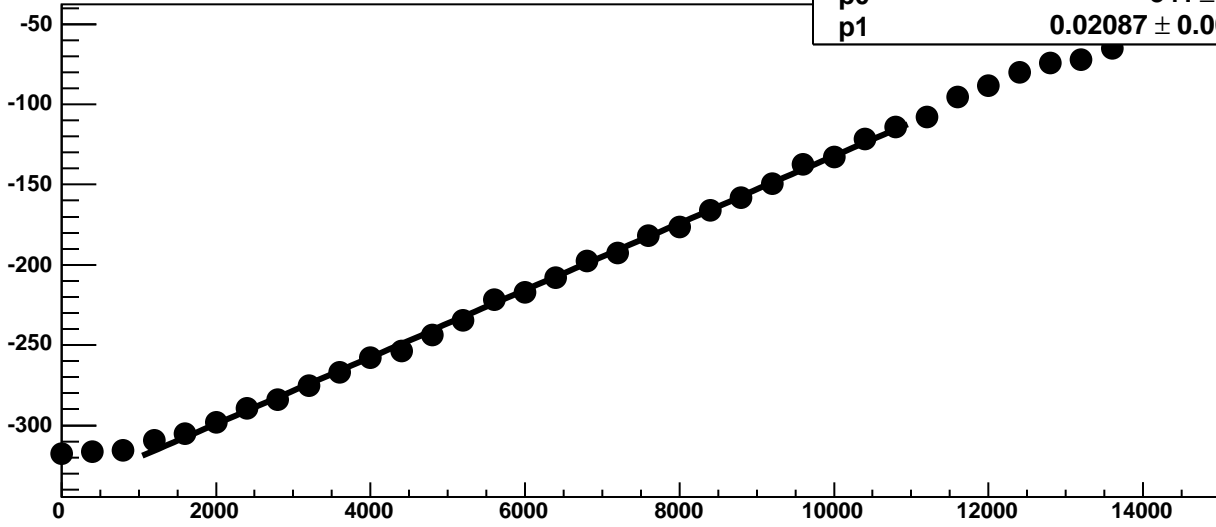
Chip 6, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

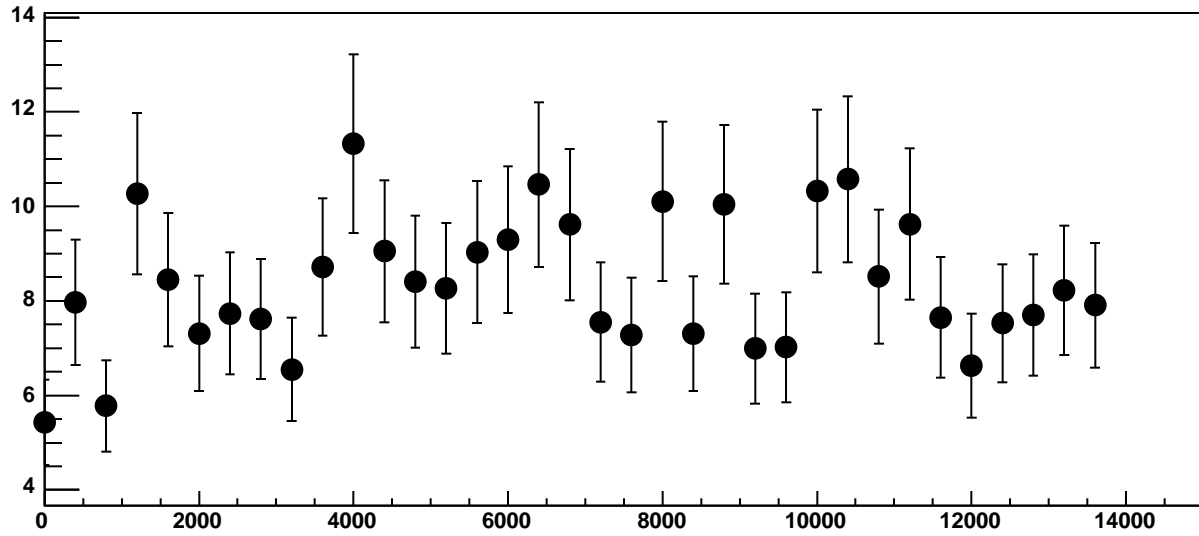


Chip 6, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

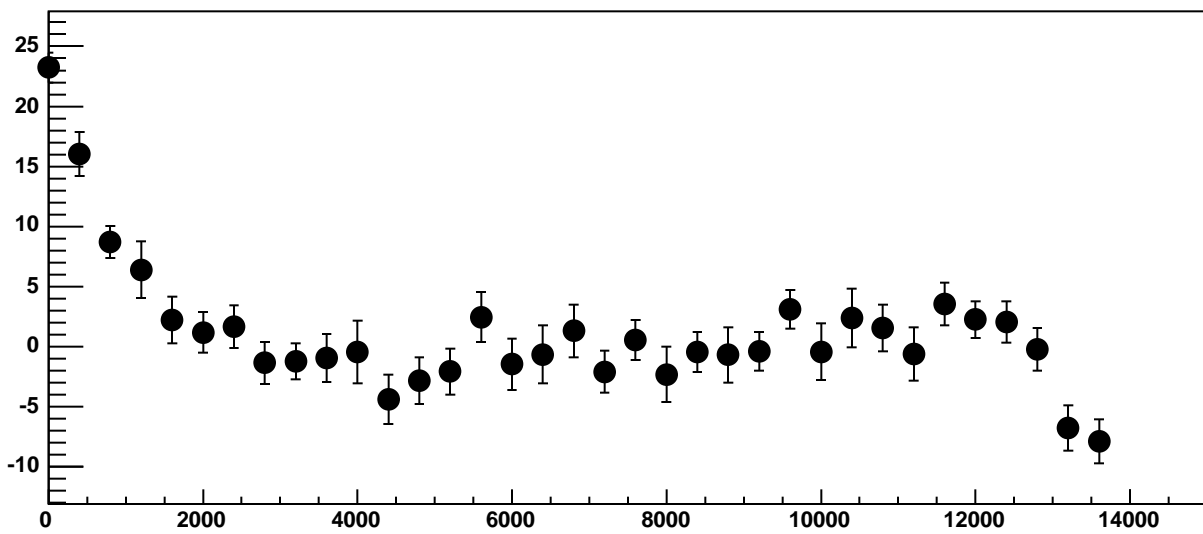


$\chi^2 / \text{ndf}$  29.85 / 23  
p0  $-341 \pm 0.8808$   
p1  $0.02087 \pm 0.0001335$

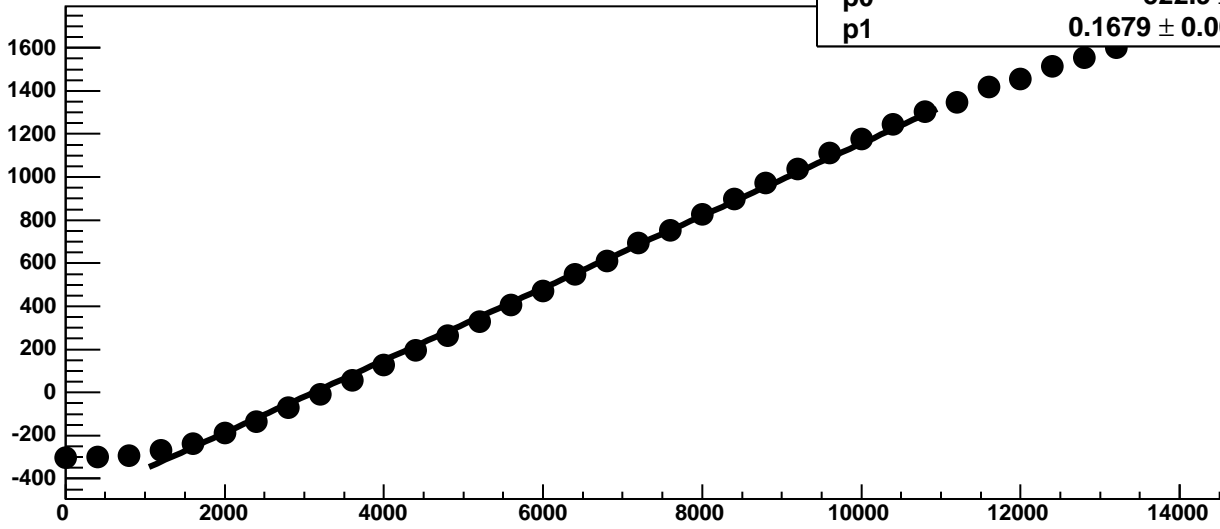
Chip 6, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



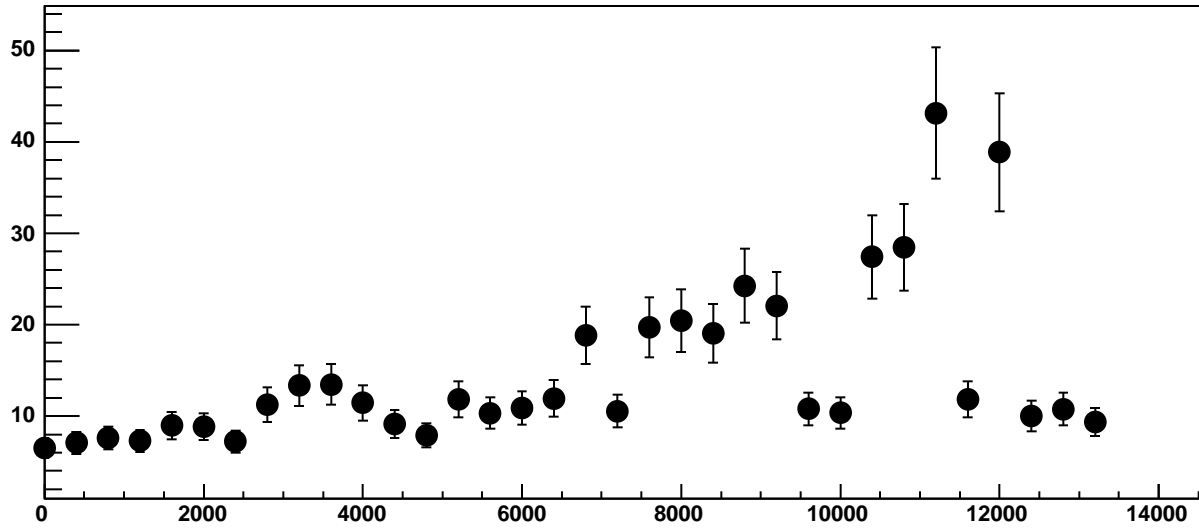
Chip 6, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



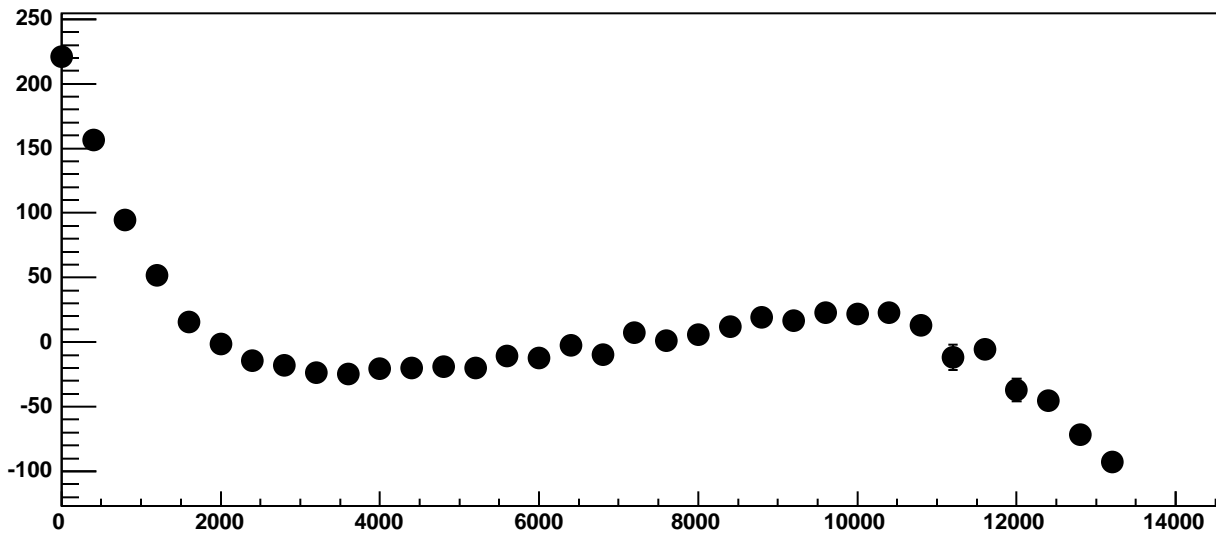
Chip 6, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

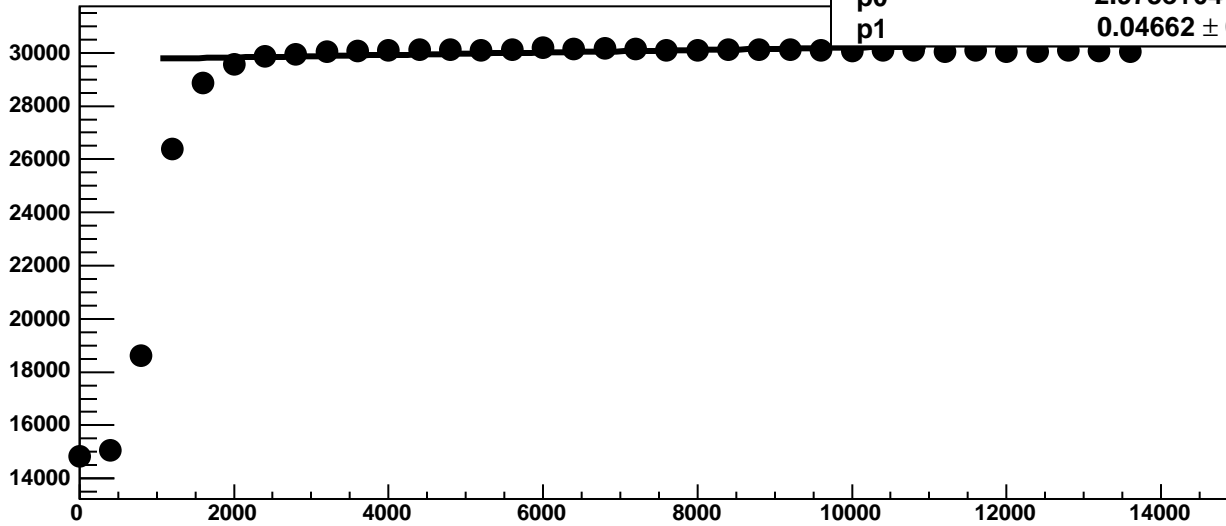


Chip 6, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

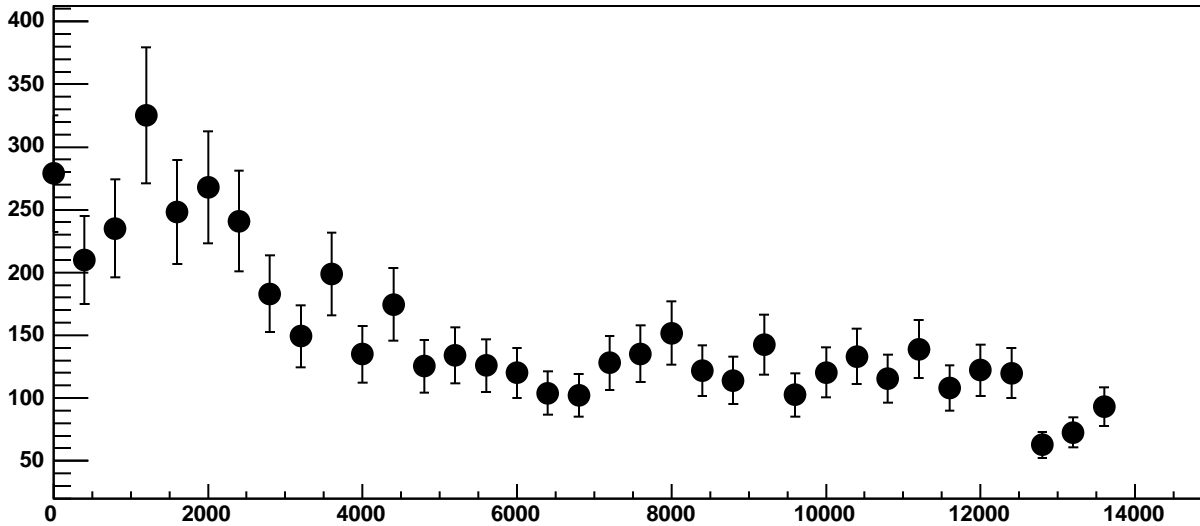


Chip 6, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

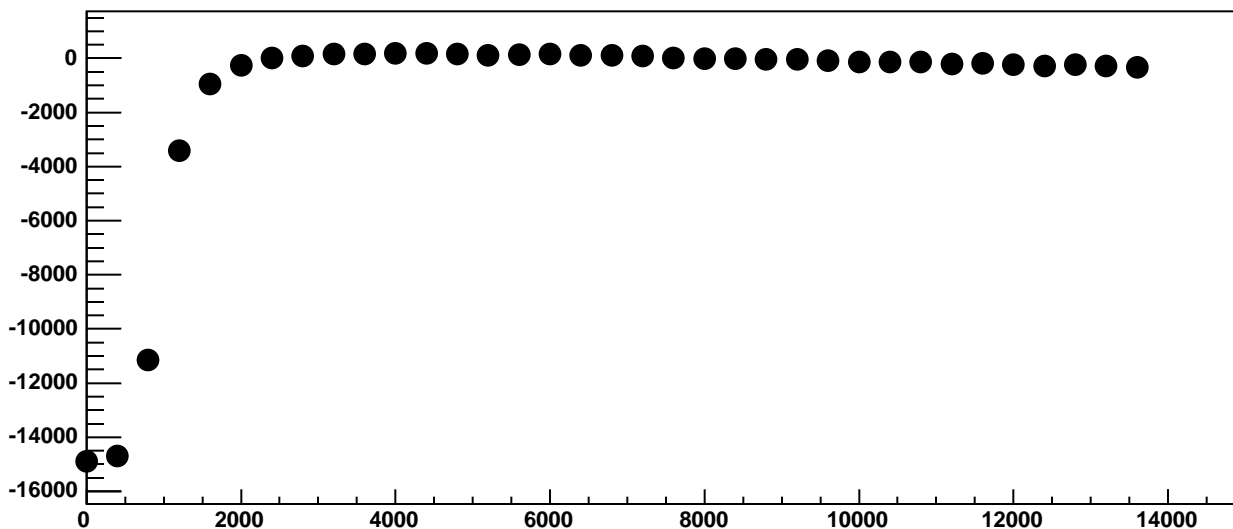
$\chi^2 / \text{ndf}$  2699 / 23  
p0  $2.973\text{e}+04 \pm 18.86$   
p1  $0.04662 \pm 0.00255$



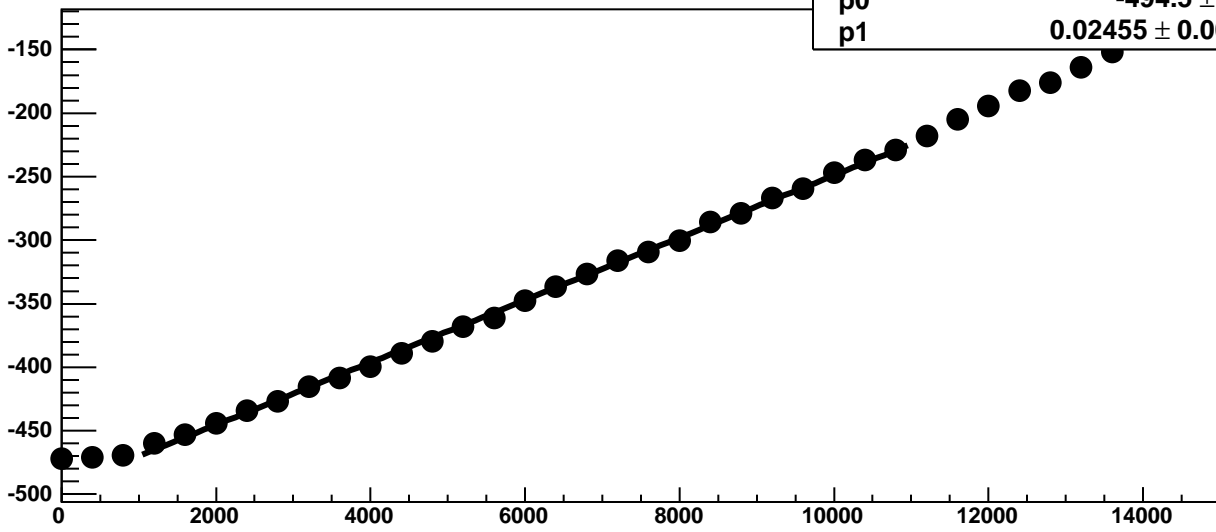
Chip 6, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

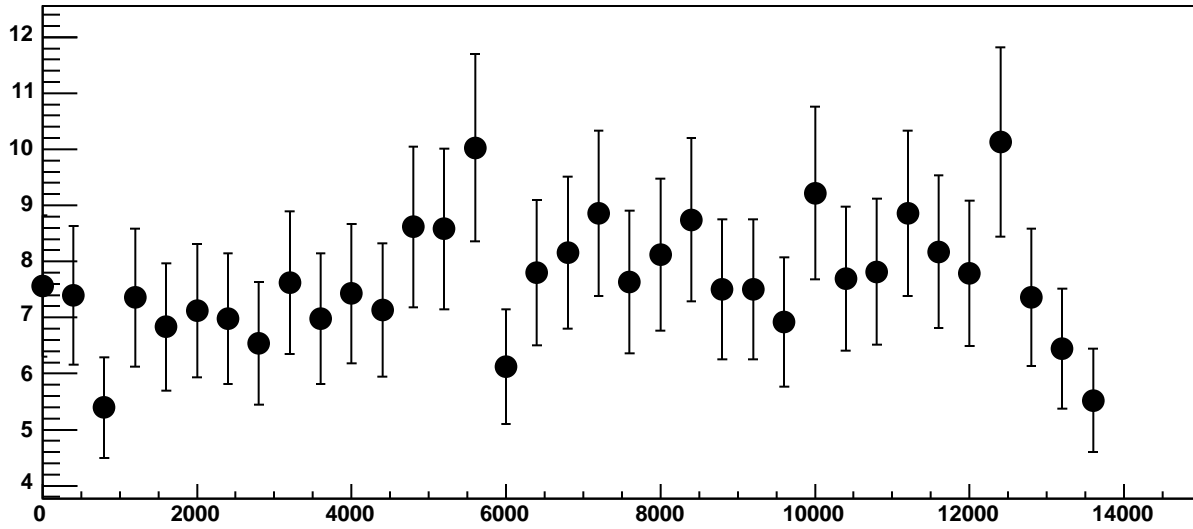


Chip 6, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

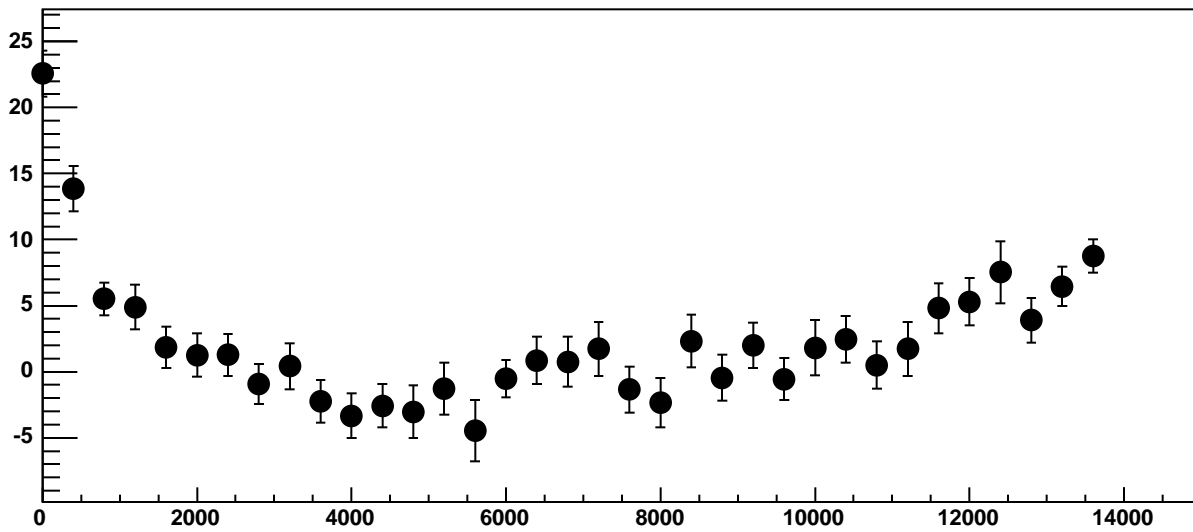


$\chi^2 / \text{ndf}$  35.32 / 23  
p0  $-494.5 \pm 0.7678$   
p1  $0.02455 \pm 0.0001187$

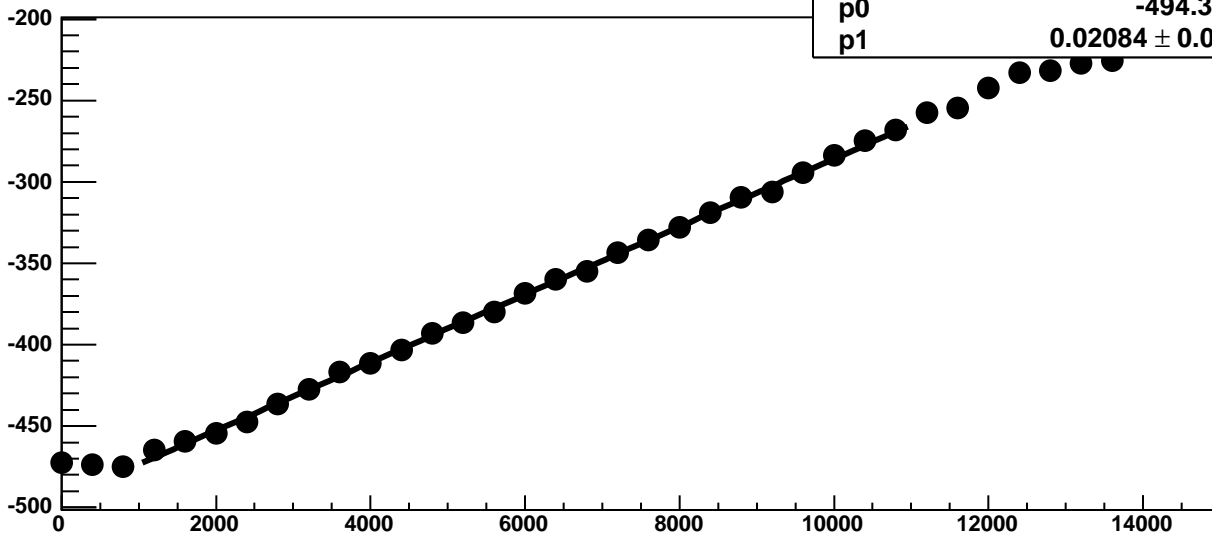
Chip 6, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



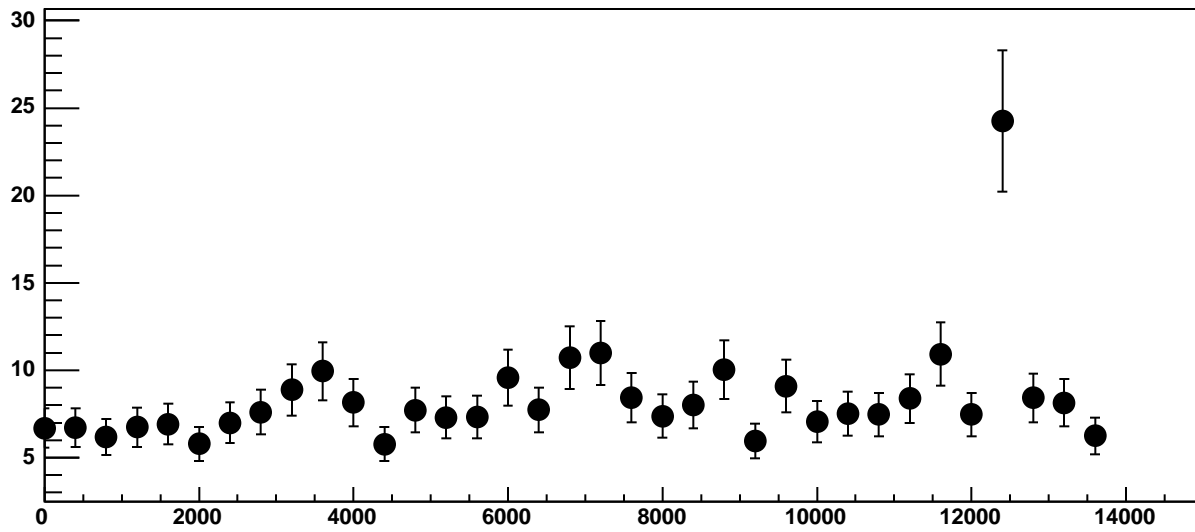
Chip 6, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



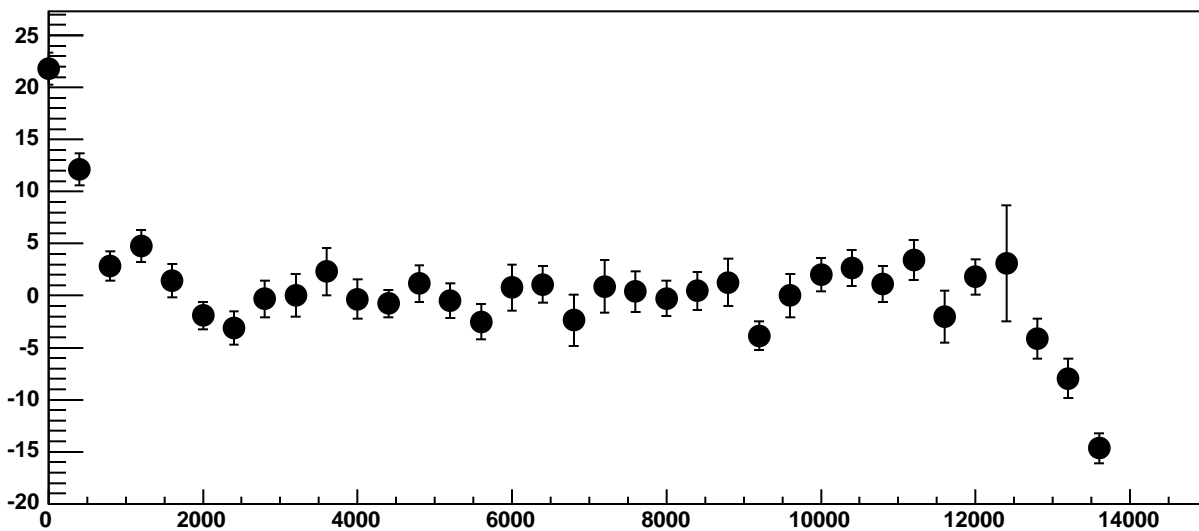
Chip 6, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



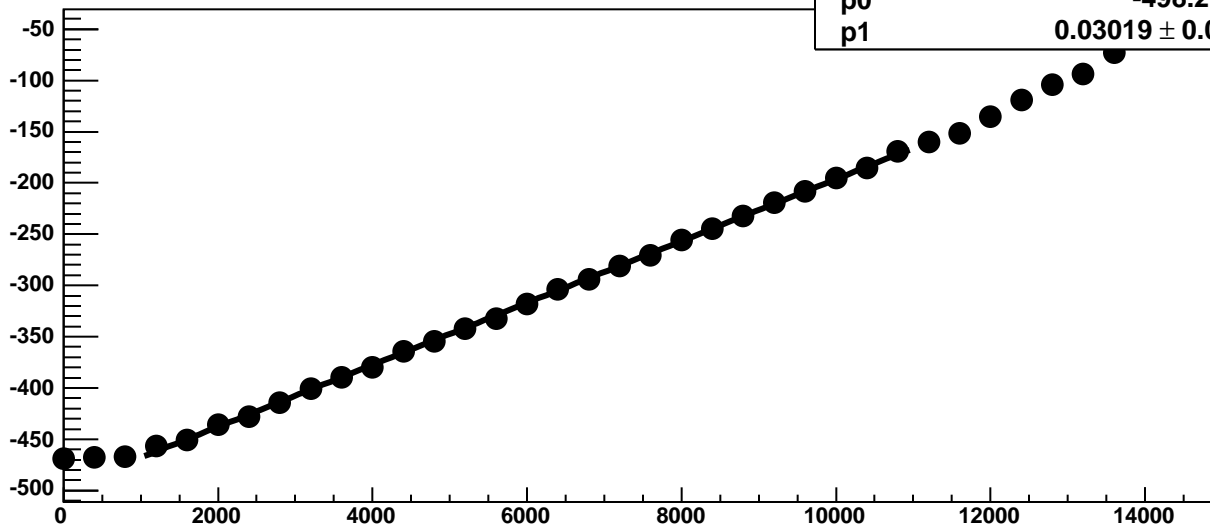
Chip 6, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



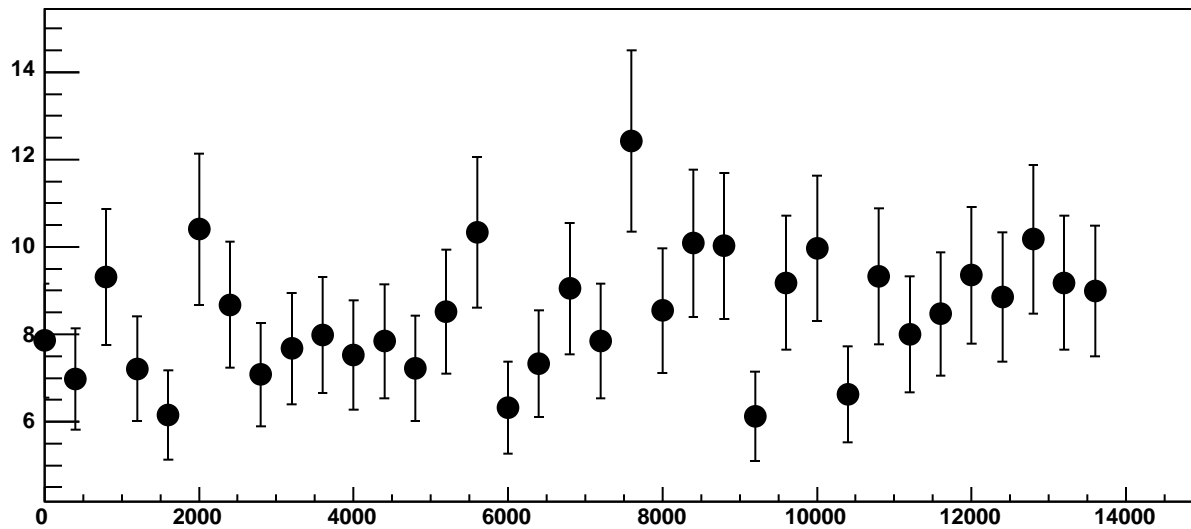
Chip 6, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



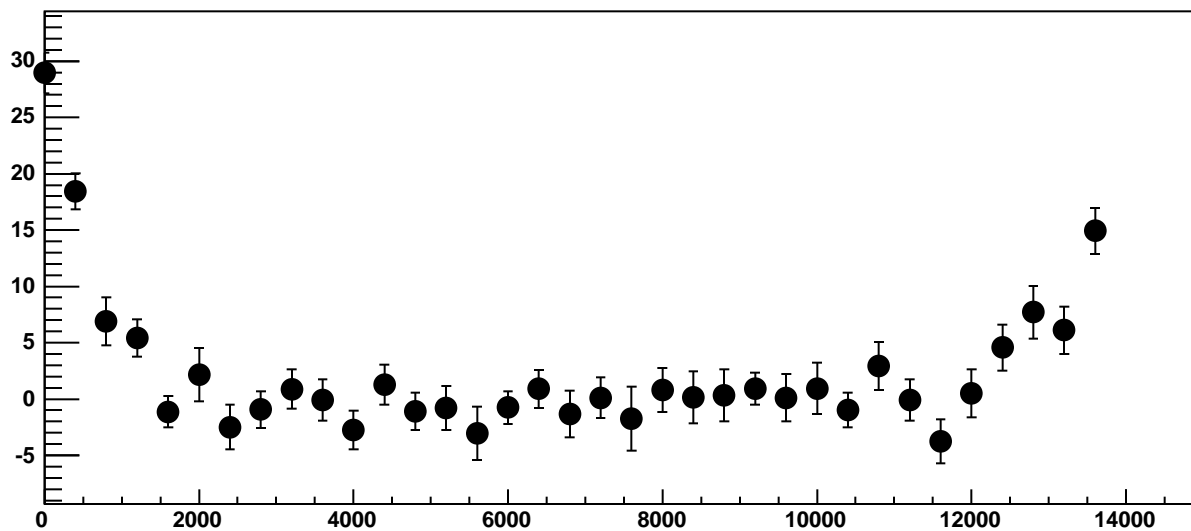
Chip 6, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



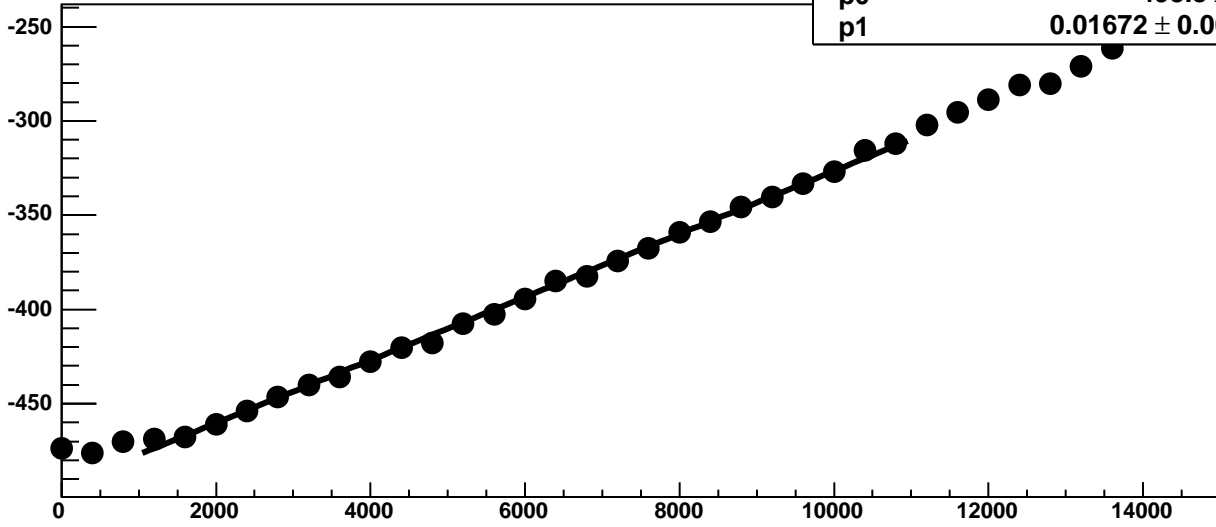
Chip 6, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

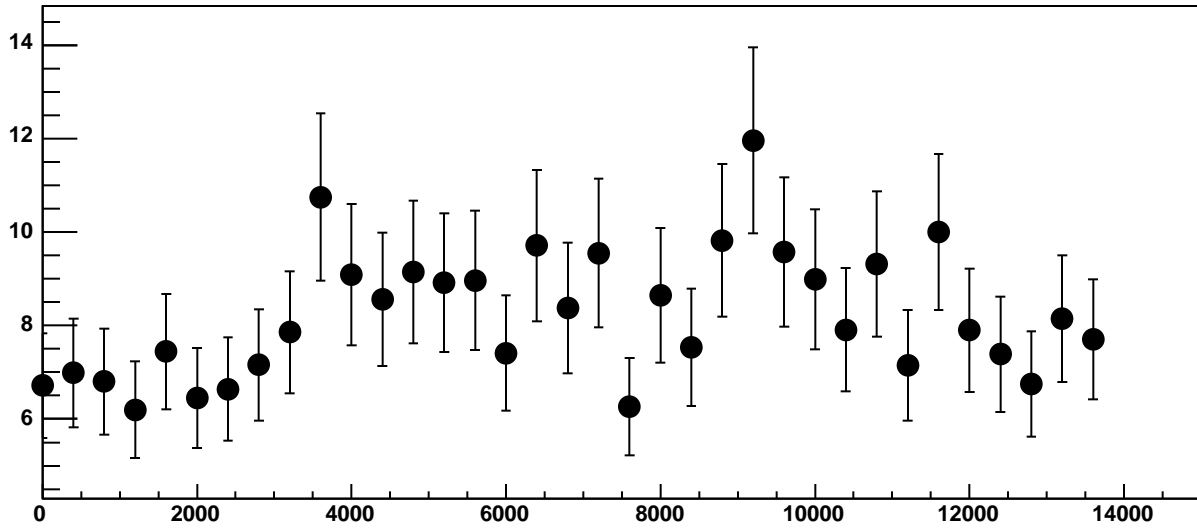


Chip 6, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

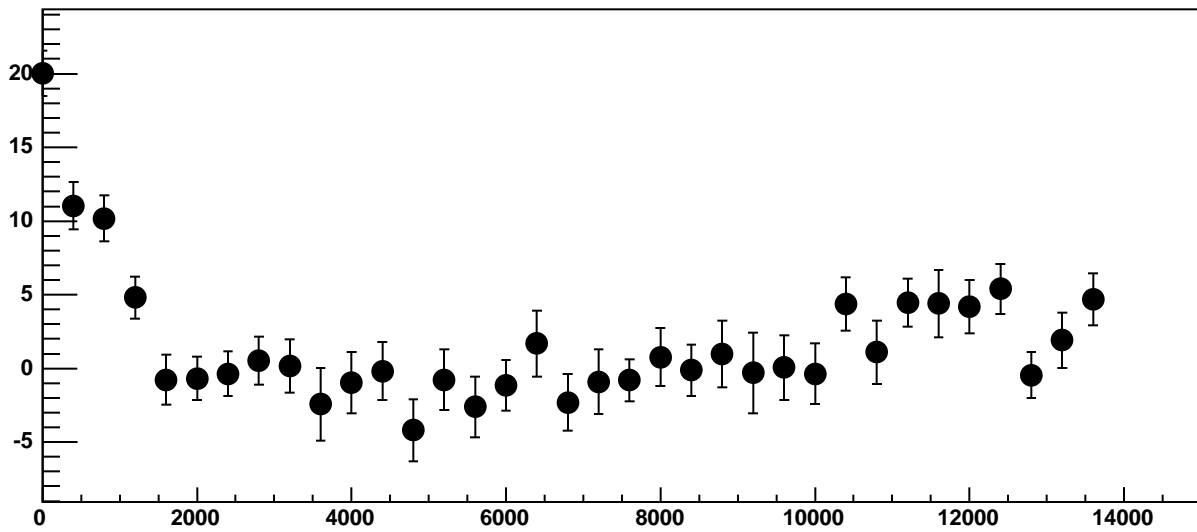


$\chi^2 / \text{ndf}$  28.36 / 23  
p0  $-493.8 \pm 0.787$   
p1  $0.01672 \pm 0.0001255$

Chip 6, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

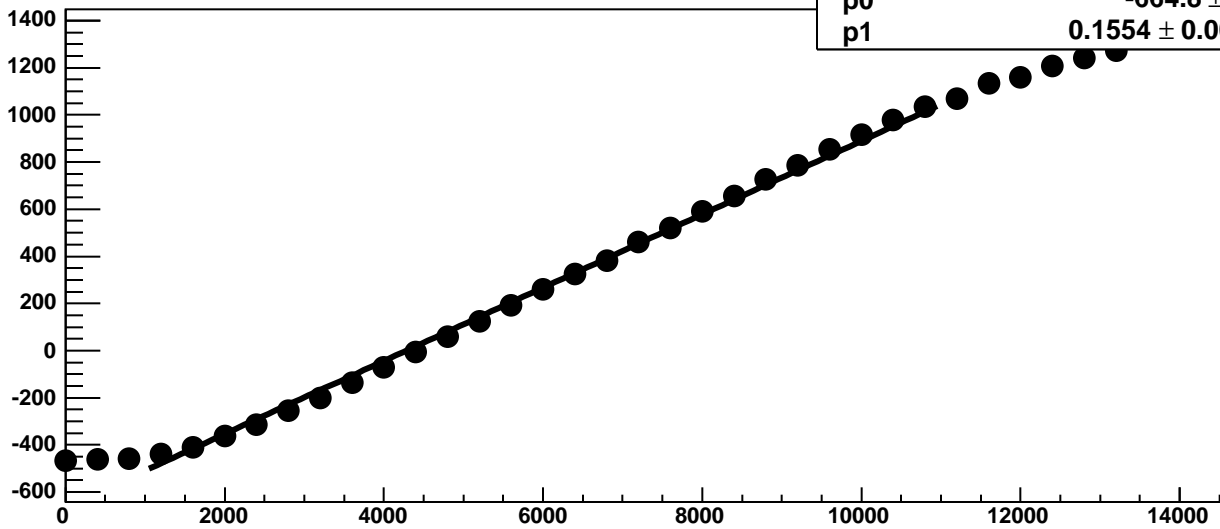


Chip 6, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

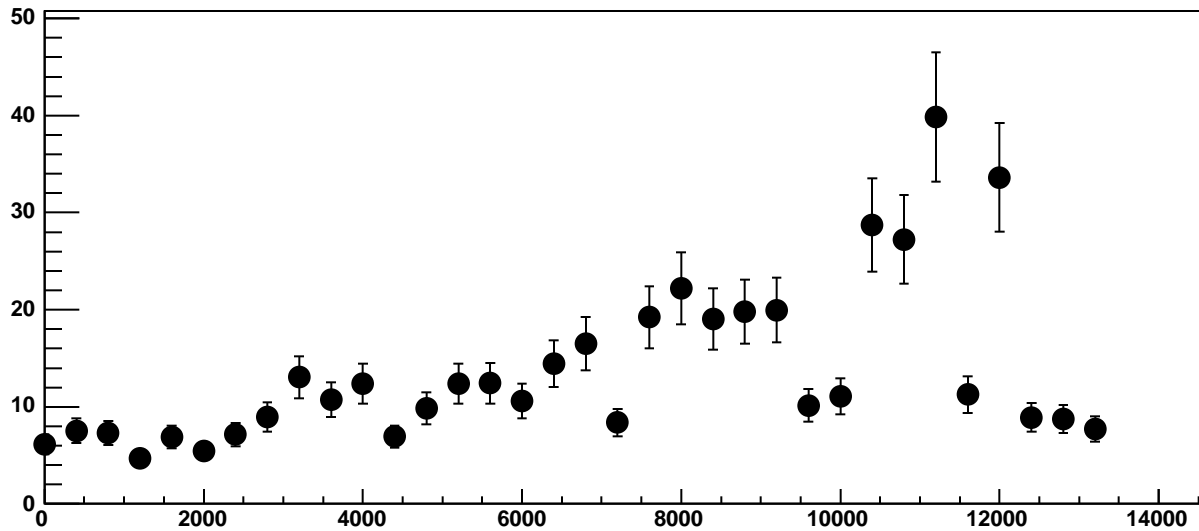




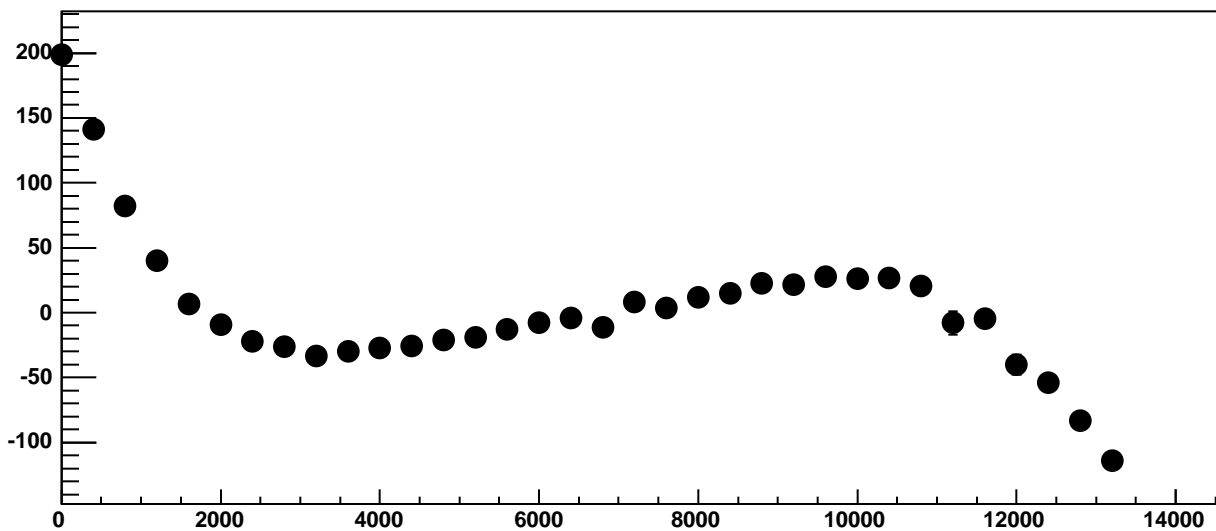
Chip 6, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

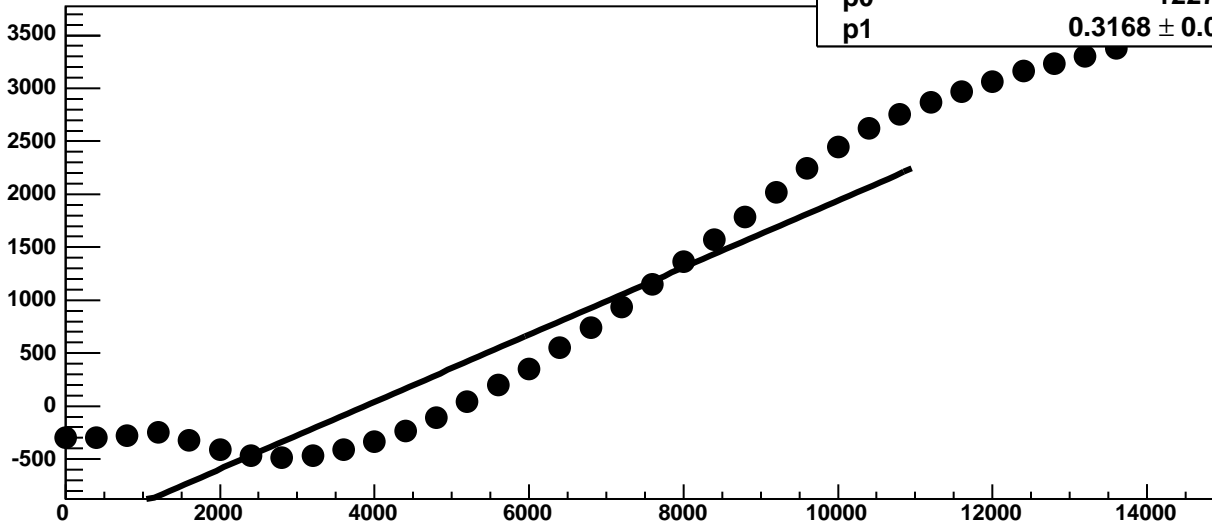


Chip 6, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

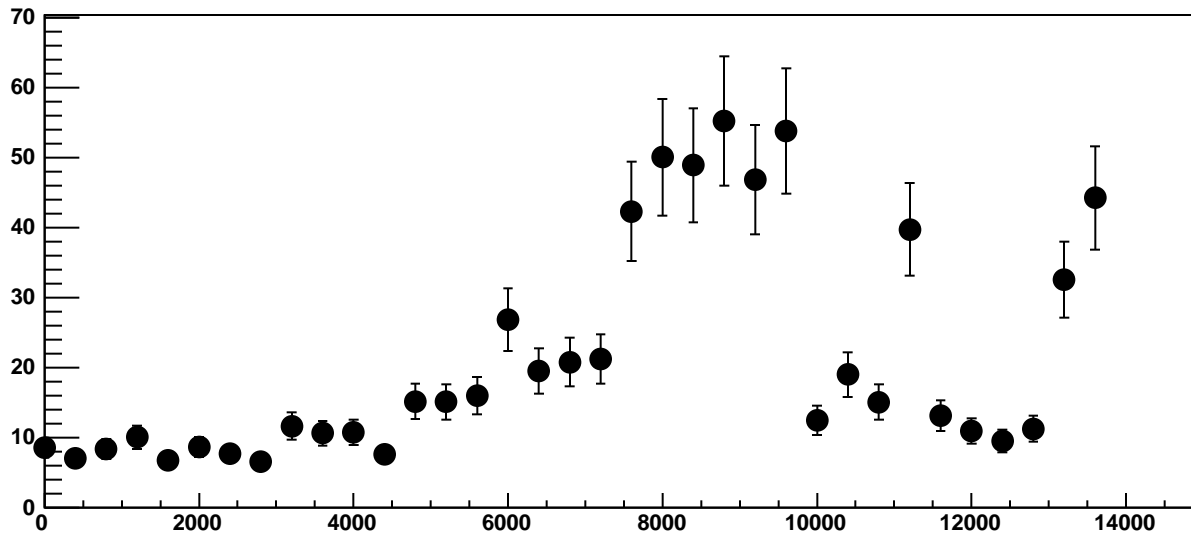


Chip 6, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

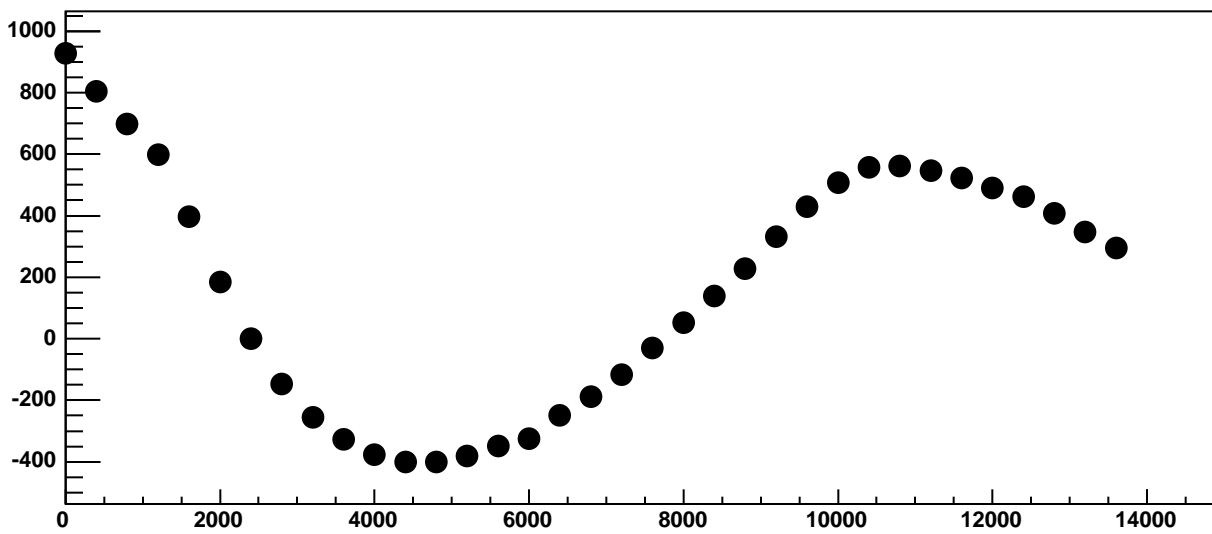
$\chi^2 / \text{ndf}$  3.717e+05 / 23  
p0 -1227 ± 1.041  
p1 0.3168 ± 0.0002277



Chip 6, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

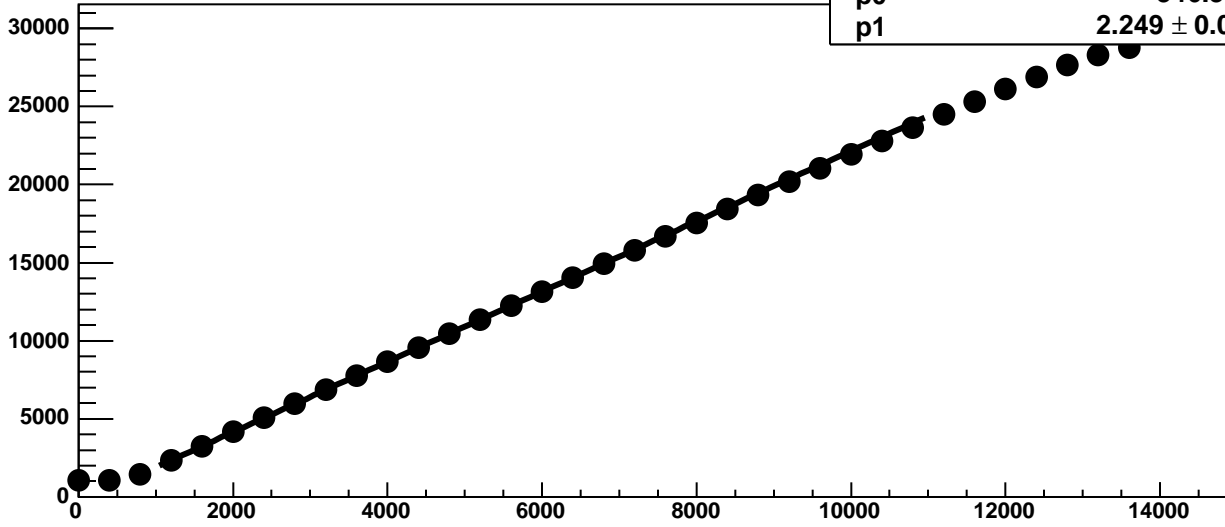


Chip 6, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

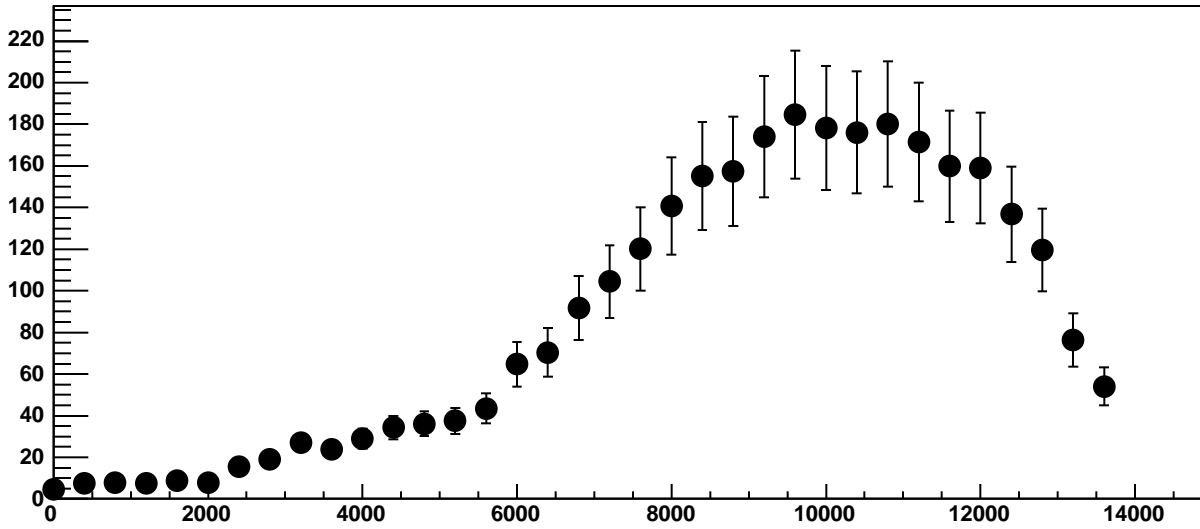


Chip 6, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

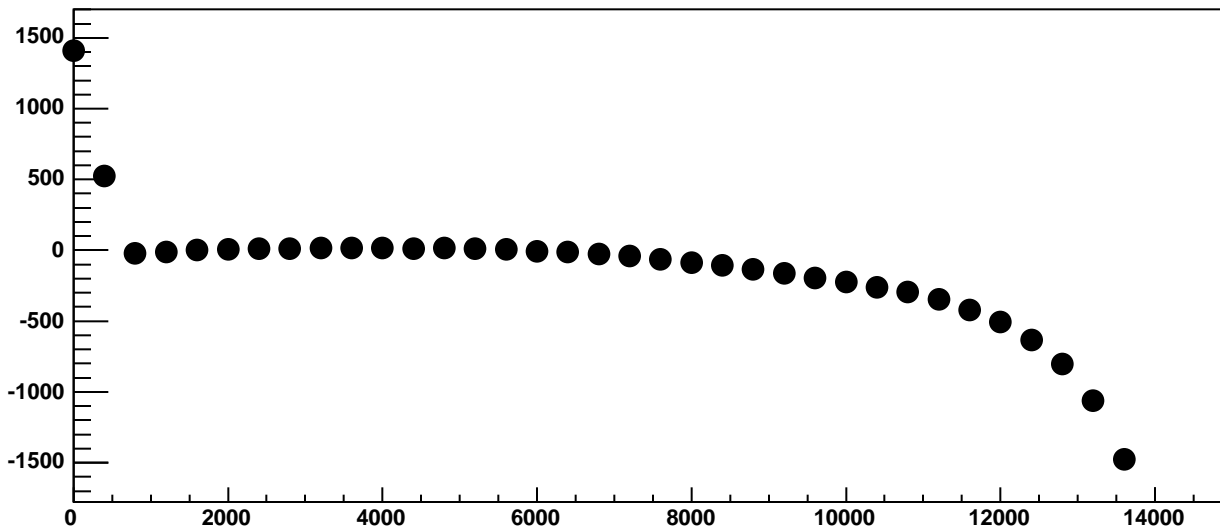
$\chi^2 / \text{ndf}$  331.9 / 23  
p0  $-346.3 \pm 1.905$   
p1  $2.249 \pm 0.0008003$



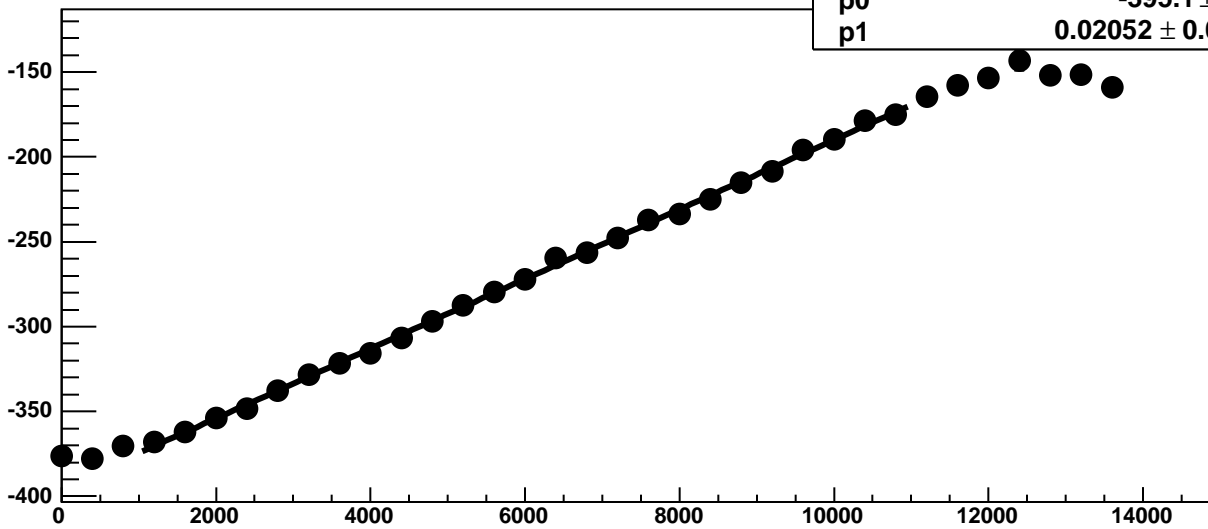
Chip 6, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

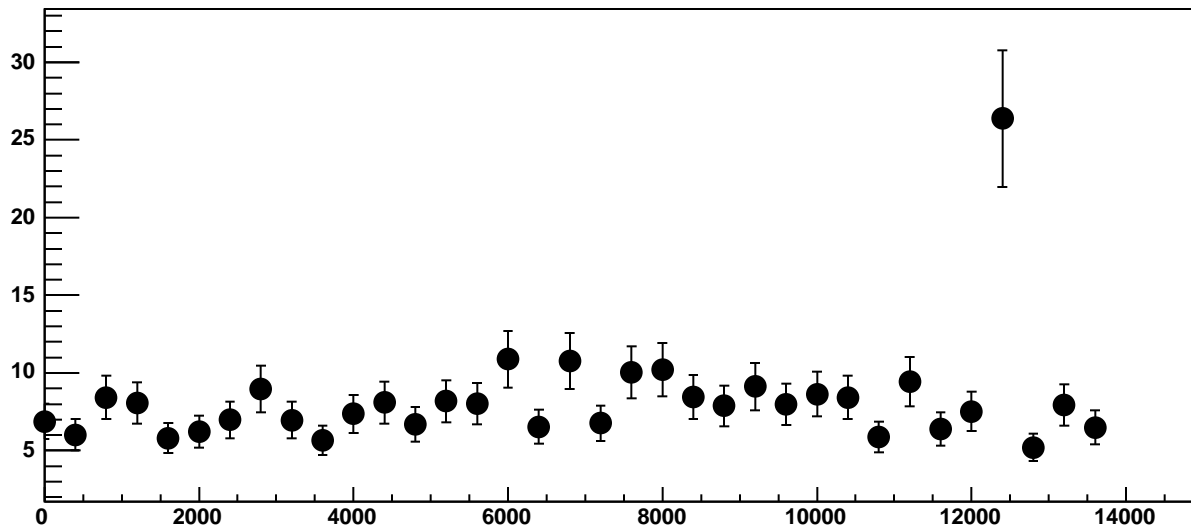


Chip 6, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

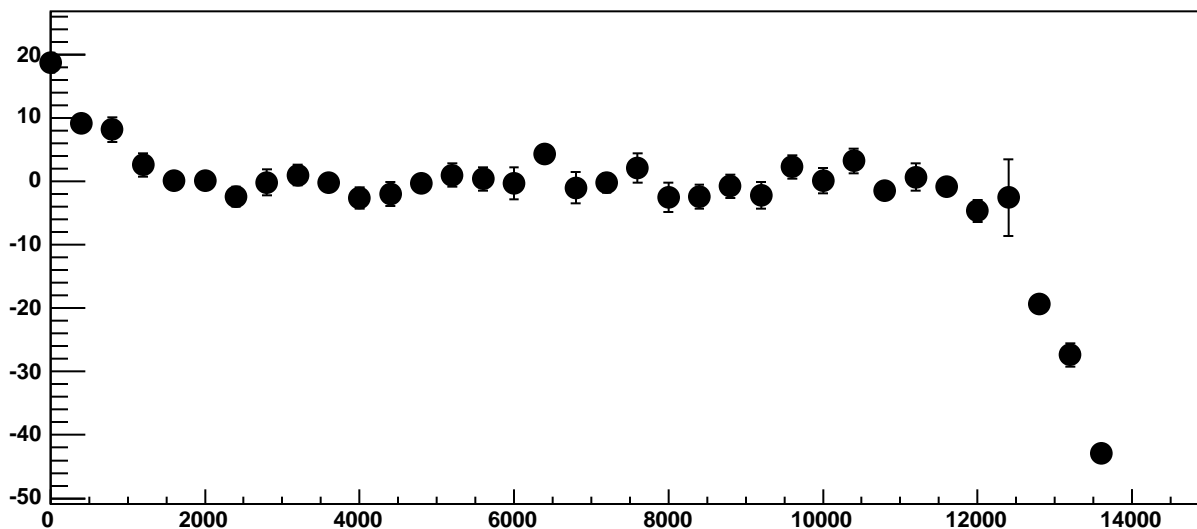


$\chi^2 / \text{ndf}$  27.61 / 23  
p0  $-395.1 \pm 0.7369$   
p1  $0.02052 \pm 0.0001151$

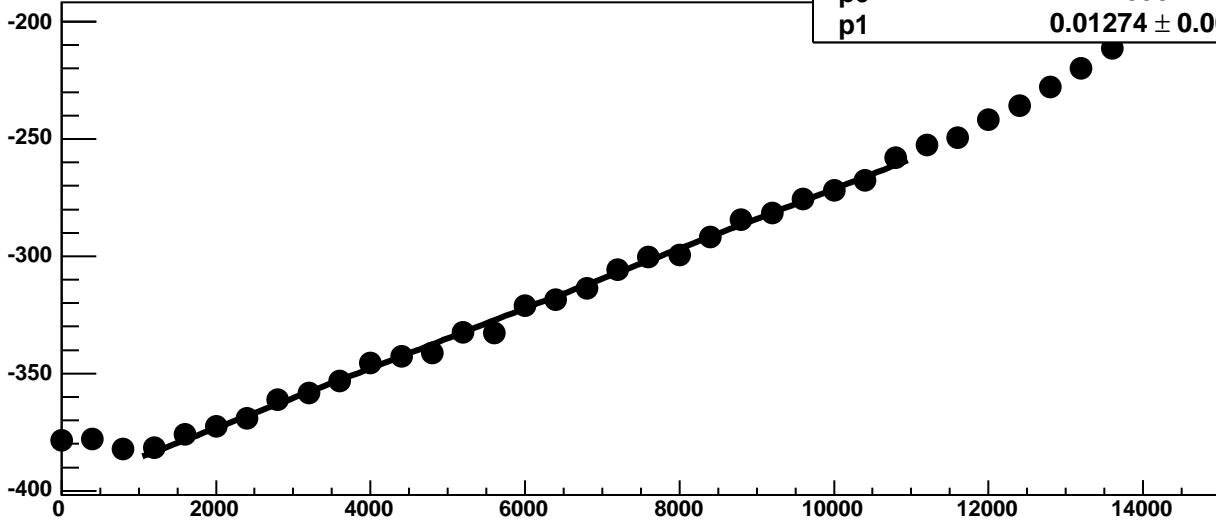
Chip 6, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

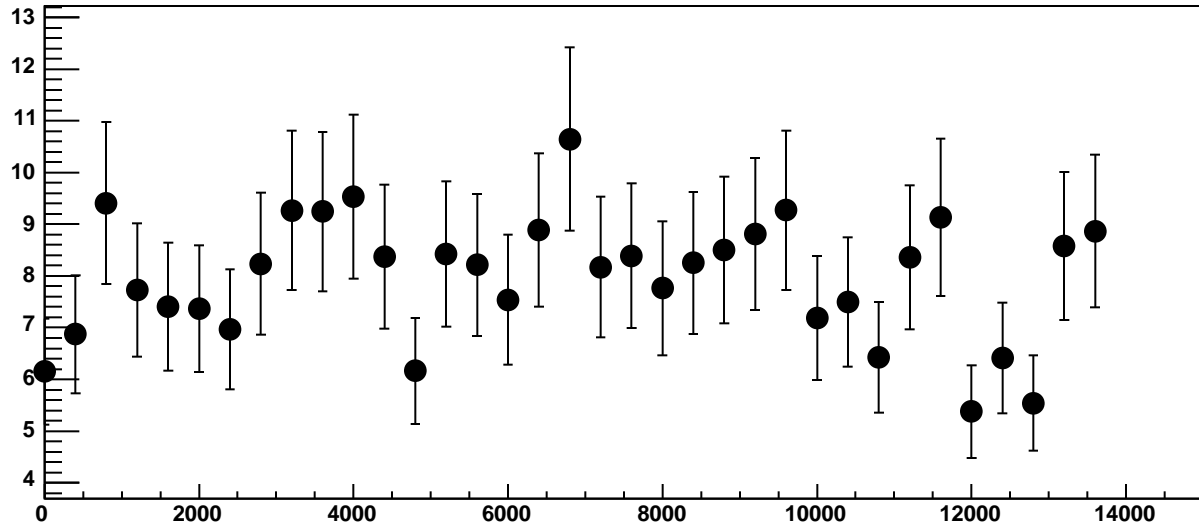


Chip 6, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

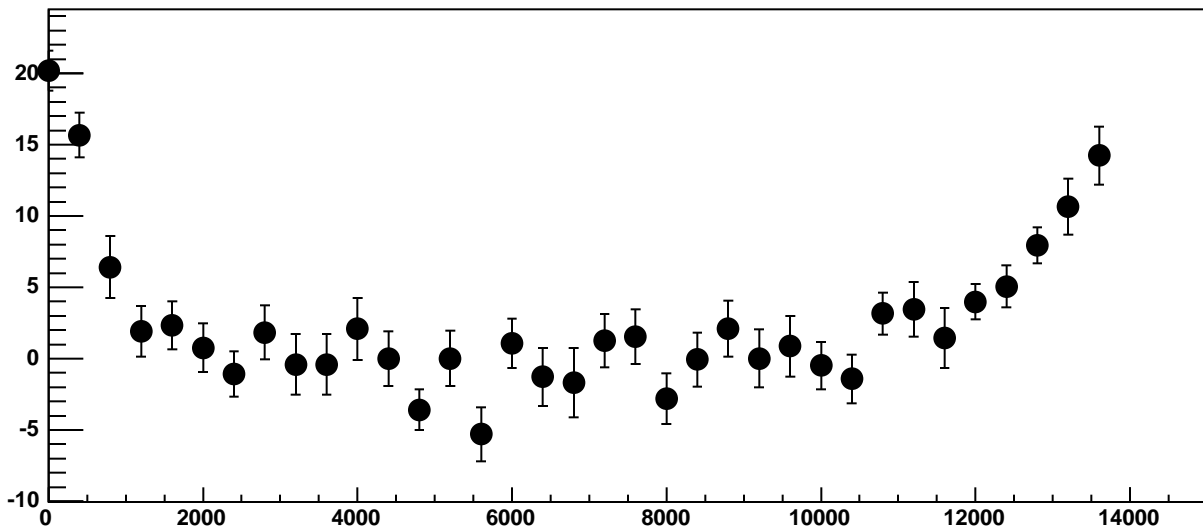


$\chi^2 / \text{ndf}$  31.49 / 23  
p0  $-398.7 \pm 0.8178$   
p1  $0.01274 \pm 0.0001216$

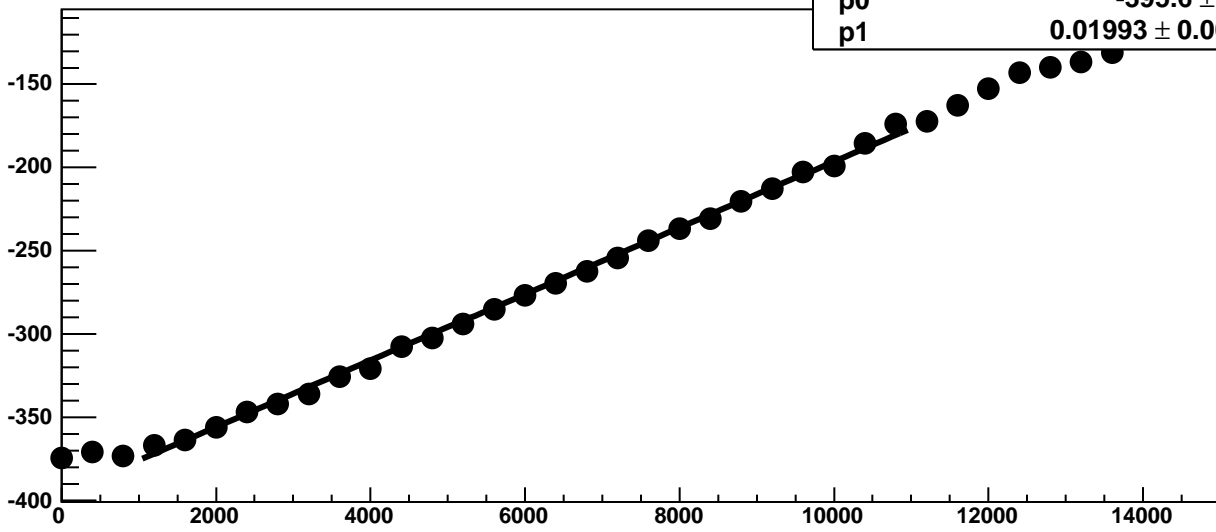
Chip 6, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

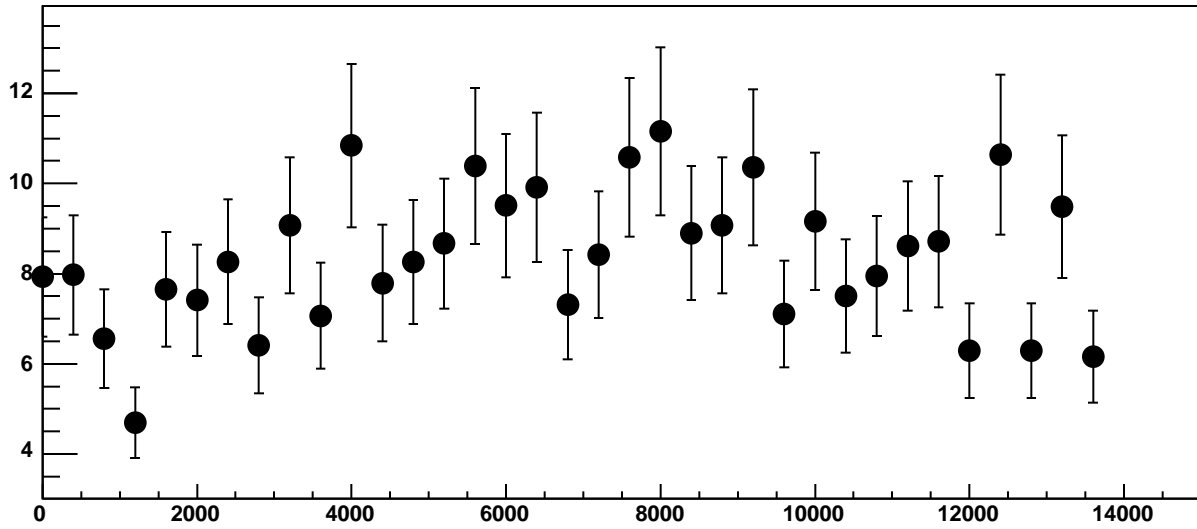


Chip 6, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

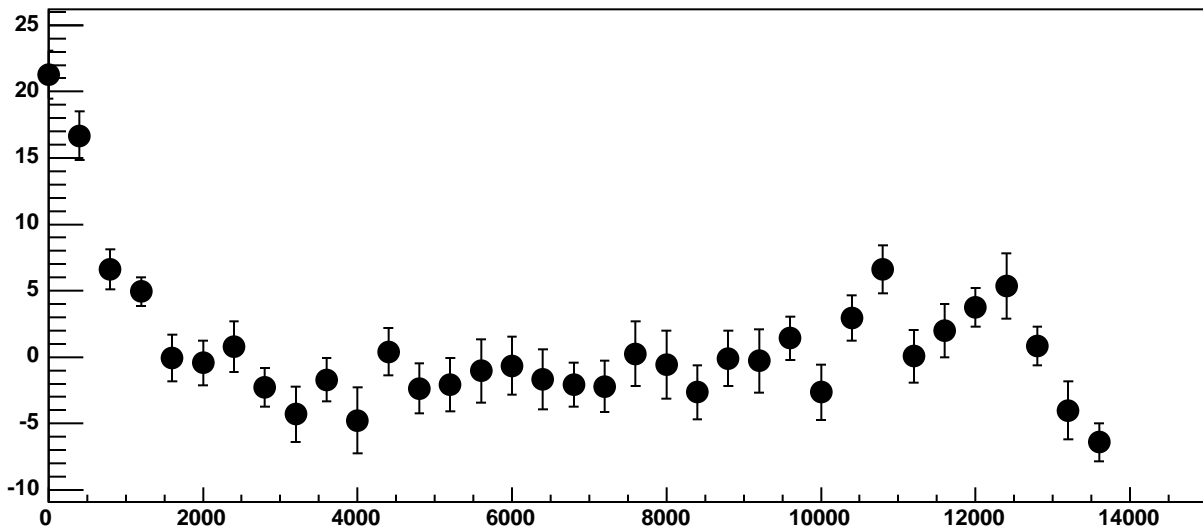


$\chi^2 / \text{ndf}$  59.2 / 23  
p0  $-395.6 \pm 0.7379$   
p1  $0.01993 \pm 0.0001176$

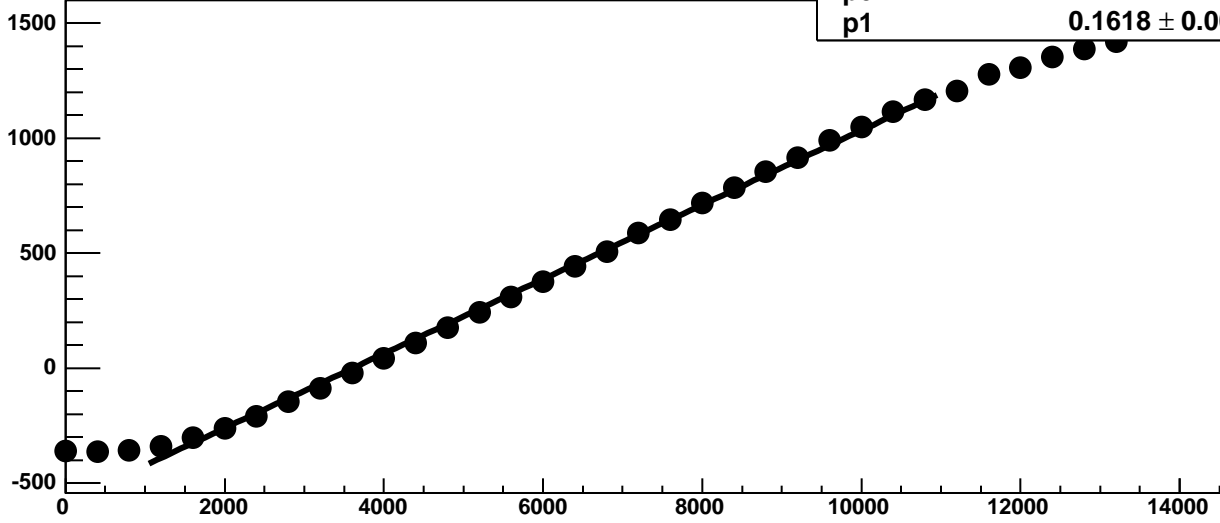
Chip 6, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



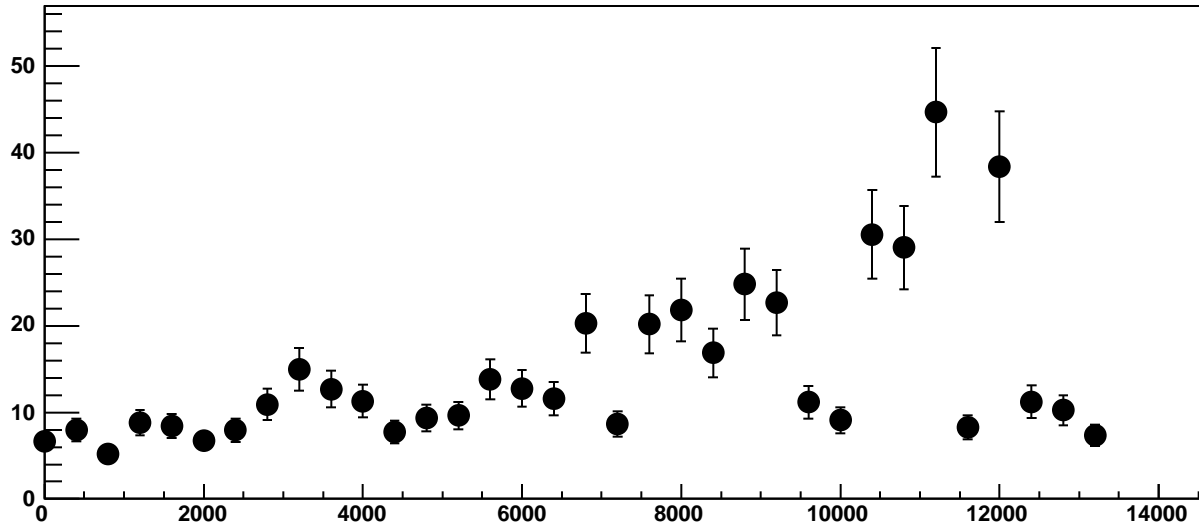
Chip 6, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



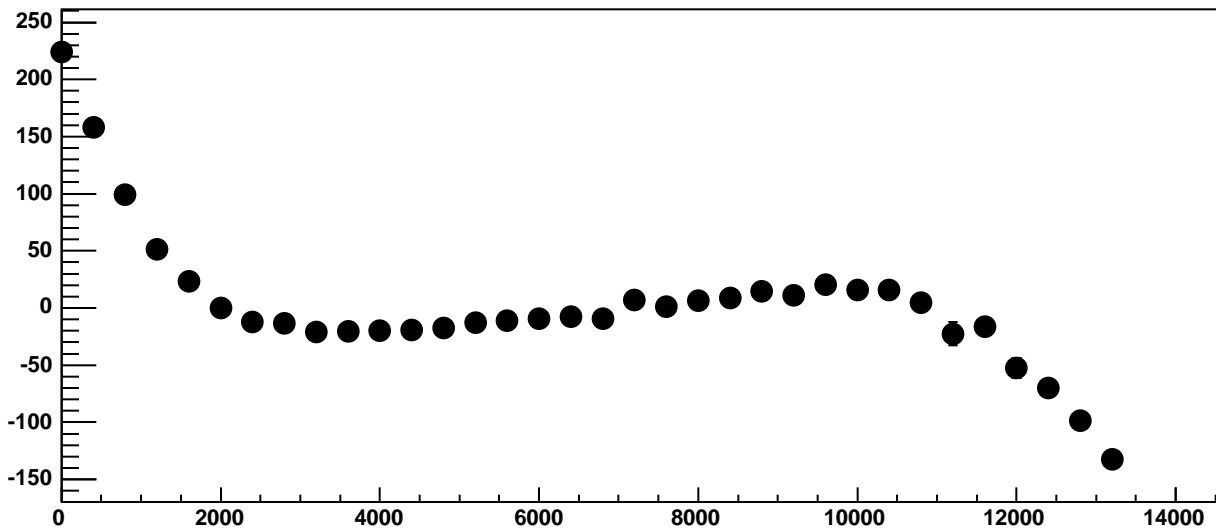
Chip 6, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



Chip 6, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

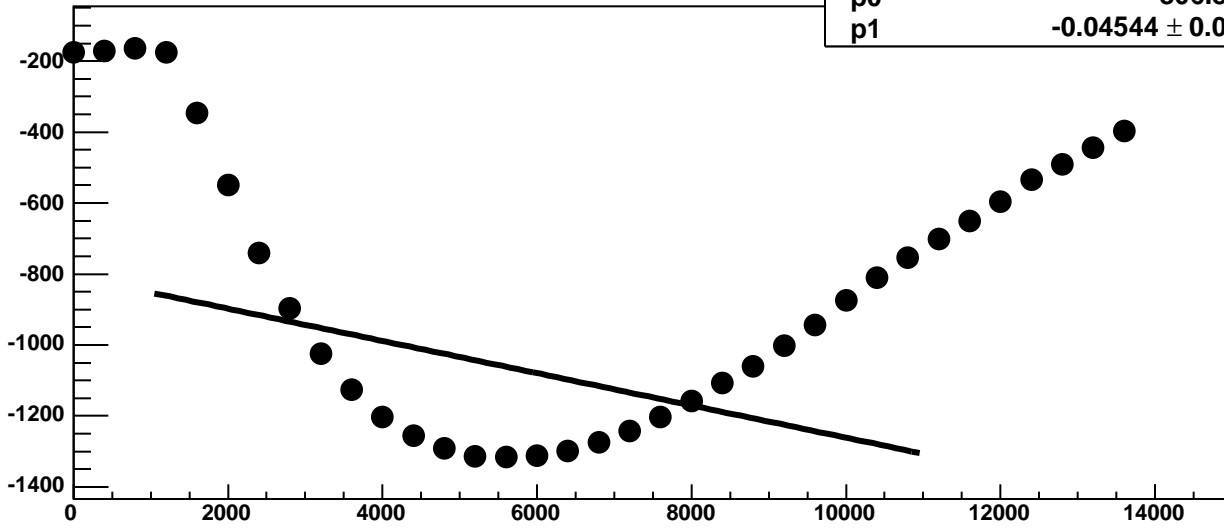


Chip 6, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

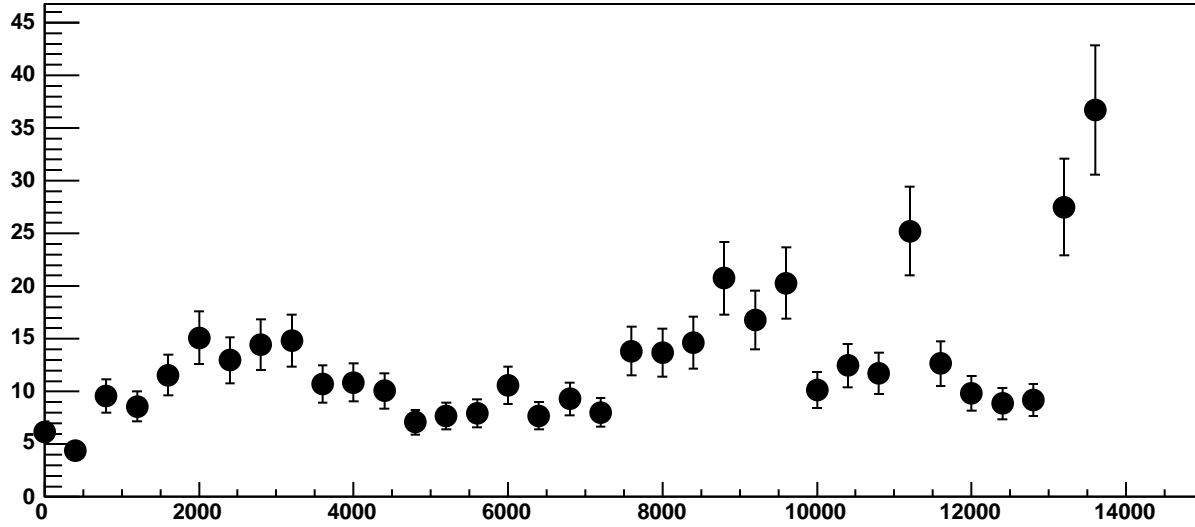


Chip 6, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

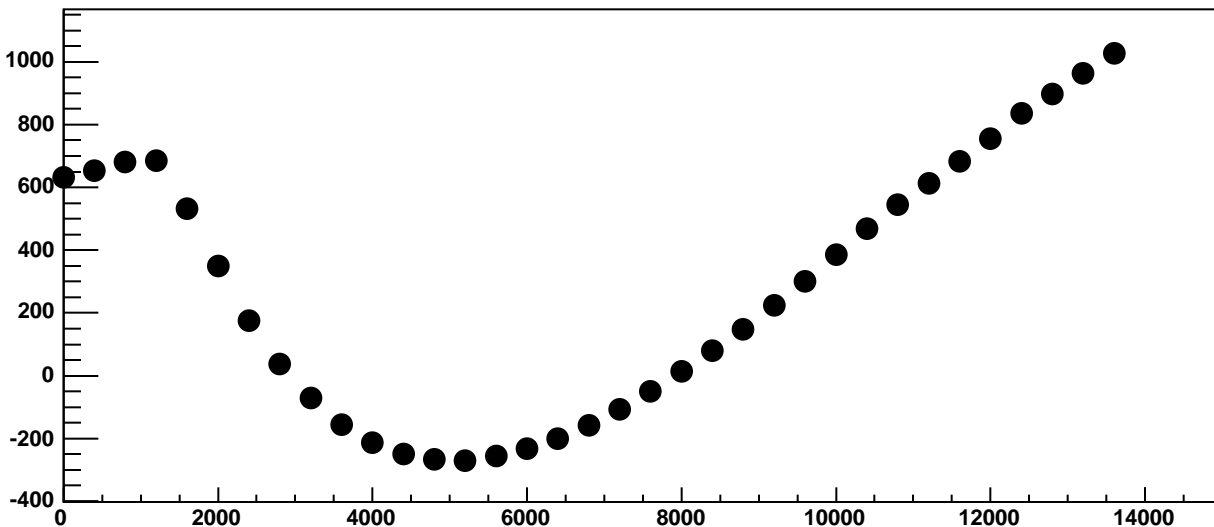
$\chi^2 / \text{ndf}$  4.041e+05 / 23  
p0 -806.8 ± 1.19  
p1 -0.04544 ± 0.0001908



Chip 6, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

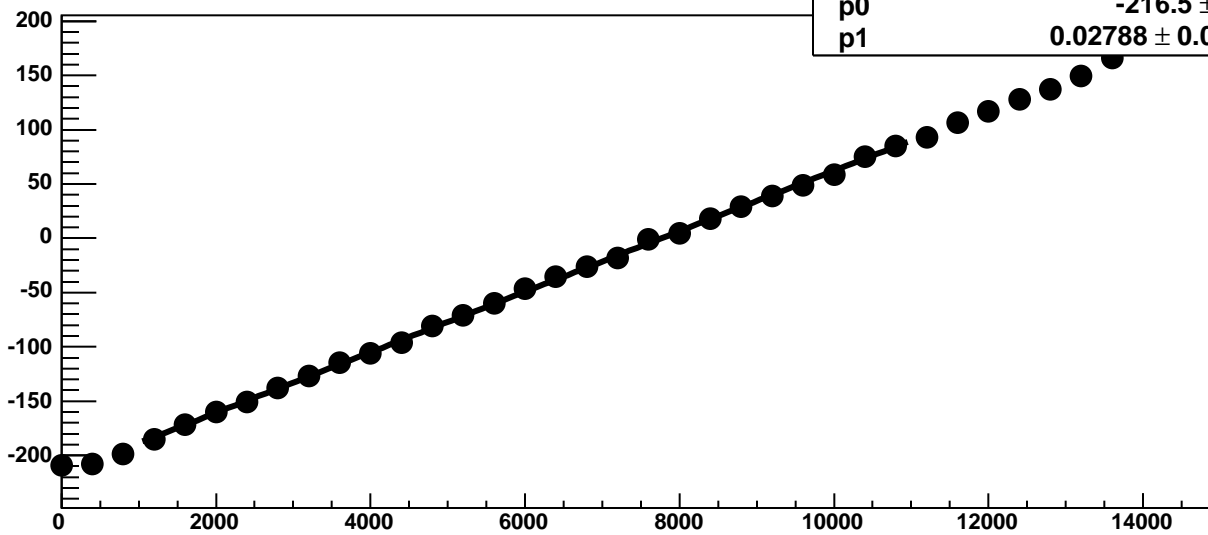


Chip 6, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



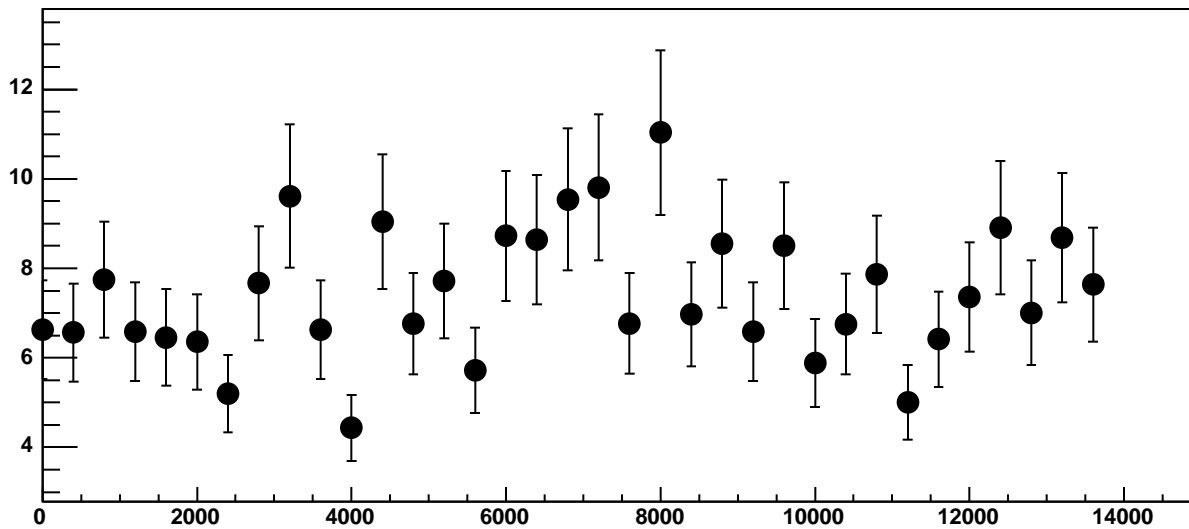


Chip 6, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

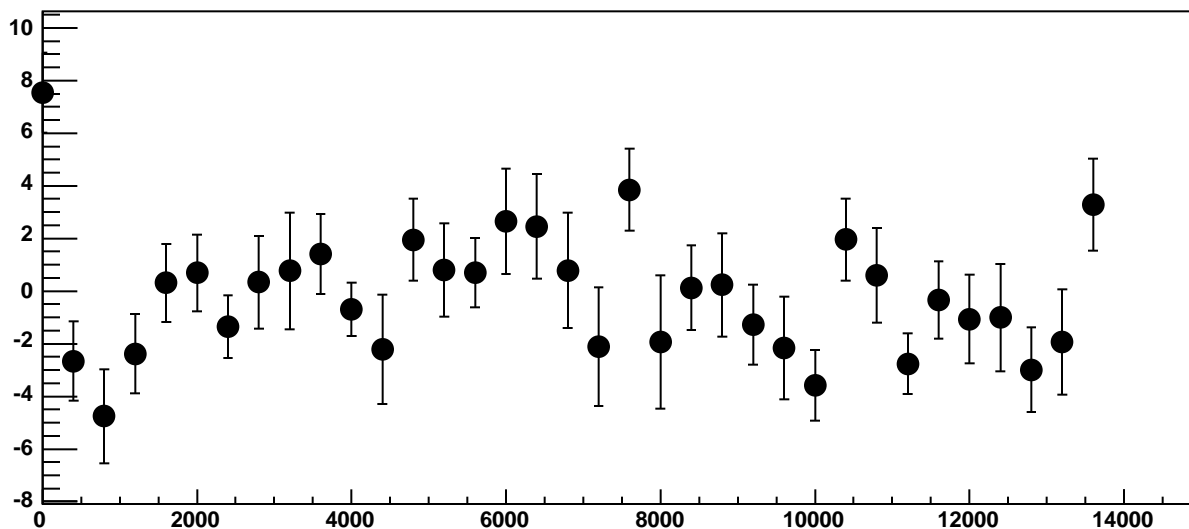


$\chi^2 / \text{ndf}$  30.45 / 23  
p0  $-216.5 \pm 0.6904$   
p1  $0.02788 \pm 0.0001083$

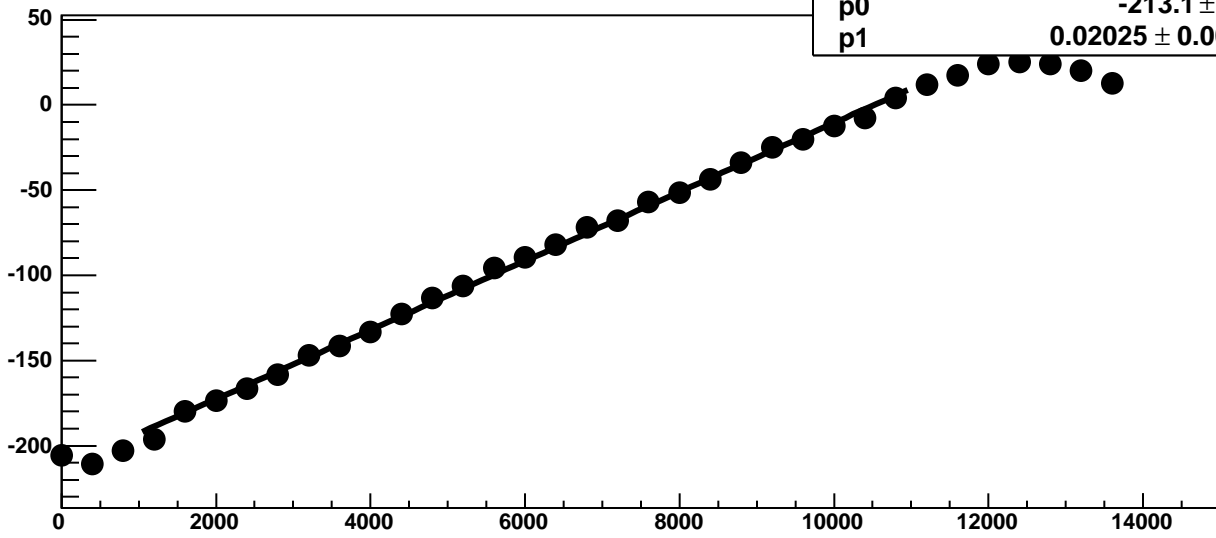
Chip 6, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



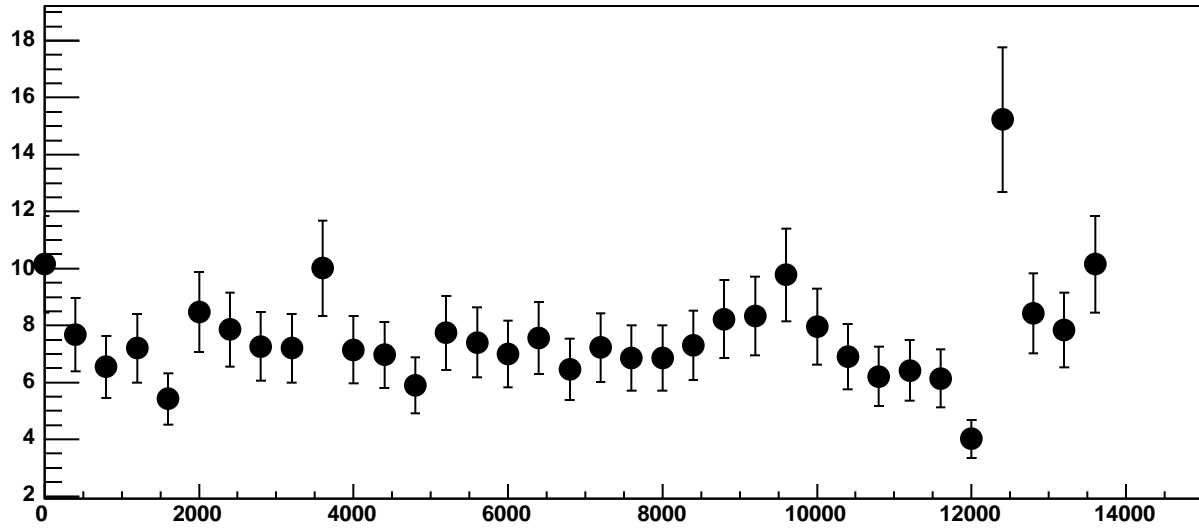
Chip 6, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



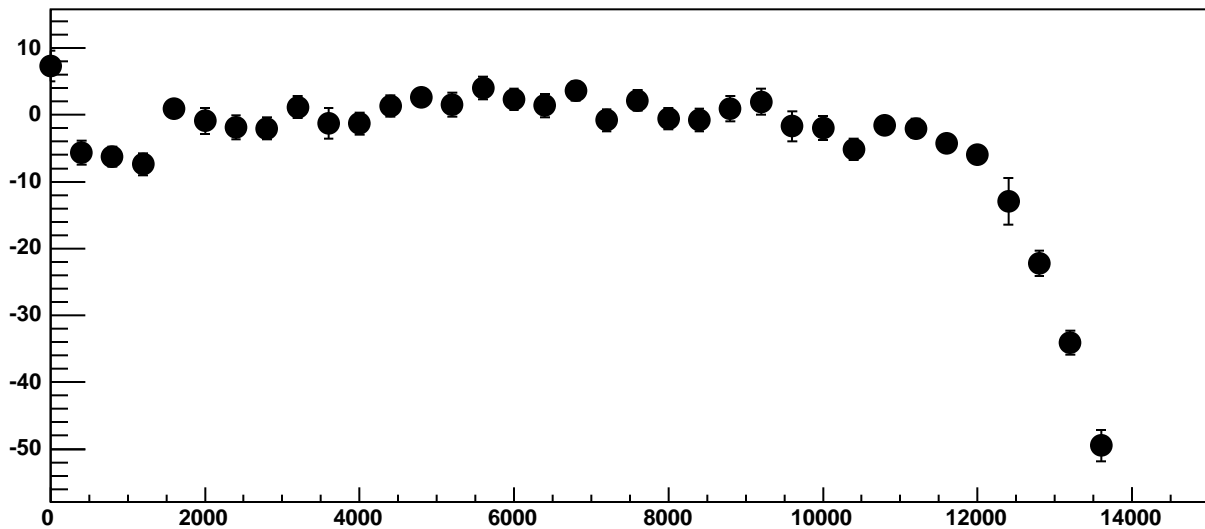
Chip 6, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



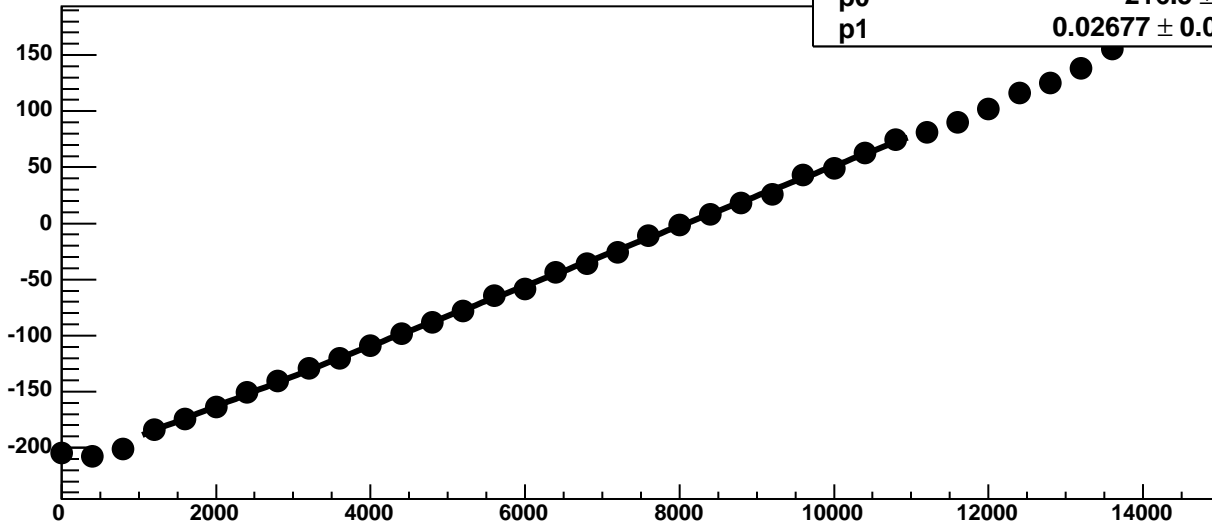
Chip 6, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

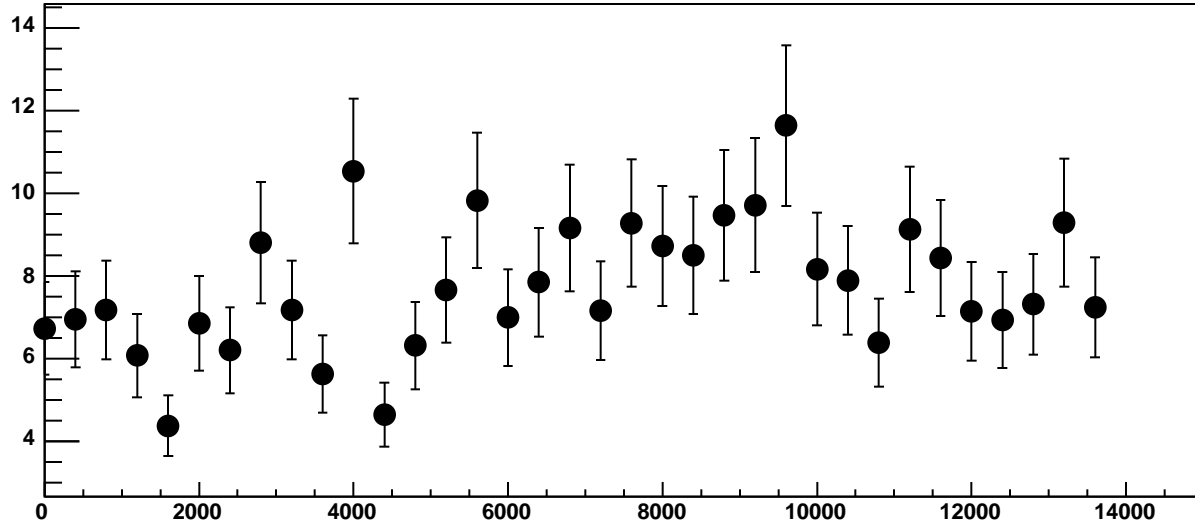


Chip 6, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

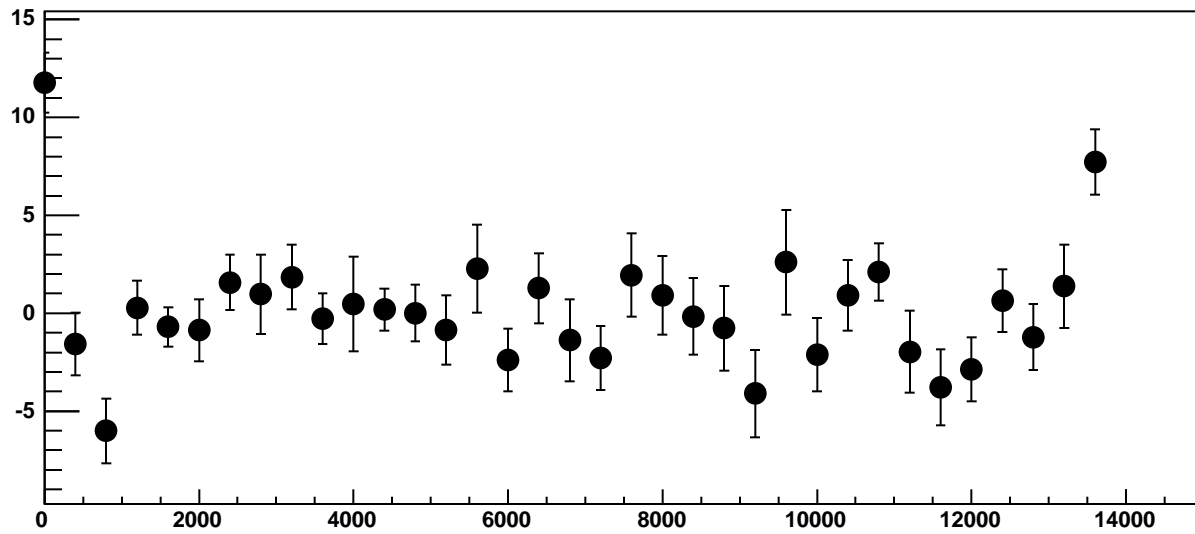


$\chi^2 / \text{ndf}$  19.13 / 23  
p0  $-216.5 \pm 0.6633$   
p1  $0.02677 \pm 0.0001109$

Chip 6, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

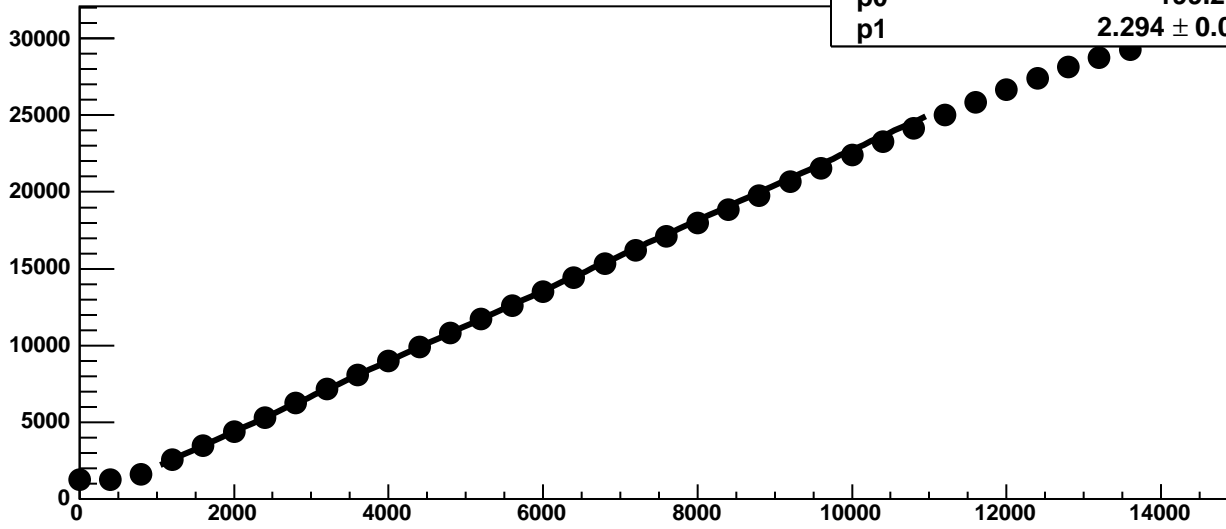


Chip 6, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

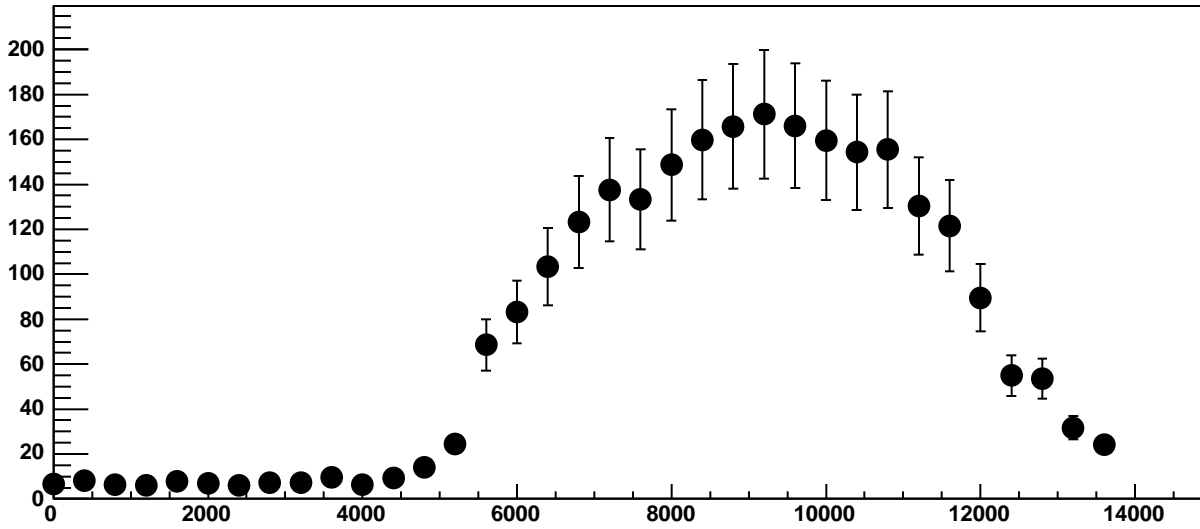


Chip 6, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

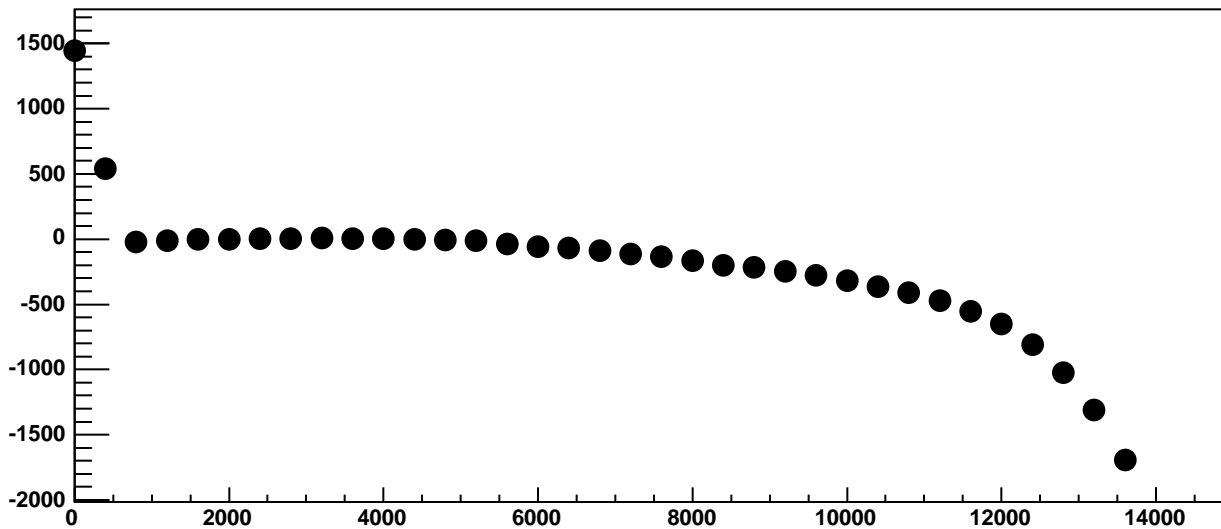
$\chi^2 / \text{ndf}$  740.8 / 23  
p0  $-199.2 \pm 1.398$   
p1  $2.294 \pm 0.0004706$



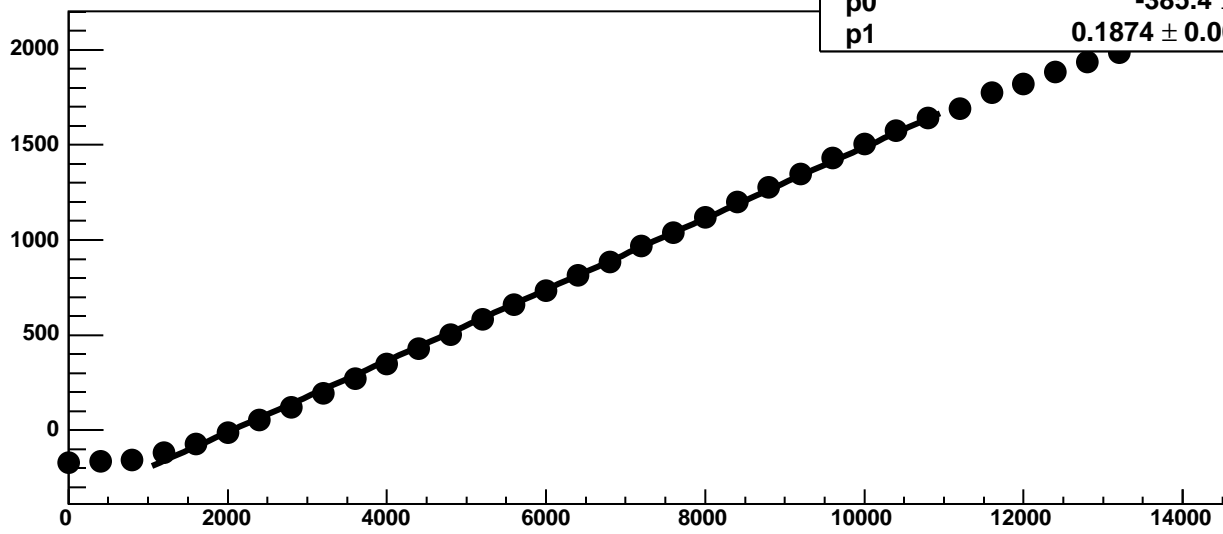
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

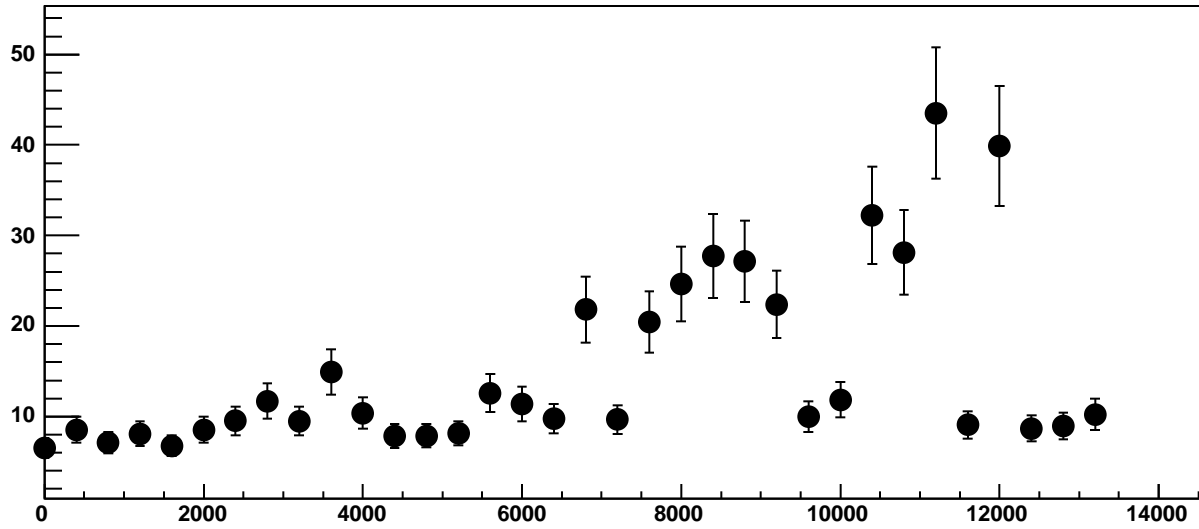


Chip 6, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

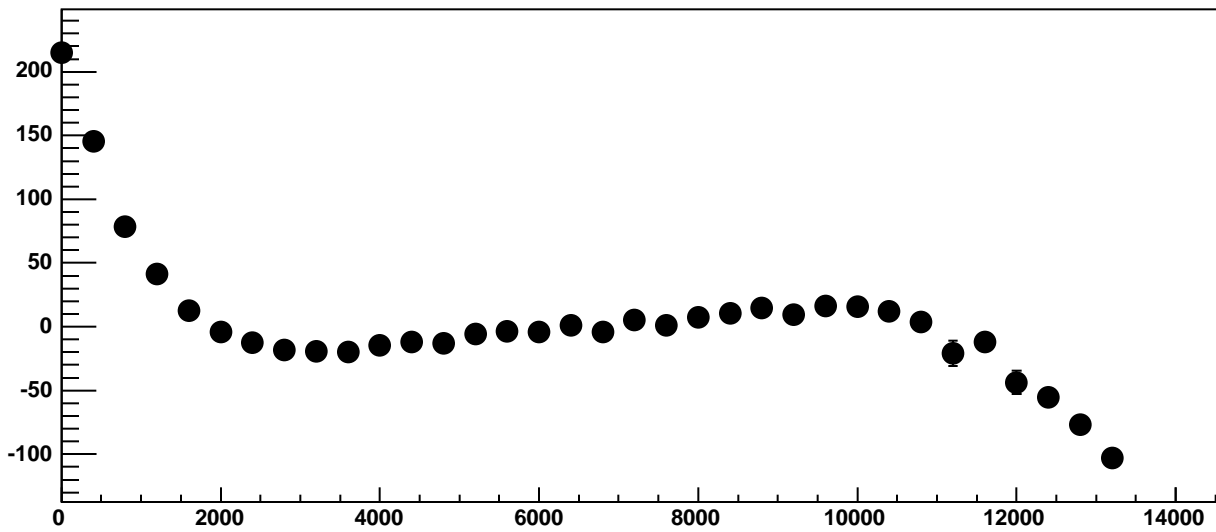


$\chi^2 / \text{ndf}$  1015 / 23  
p0  $-385.4 \pm 1.027$   
p1  $0.1874 \pm 0.0001953$

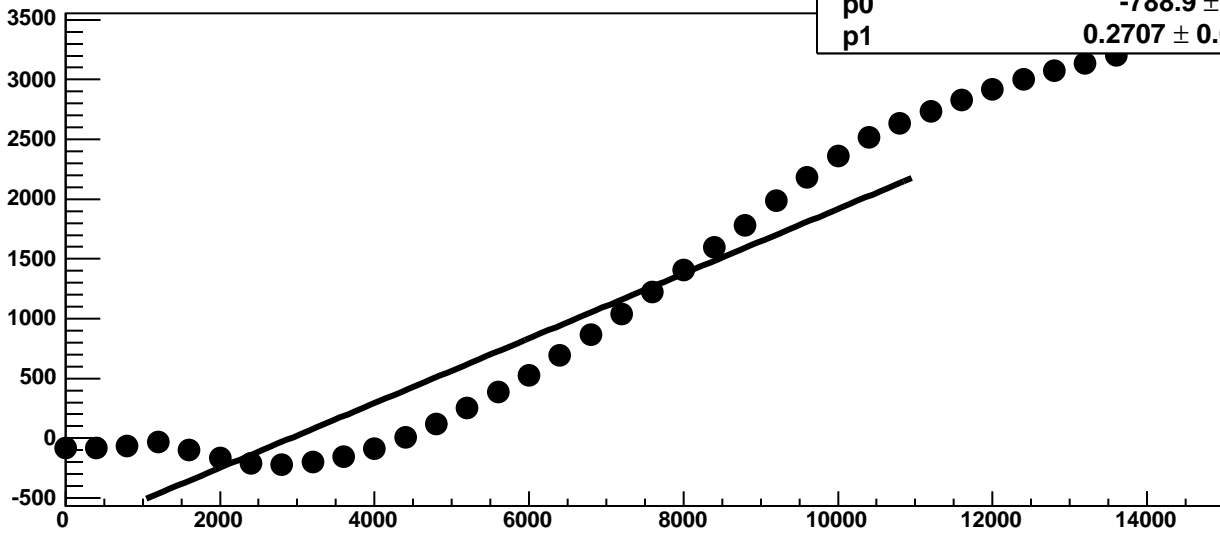
Chip 6, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

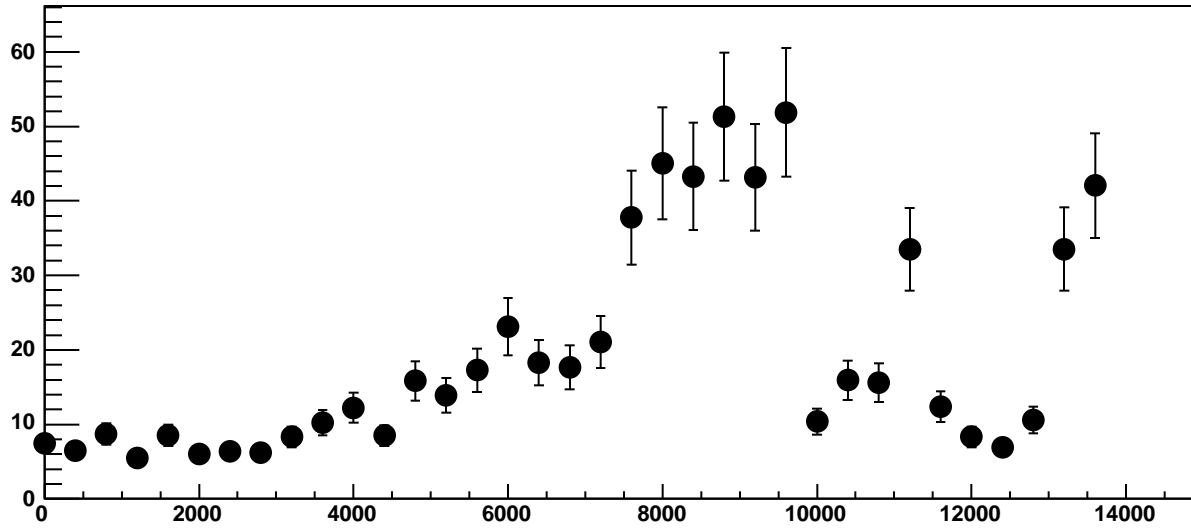


Chip 6, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

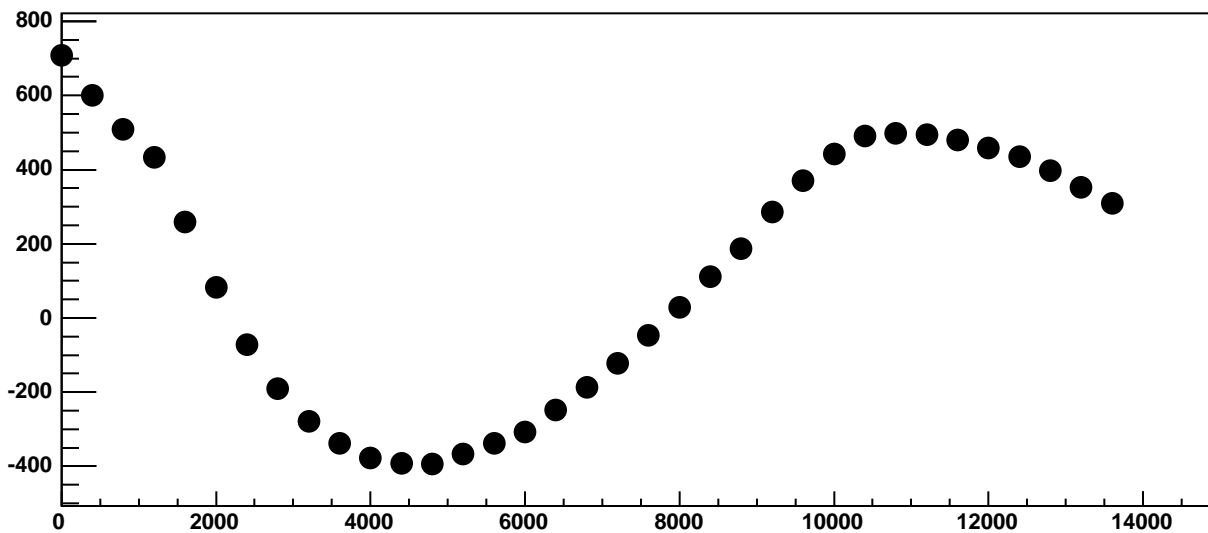


$\chi^2 / \text{ndf}$  3.751e+05 / 23  
p0 -788.9 ± 0.8578  
p1 0.2707 ± 0.000197

Chip 6, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

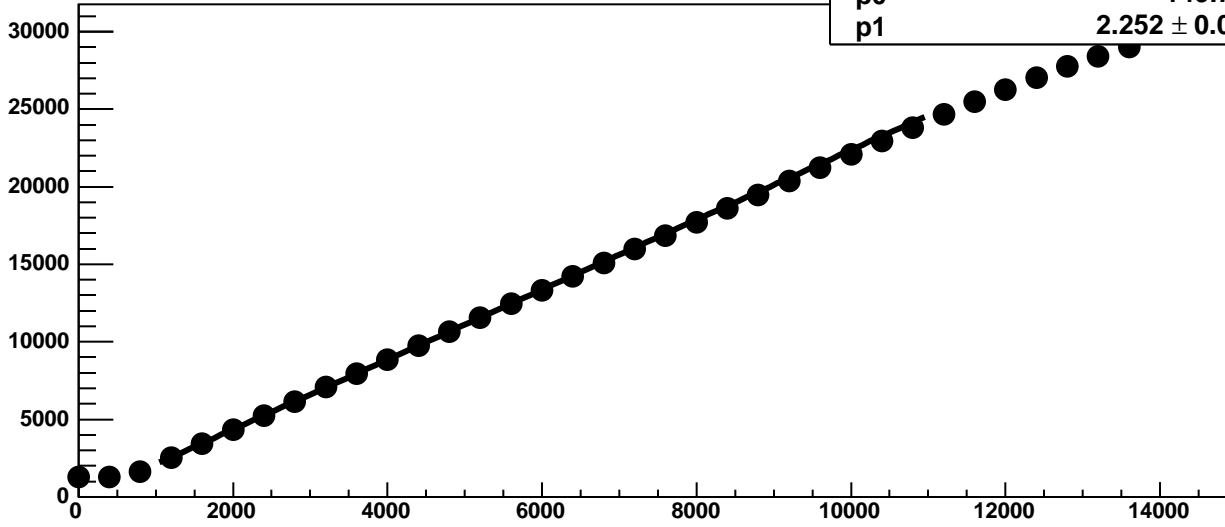


Chip 6, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

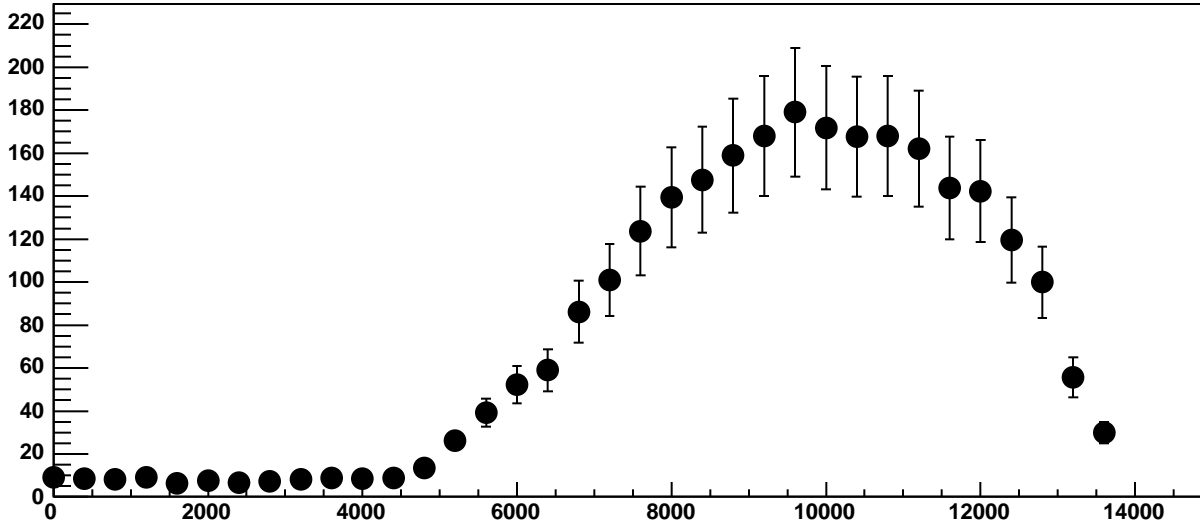


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

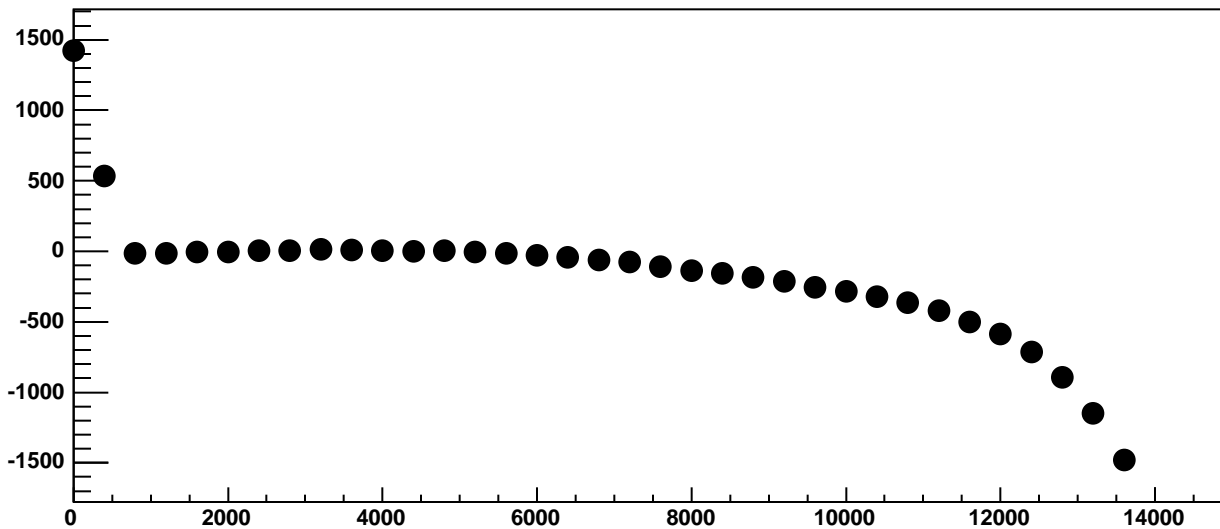
$\chi^2 / \text{ndf}$  530.8 / 23  
p0  $-149.7 \pm 1.54$   
p1  $2.252 \pm 0.0005063$



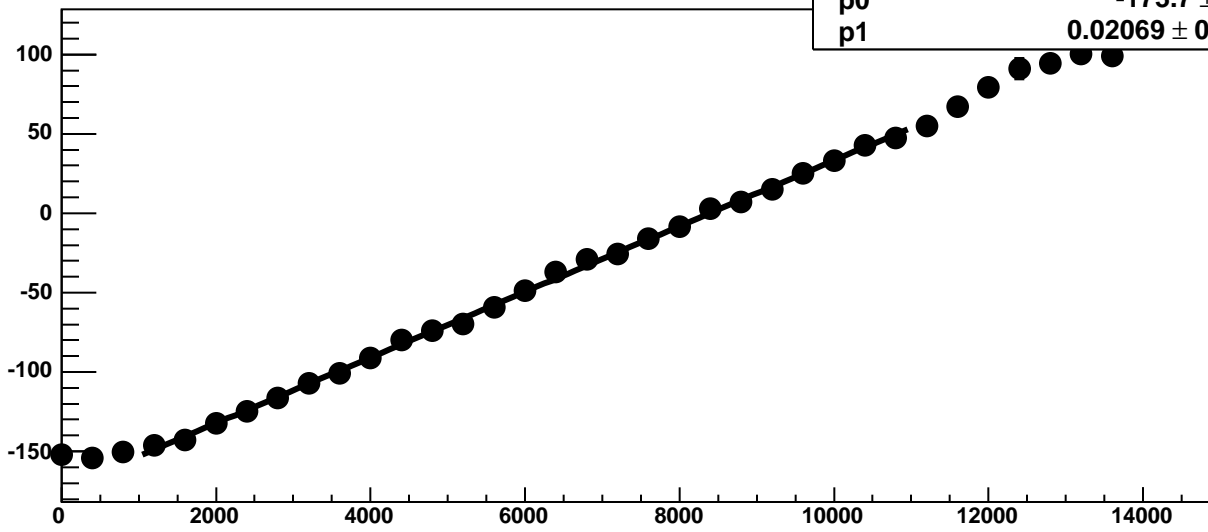
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

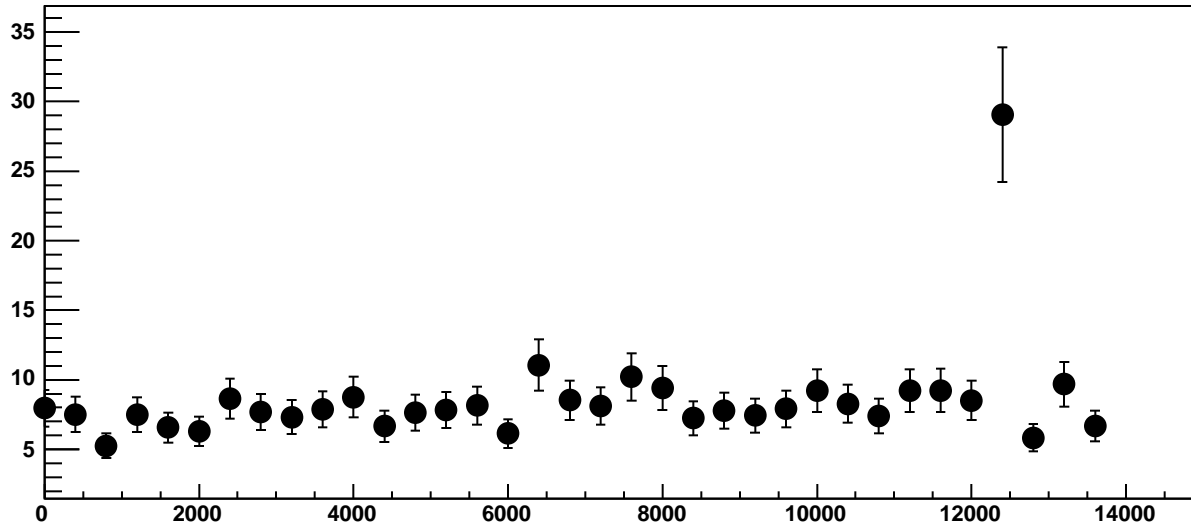


Chip 6, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

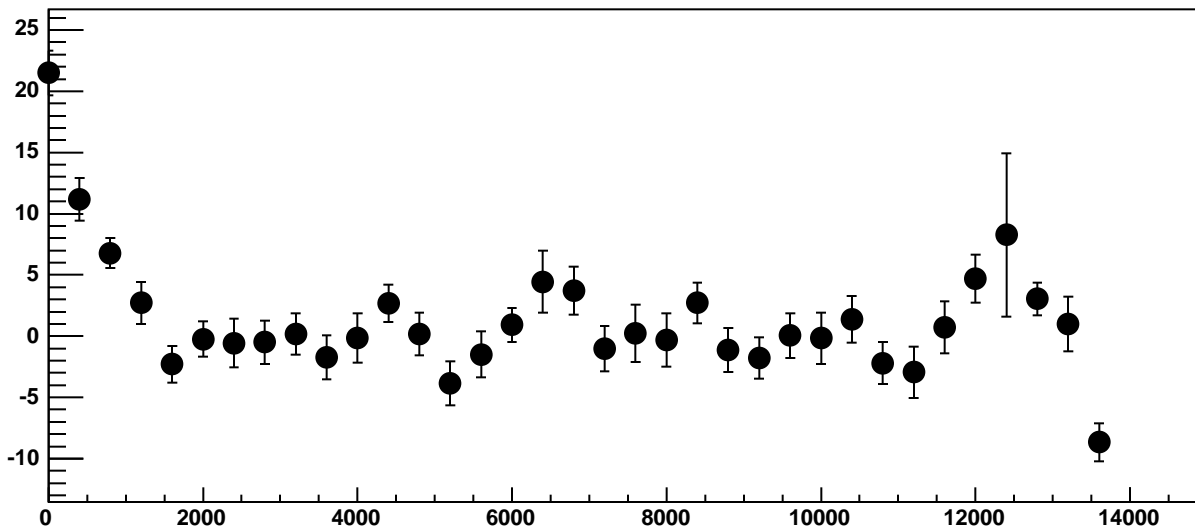


$\chi^2 / \text{ndf}$  28.08 / 23  
p0  $-173.7 \pm 0.7805$   
p1  $0.02069 \pm 0.000121$

Chip 6, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

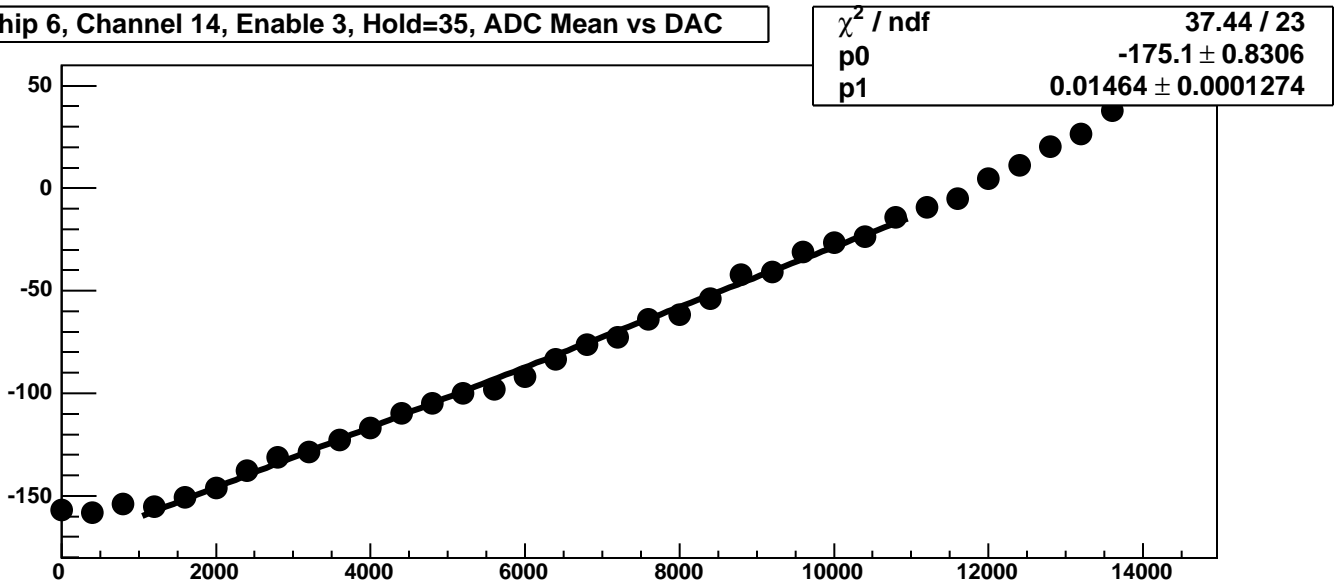


Chip 6, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

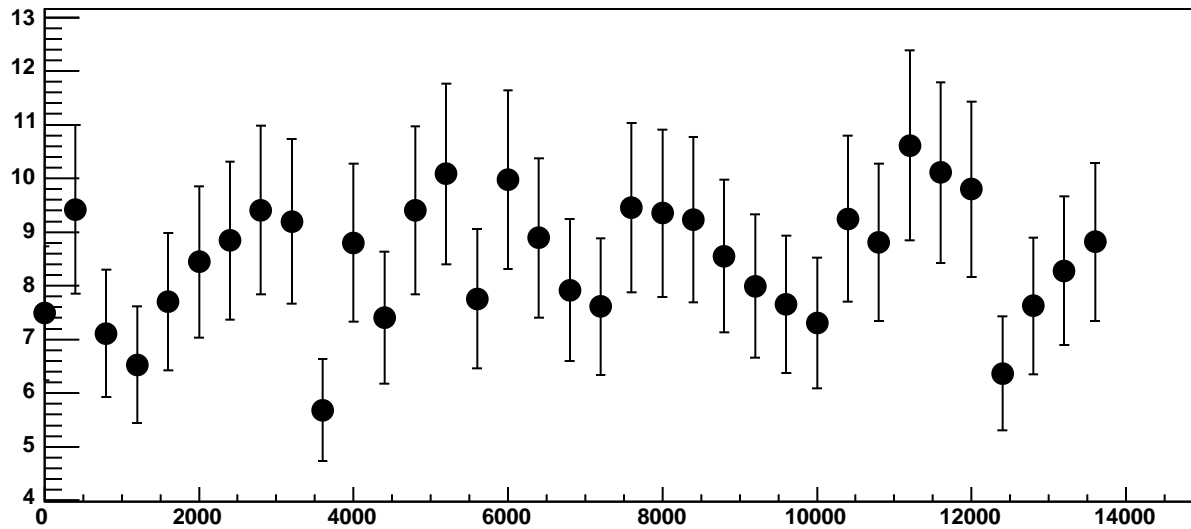




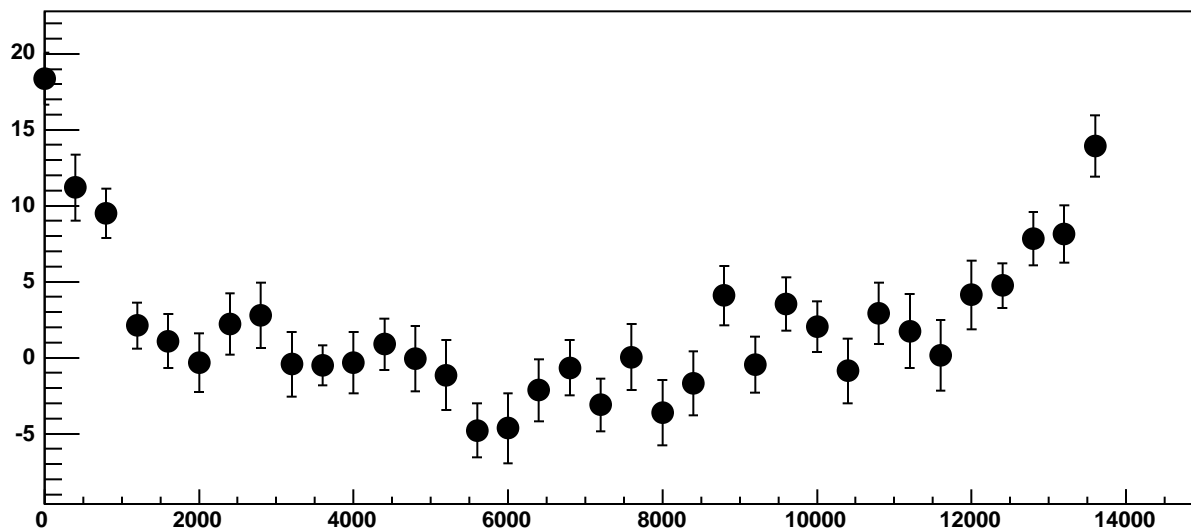
Chip 6, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



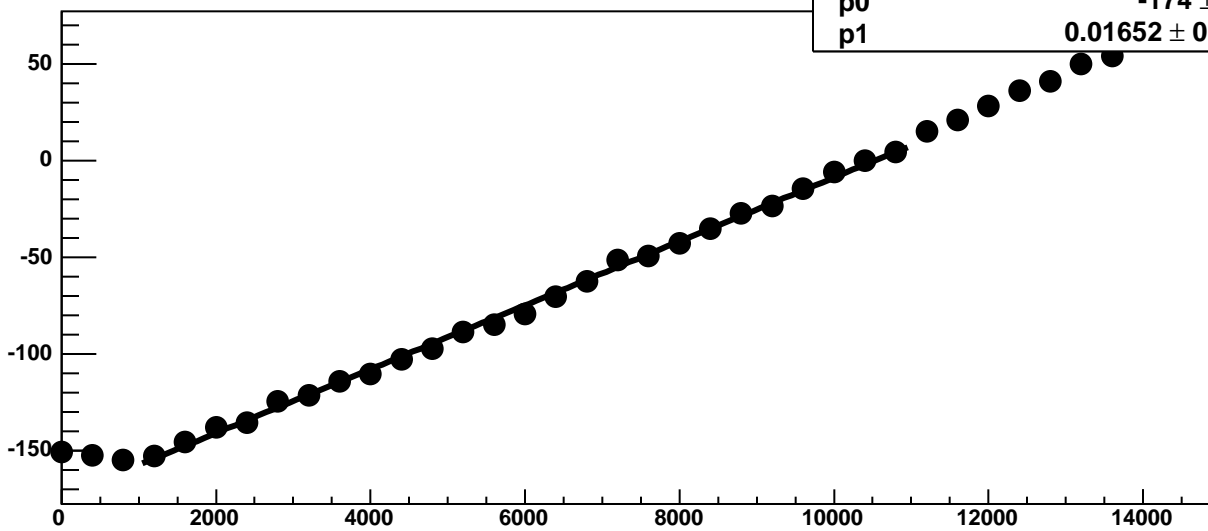
Chip 6, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



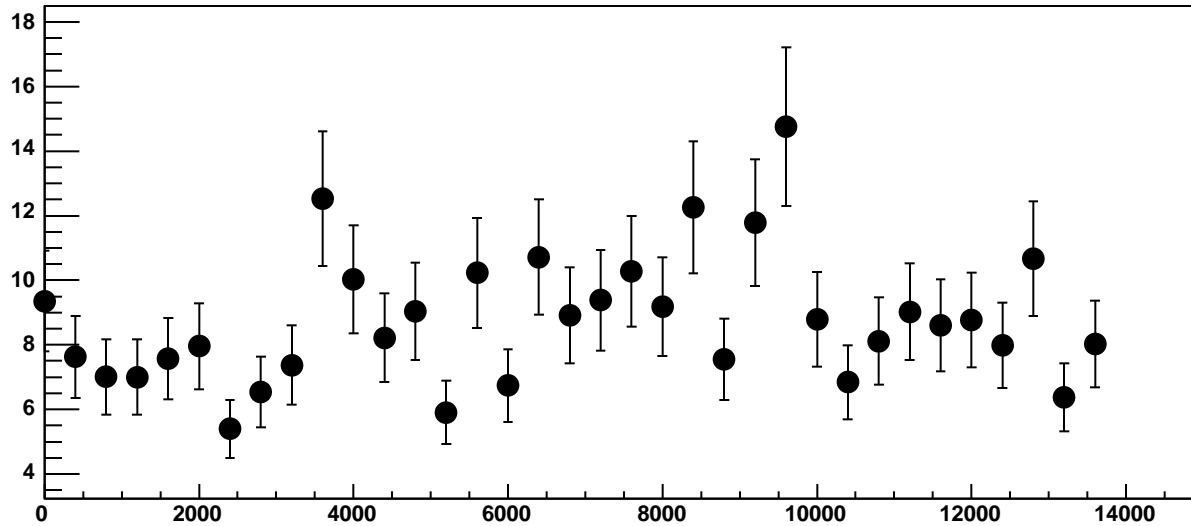
Chip 6, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



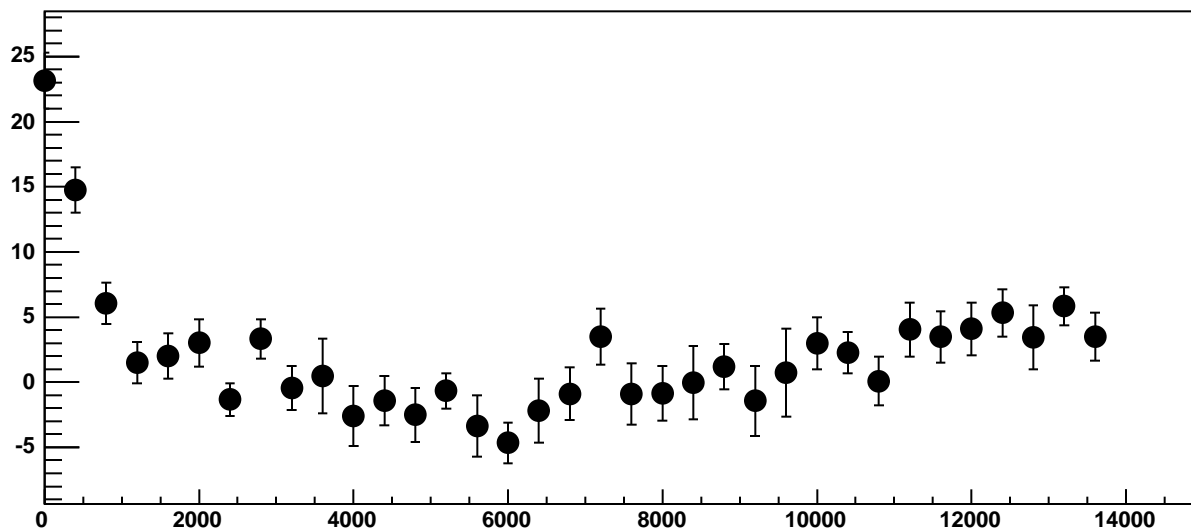
Chip 6, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



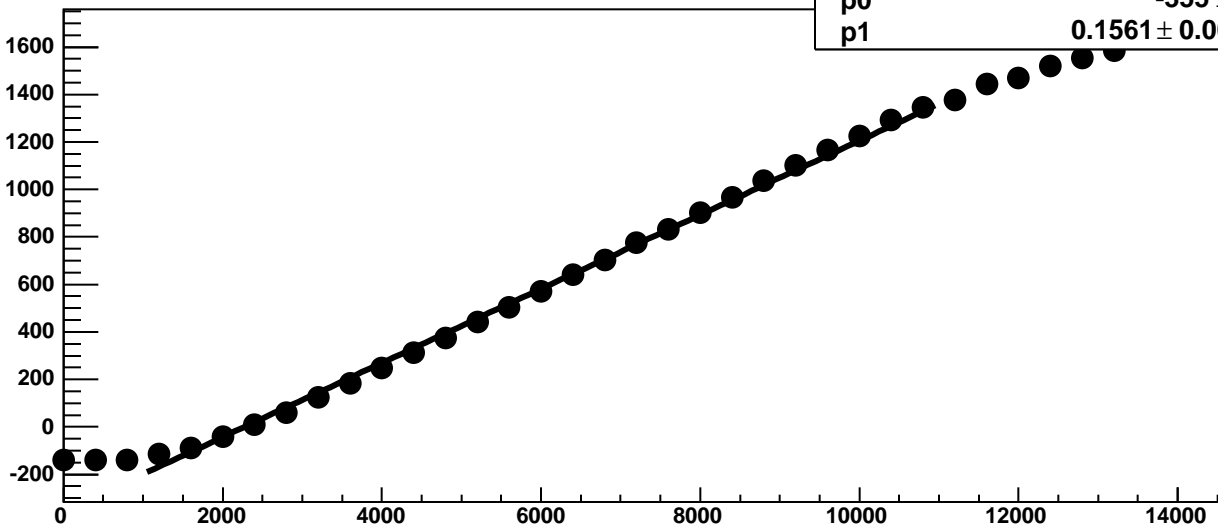
Chip 6, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



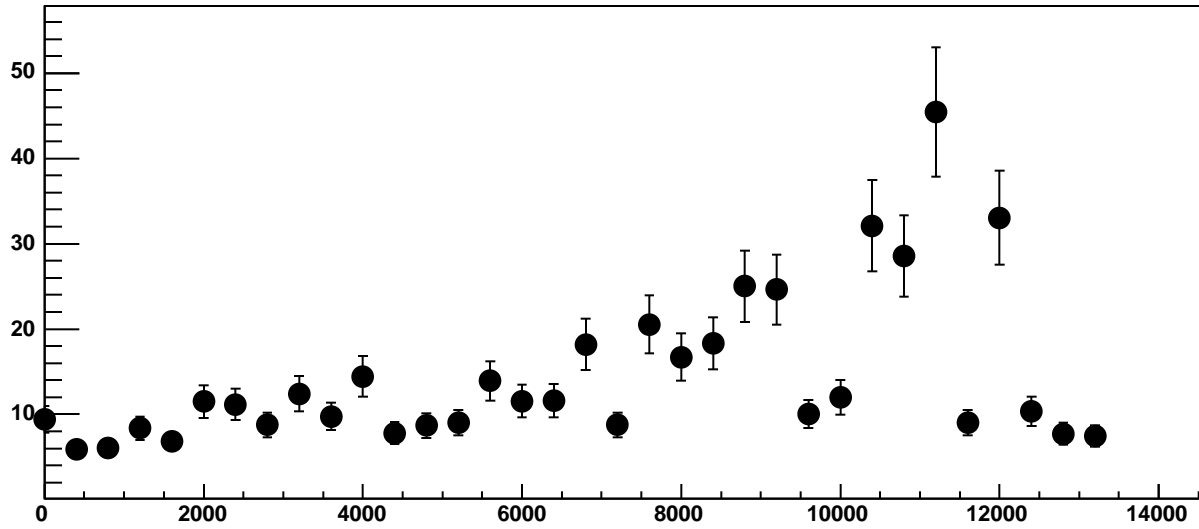
Chip 6, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



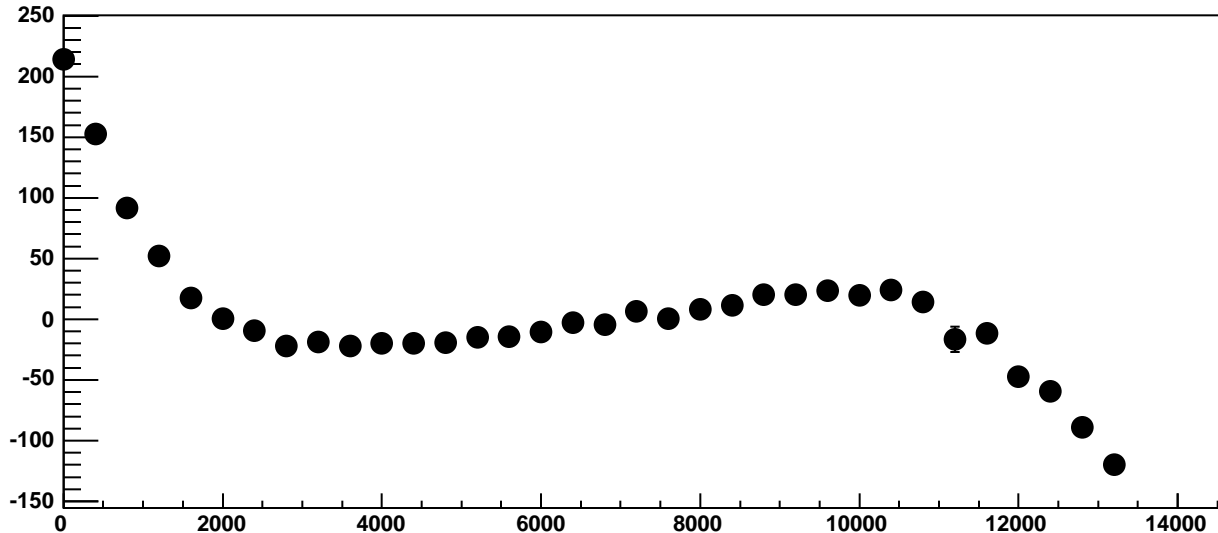
Chip 6, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



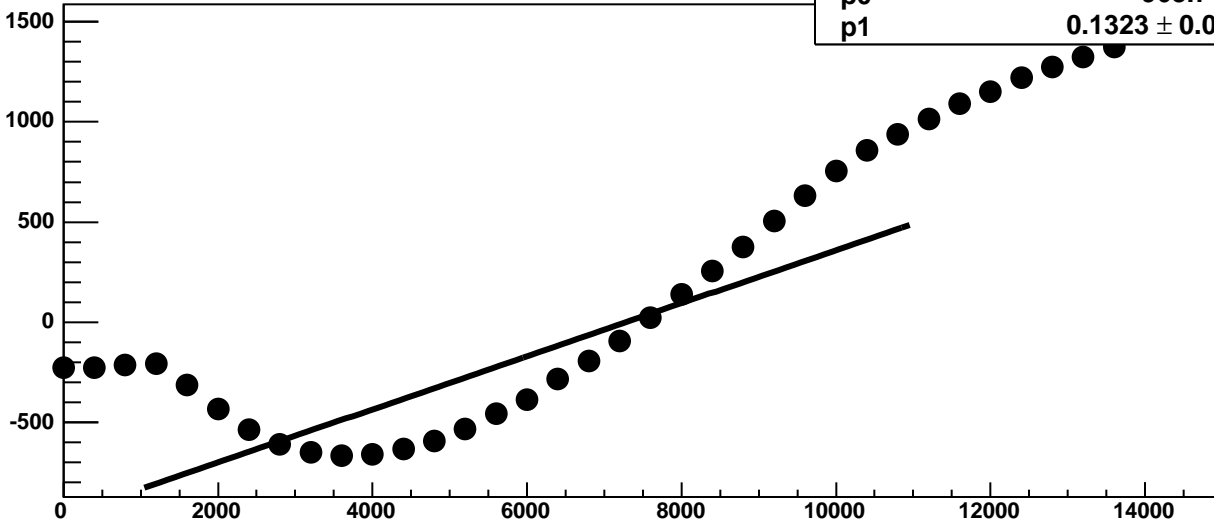
Chip 6, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

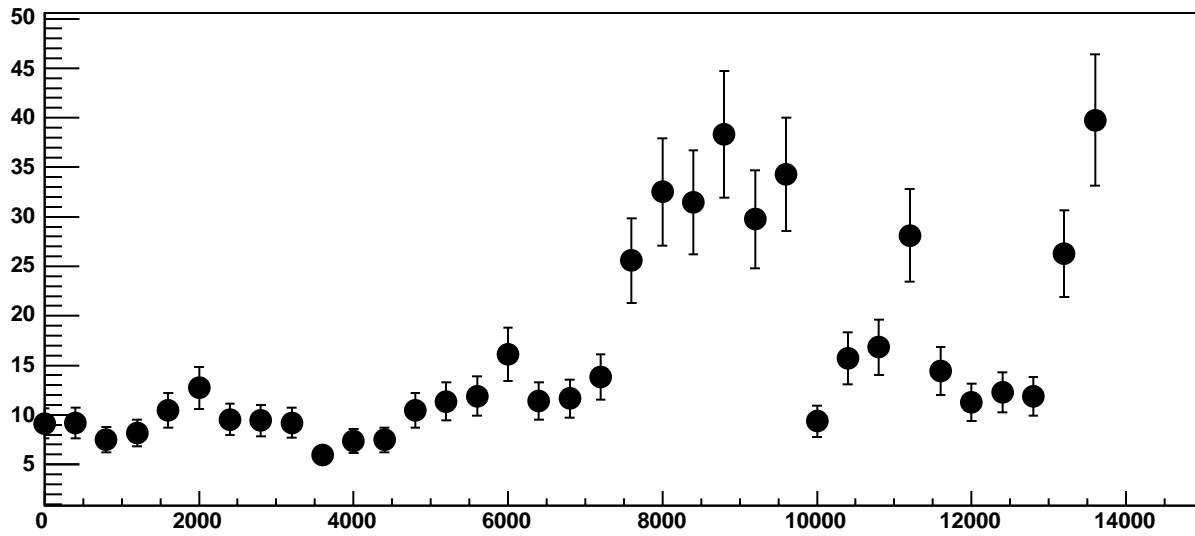


Chip 6, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

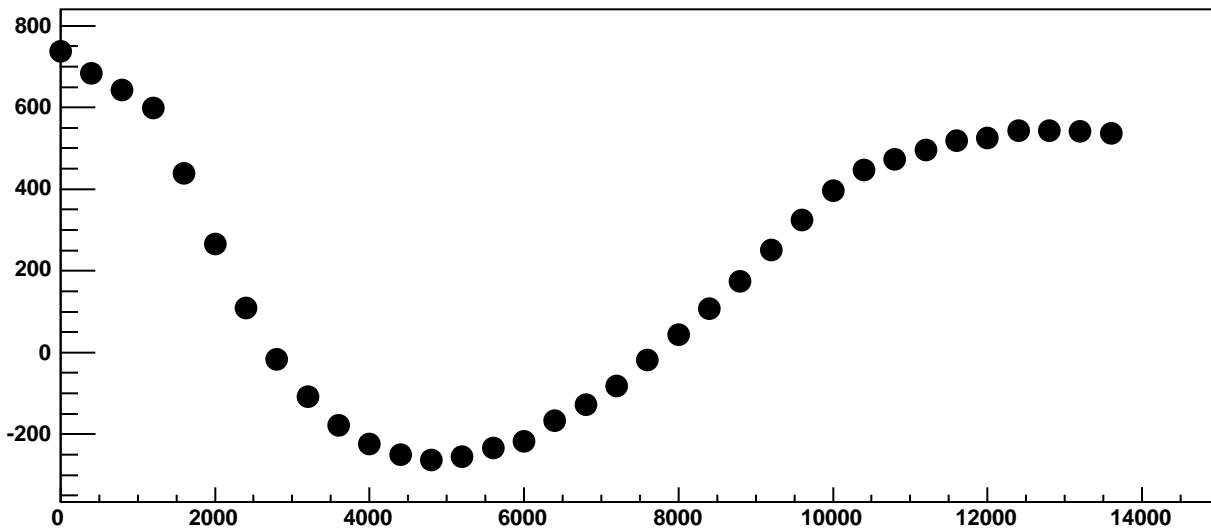


$\chi^2 / \text{ndf}$  3.123e+05 / 23  
p0 -963.7 ± 1.066  
p1 0.1323 ± 0.0002044

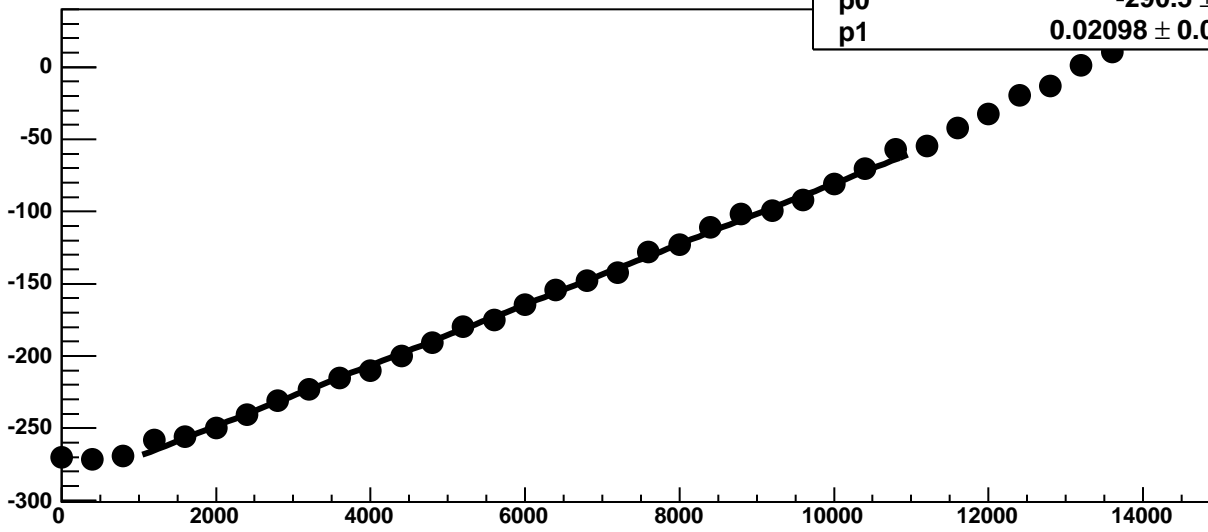
Chip 6, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

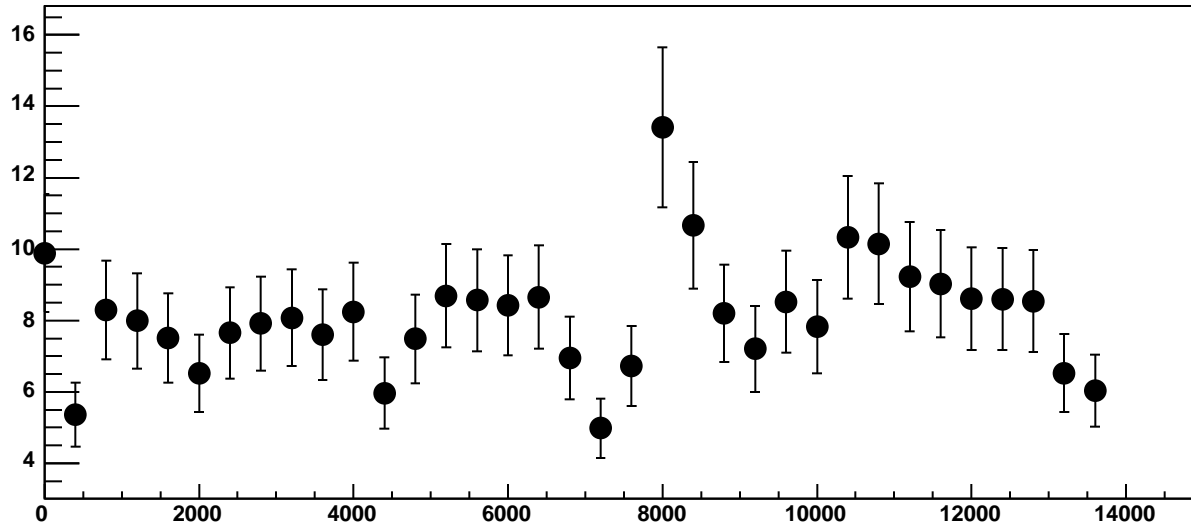


Chip 6, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

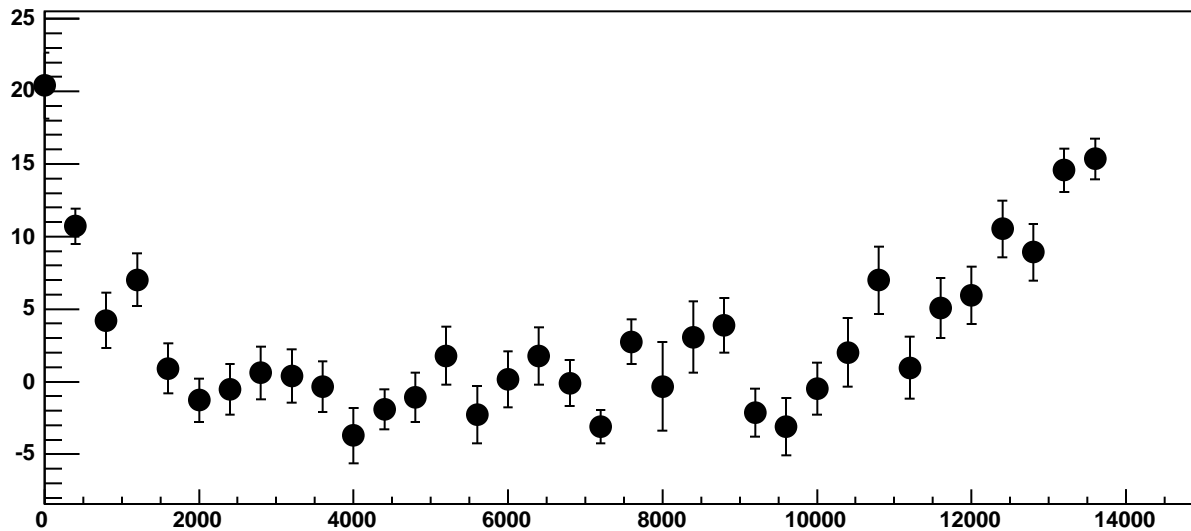


$\chi^2 / \text{ndf}$  55.59 / 23  
p0  $-290.5 \pm 0.8179$   
p1  $0.02098 \pm 0.0001286$

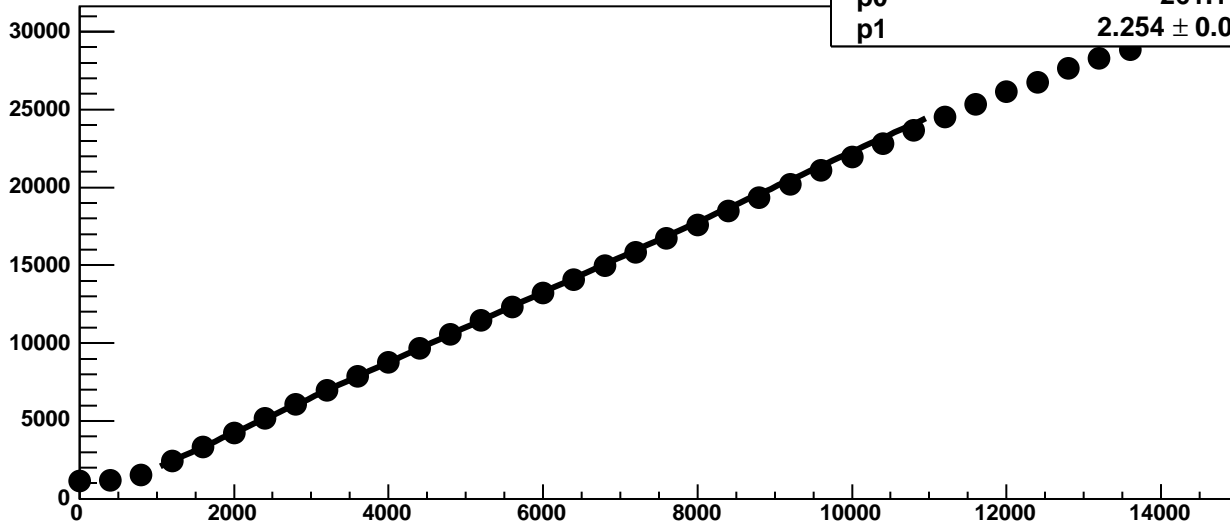
Chip 6, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 6, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

640.7 / 23

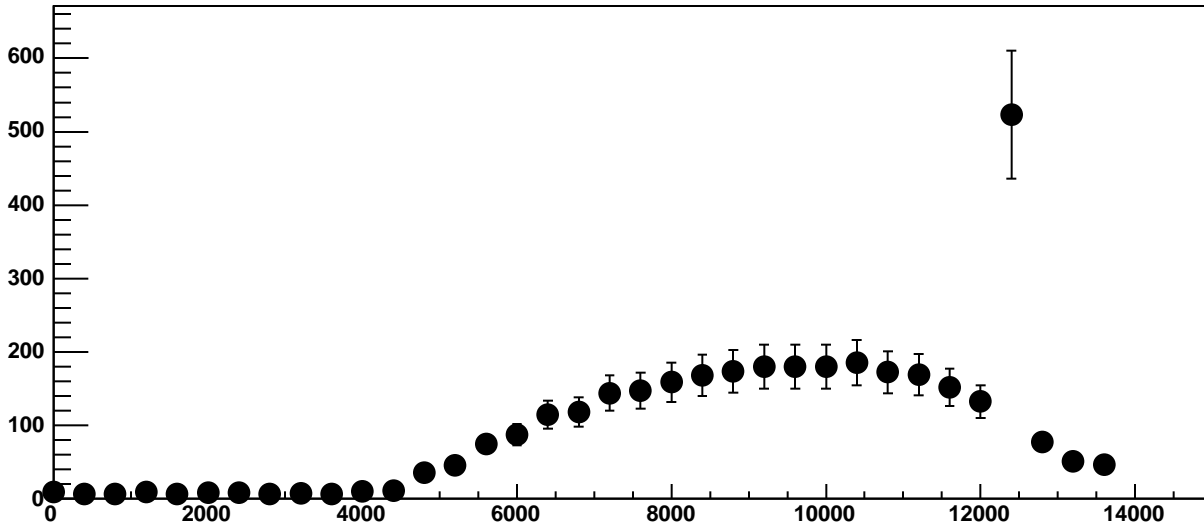
p0

$-261.1 \pm 1.772$

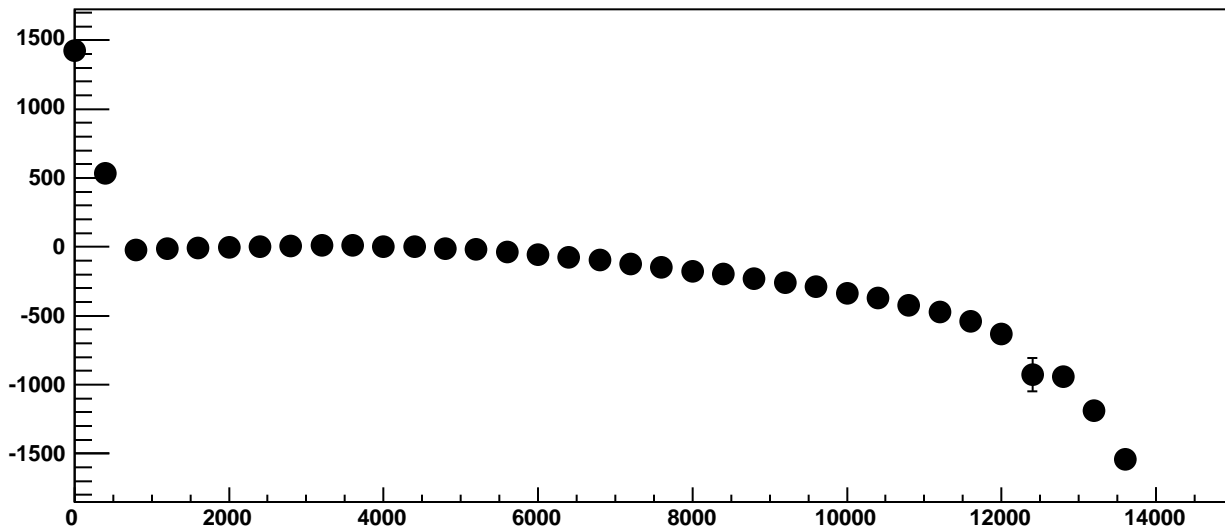
p1

$2.254 \pm 0.0006036$

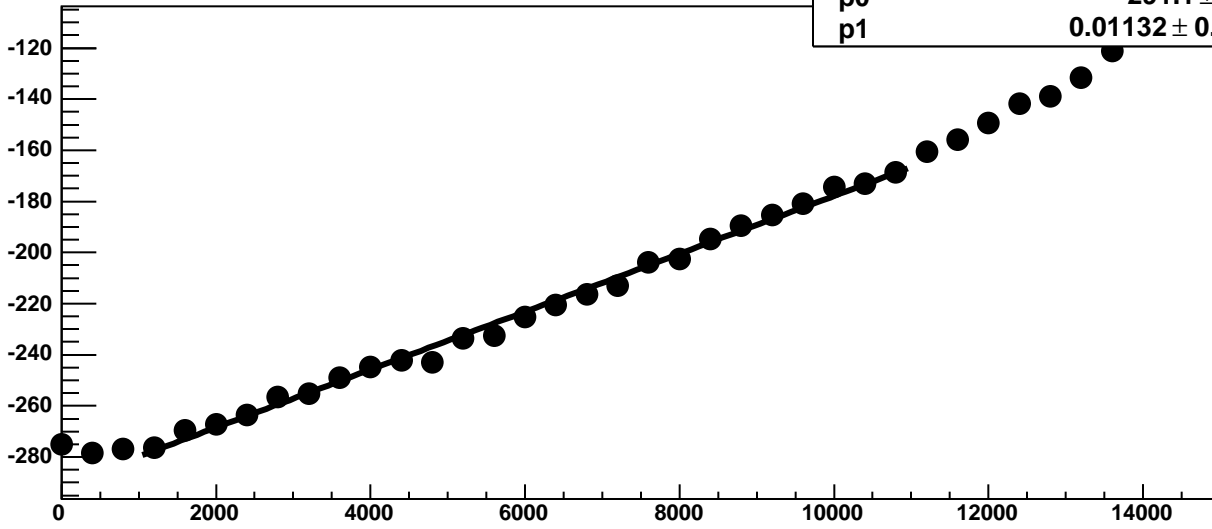
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

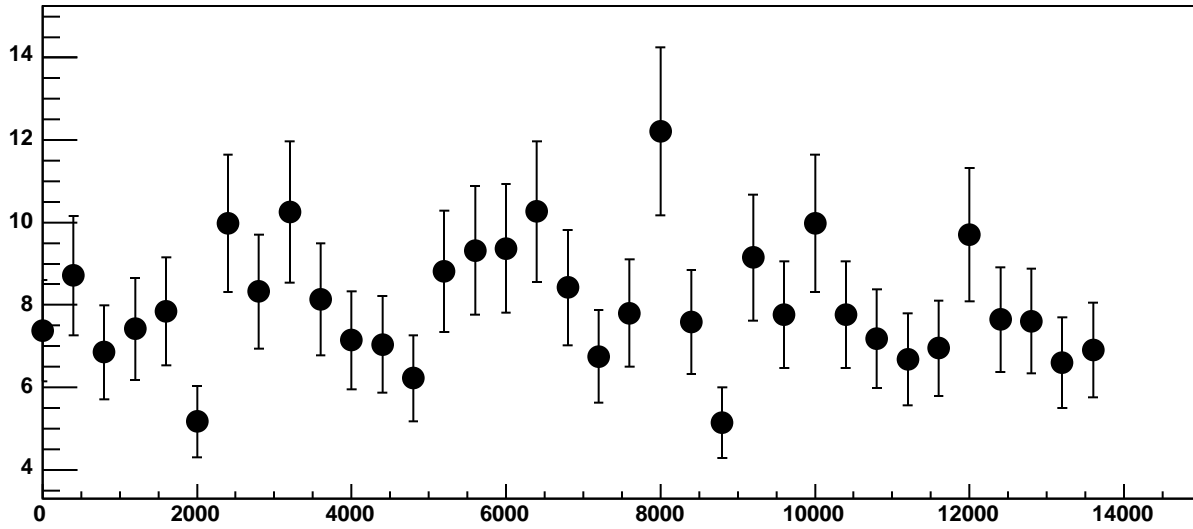


Chip 6, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

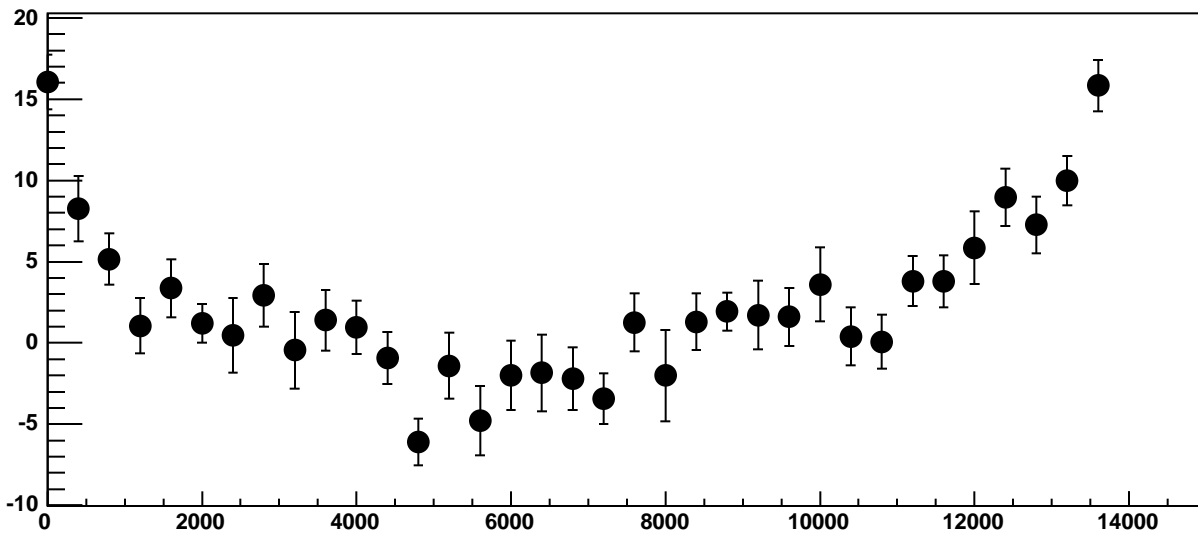


$\chi^2 / \text{ndf}$  48.15 / 23  
p0  $-291.1 \pm 0.7872$   
p1  $0.01132 \pm 0.000119$

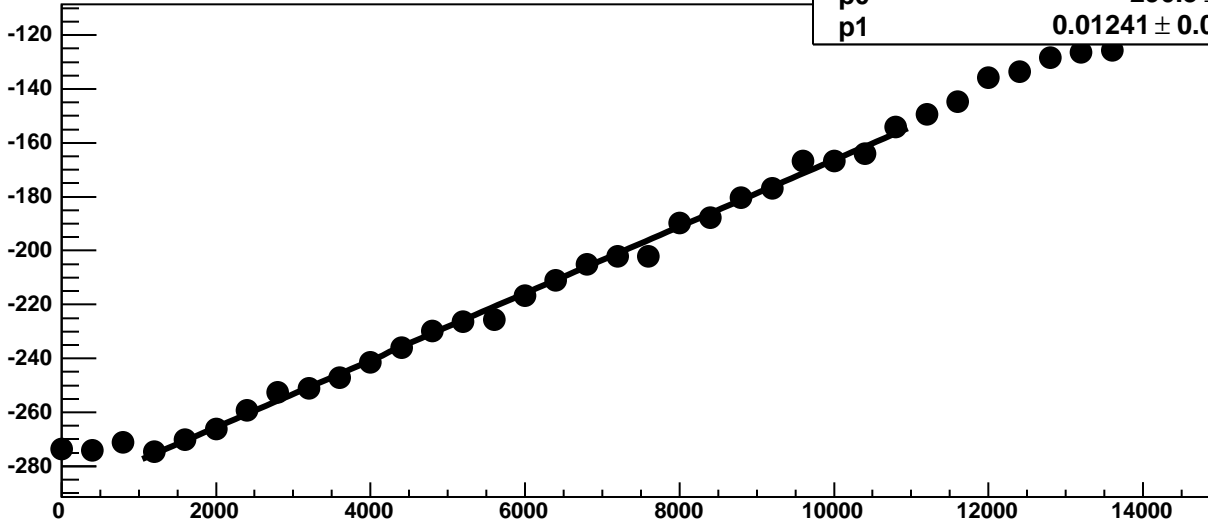
Chip 6, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

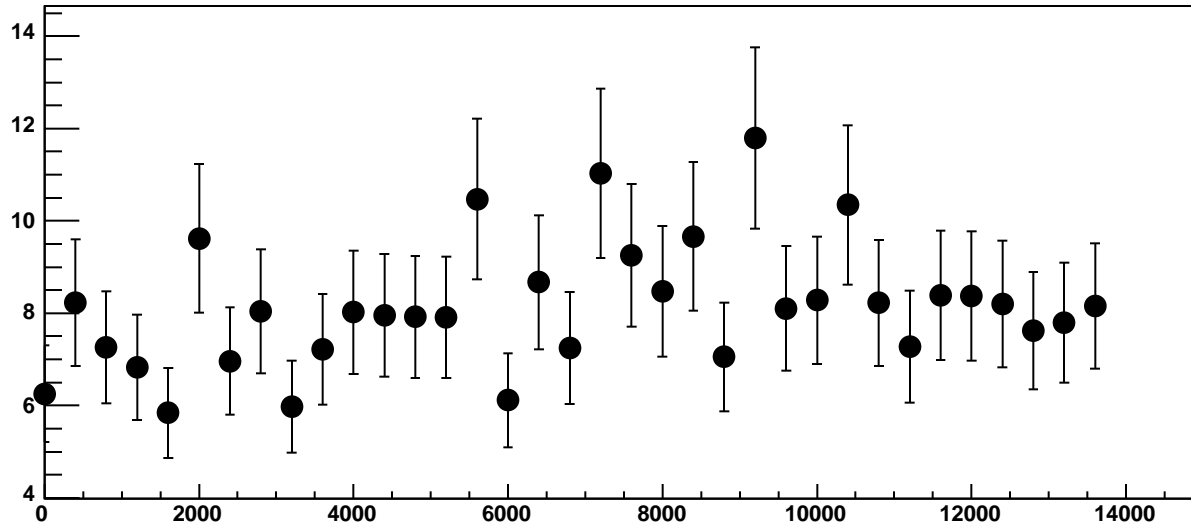


Chip 6, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

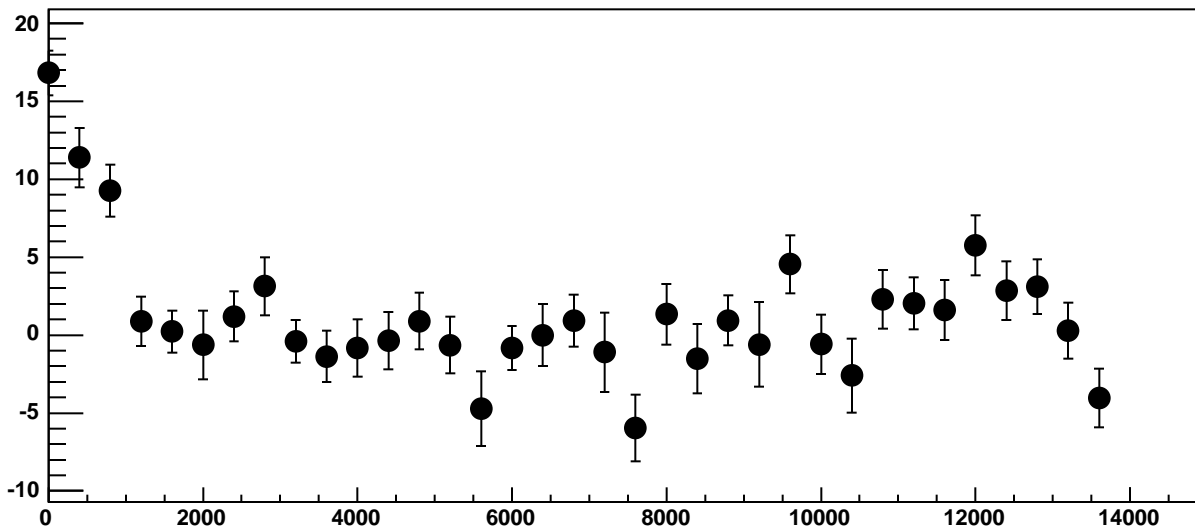


$\chi^2 / \text{ndf}$  27.89 / 23  
p0  $-290.3 \pm 0.7741$   
p1  $0.01241 \pm 0.0001248$

Chip 6, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

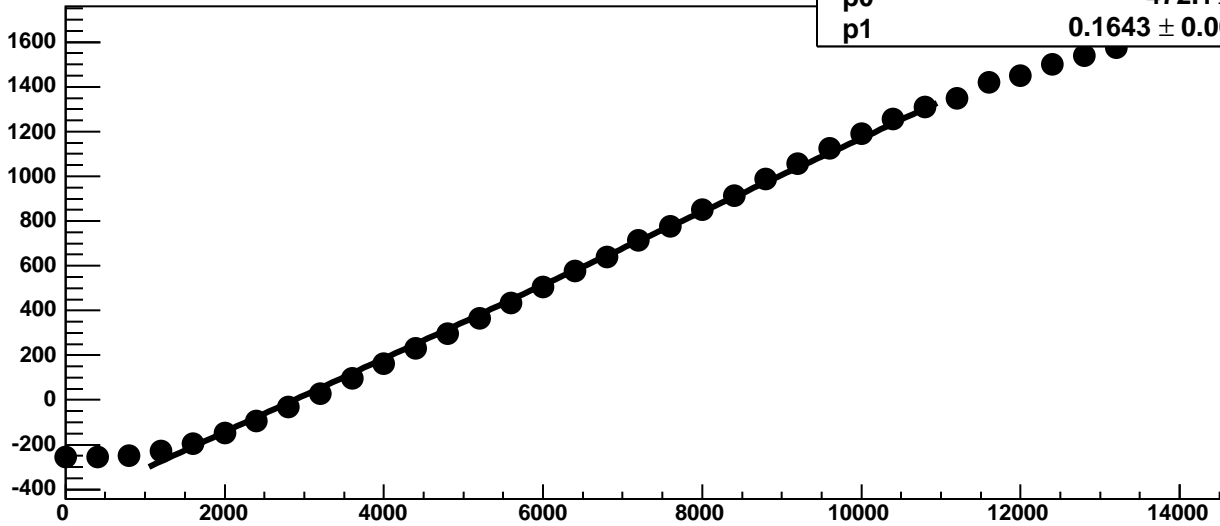


Chip 6, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

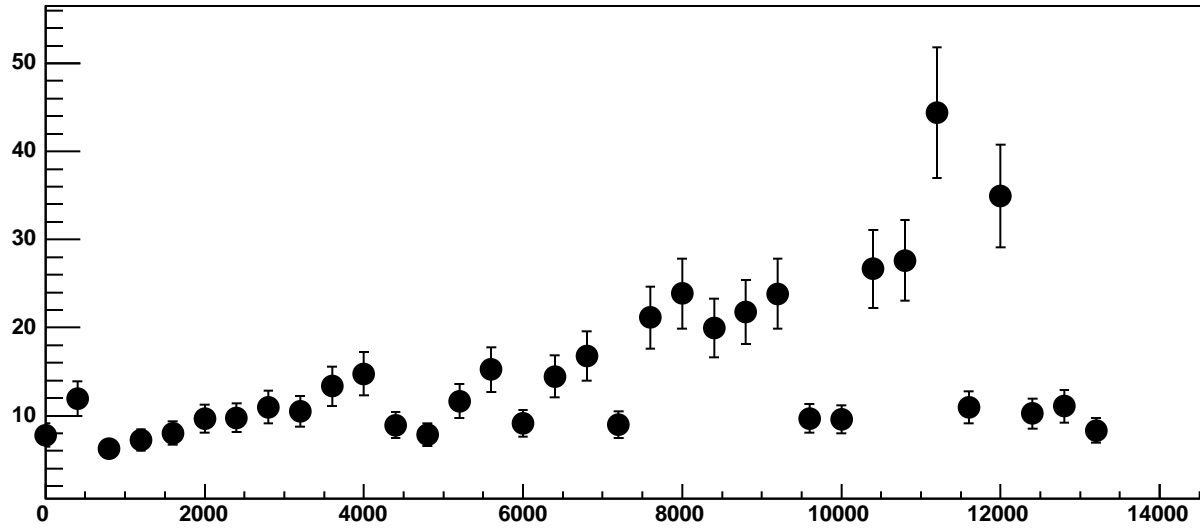




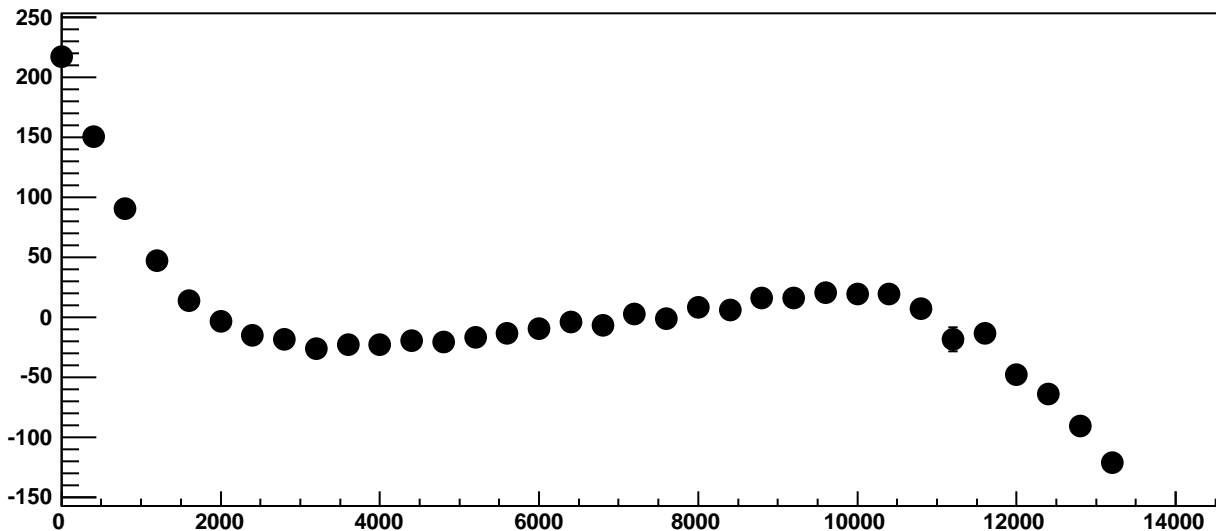
Chip 6, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



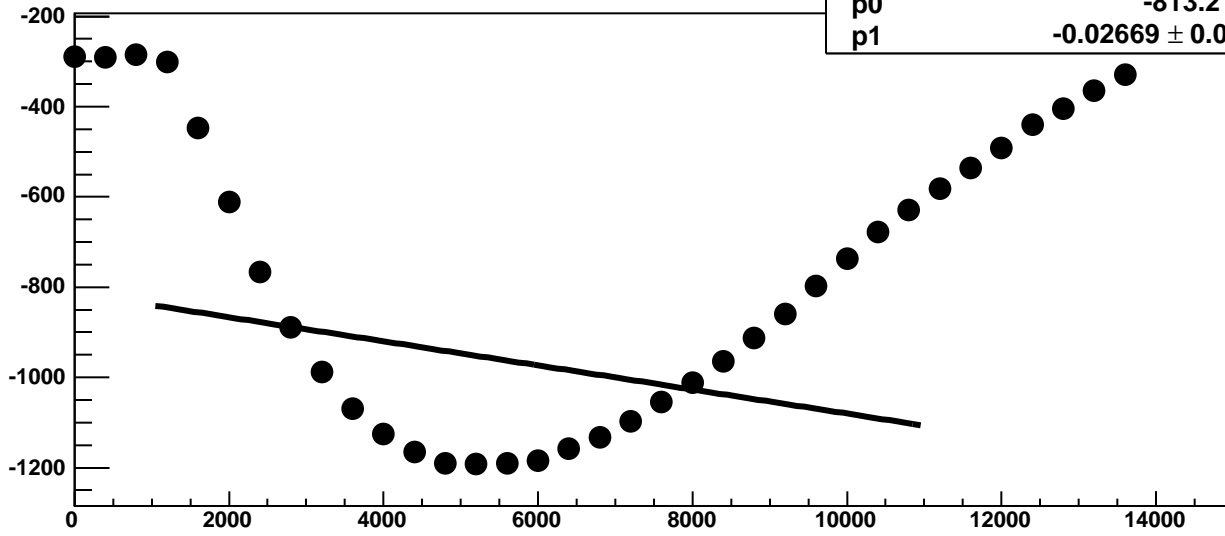
Chip 6, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 6, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

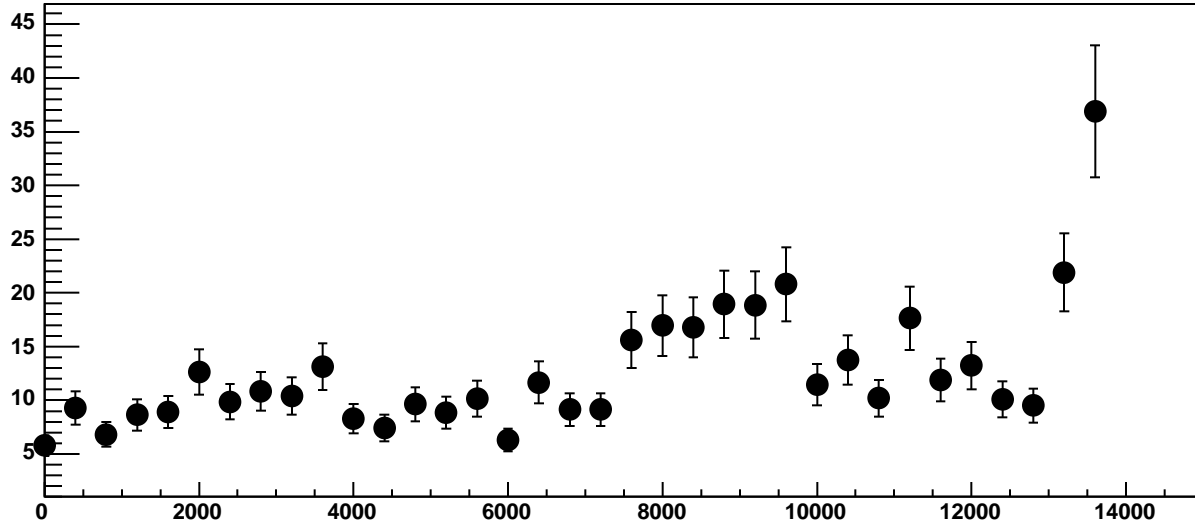


Chip 6, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

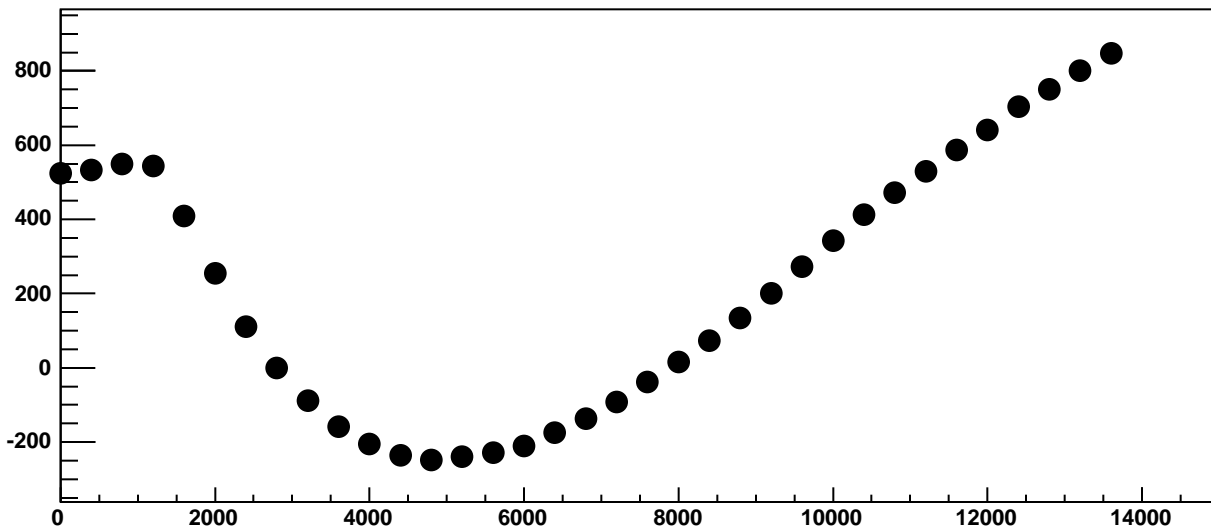


$\chi^2 / \text{ndf}$  3.107e+05 / 23  
p0 -813.2 ± 1.075  
p1 -0.02669 ± 0.0001815

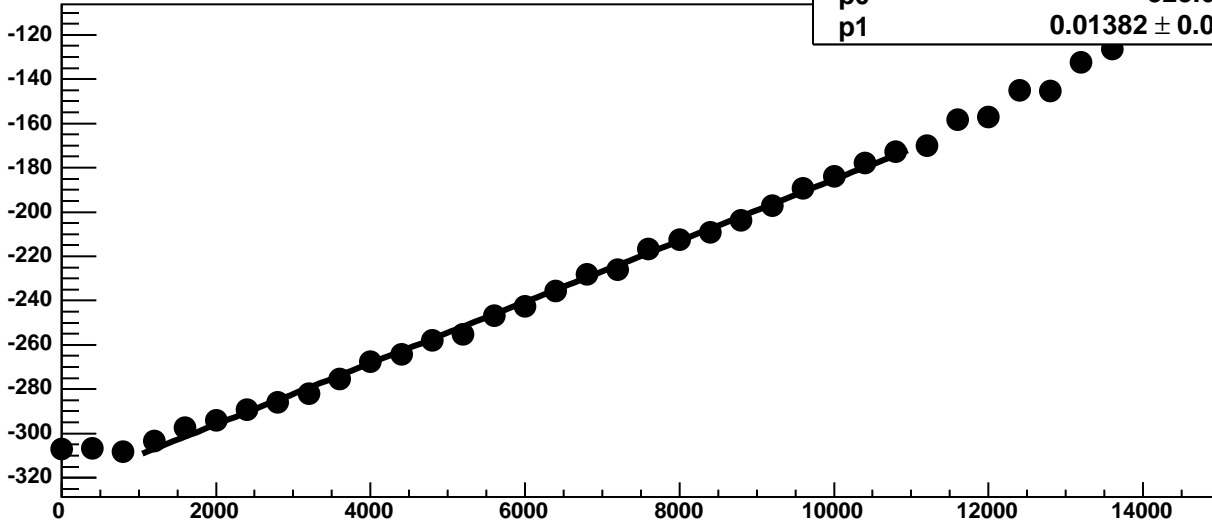
Chip 6, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



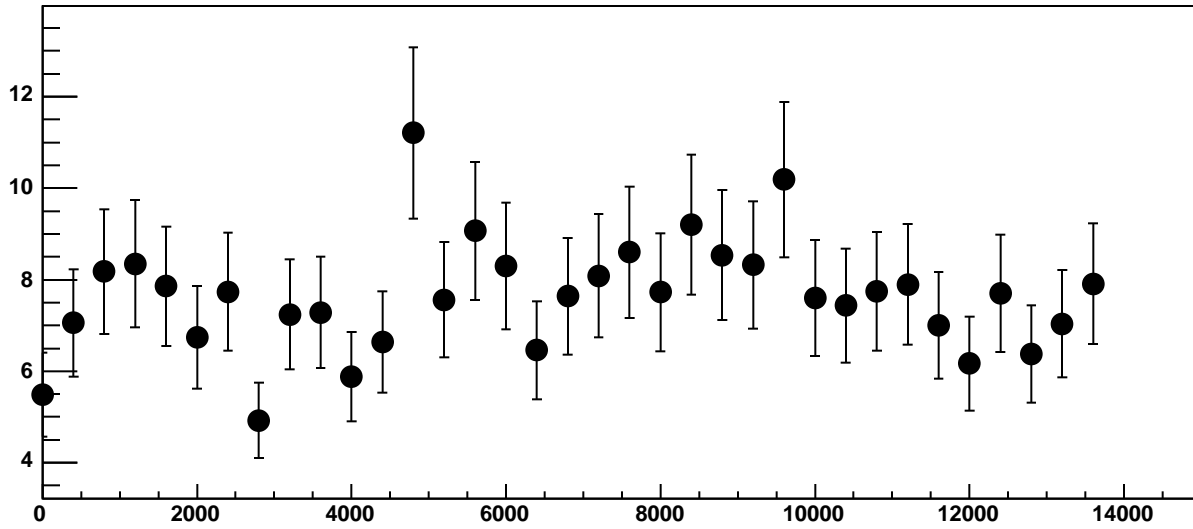
Chip 6, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



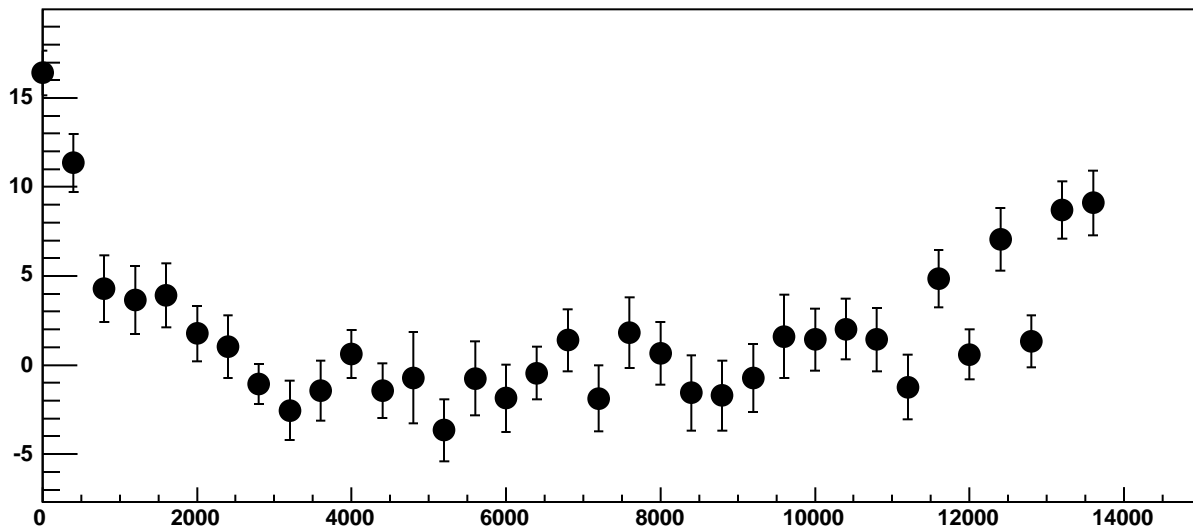
Chip 6, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



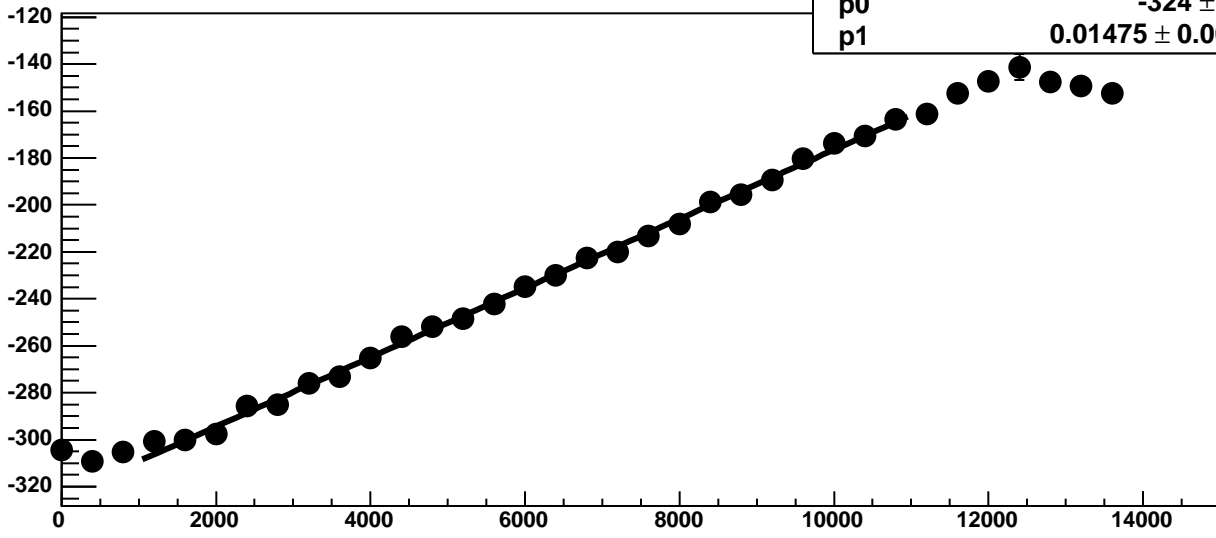
Chip 6, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

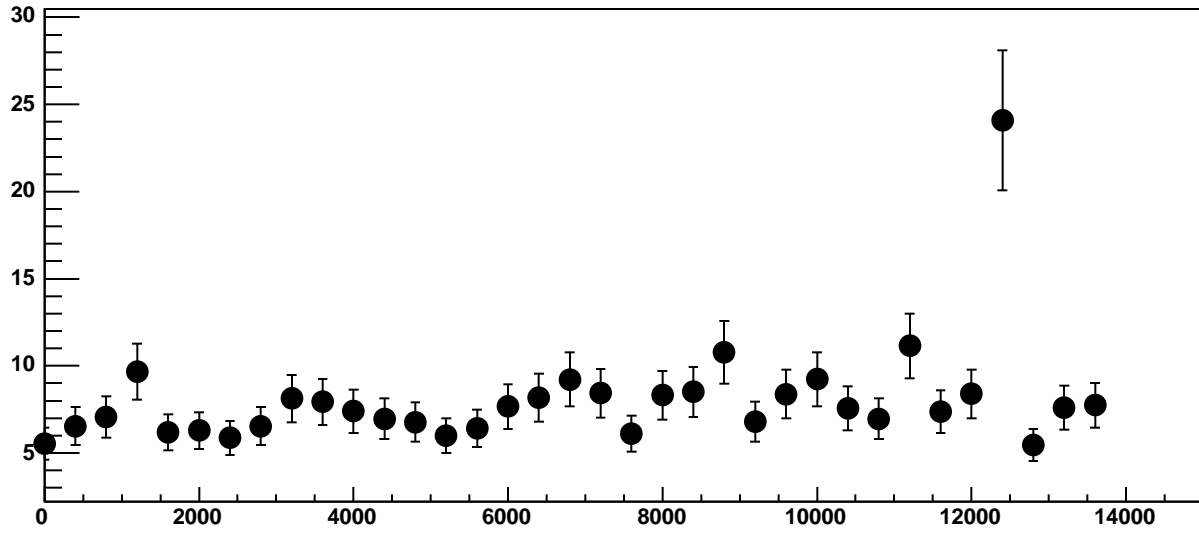


Chip 6, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

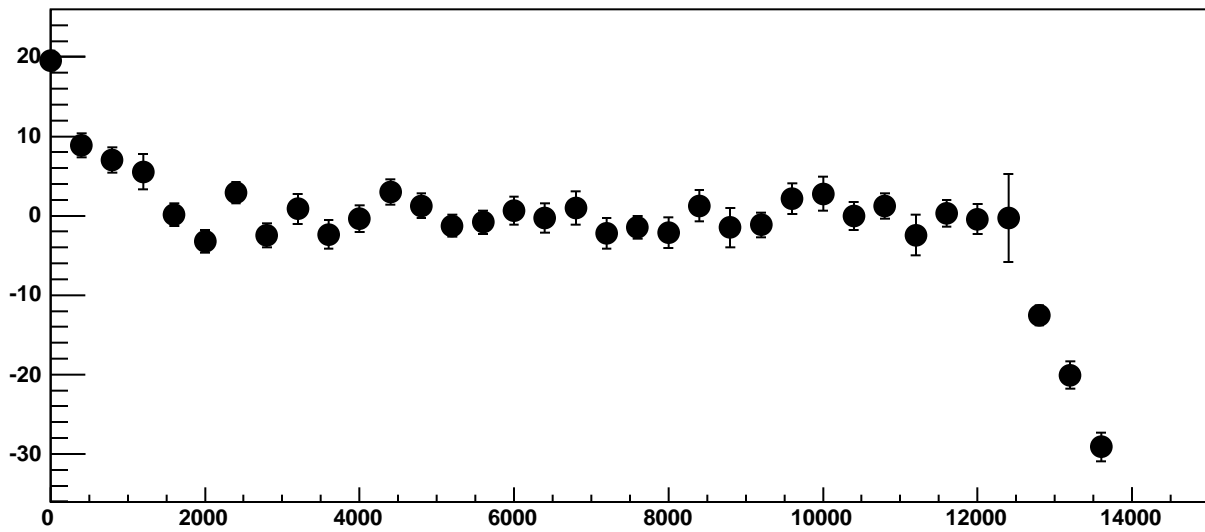


$\chi^2 / \text{ndf}$  34.82 / 23  
p0  $-324 \pm 0.7464$   
p1  $0.01475 \pm 0.0001173$

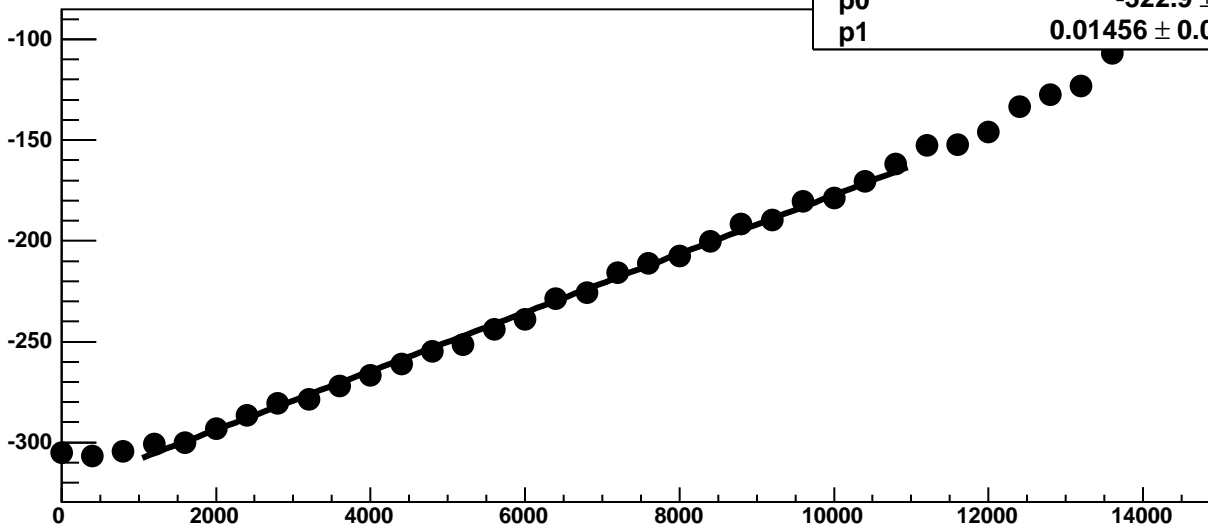
Chip 6, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

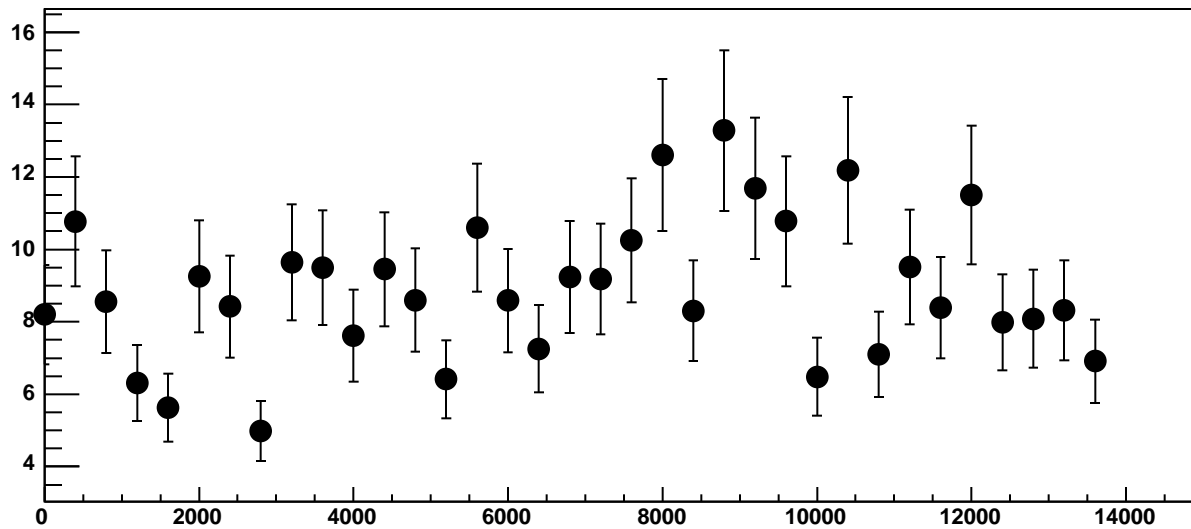


Chip 6, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

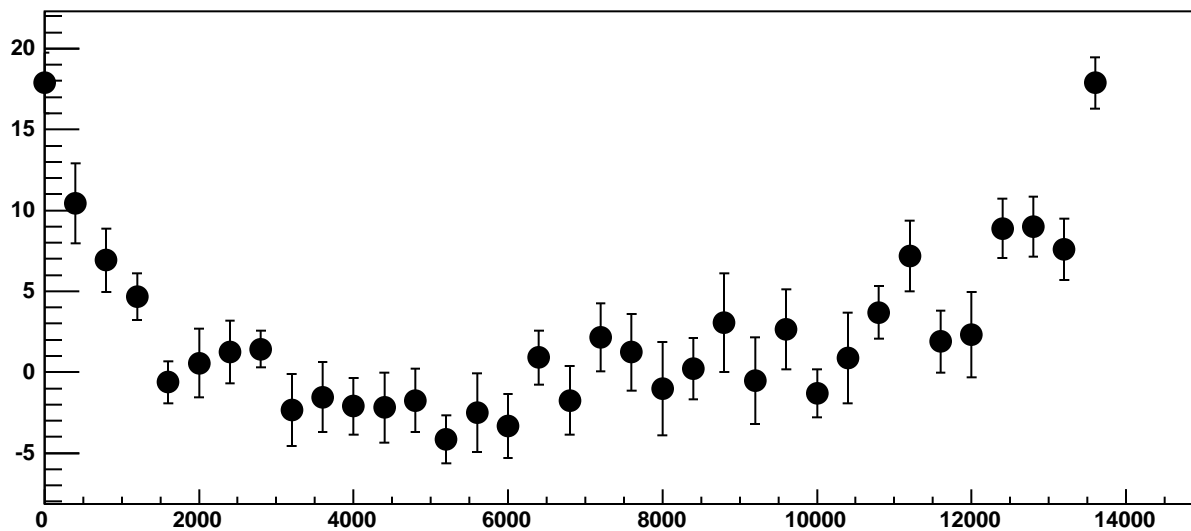


$\chi^2 / \text{ndf}$  39.98 / 23  
p0  $-322.9 \pm 0.7579$   
p1  $0.01456 \pm 0.0001235$

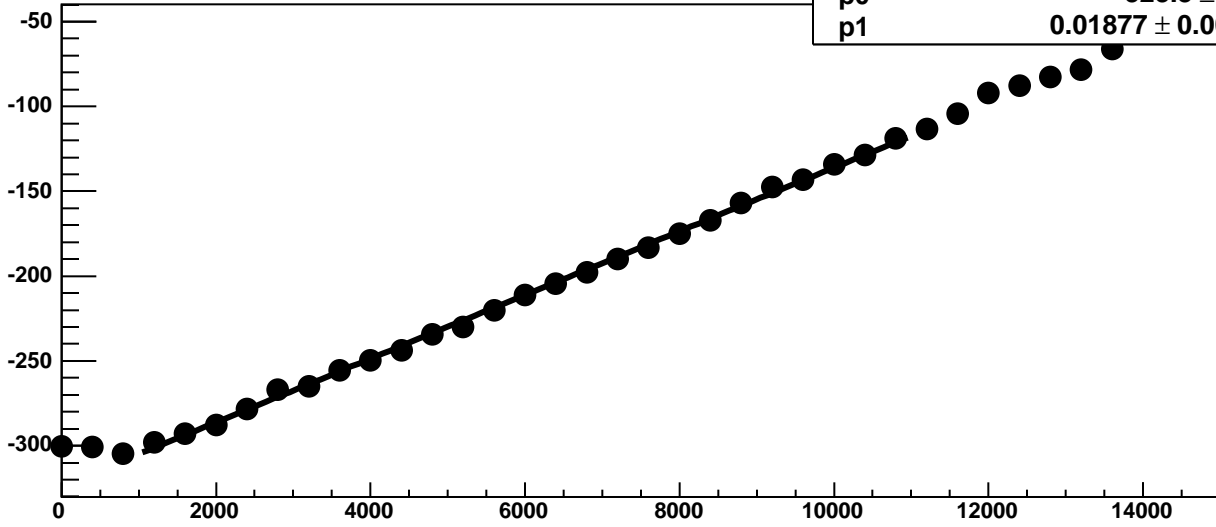
Chip 6, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

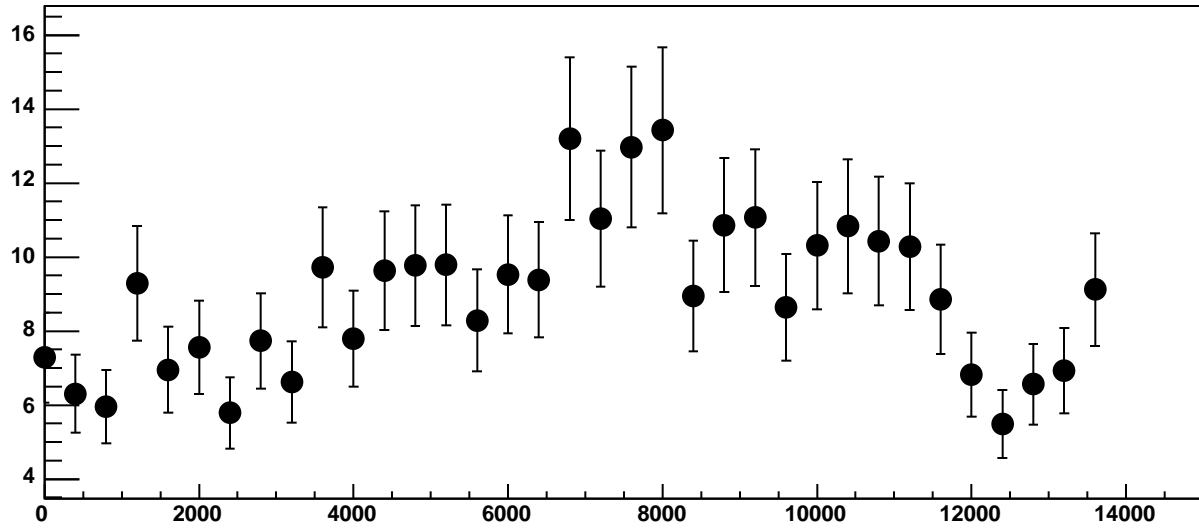


Chip 6, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

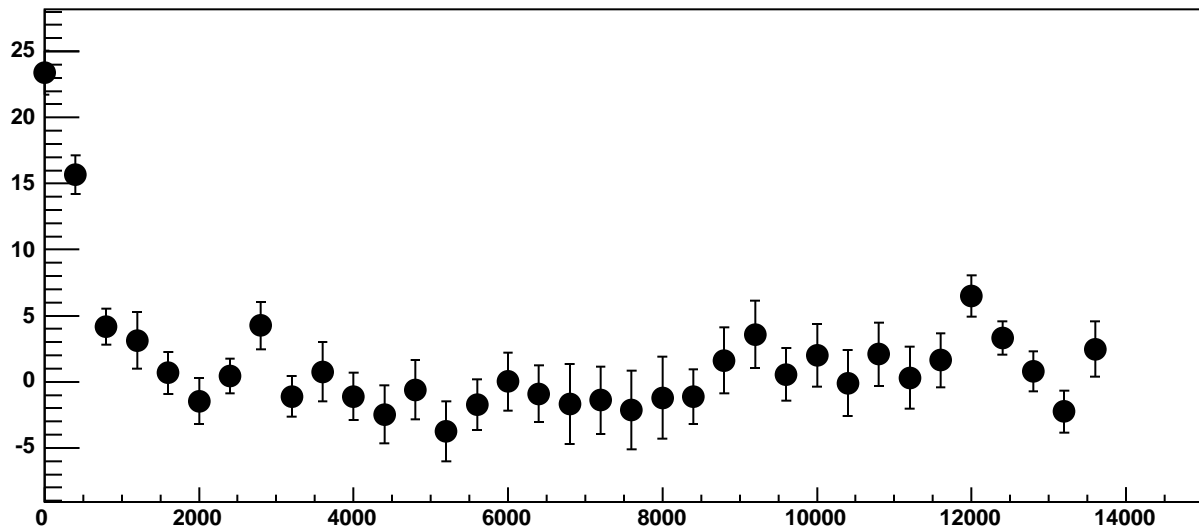


$\chi^2 / \text{ndf}$  20.57 / 23  
p0  $-323.8 \pm 0.8403$   
p1  $0.01877 \pm 0.0001416$

Chip 6, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

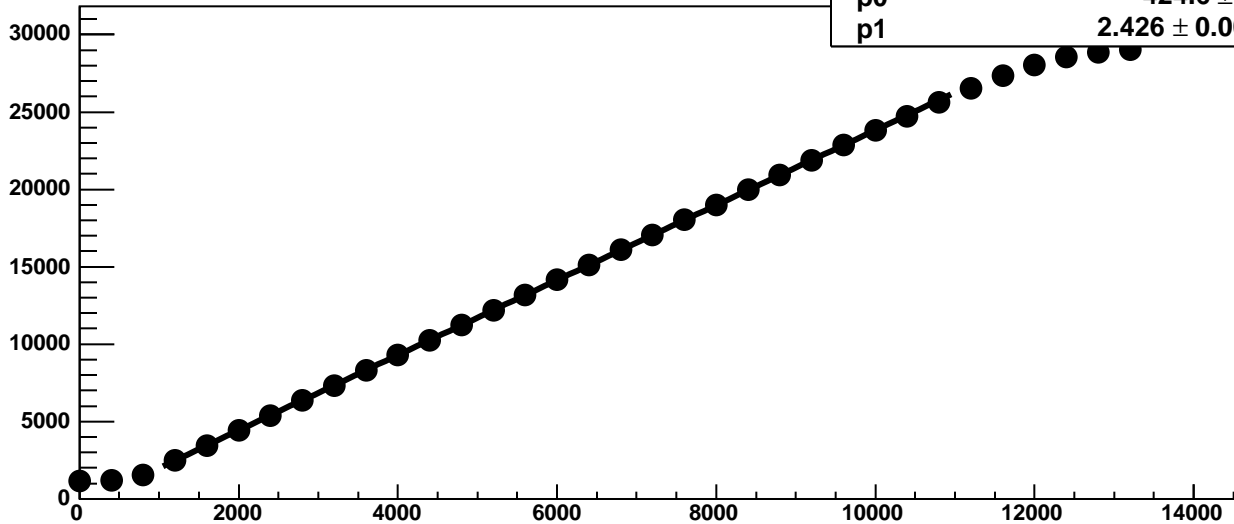


Chip 6, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

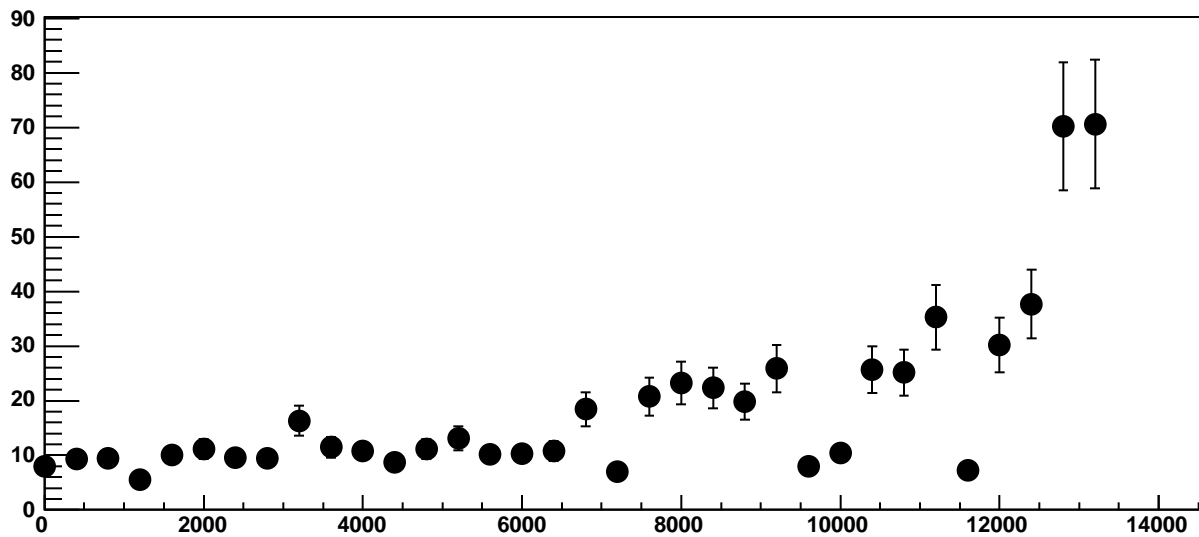


Chip 6, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

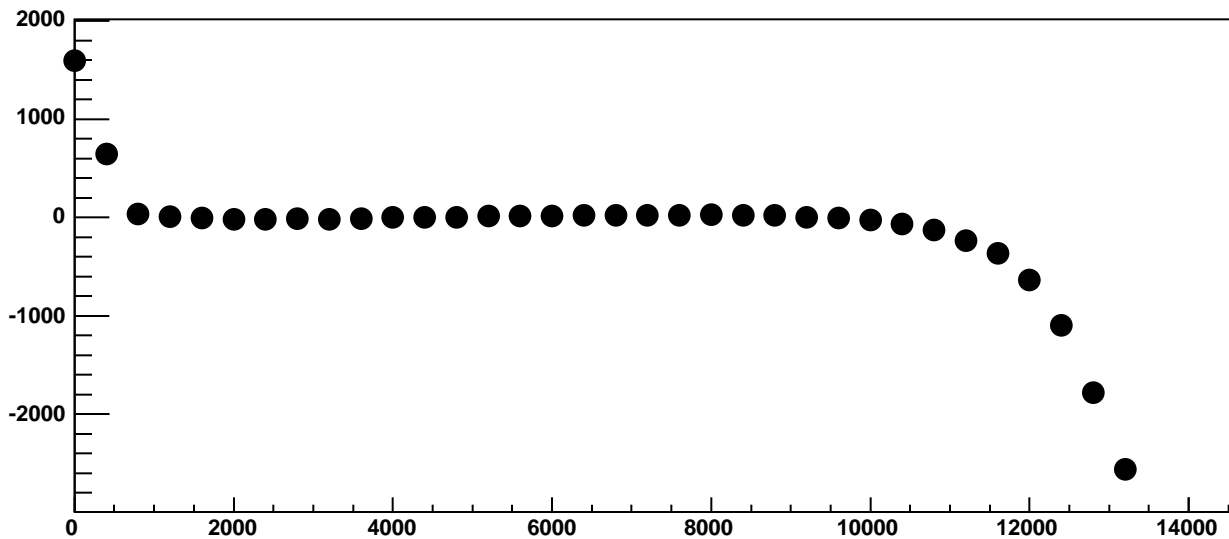
$\chi^2 / \text{ndf}$  1558 / 23  
p0  $-424.6 \pm 0.9858$   
p1  $2.426 \pm 0.0001735$



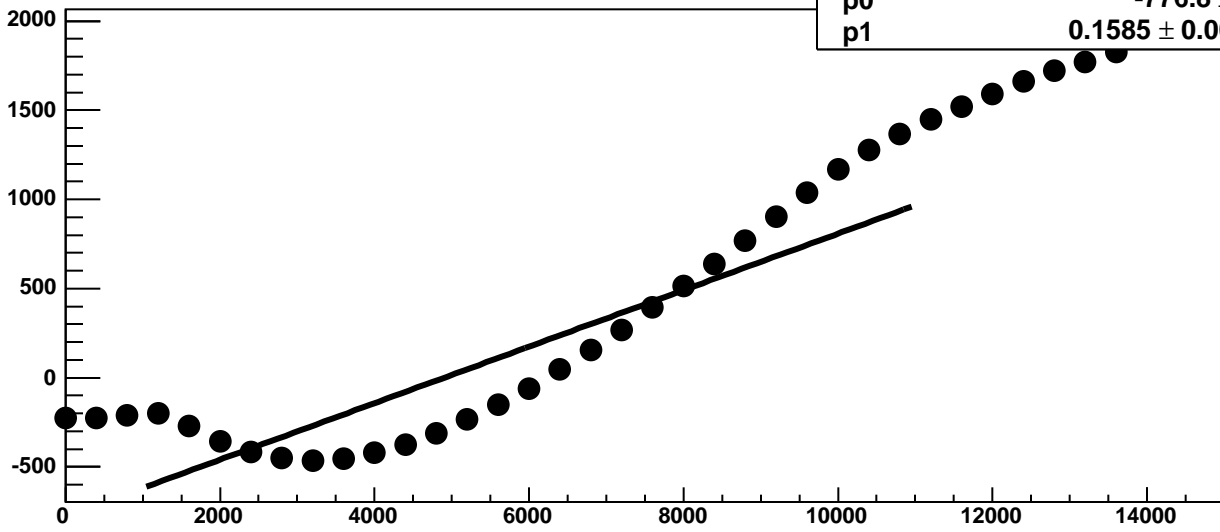
Chip 6, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

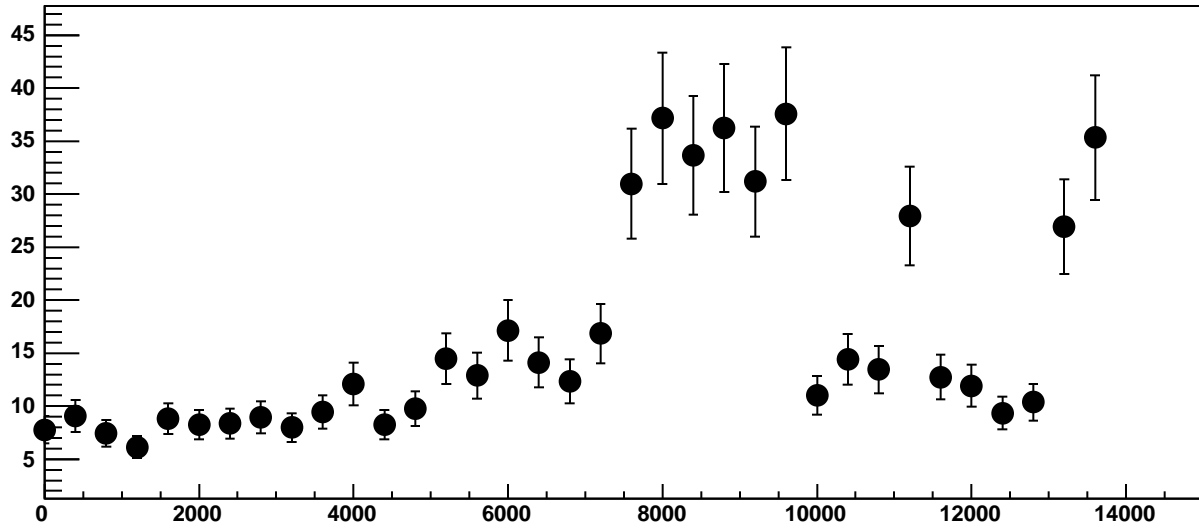


Chip 6, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

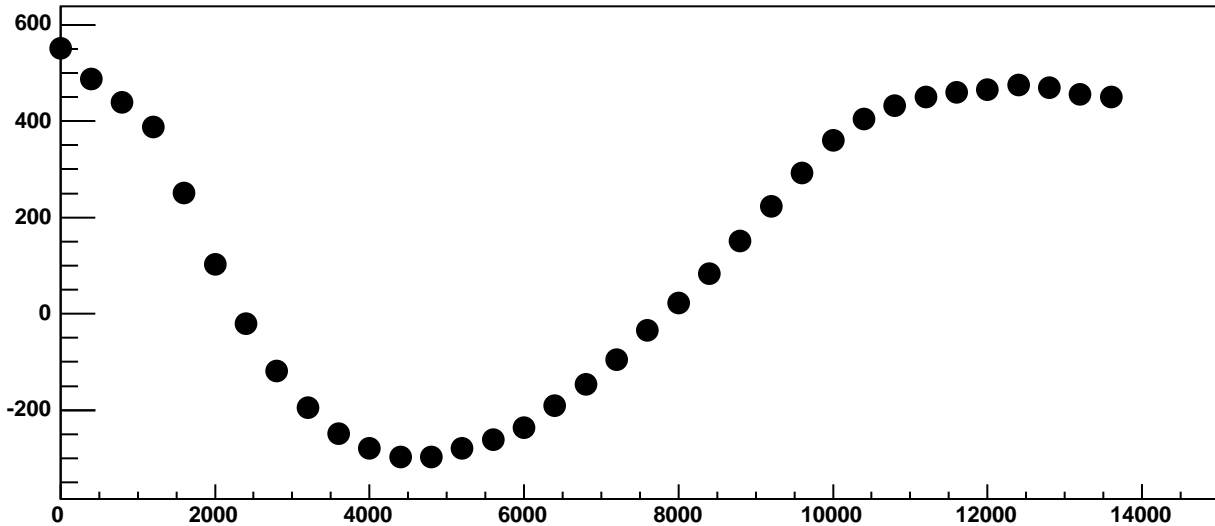


$\chi^2 / \text{ndf}$  2.568e+05 / 23  
p0 -776.8 ± 0.955  
p1 0.1585 ± 0.0001923

Chip 6, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

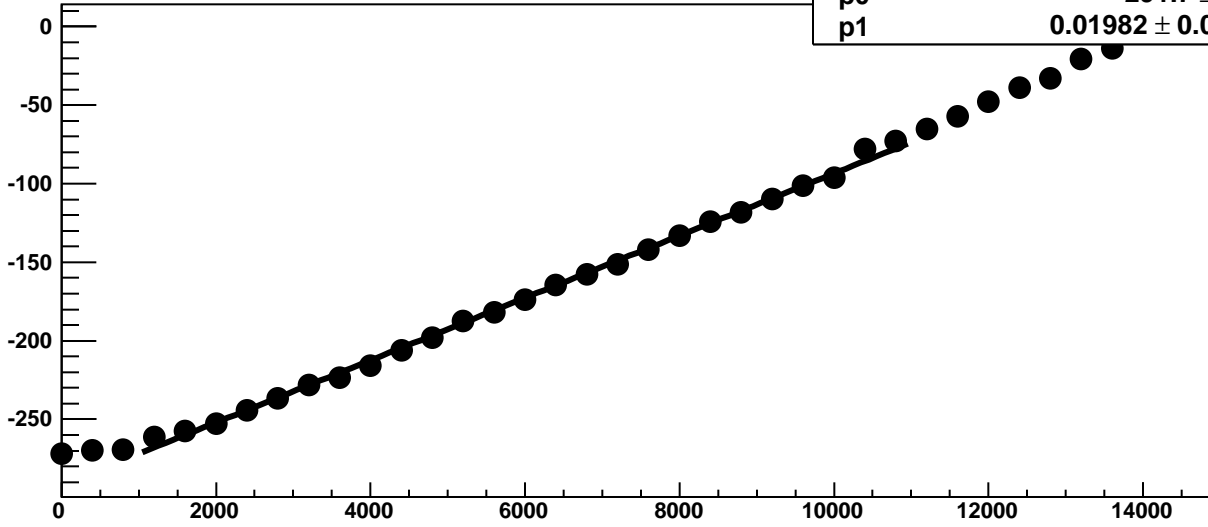


Chip 6, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

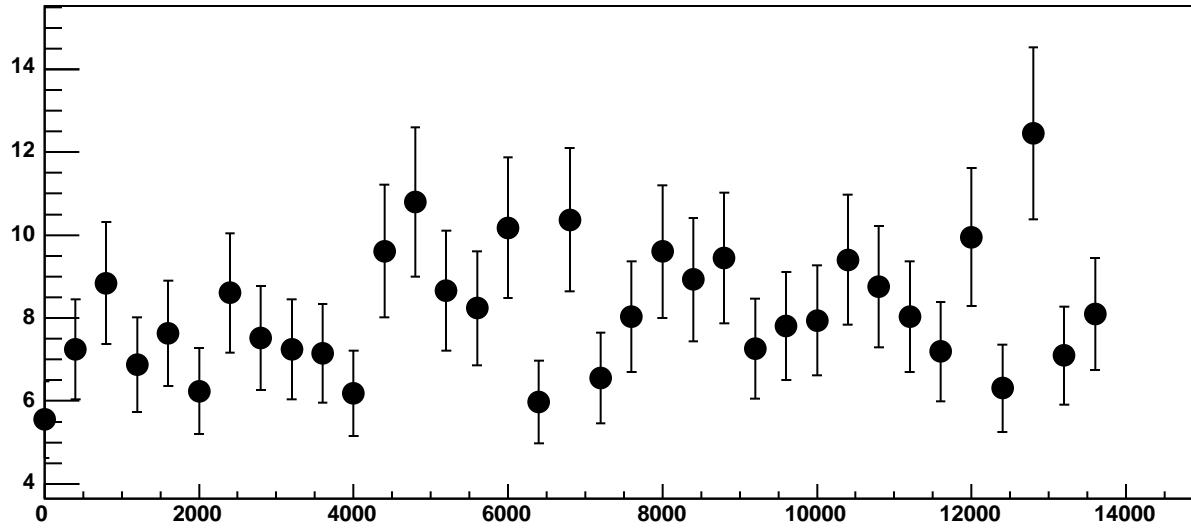




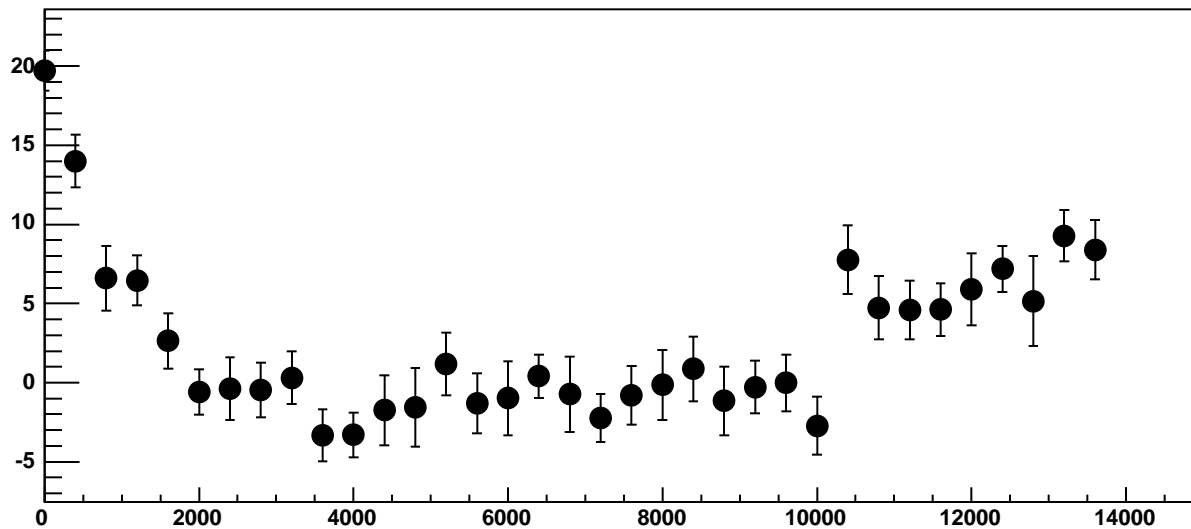
Chip 6, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



Chip 6, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

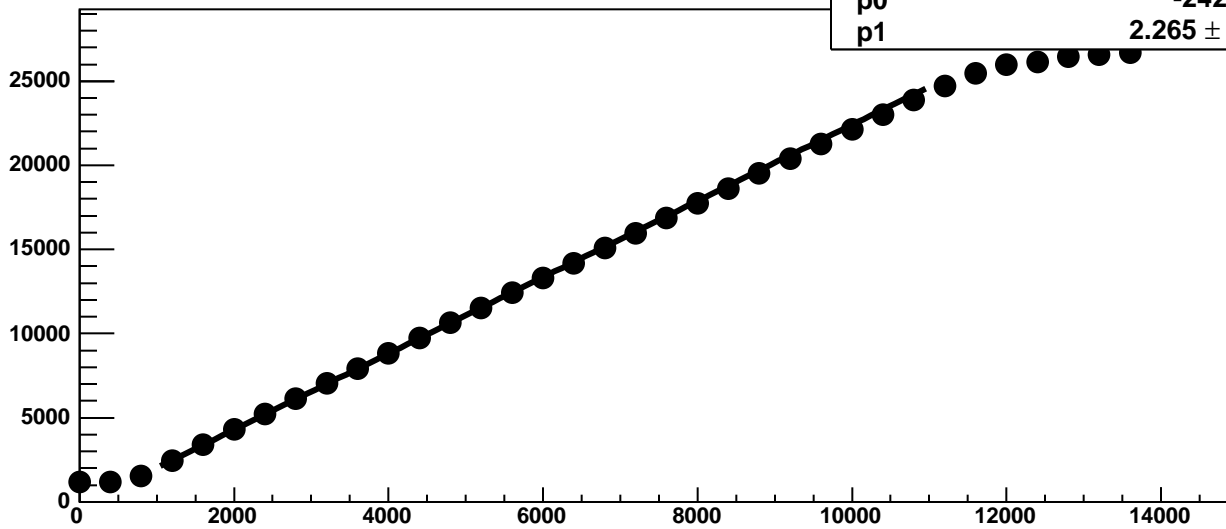


Chip 6, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

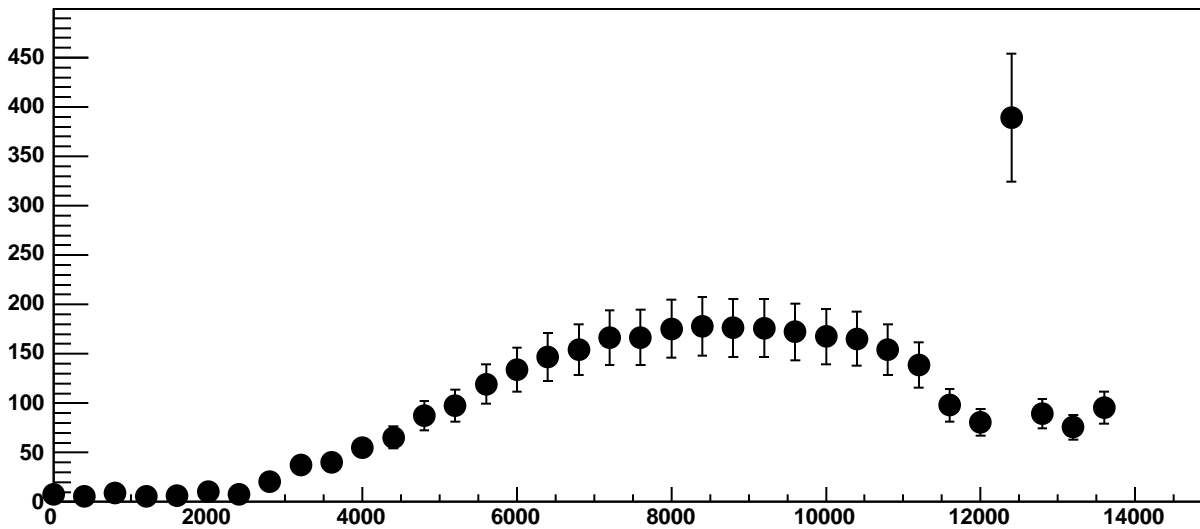


Chip 6, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC

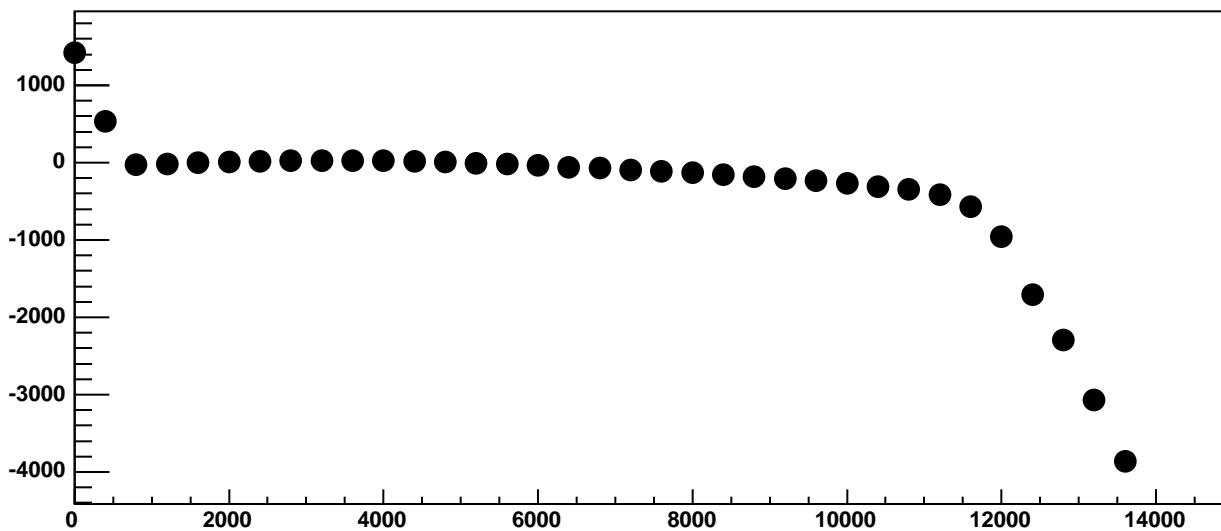
$\chi^2 / \text{ndf}$  635.2 / 23  
p0  $-242.5 \pm 2$   
p1  $2.265 \pm 0.00101$



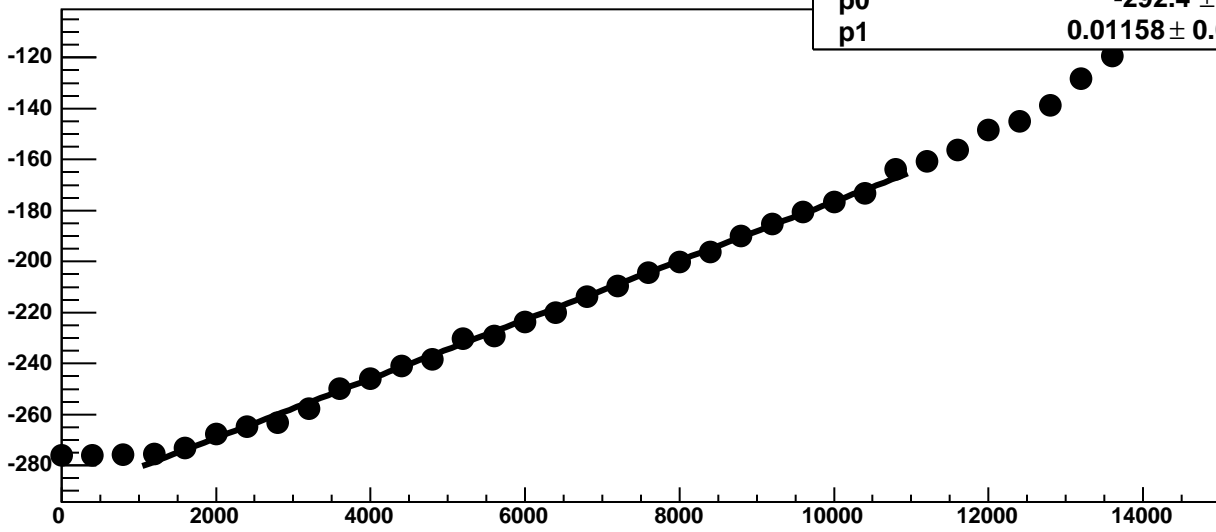
Chip 6, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 6, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

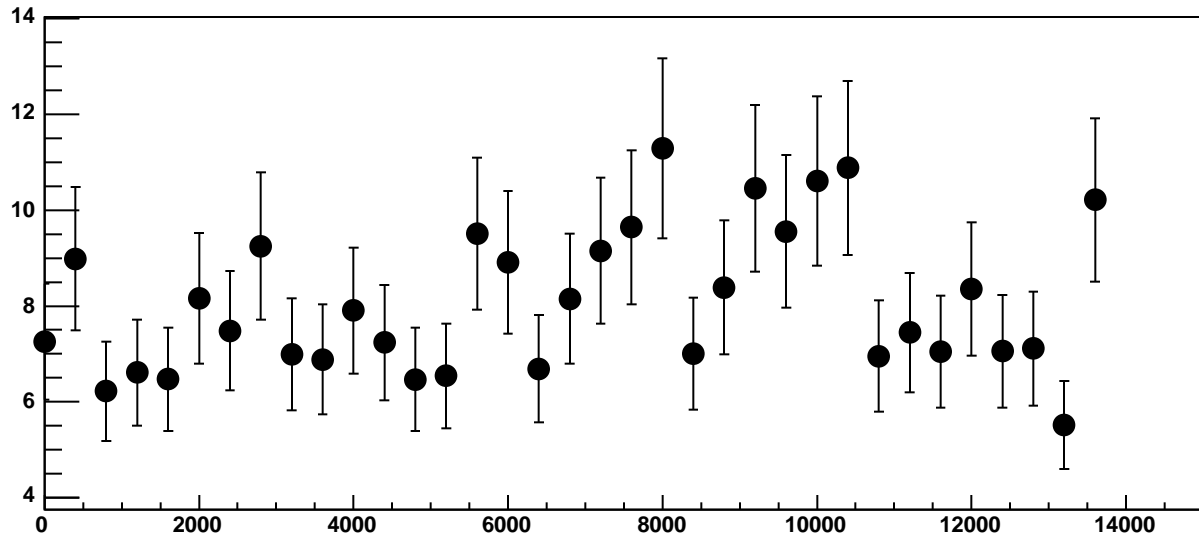


Chip 6, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

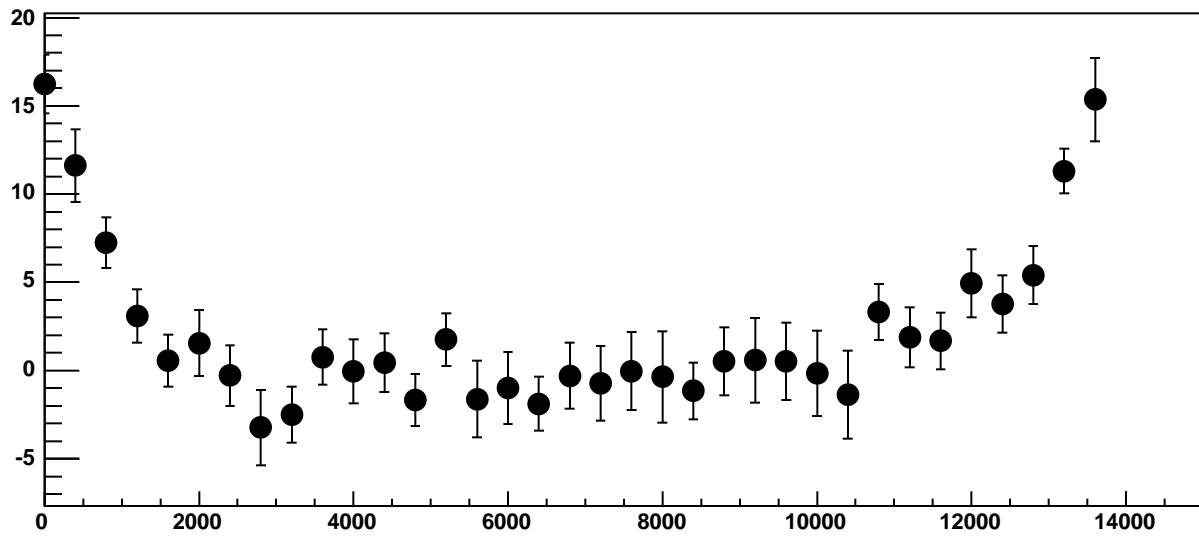


$\chi^2 / \text{ndf}$  20.46 / 23  
p0  $-292.4 \pm 0.7869$   
p1  $0.01158 \pm 0.000127$

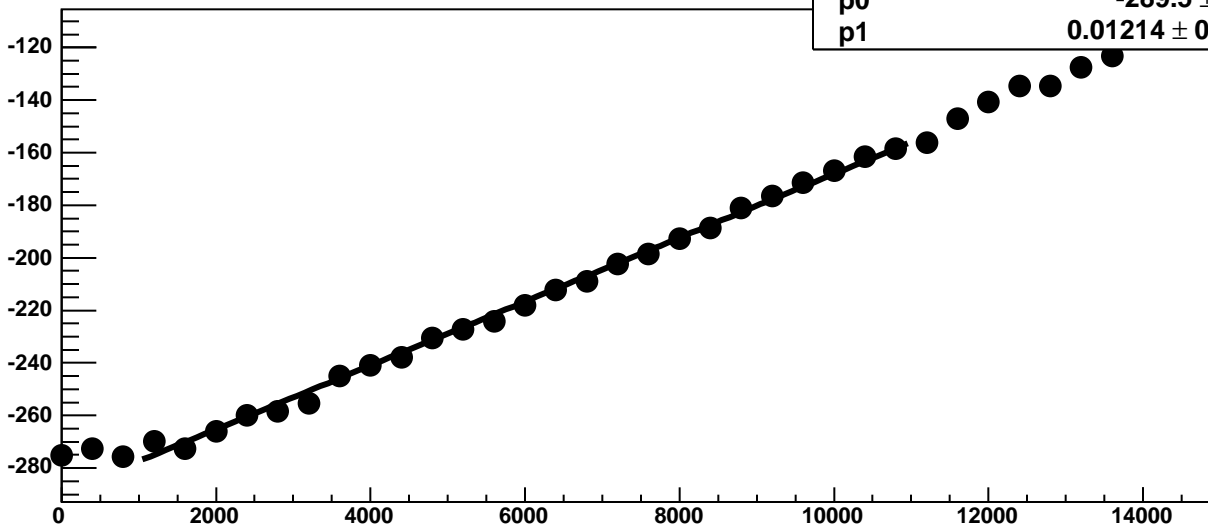
Chip 6, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



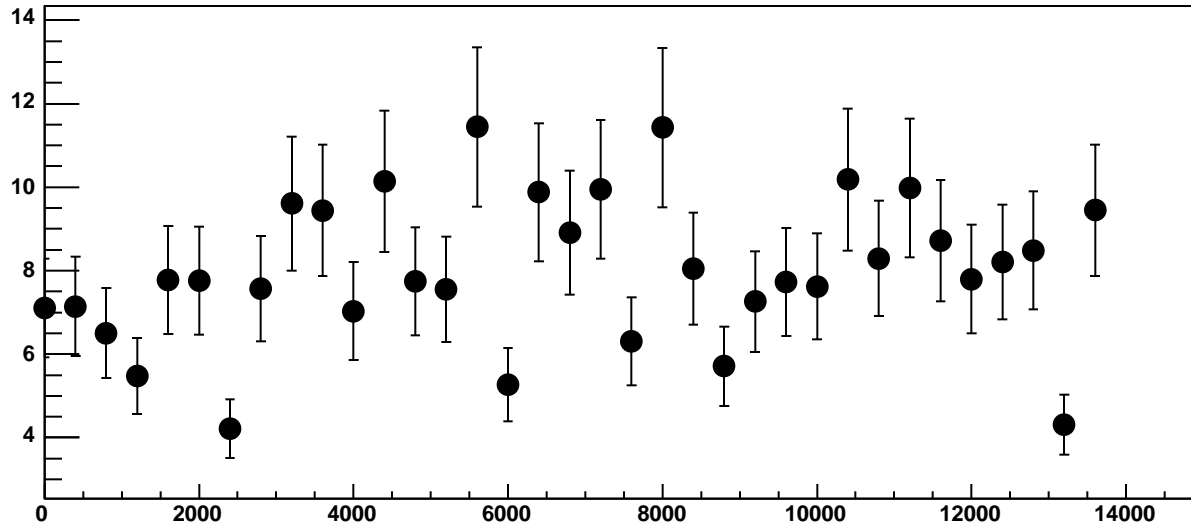
Chip 6, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



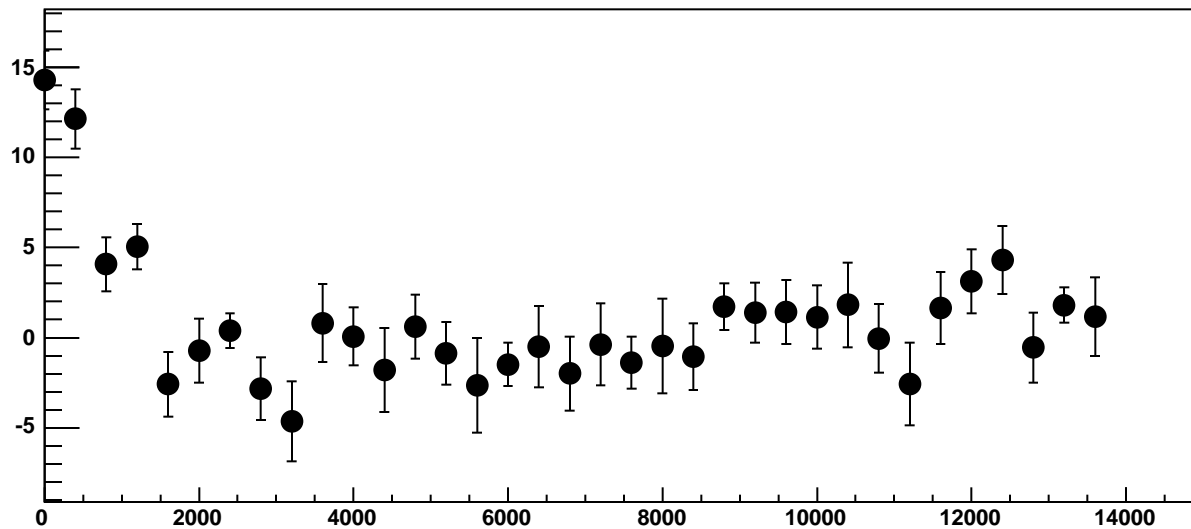
Chip 6, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 6, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

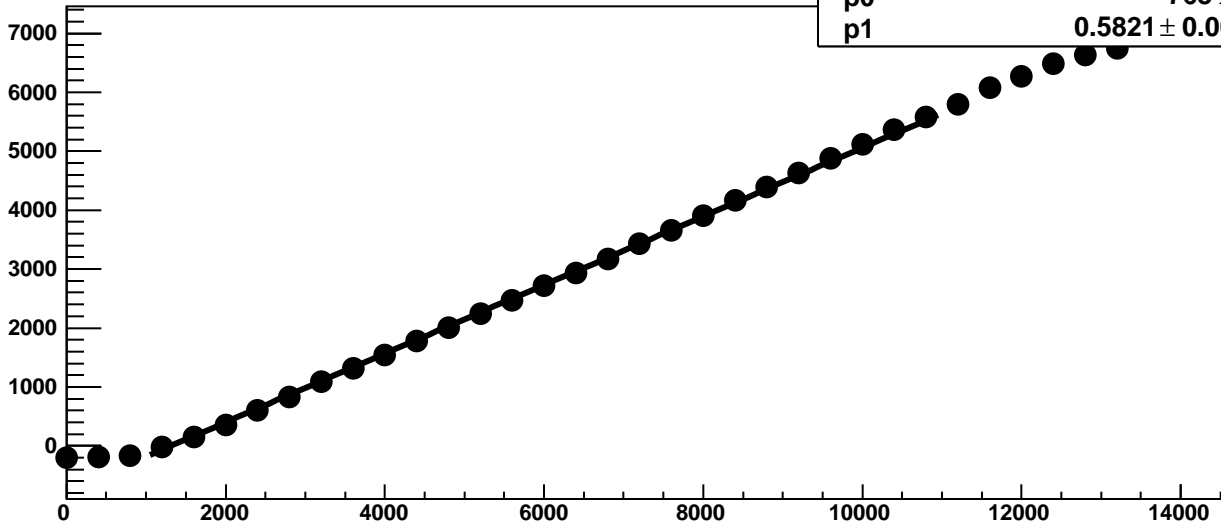


Chip 6, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

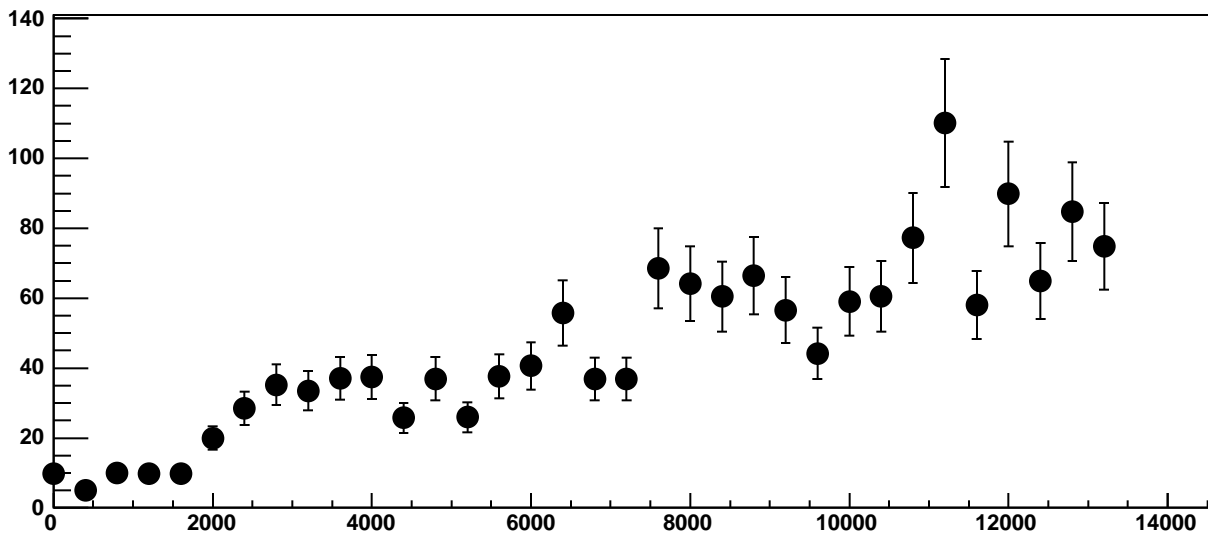


Chip 6, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

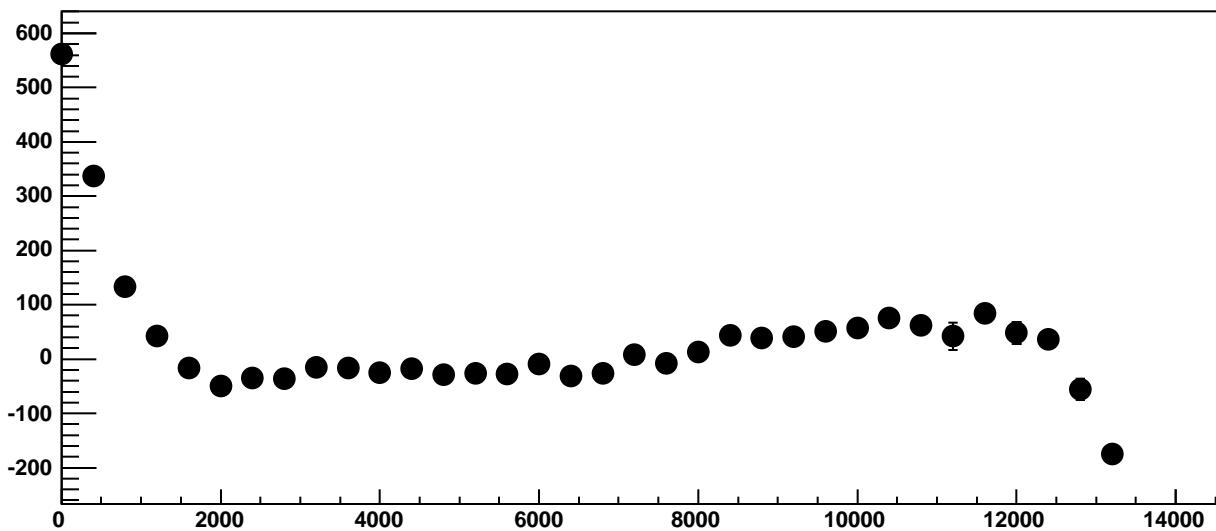
$\chi^2 / \text{ndf}$  761.9 / 23  
p0  $-763 \pm 1.899$   
p1  $0.5821 \pm 0.0005035$



Chip 6, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

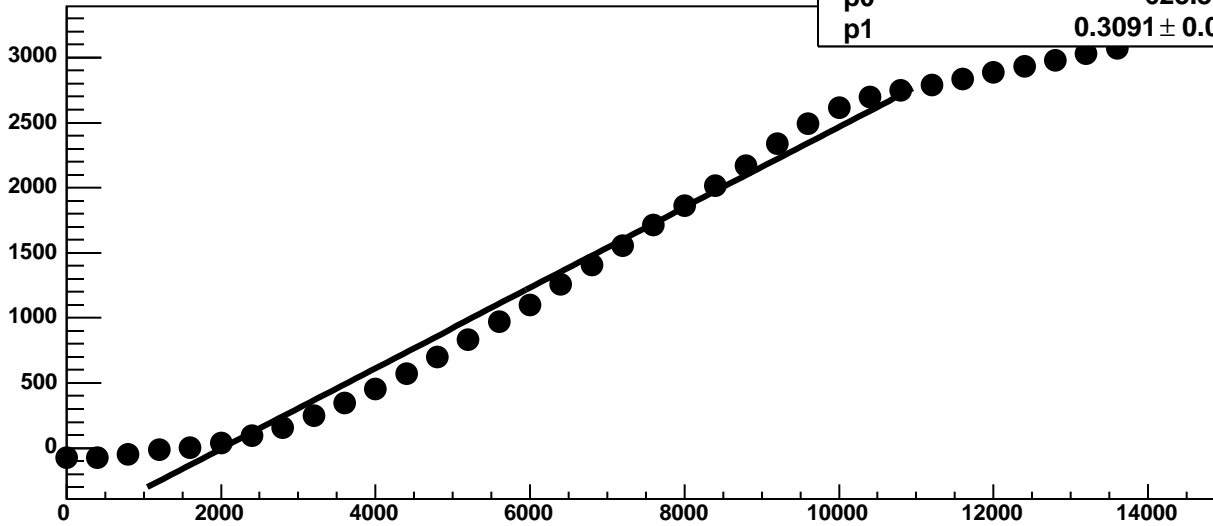


Chip 6, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

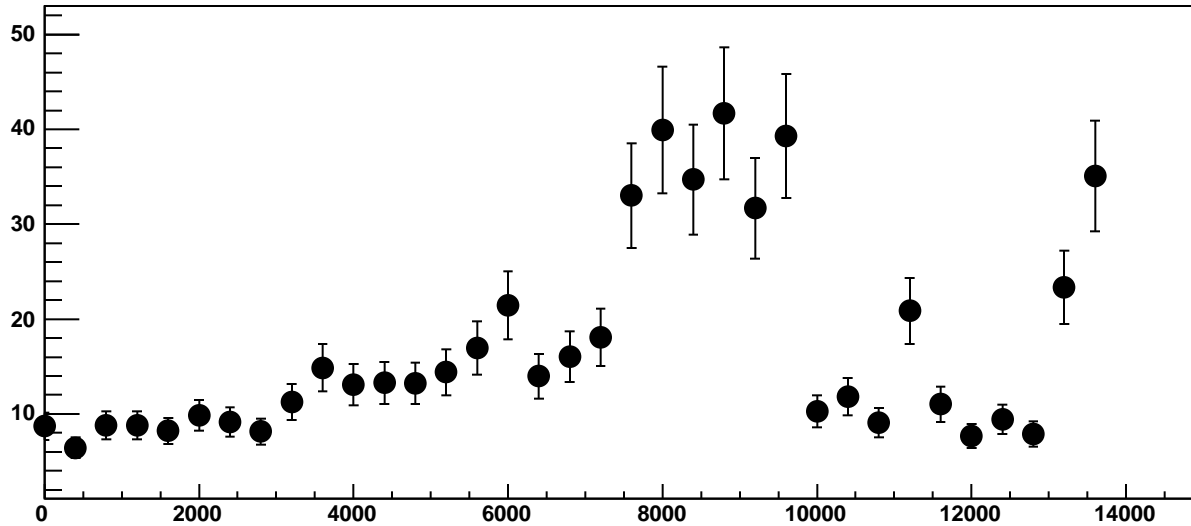


Chip 7, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

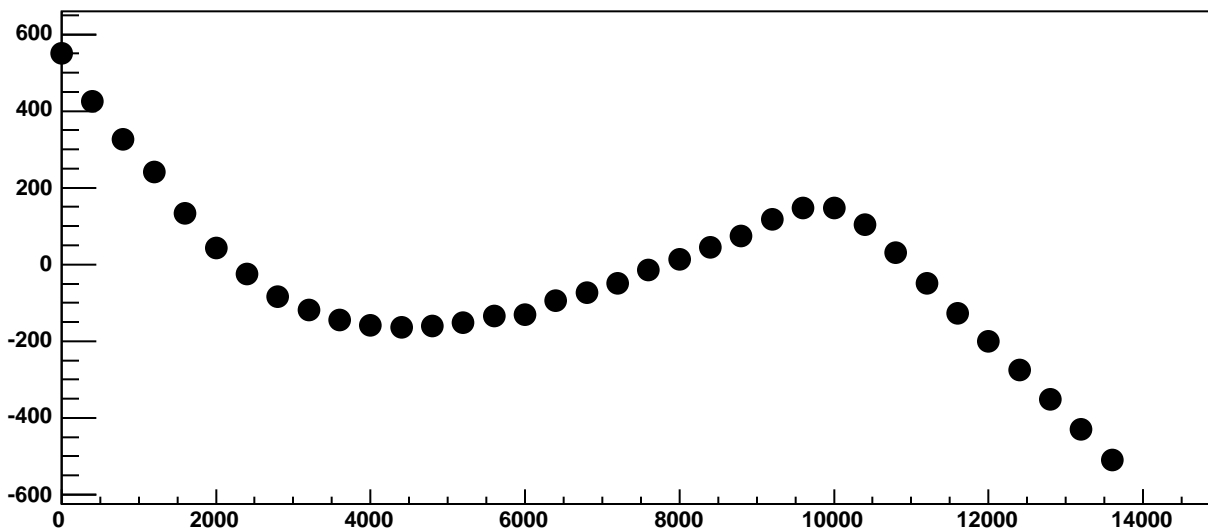
$\chi^2 / \text{ndf}$  4.578e+04 / 23  
p0 -623.3 ± 1.056  
p1 0.3091 ± 0.0001815



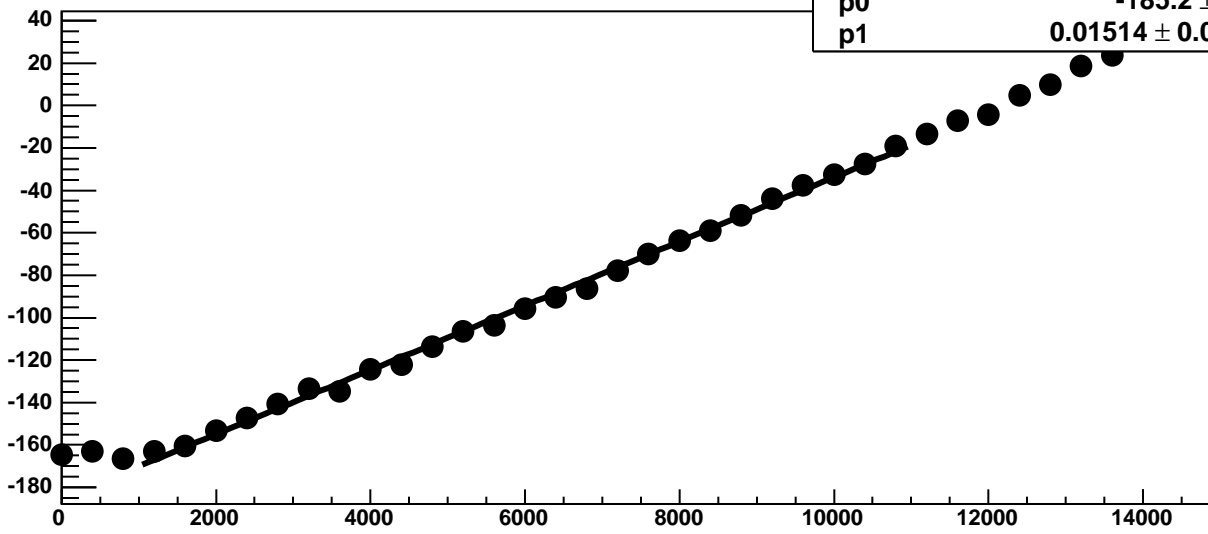
Chip 7, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

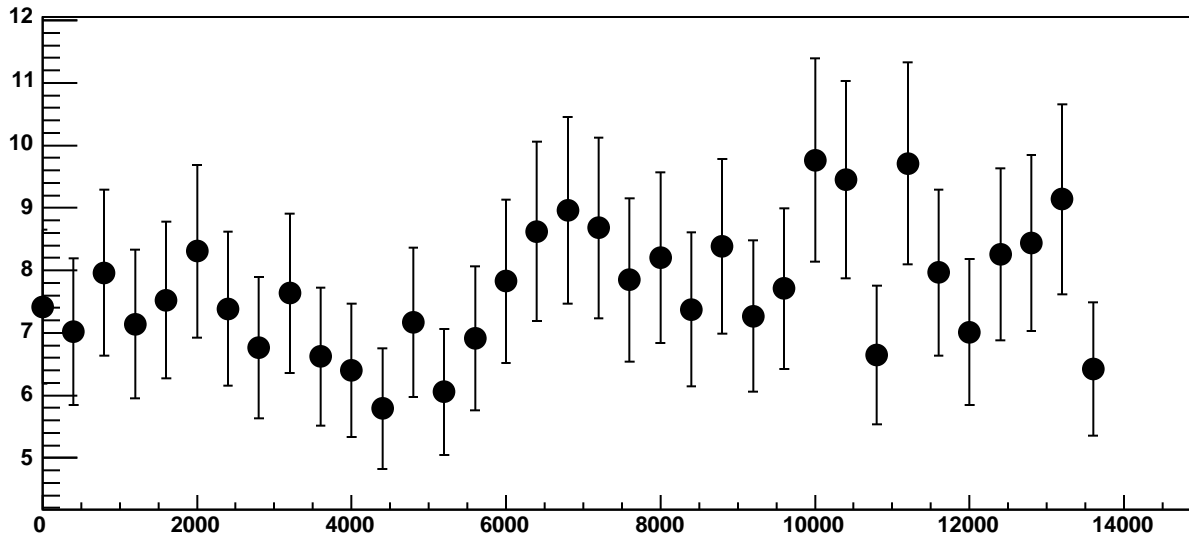


Chip 7, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

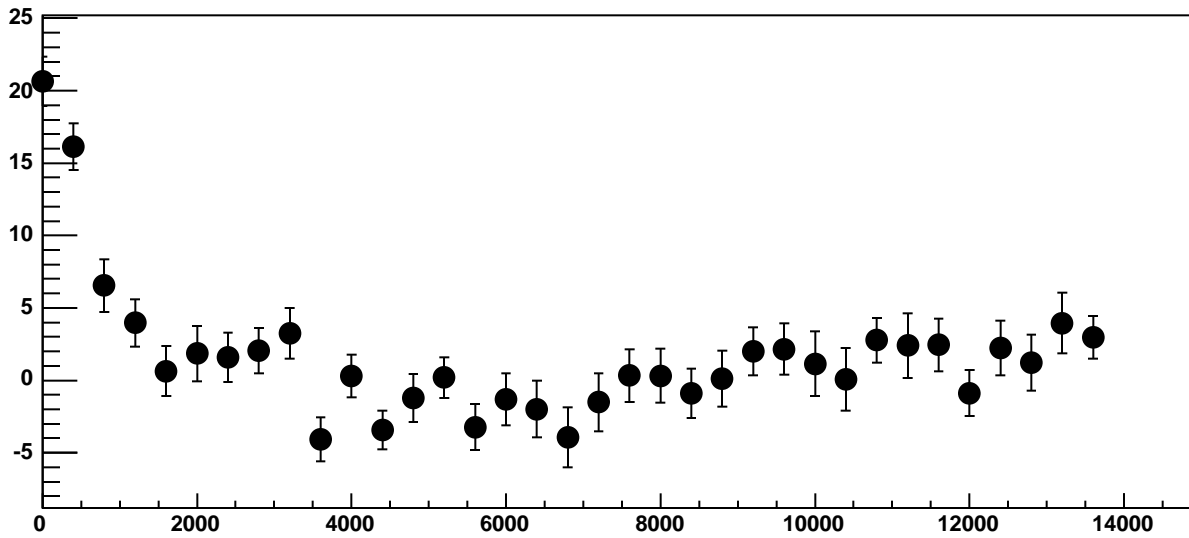


$\chi^2 / \text{ndf}$  44.21 / 23  
p0  $-185.2 \pm 0.7705$   
p1  $0.01514 \pm 0.0001208$

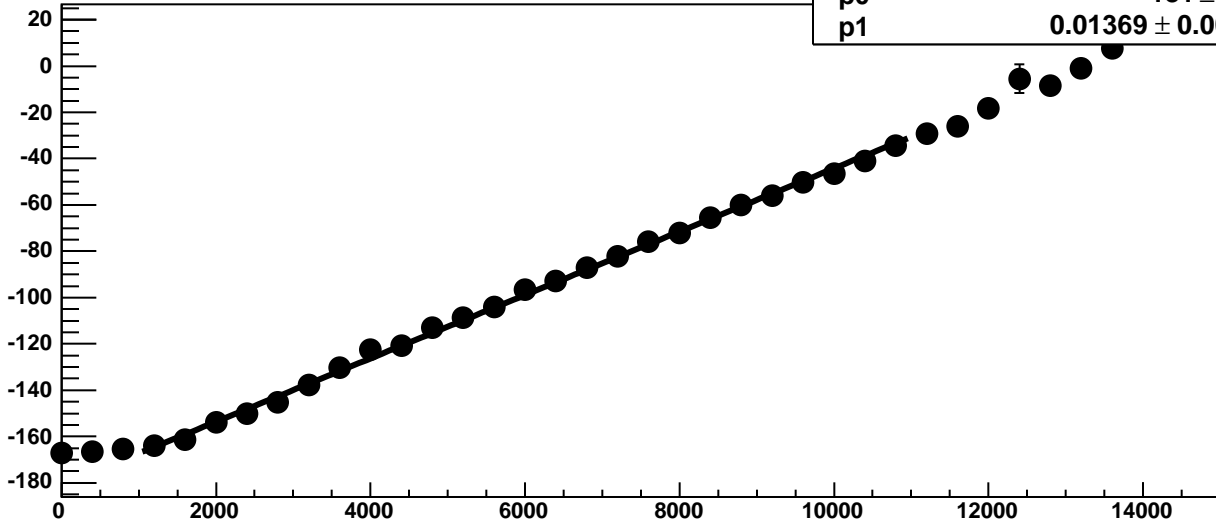
Chip 7, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



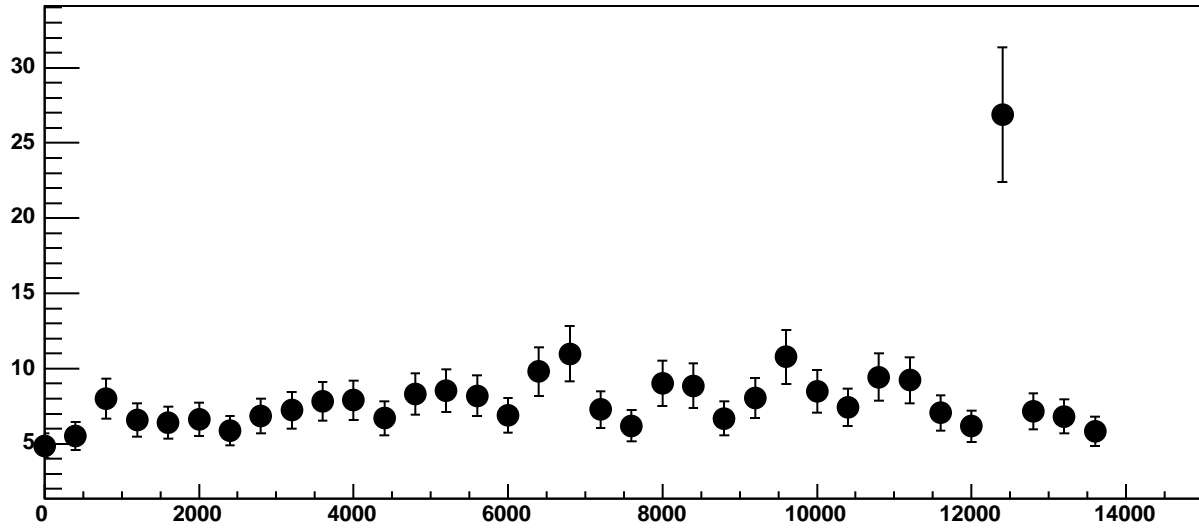
Chip 7, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



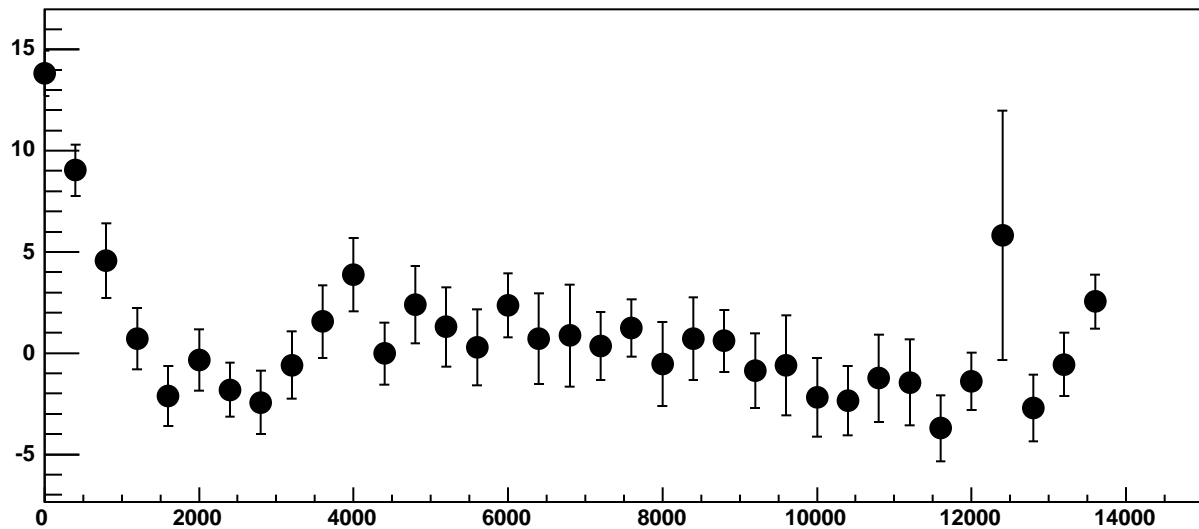
Chip 7, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



Chip 7, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

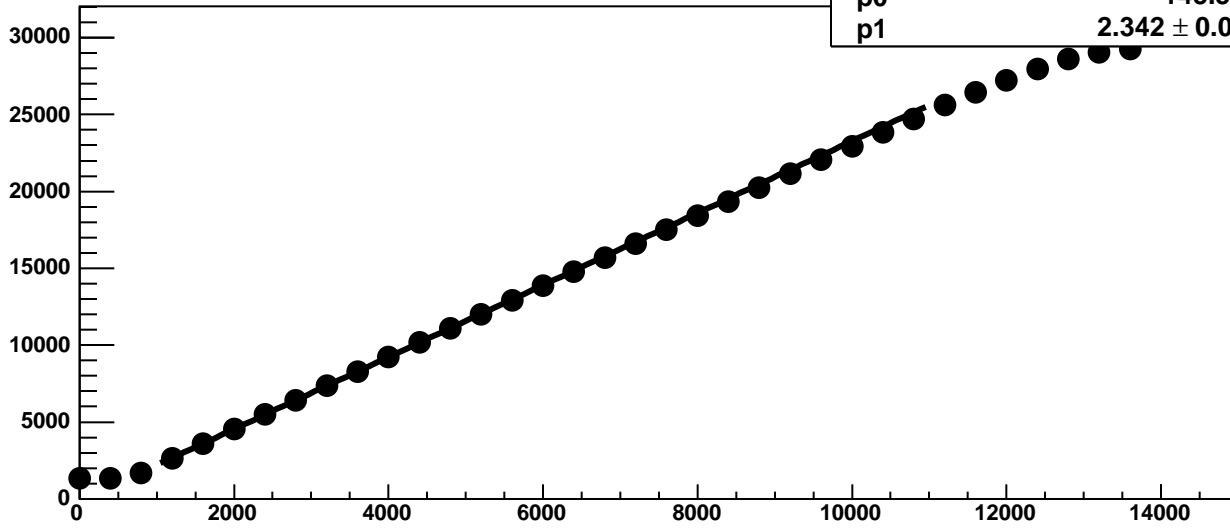


Chip 7, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC





Chip 7, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

597 / 23

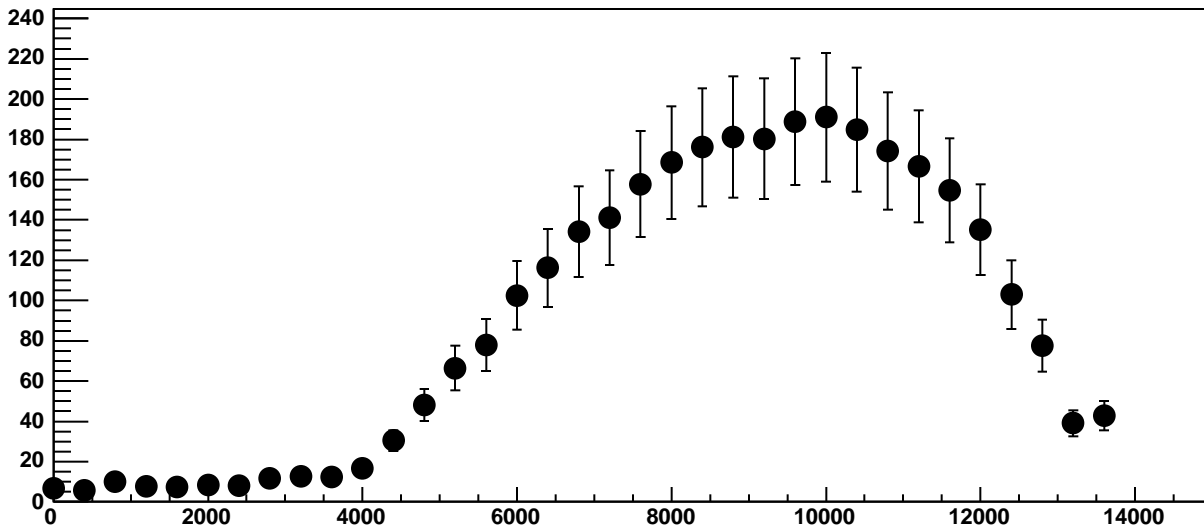
p0

$-145.5 \pm 1.921$

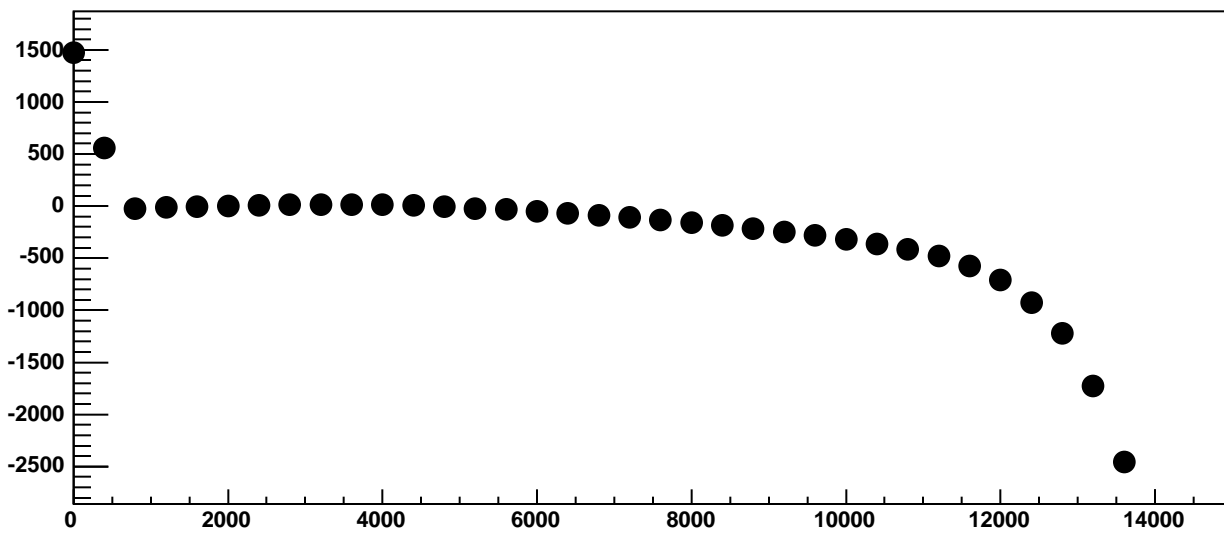
p1

$2.342 \pm 0.0007722$

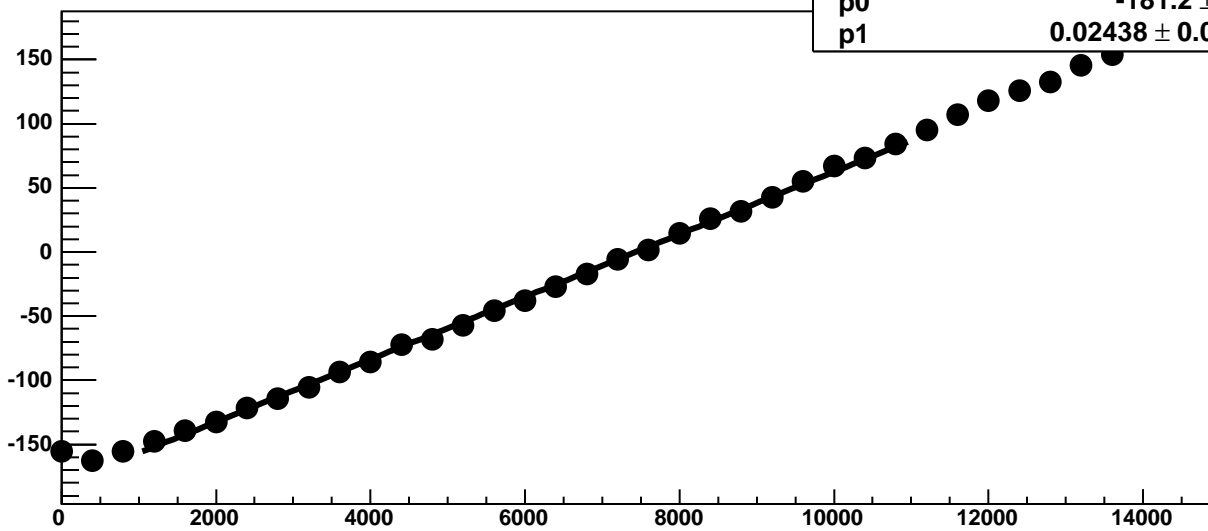
Chip 7, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC

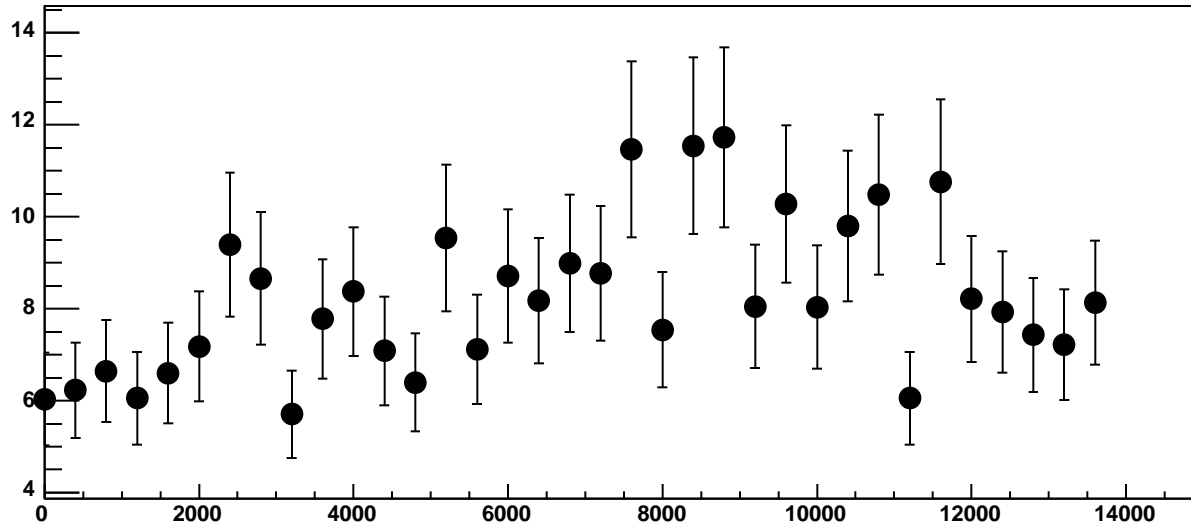


Chip 7, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

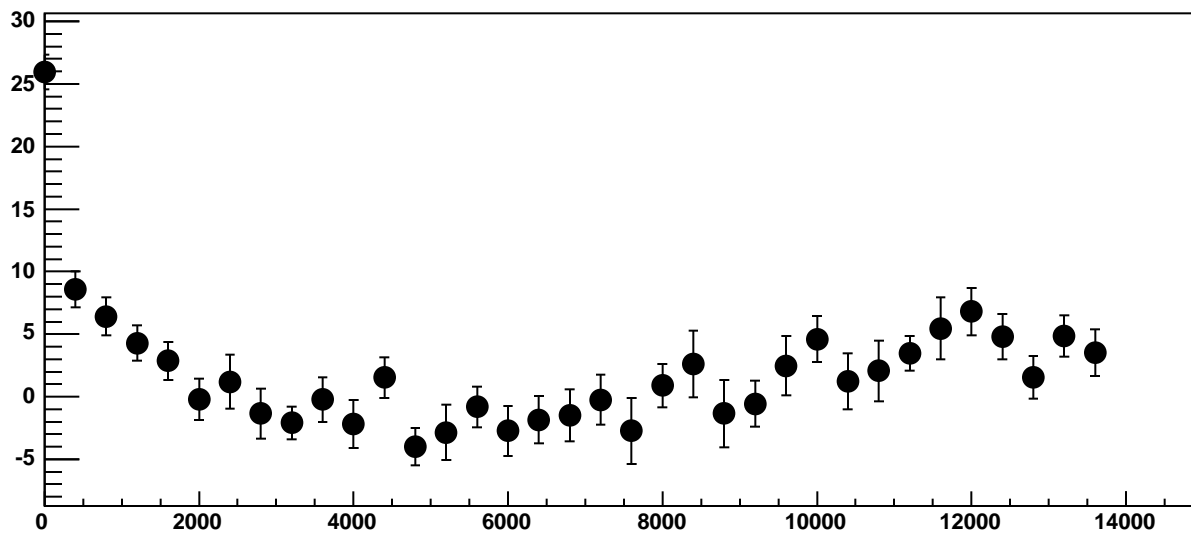


$\chi^2 / \text{ndf}$  42.38 / 23  
p0 -181.2 ± 0.7804  
p1 0.02438 ± 0.0001299

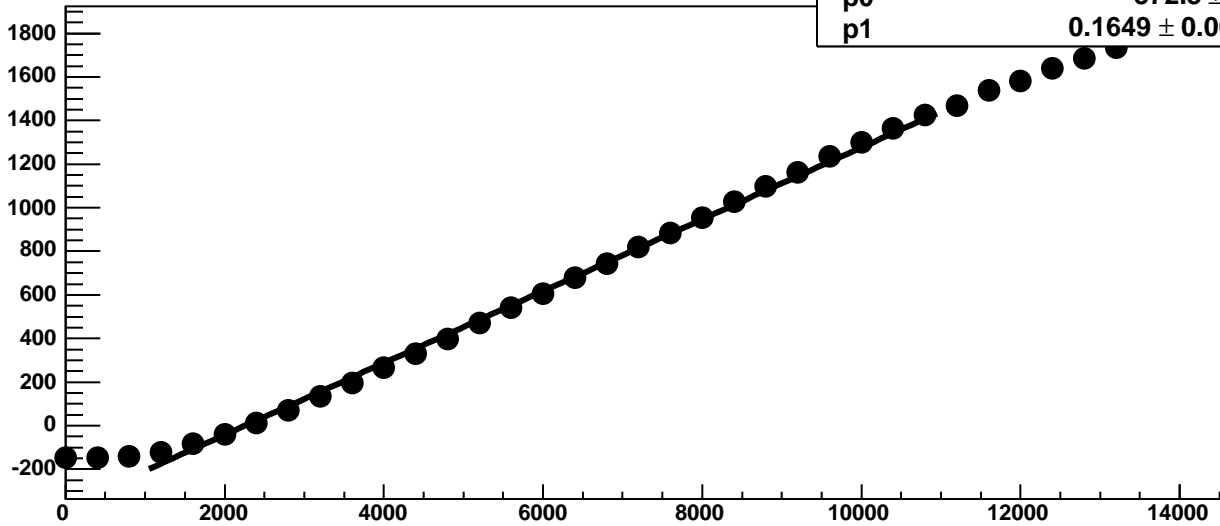
Chip 7, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



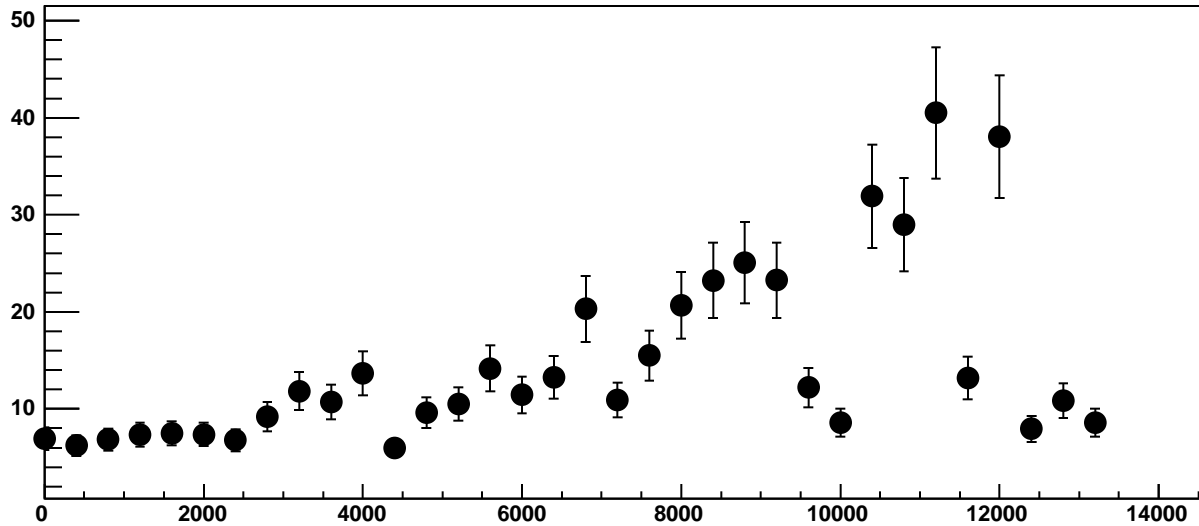
Chip 7, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



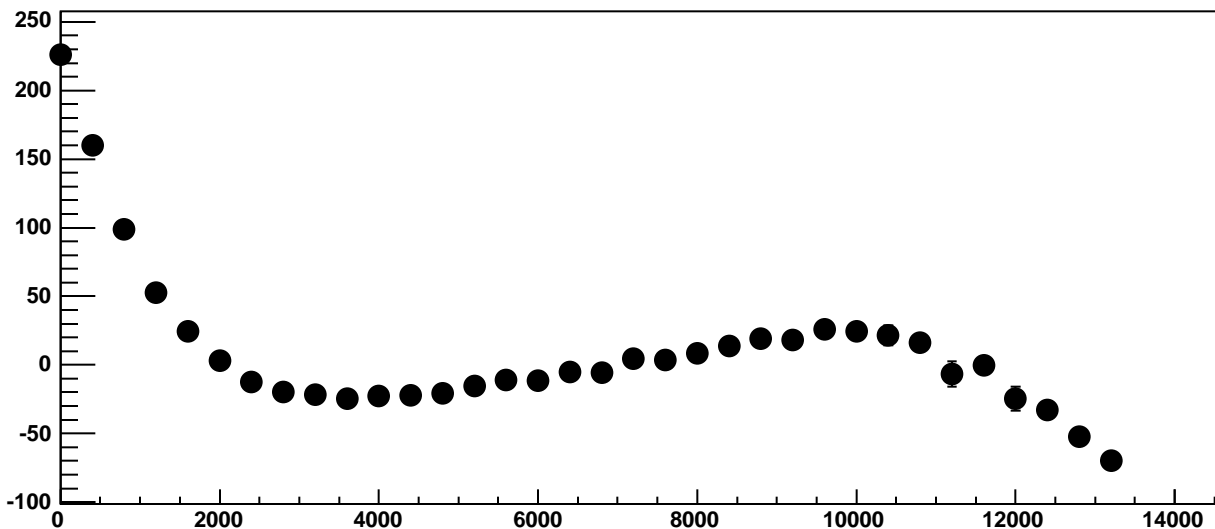
Chip 7, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



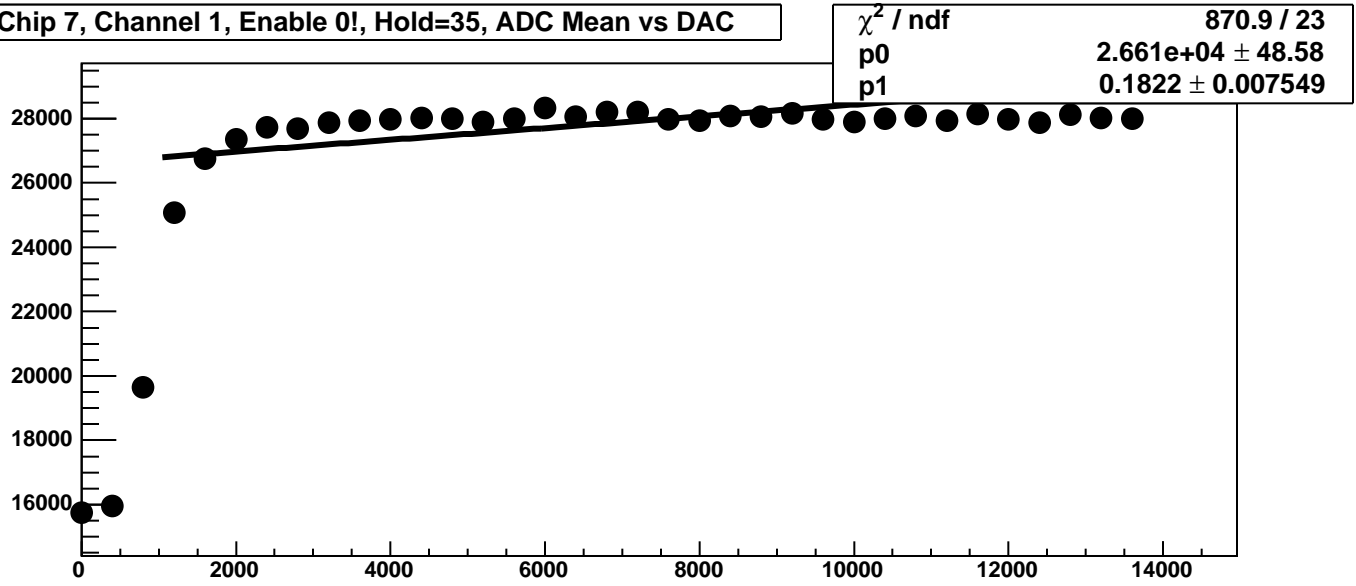
Chip 7, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



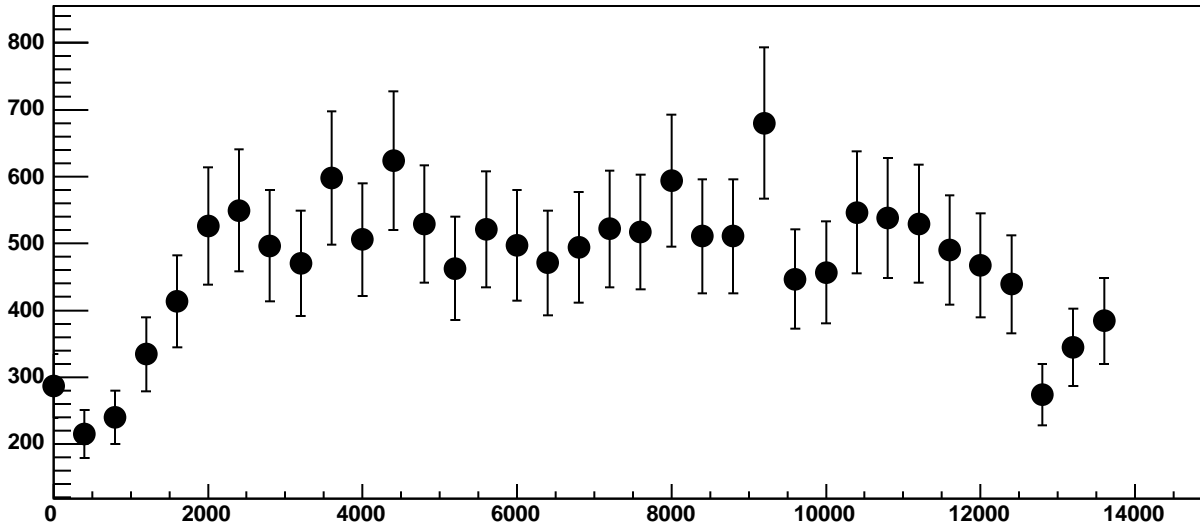
Chip 7, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



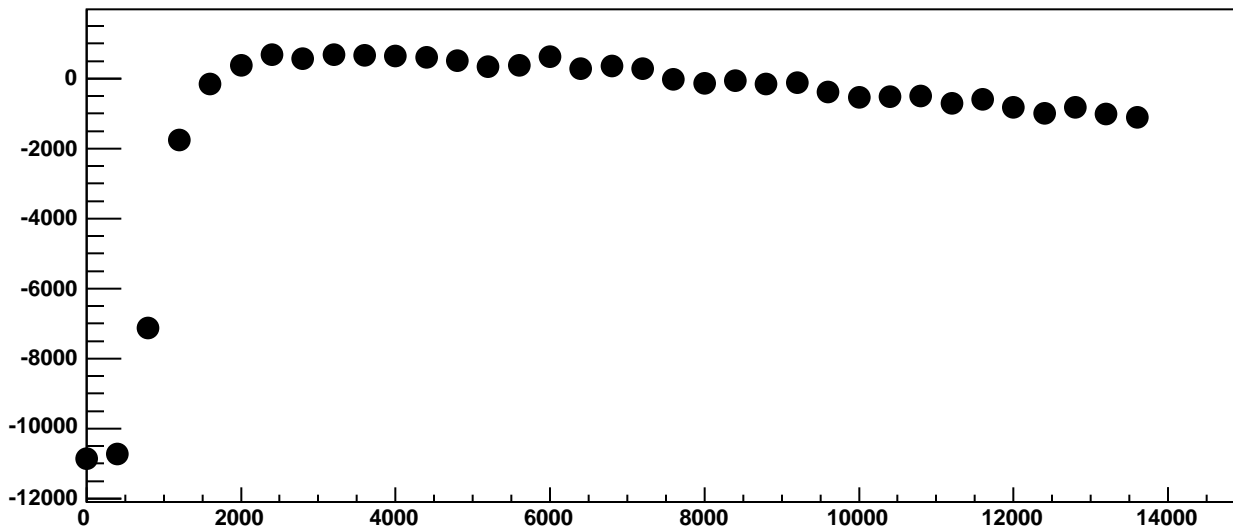
Chip 7, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC



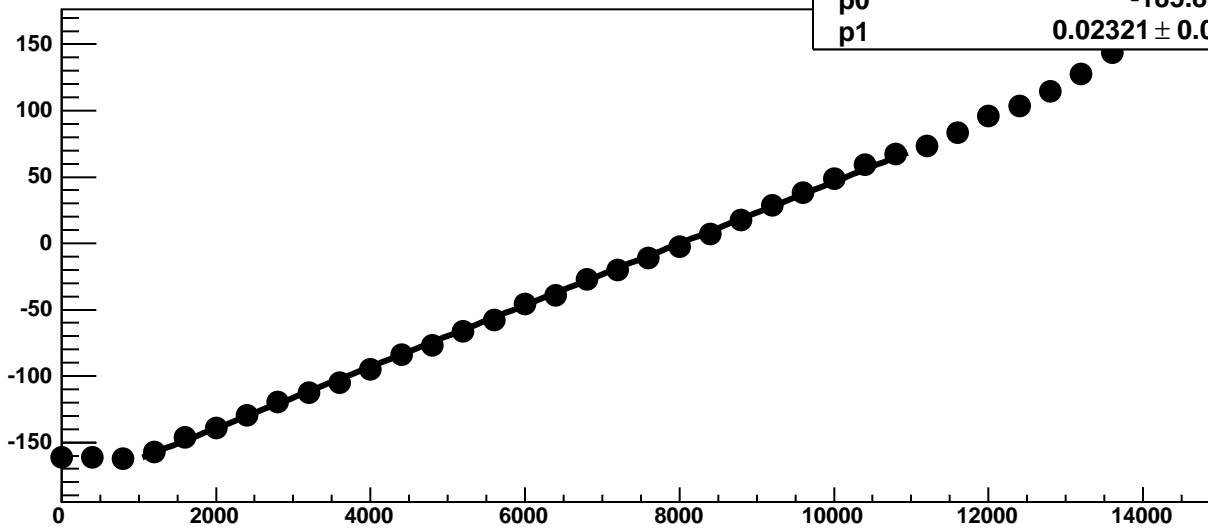
Chip 7, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

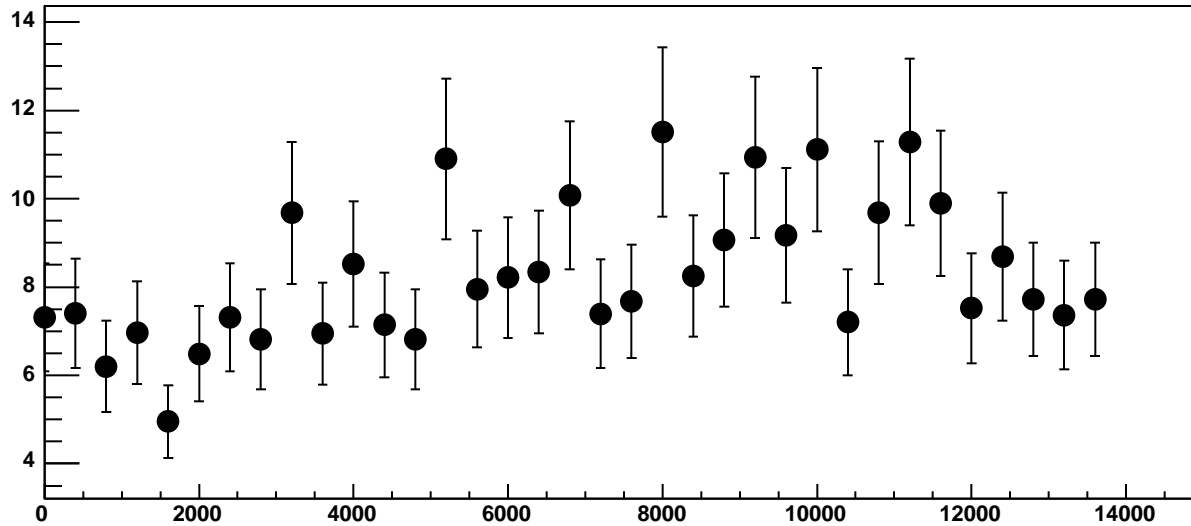


Chip 7, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

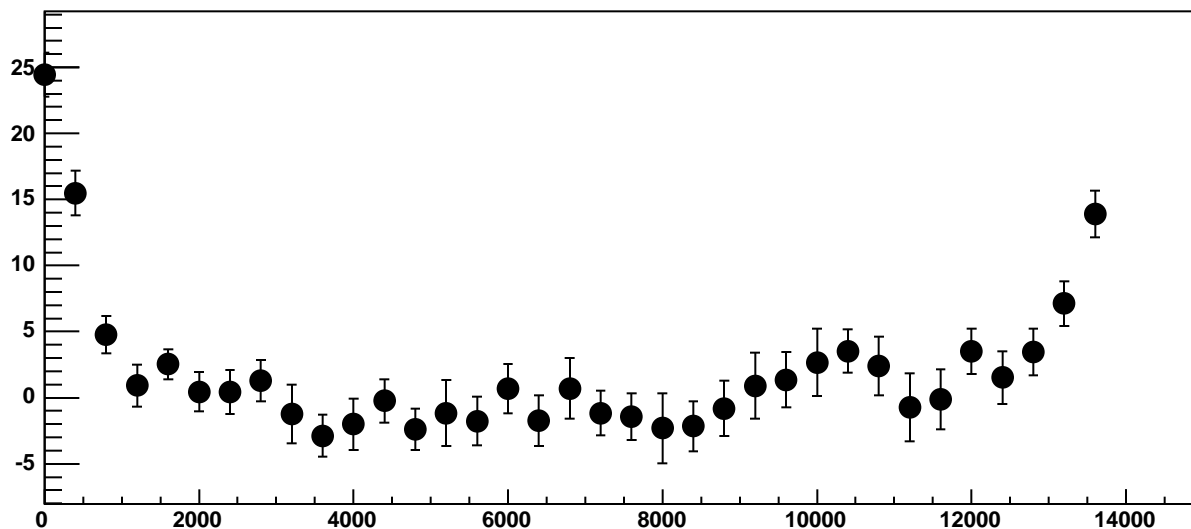


$\chi^2 / \text{ndf}$  26.14 / 23  
p0  $-185.8 \pm 0.738$   
p1  $0.02321 \pm 0.0001225$

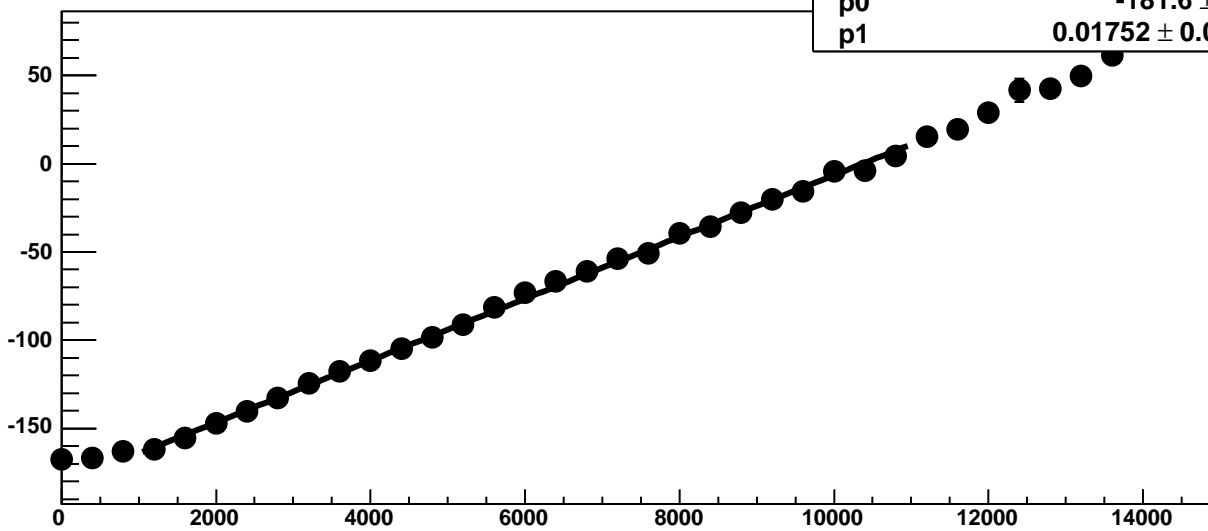
Chip 7, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



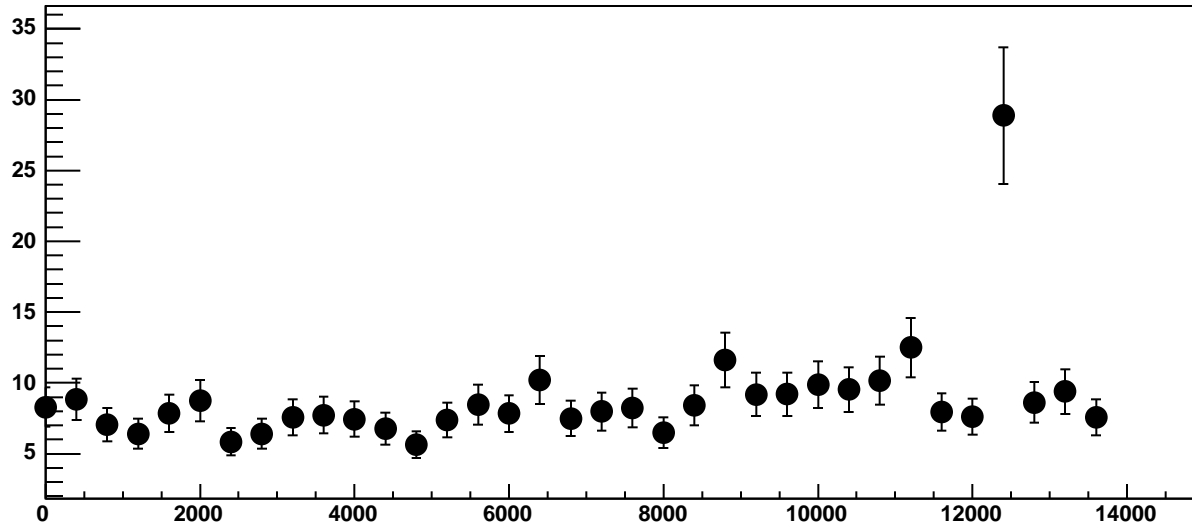
Chip 7, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



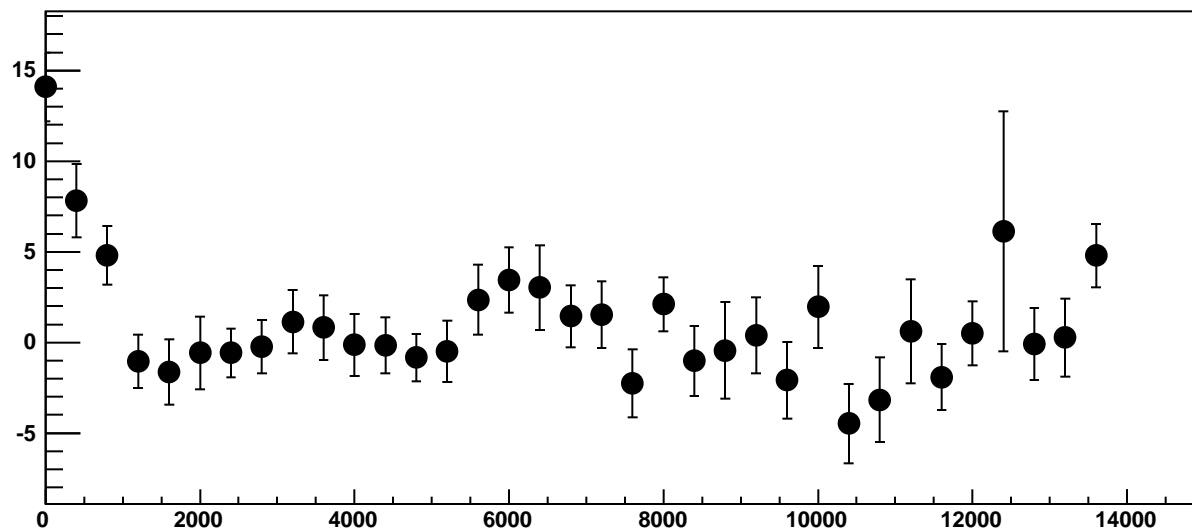
Chip 7, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



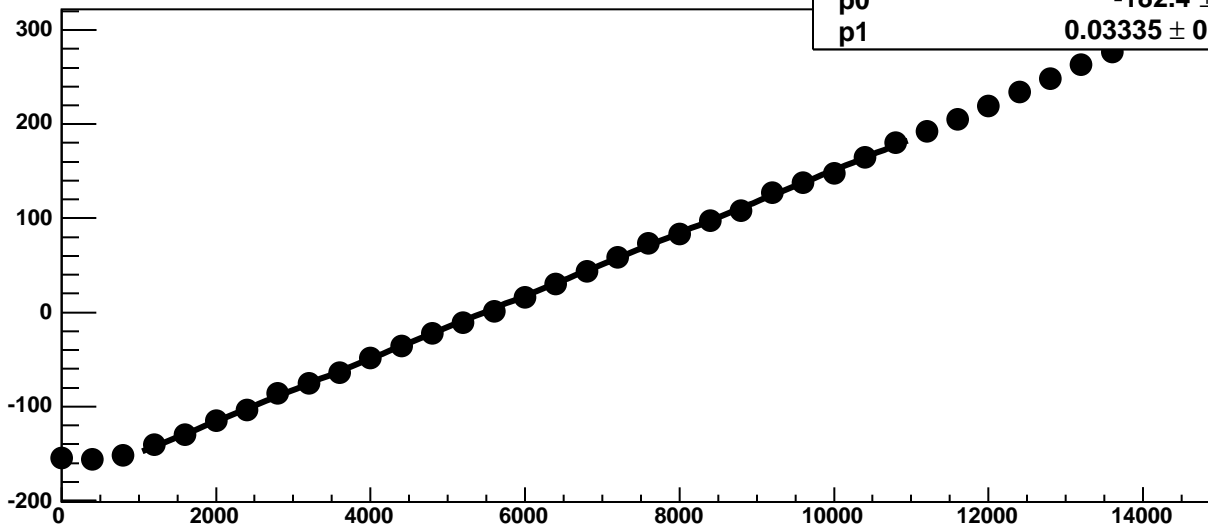
Chip 7, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

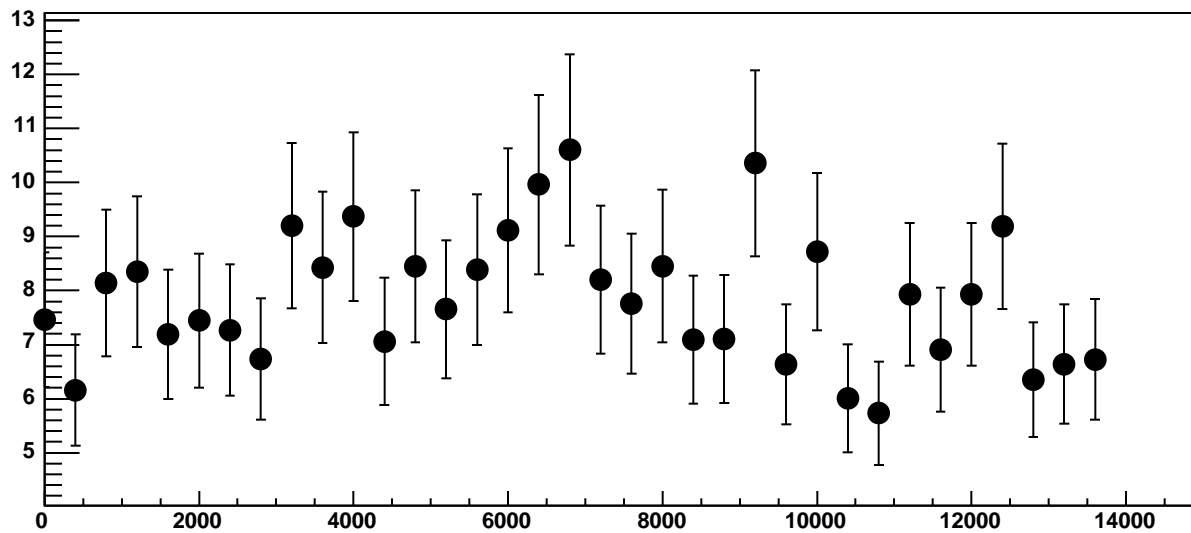


Chip 7, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

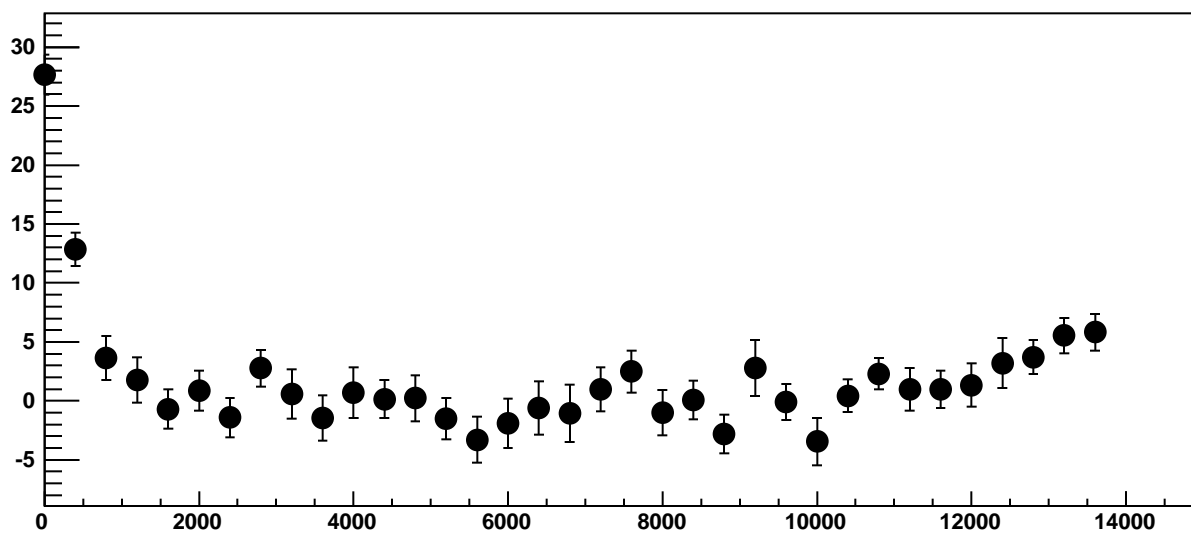


$\chi^2 / \text{ndf}$  23.63 / 23  
p0 -182.4 ± 0.8002  
p1 0.03335 ± 0.000115

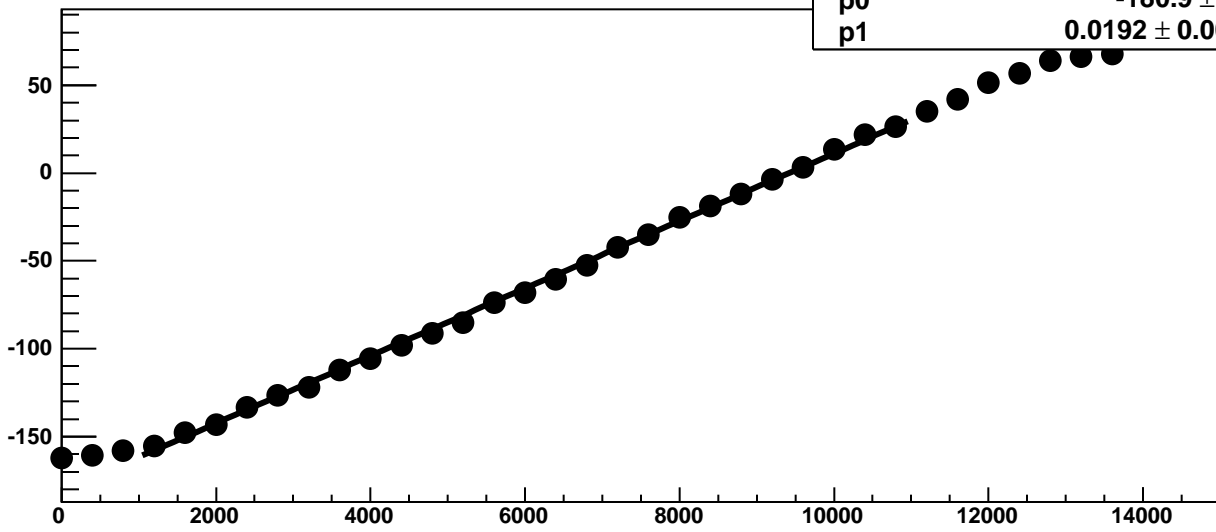
Chip 7, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



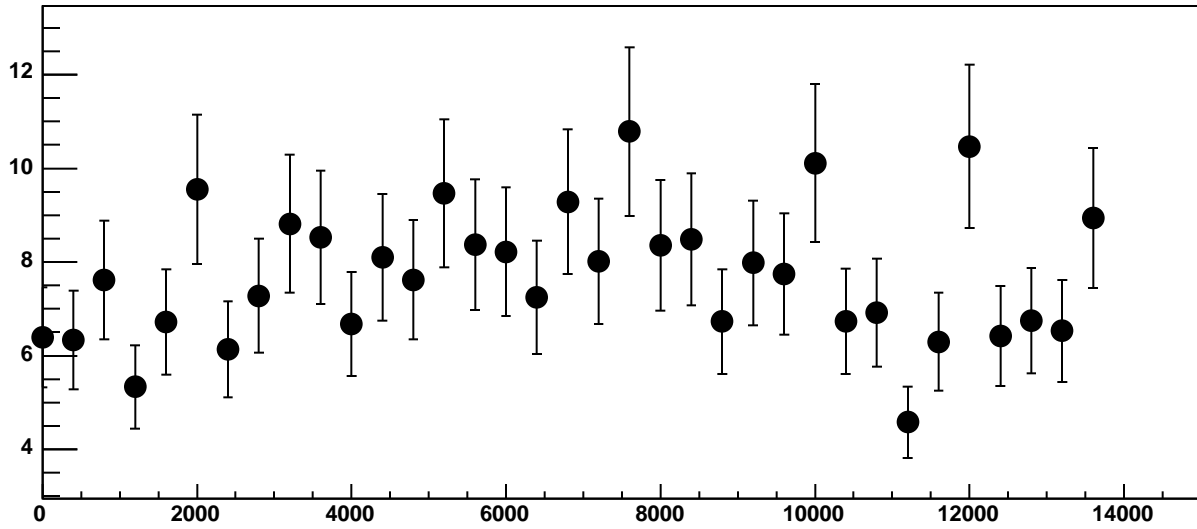
Chip 7, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



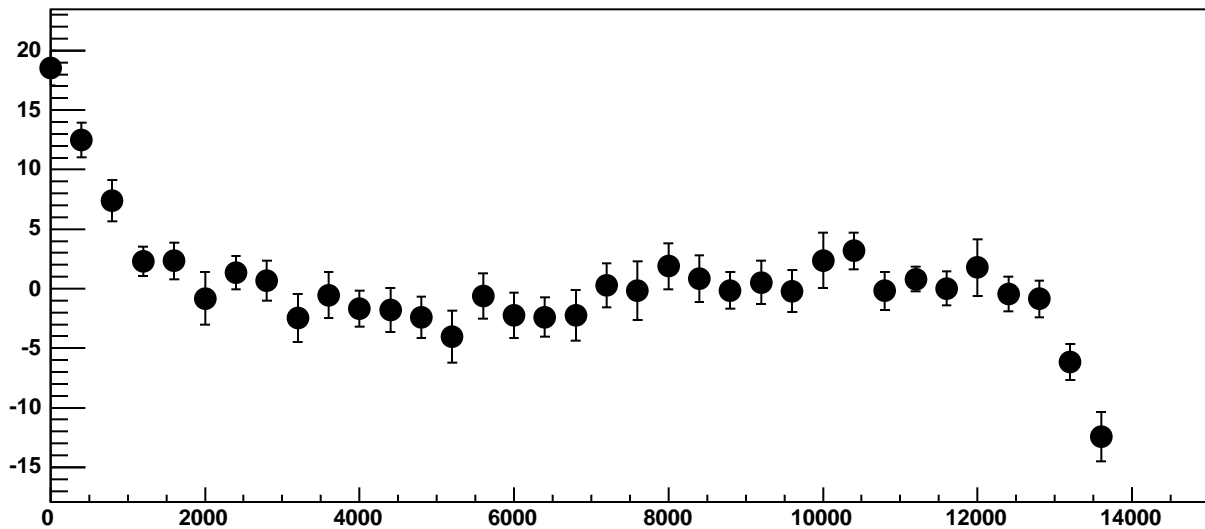
Chip 7, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



Chip 7, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

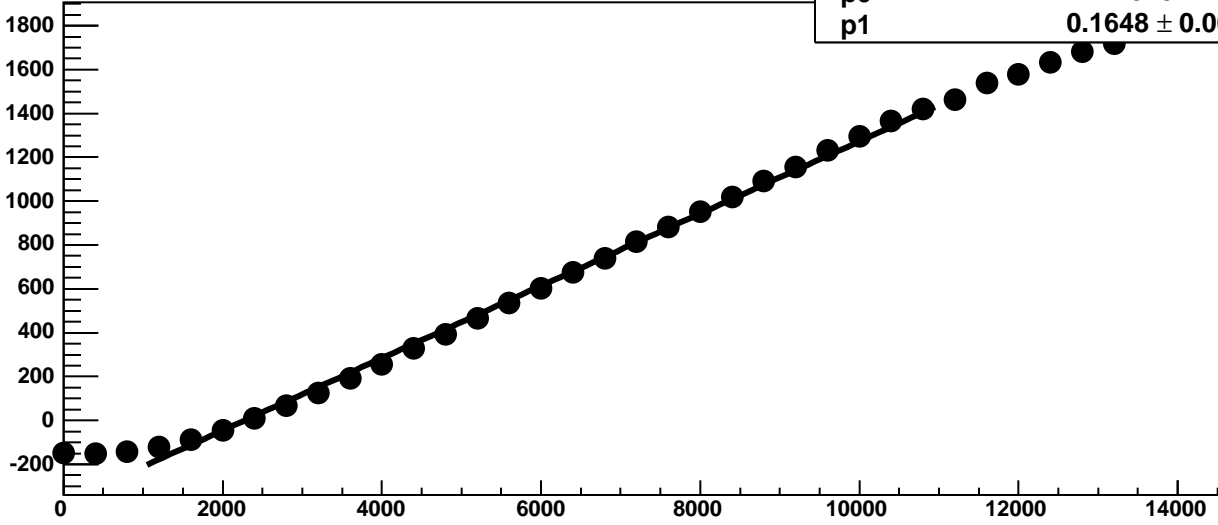


Chip 7, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



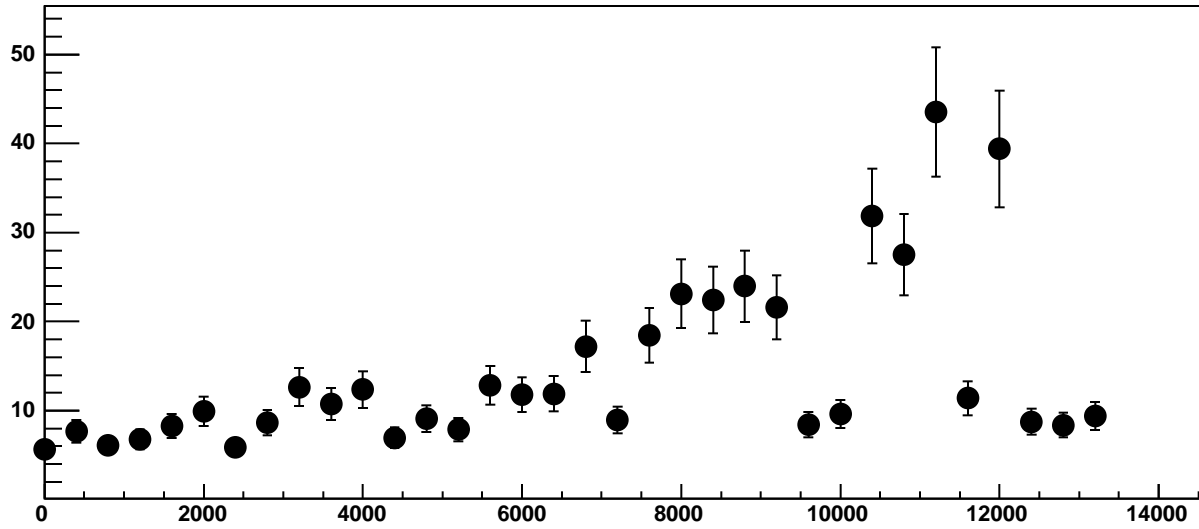


Chip 7, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

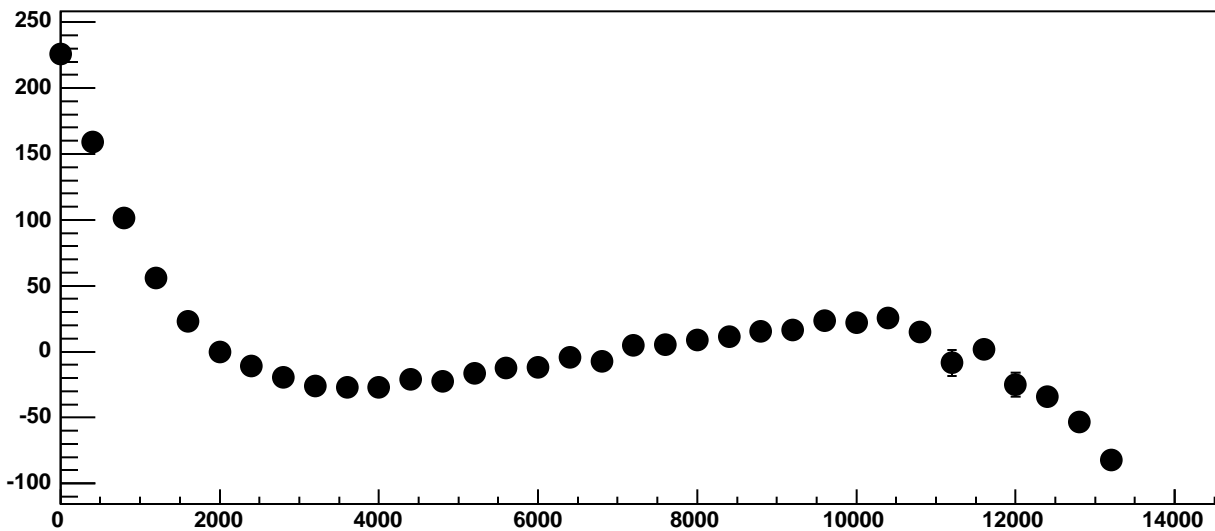


$\chi^2 / \text{ndf}$  2584 / 23  
p0  $-375.1 \pm 0.9407$   
p1  $0.1648 \pm 0.0001754$

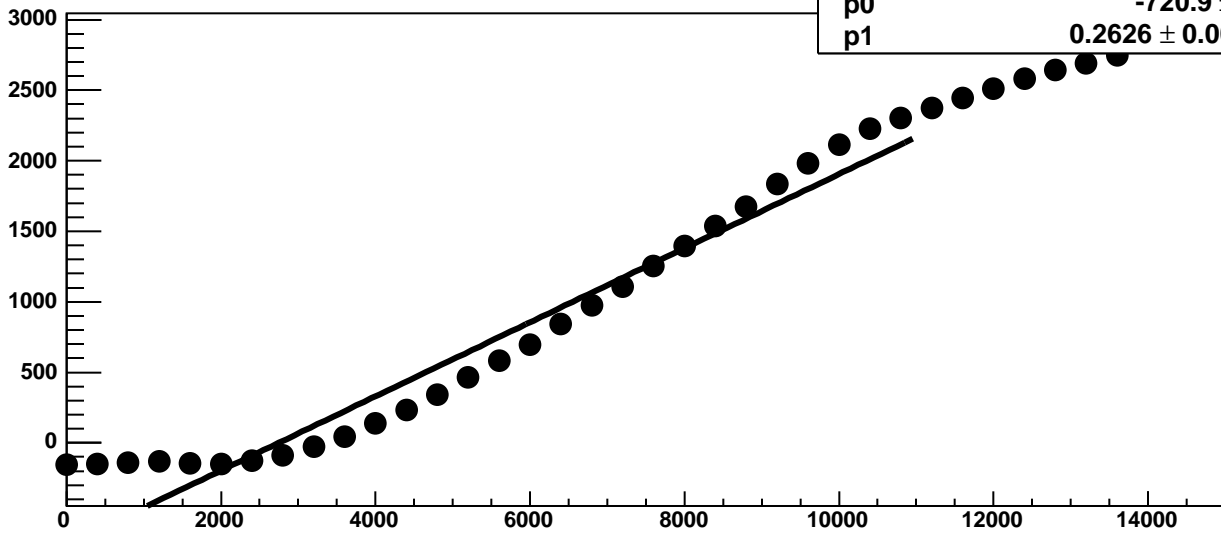
Chip 7, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

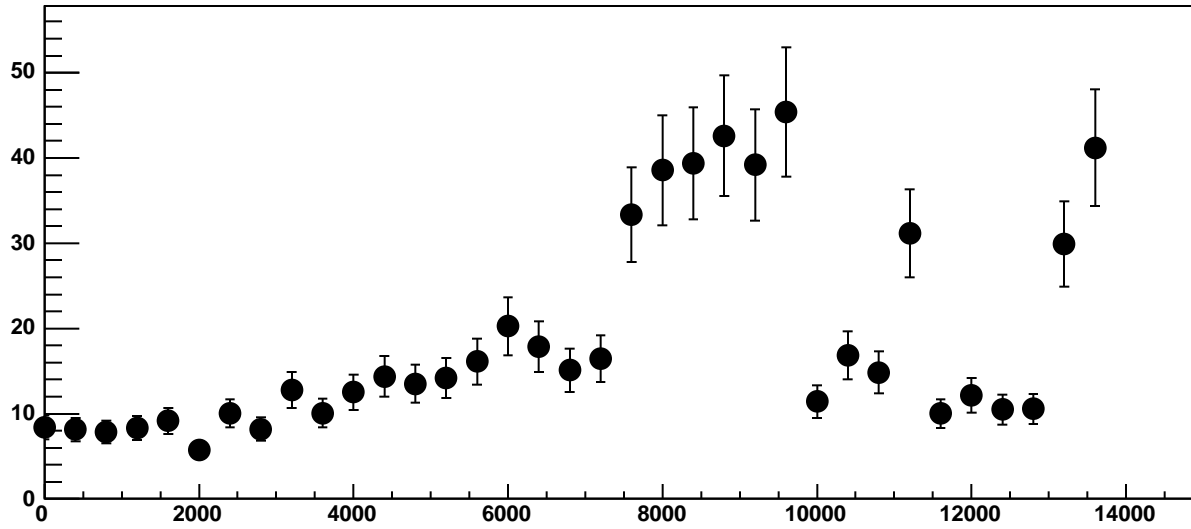


Chip 7, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

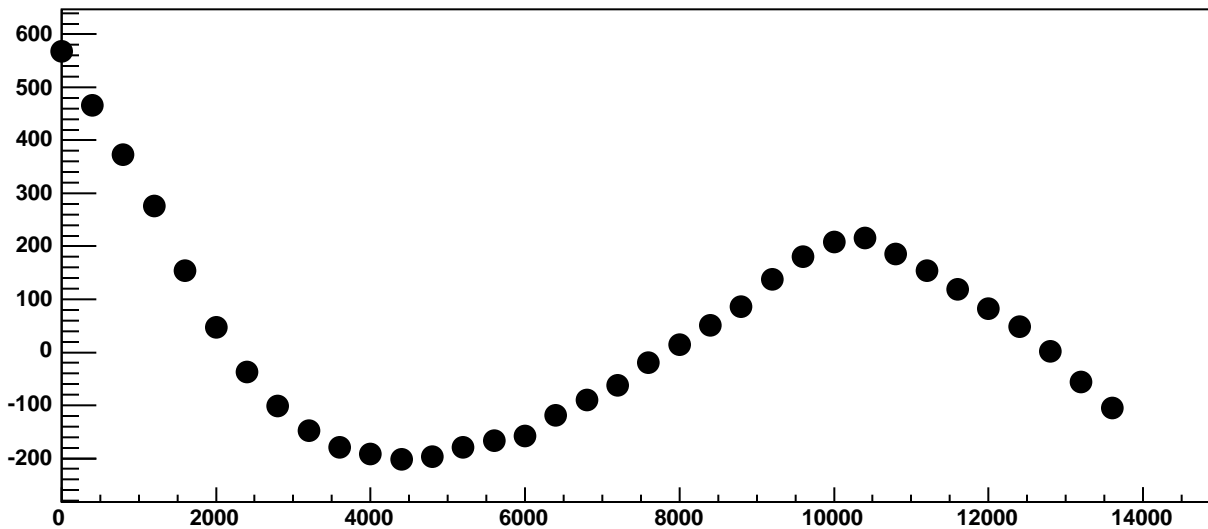


$\chi^2 / \text{ndf}$  7.232e+04 / 23  
p0 -720.9 ± 1.022  
p1 0.2626 ± 0.0002106

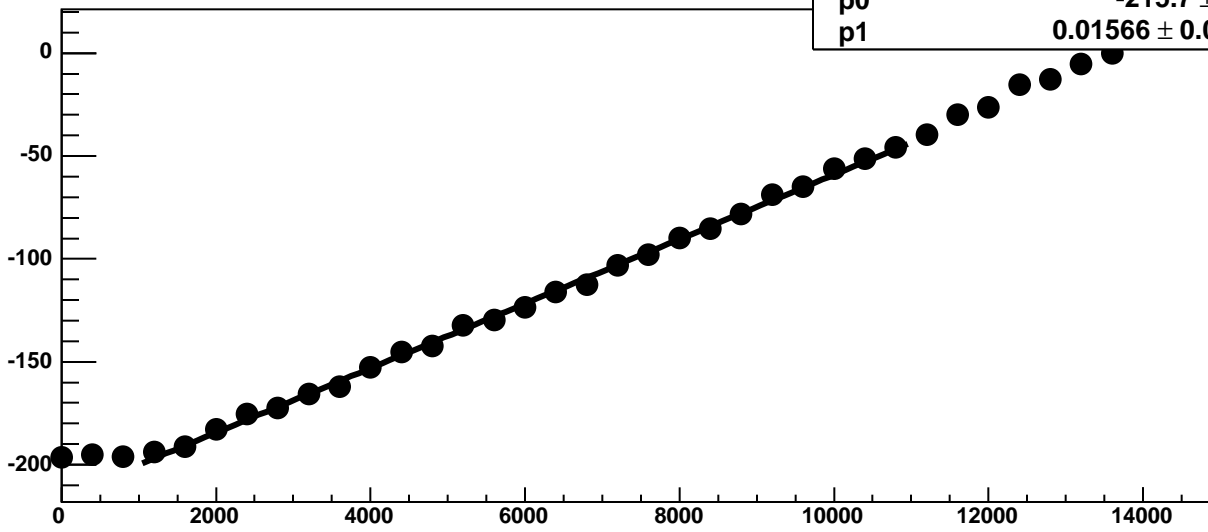
Chip 7, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

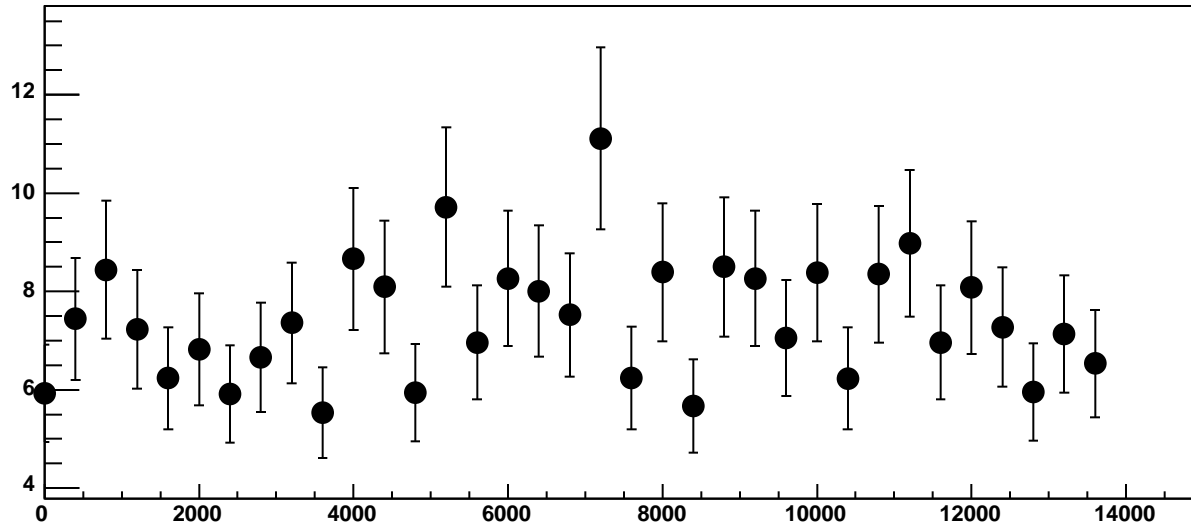


Chip 7, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

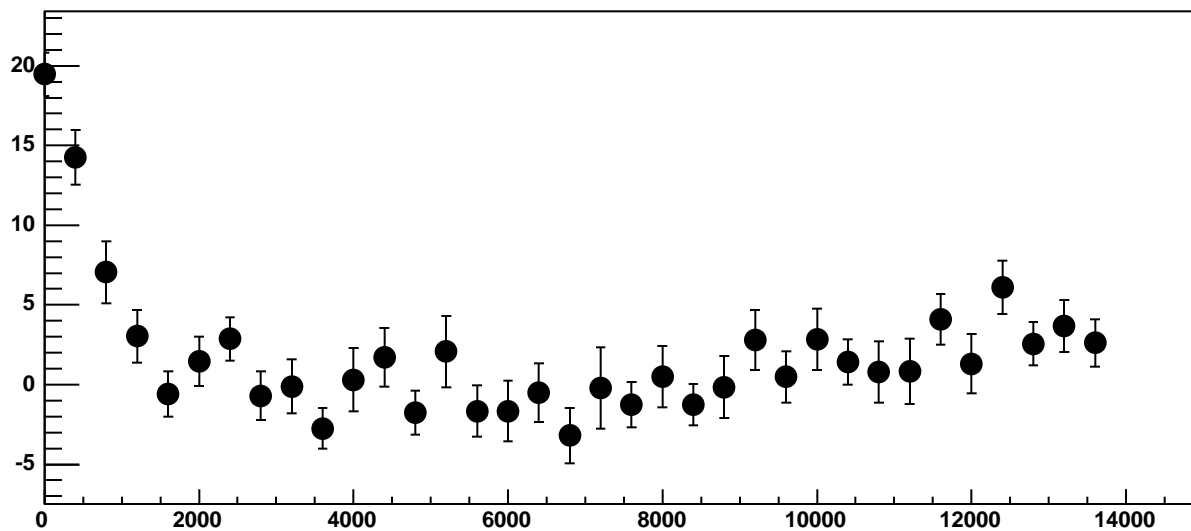


$\chi^2 / \text{ndf}$  29.98 / 23  
p0  $-215.7 \pm 0.7199$   
p1  $0.01566 \pm 0.0001115$

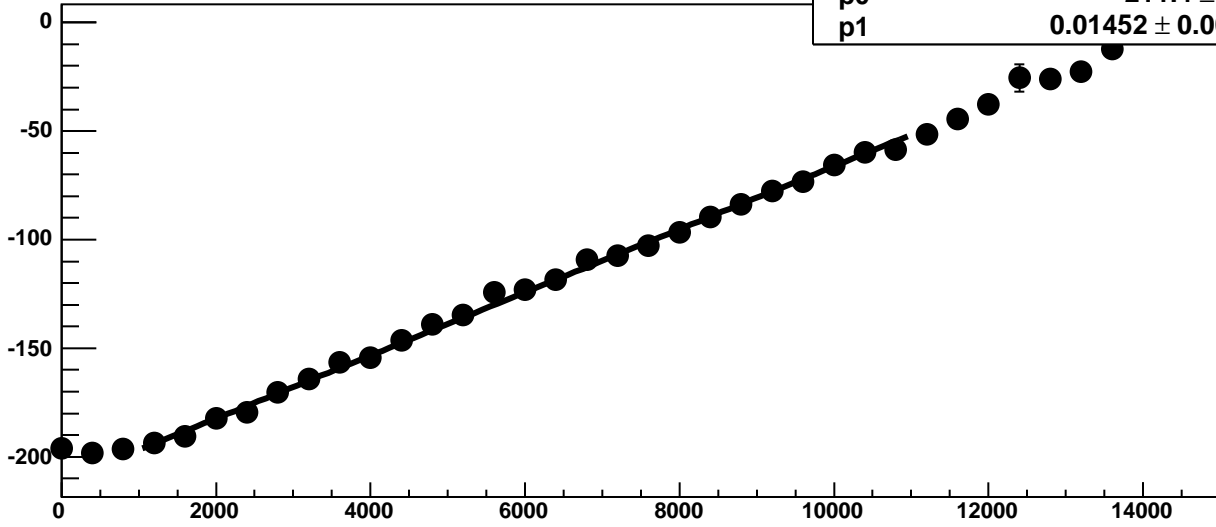
Chip 7, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



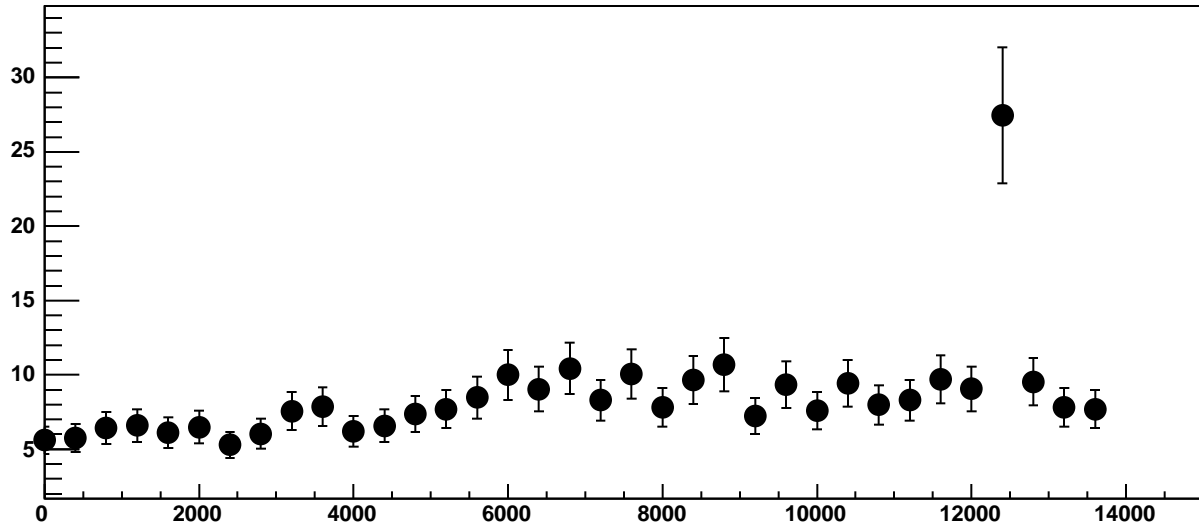
Chip 7, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



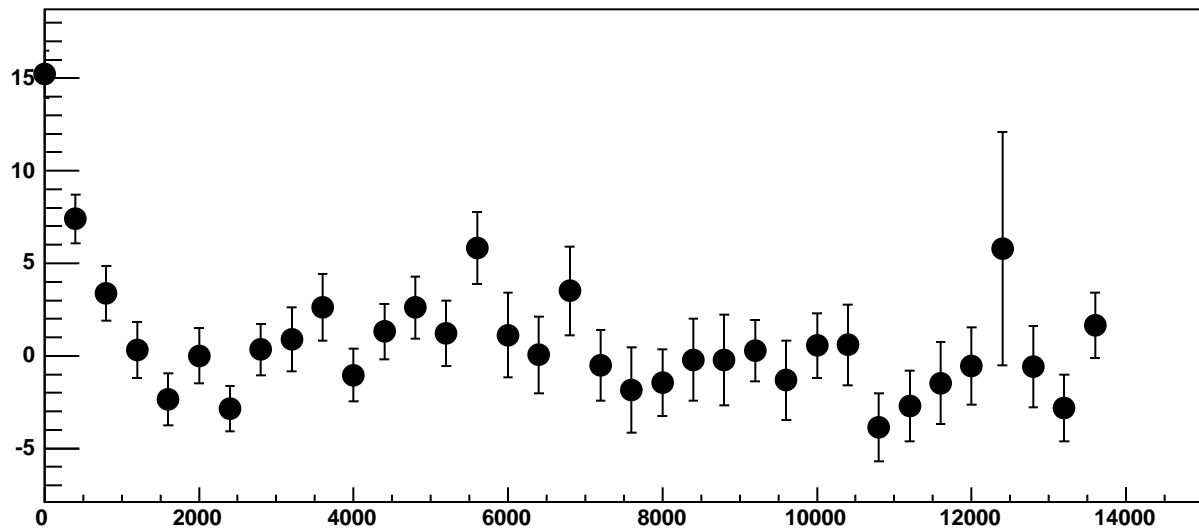
Chip 7, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



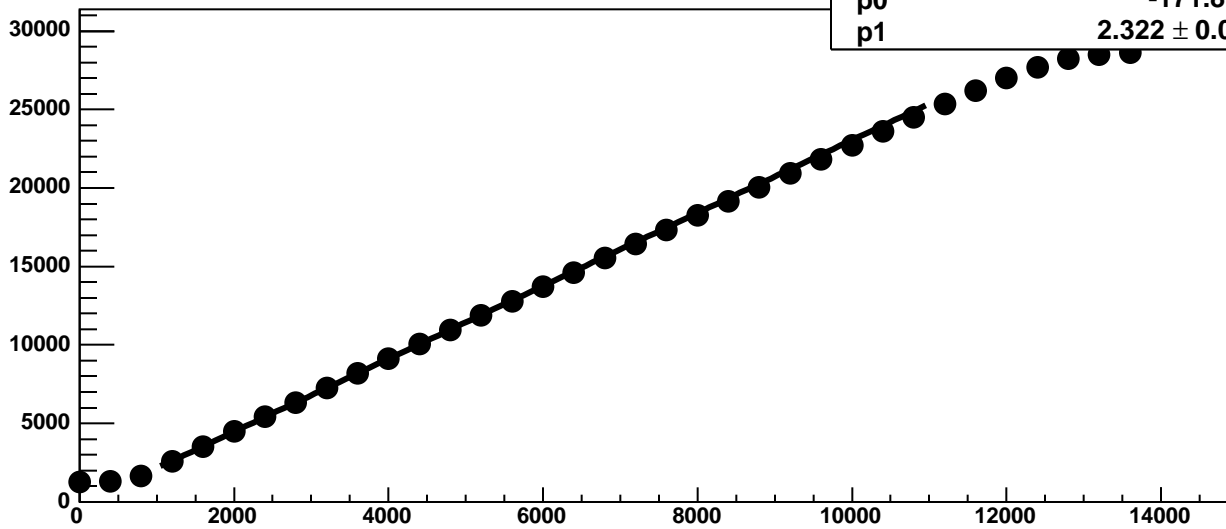
Chip 7, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



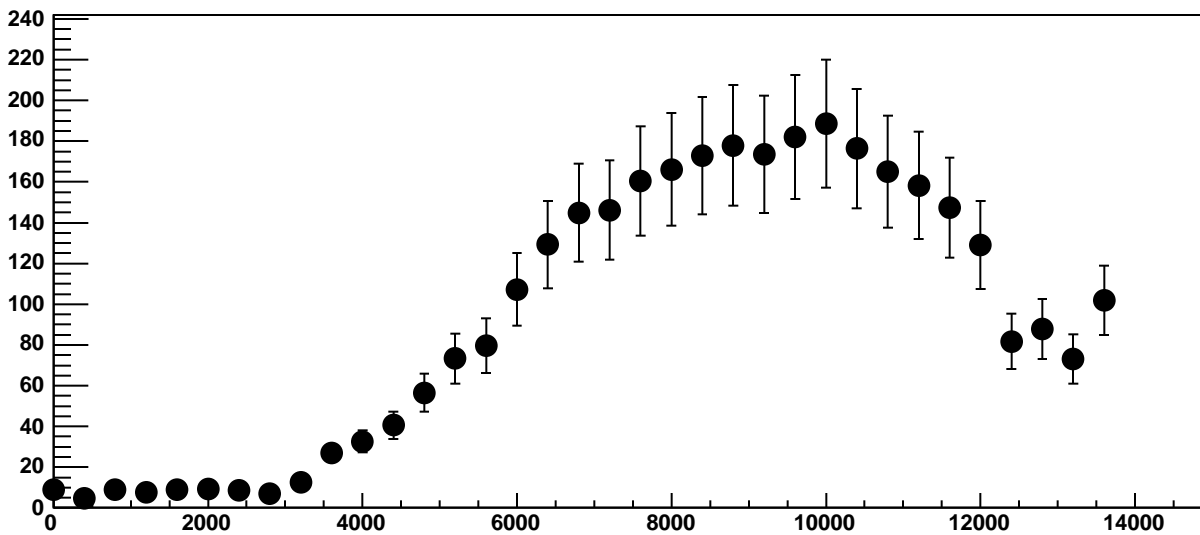
Chip 7, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



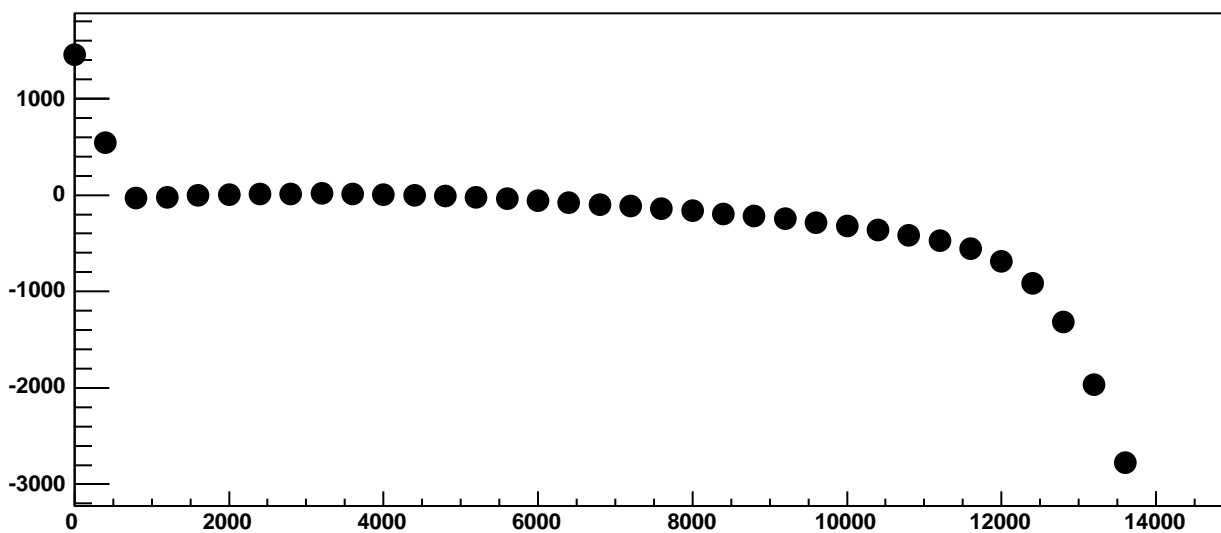
Chip 7, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



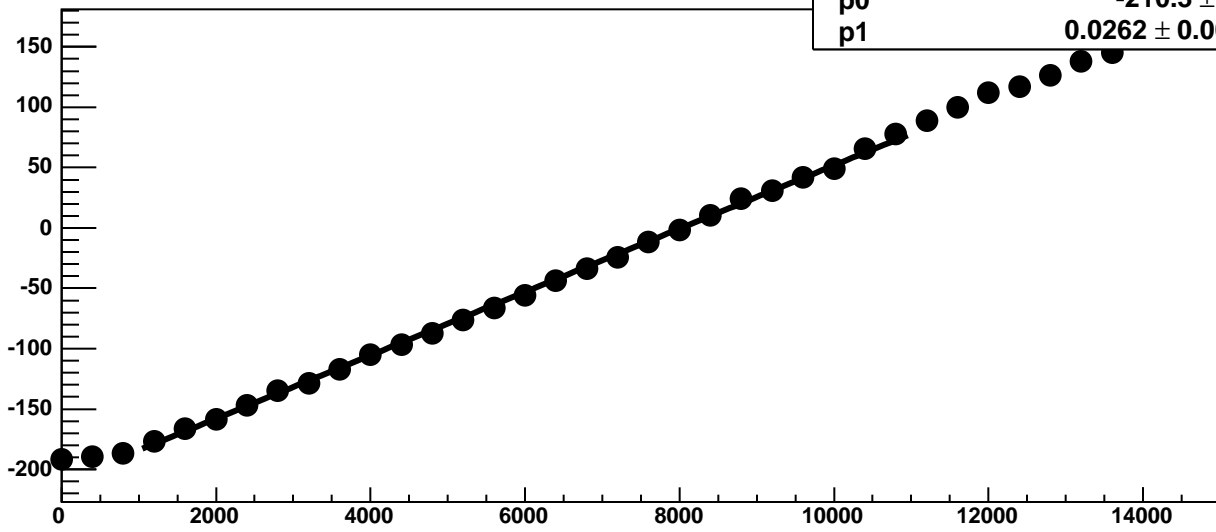
Chip 7, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

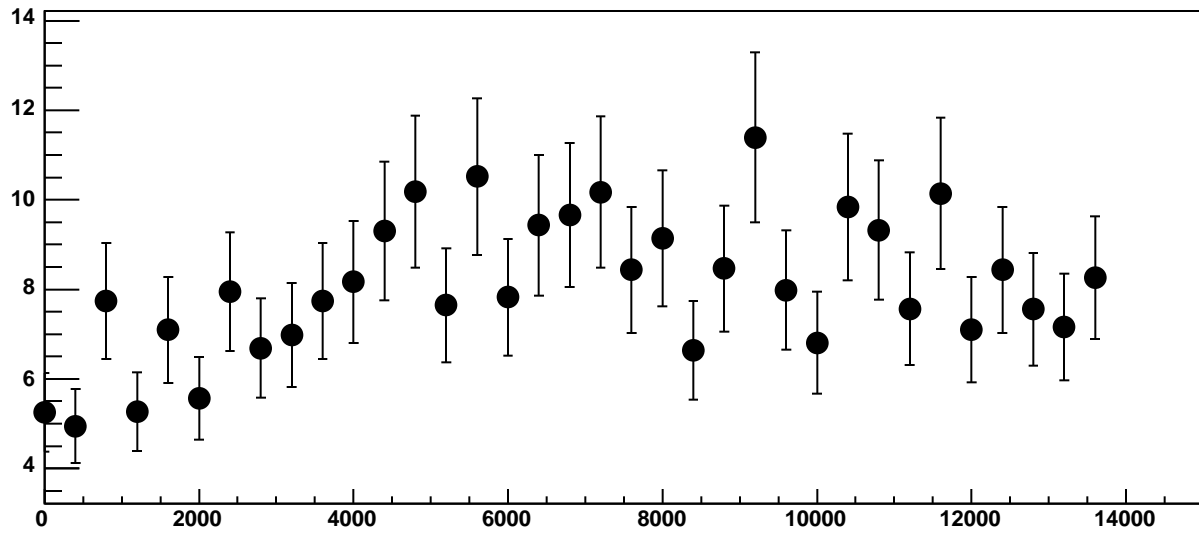


Chip 7, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

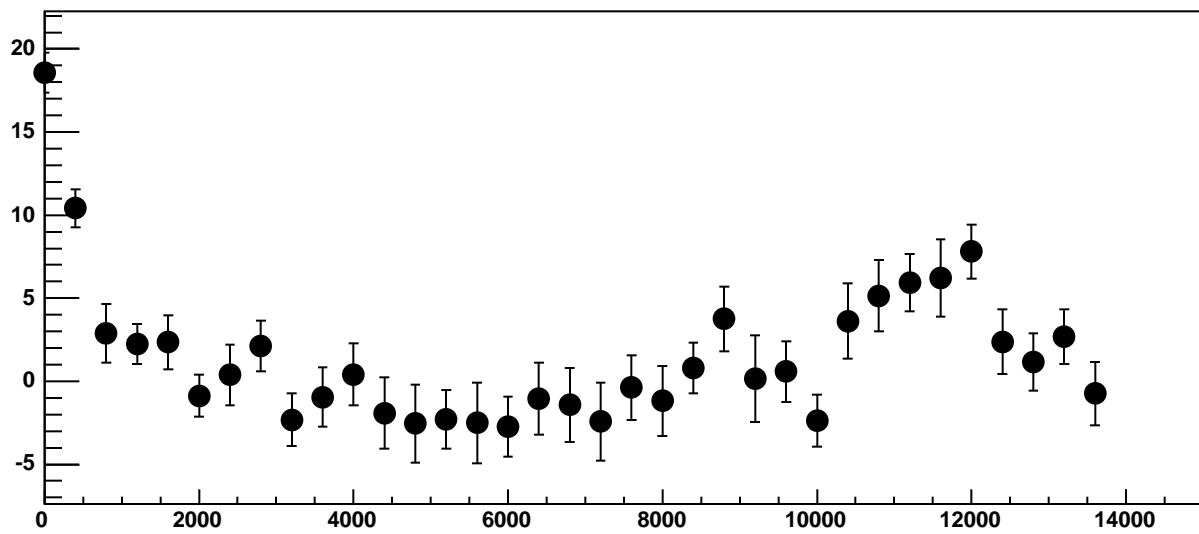


$\chi^2 / \text{ndf}$  34.33 / 23  
p0 -210.3 ± 0.7259  
p1 0.0262 ± 0.0001176

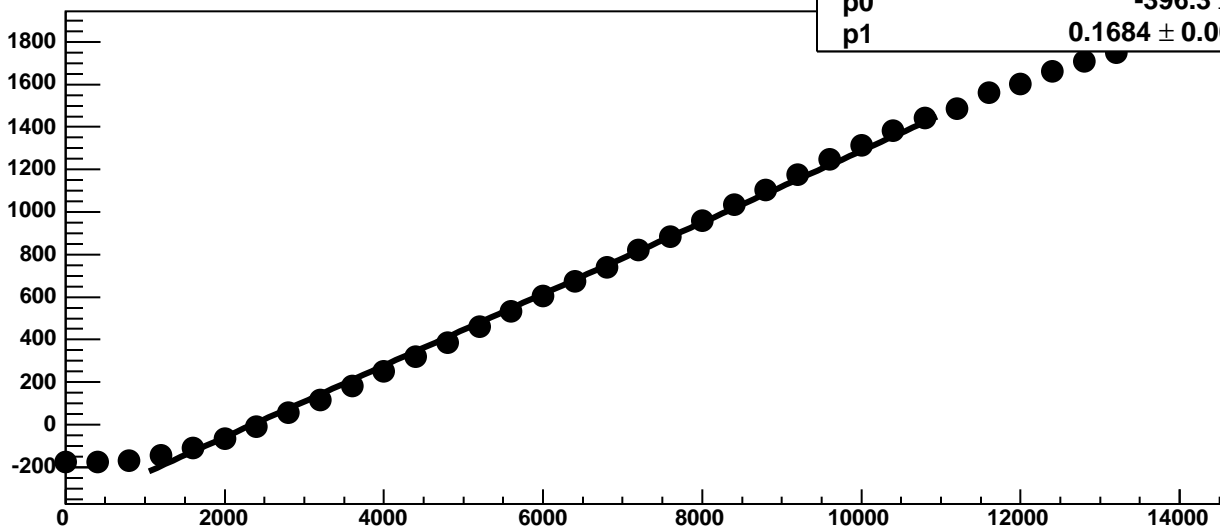
Chip 7, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



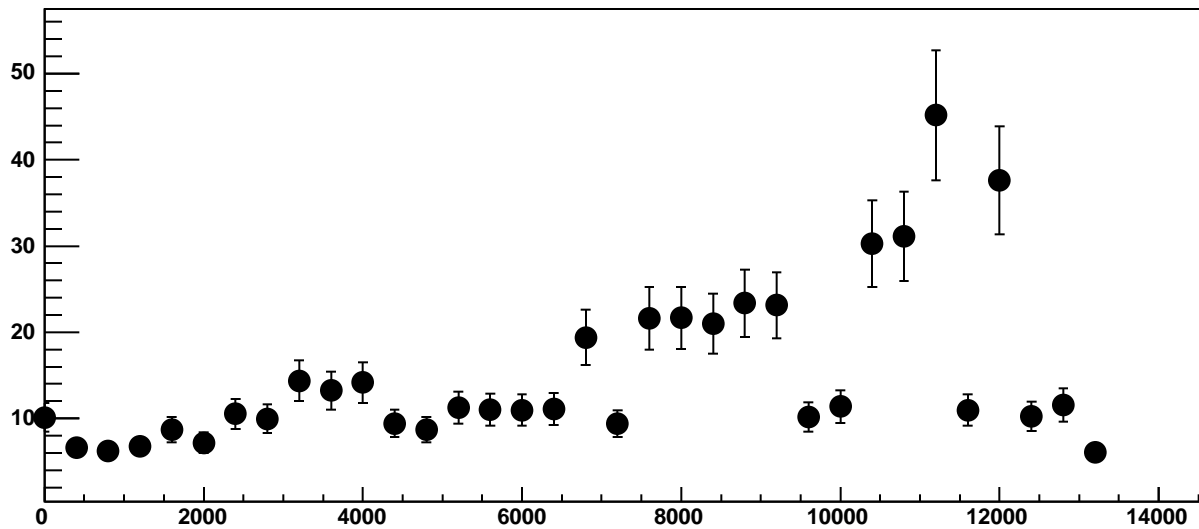
Chip 7, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



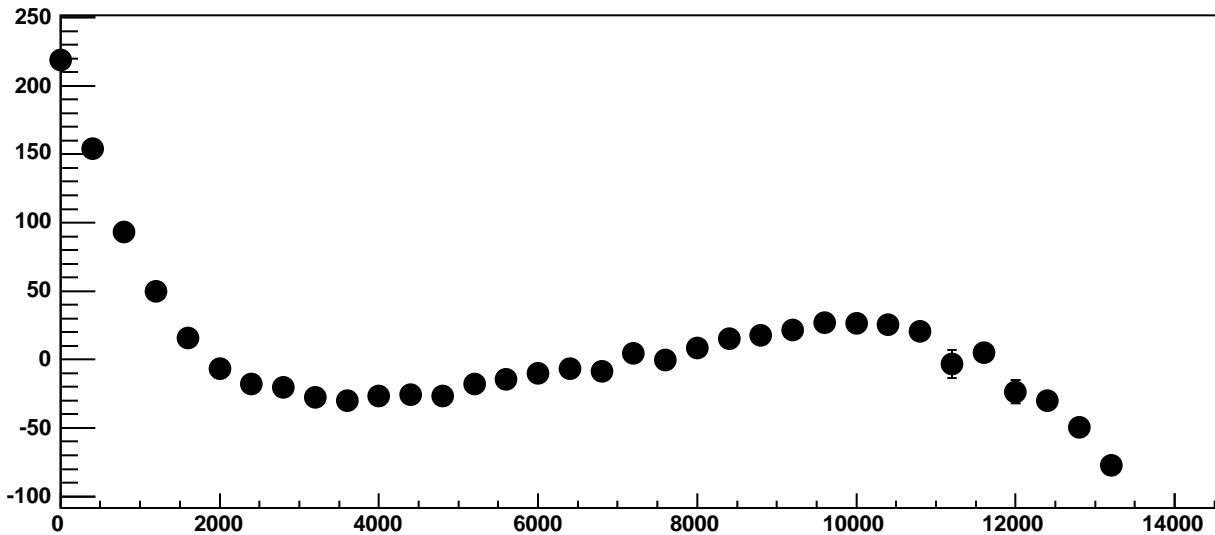
Chip 7, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



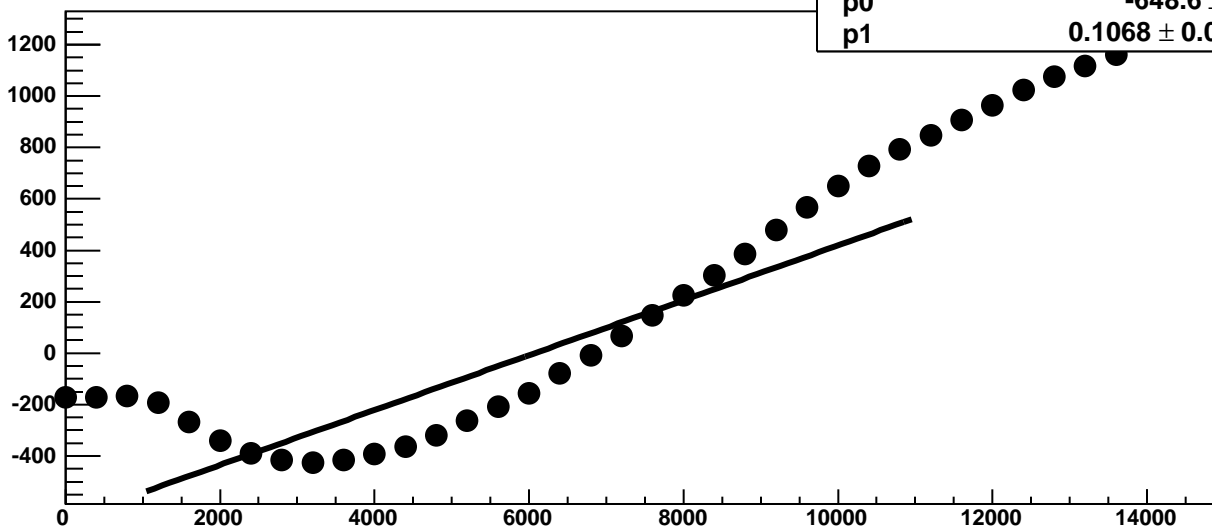
Chip 7, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

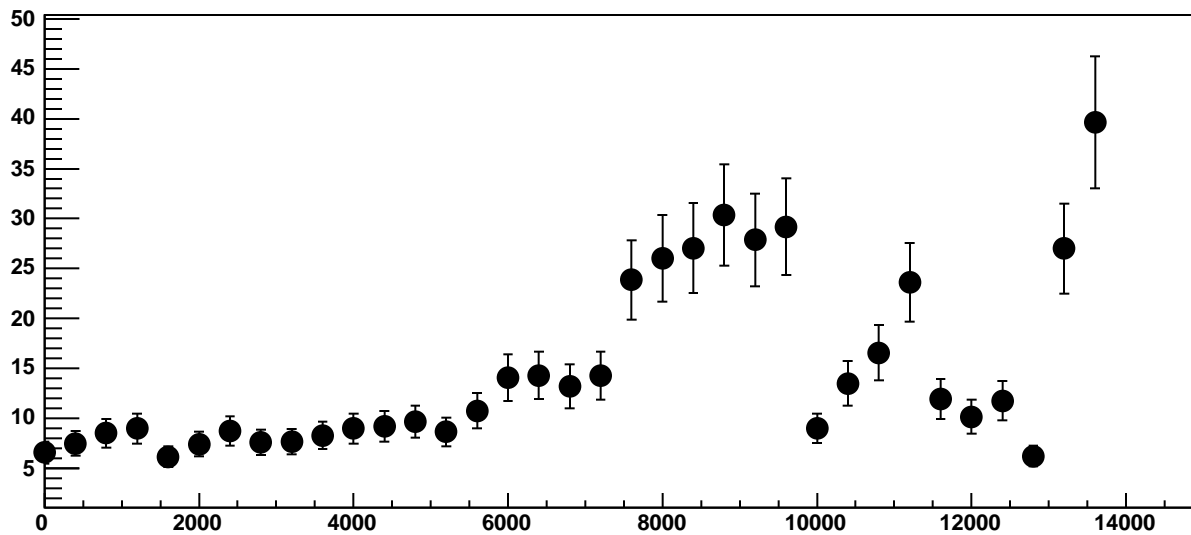


Chip 7, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

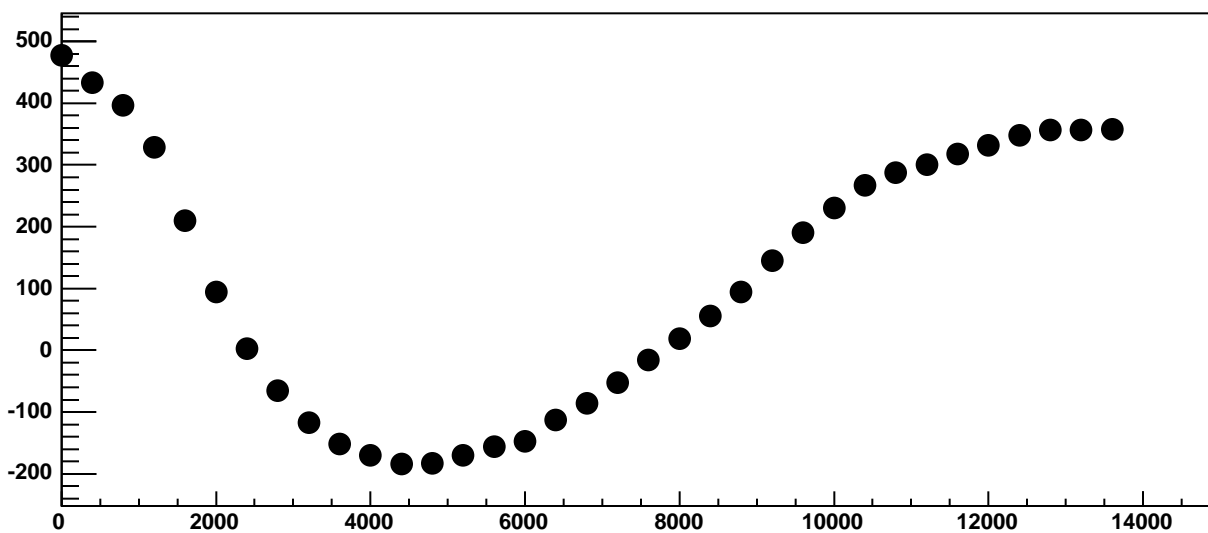


$\chi^2 / \text{ndf}$  1.269e+05 / 23  
p0 -648.6 ± 0.9251  
p1 0.1068 ± 0.0001829

Chip 7, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

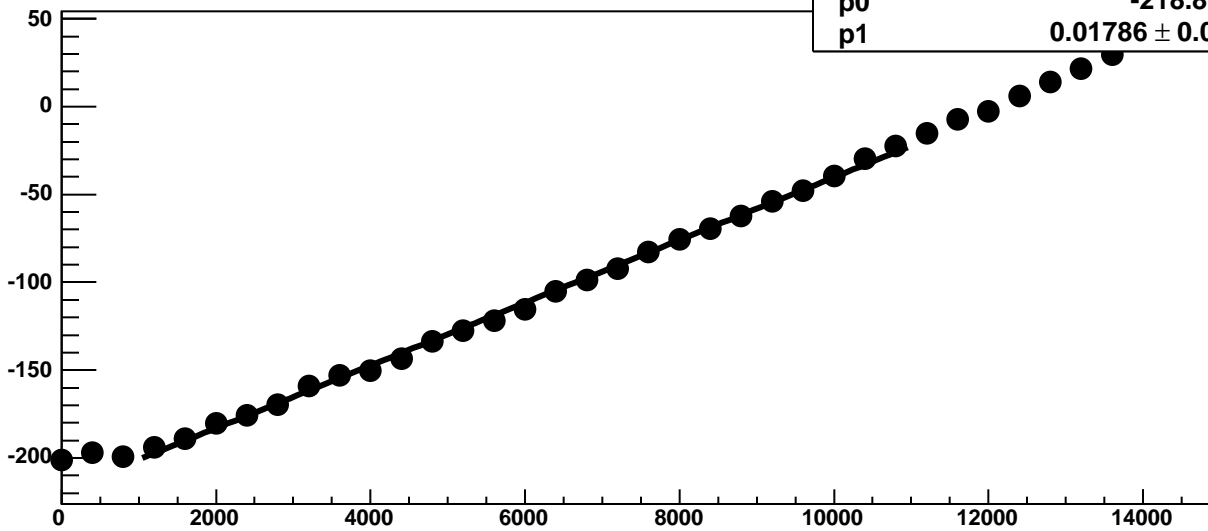


Chip 7, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



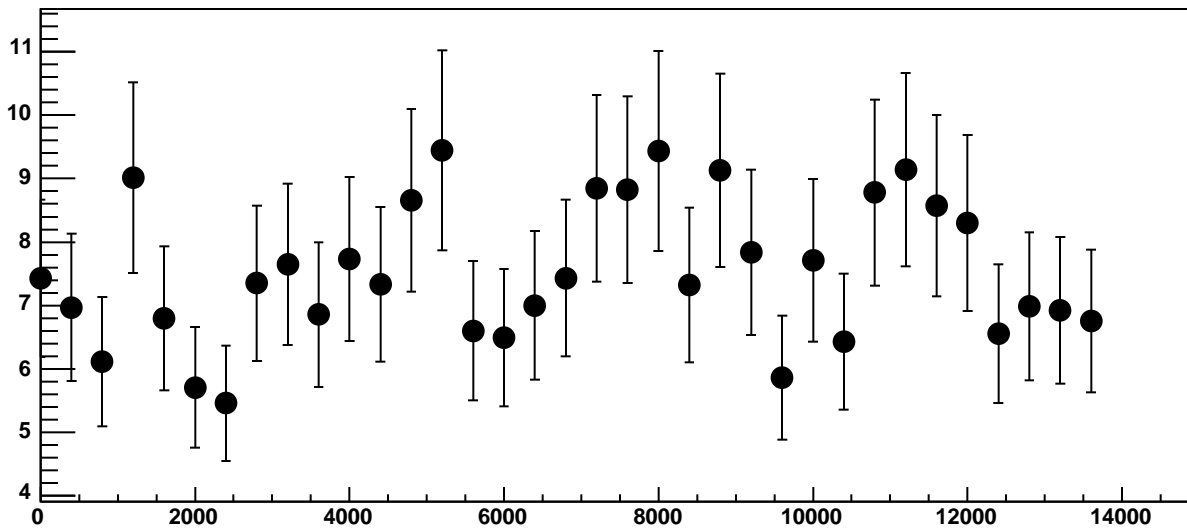


Chip 7, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

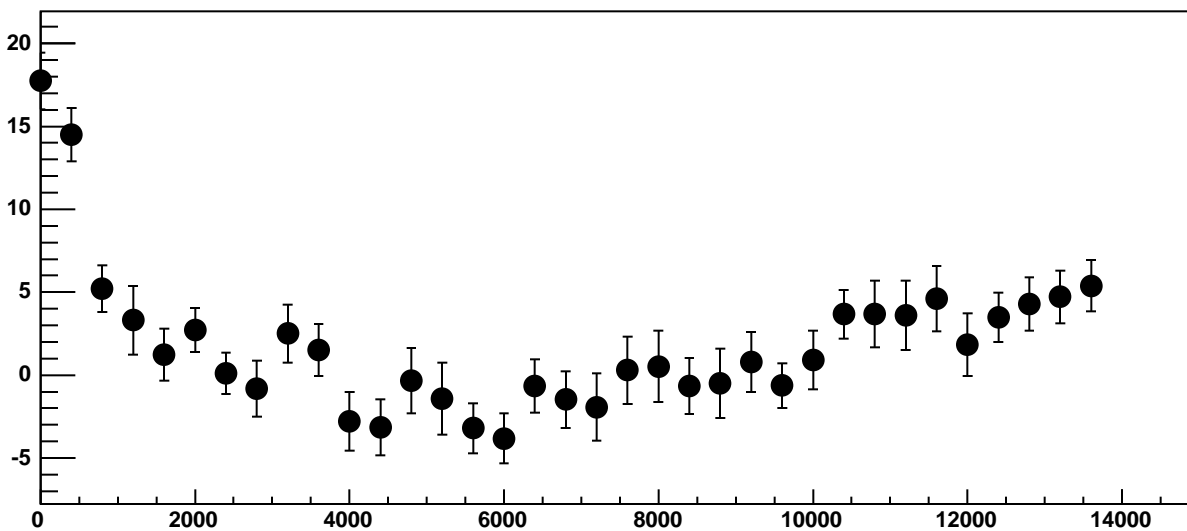


$\chi^2 / \text{ndf}$  40.43 / 23  
p0  $-218.8 \pm 0.737$   
p1  $0.01786 \pm 0.0001134$

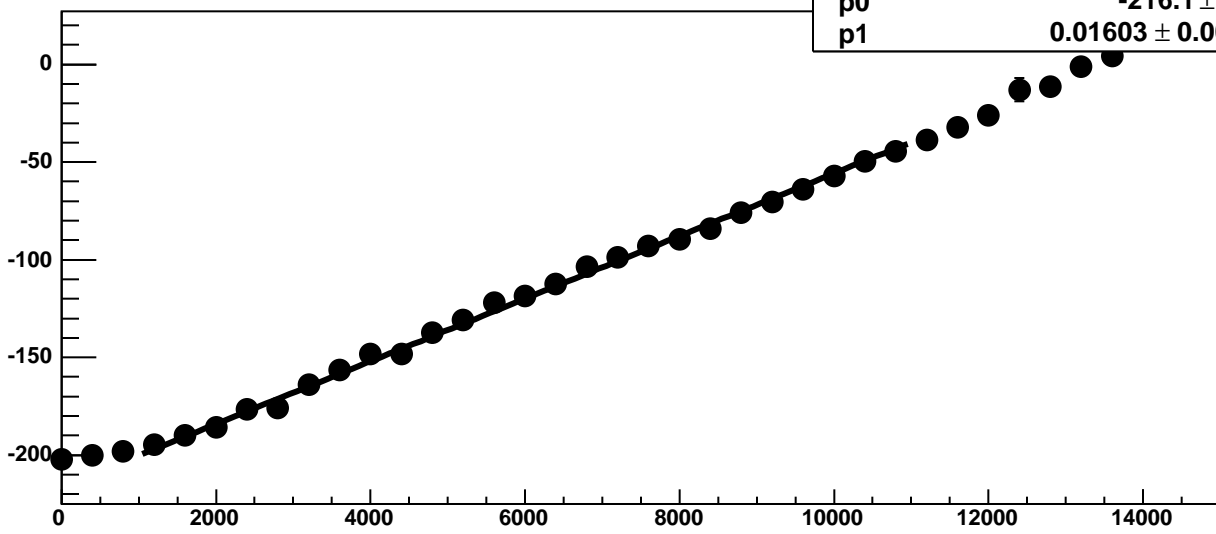
Chip 7, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



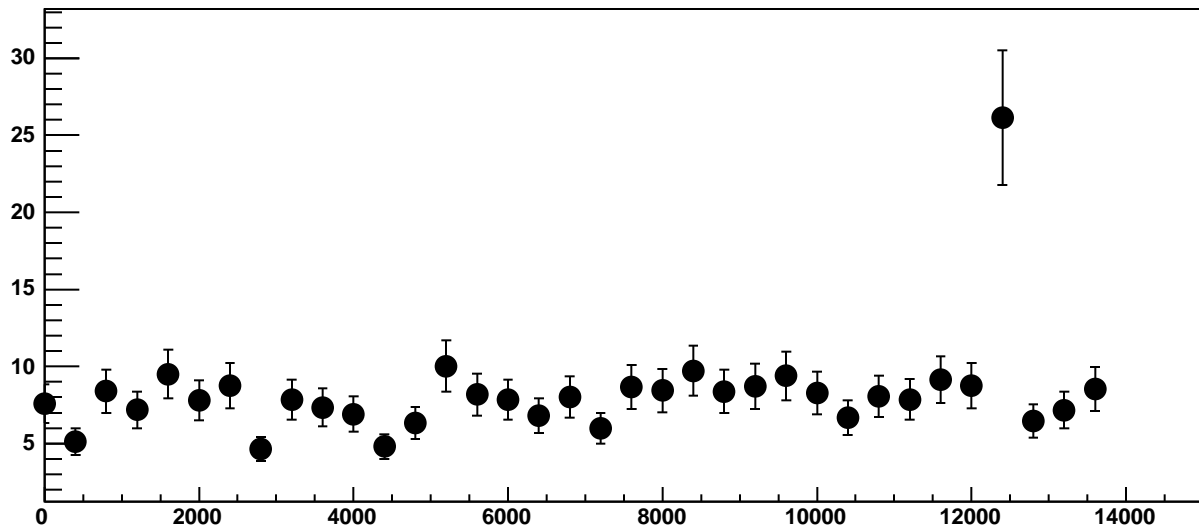
Chip 7, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



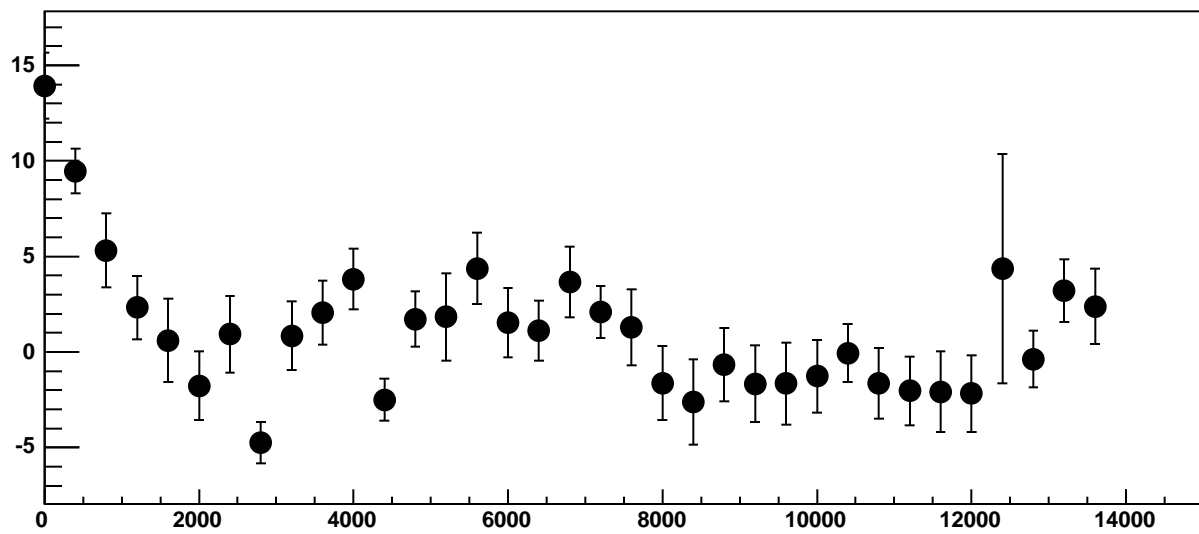
Chip 7, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



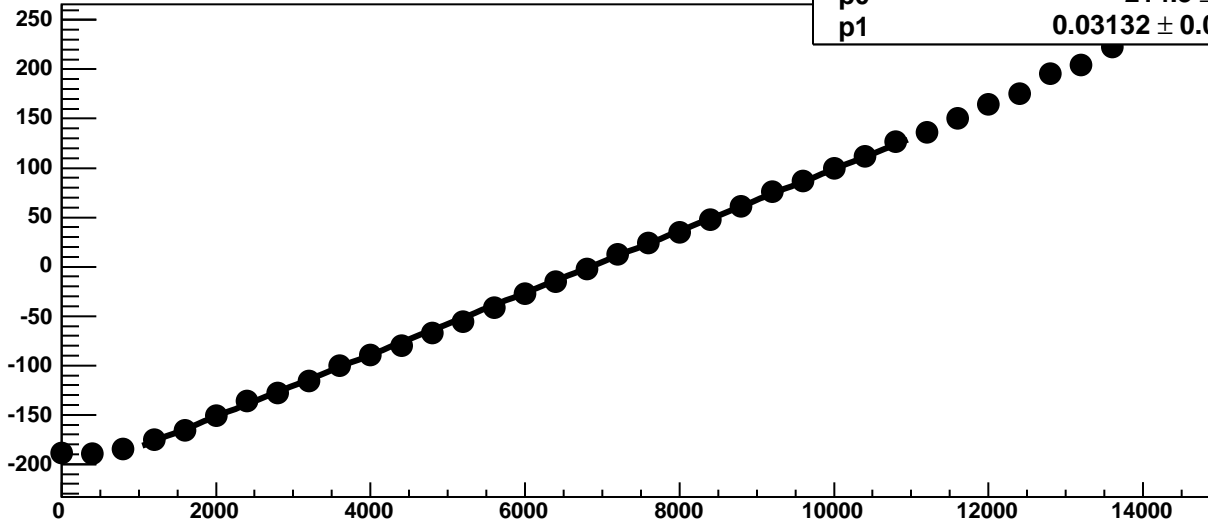
Chip 7, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

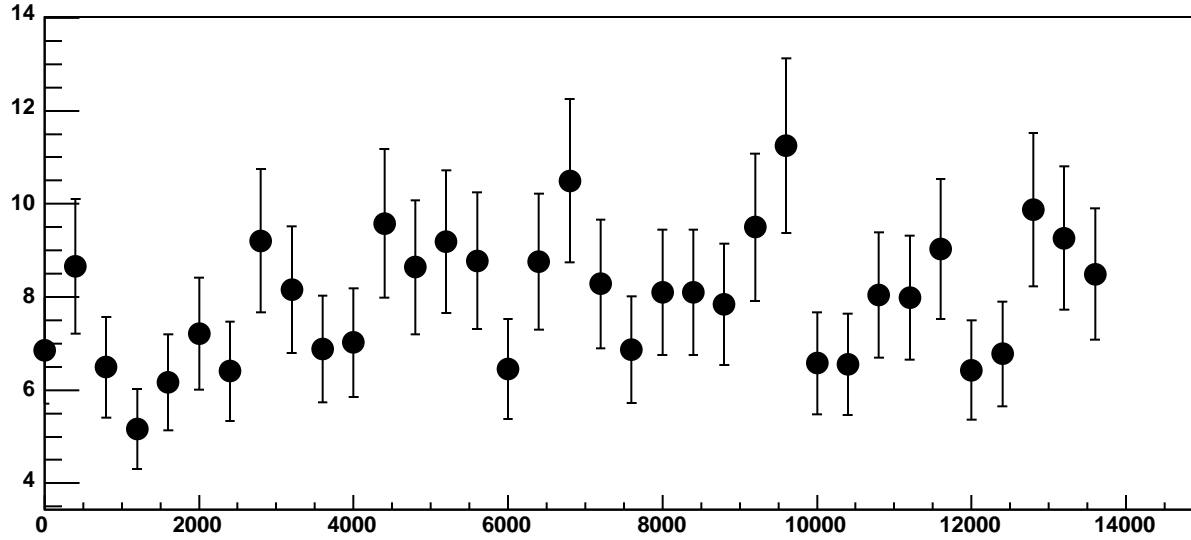


Chip 7, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

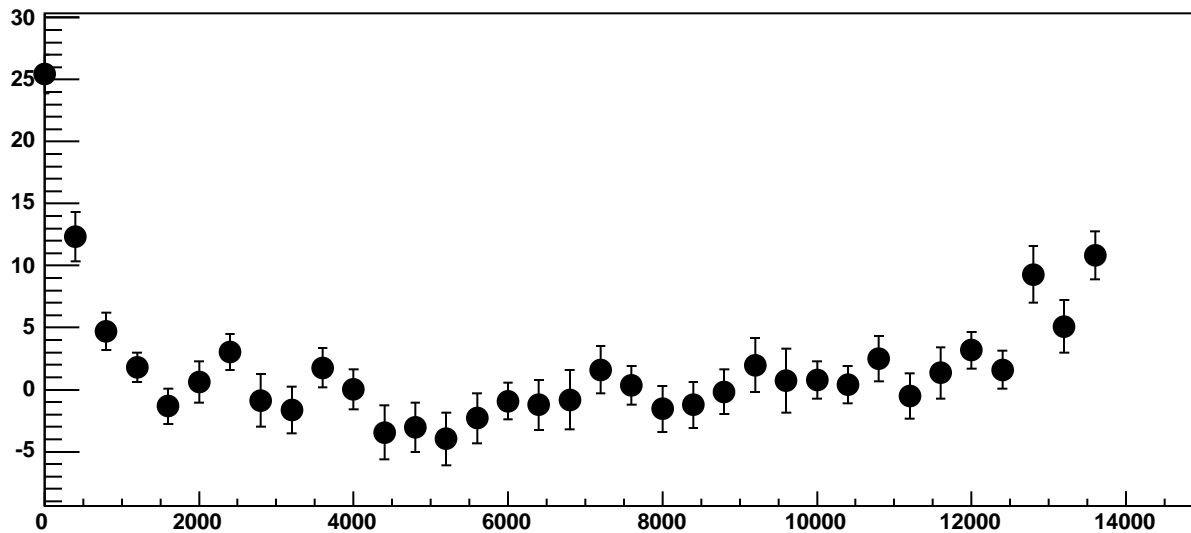


$\chi^2 / \text{ndf}$  25.31 / 23  
p0  $-214.3 \pm 0.7193$   
p1  $0.03132 \pm 0.0001122$

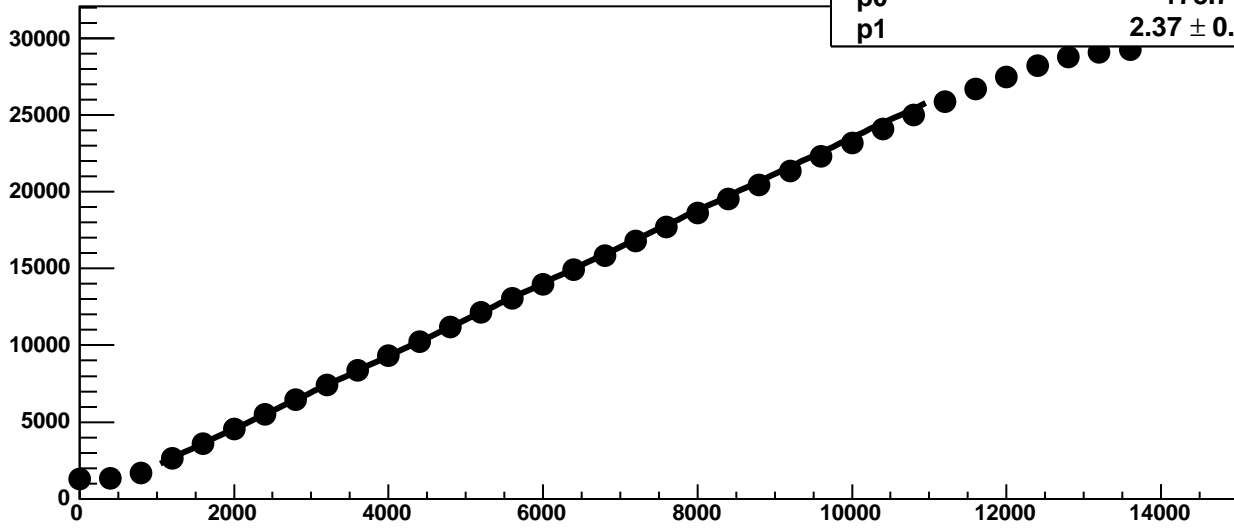
Chip 7, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



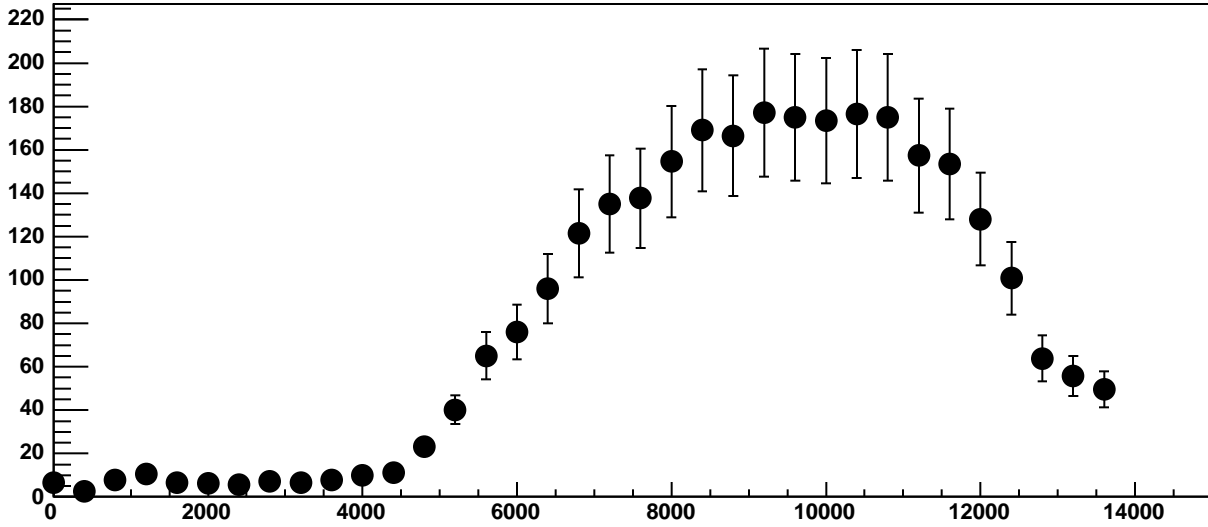
Chip 7, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



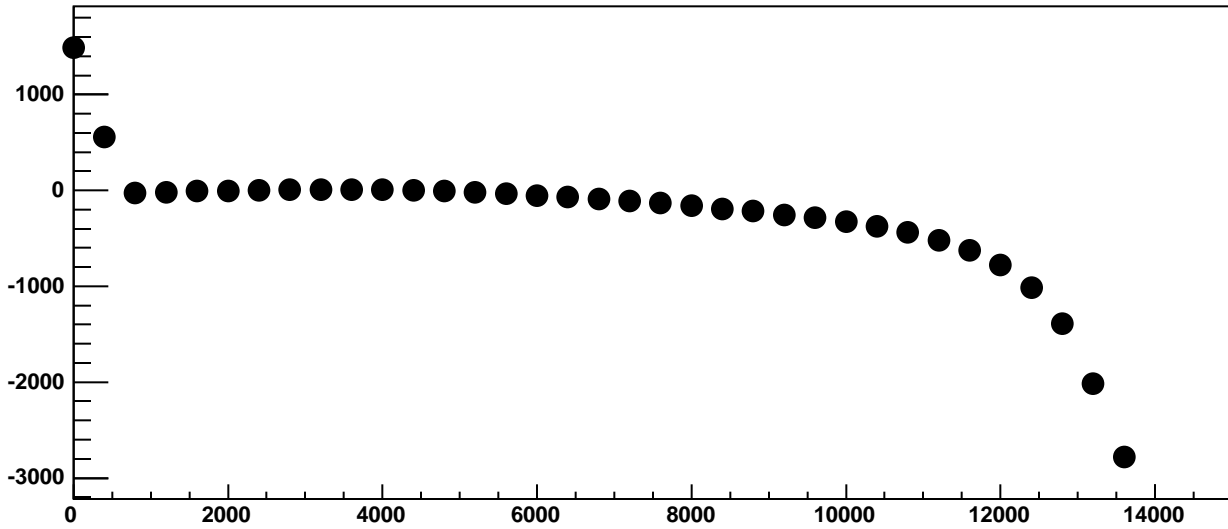
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC



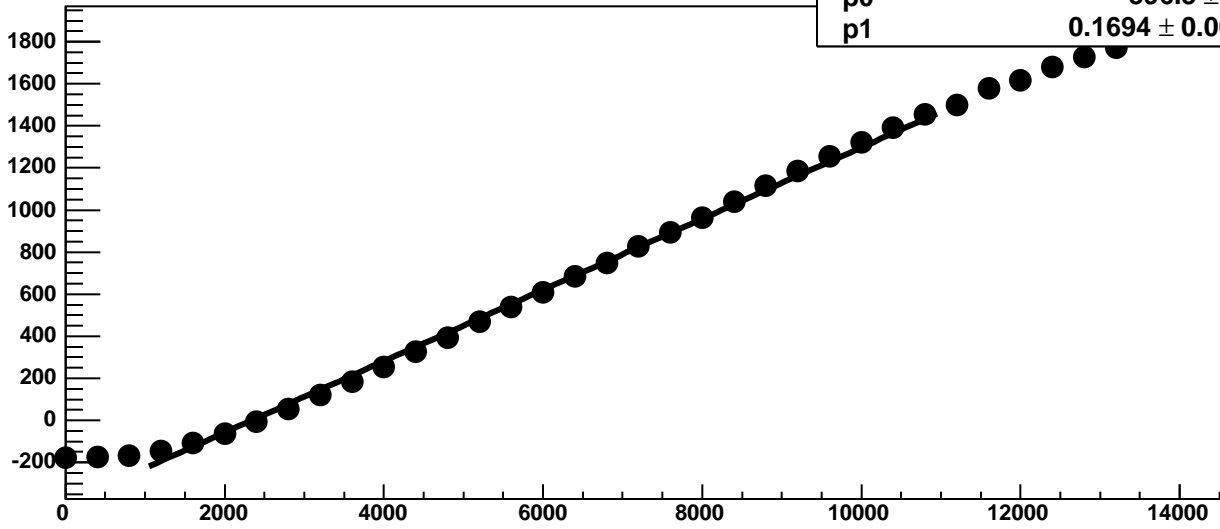
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



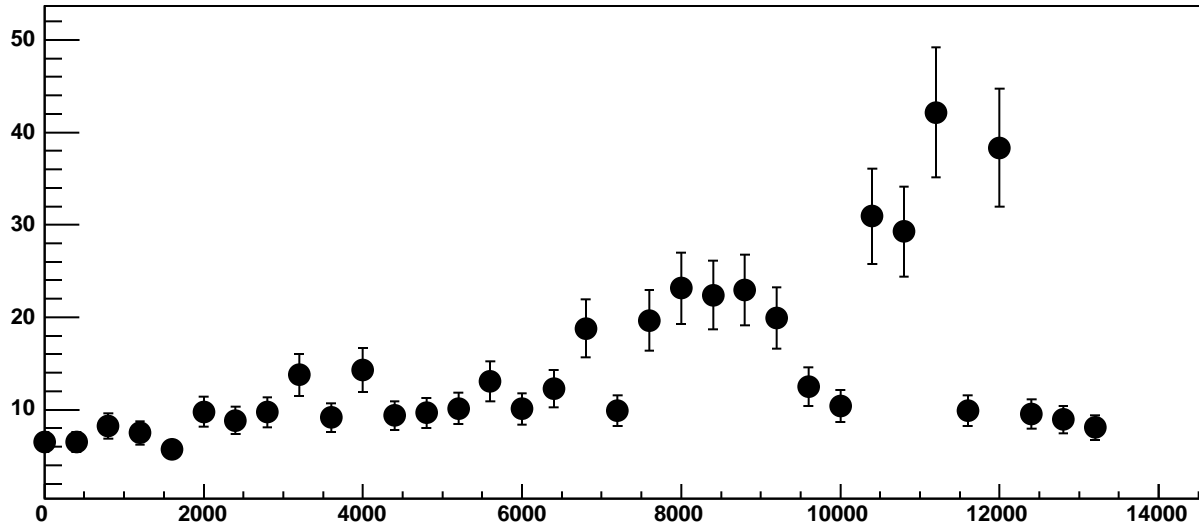
Chip 7, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



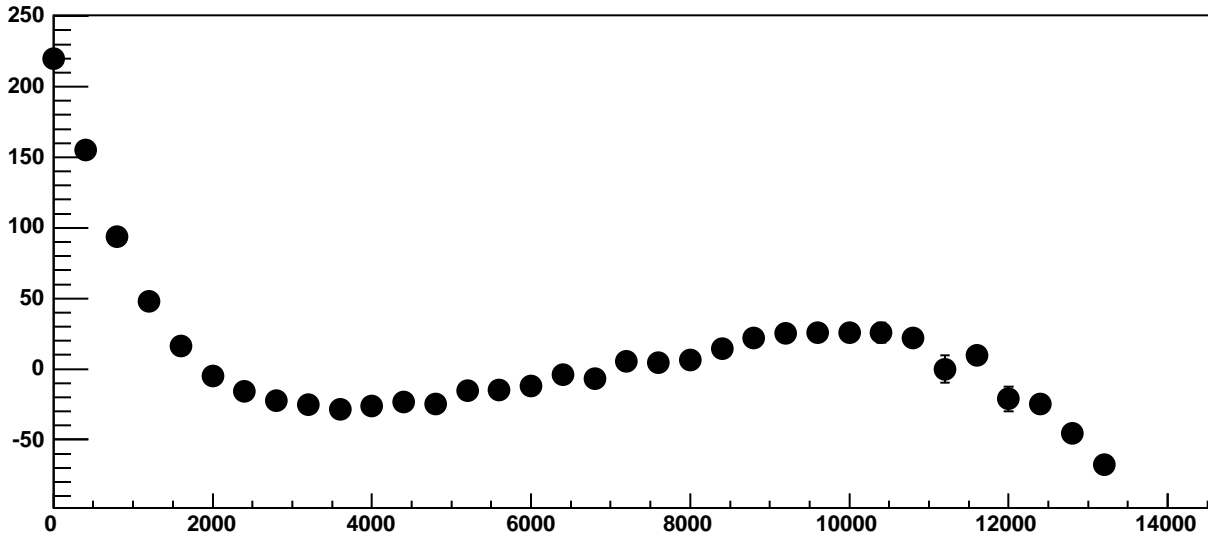
Chip 7, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

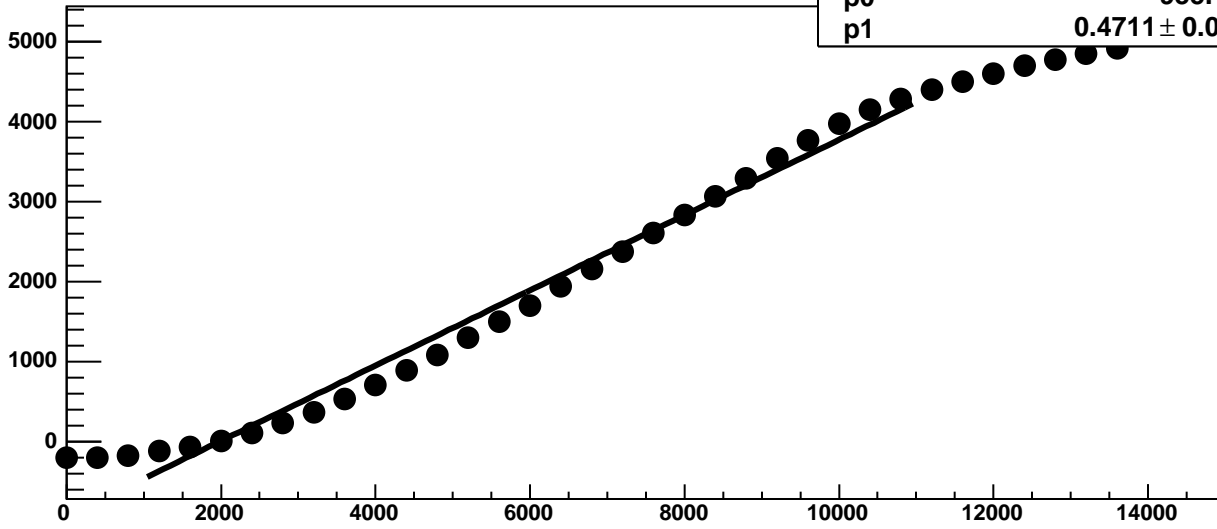


Chip 7, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

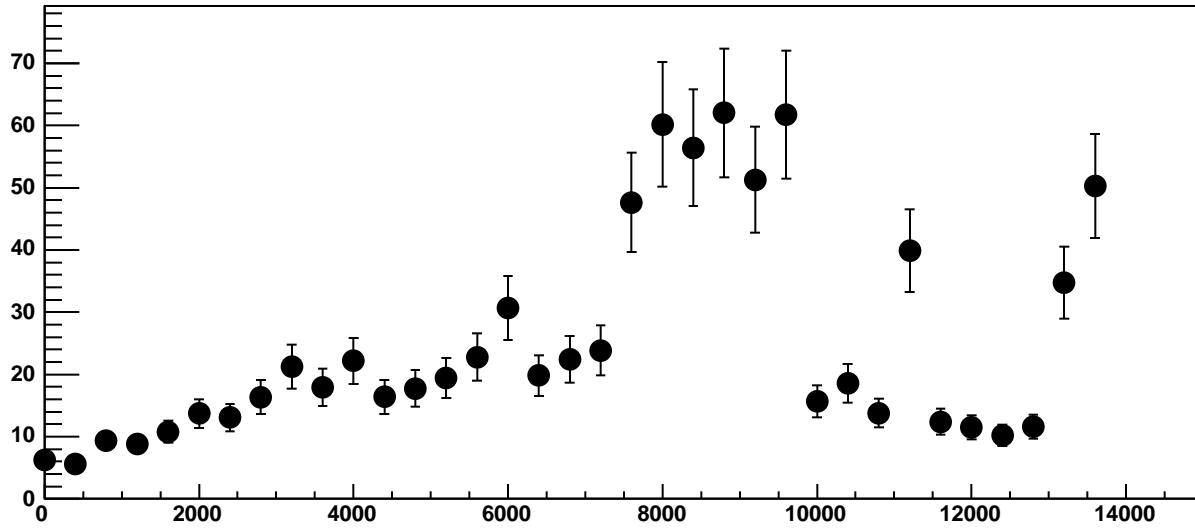


Chip 7, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

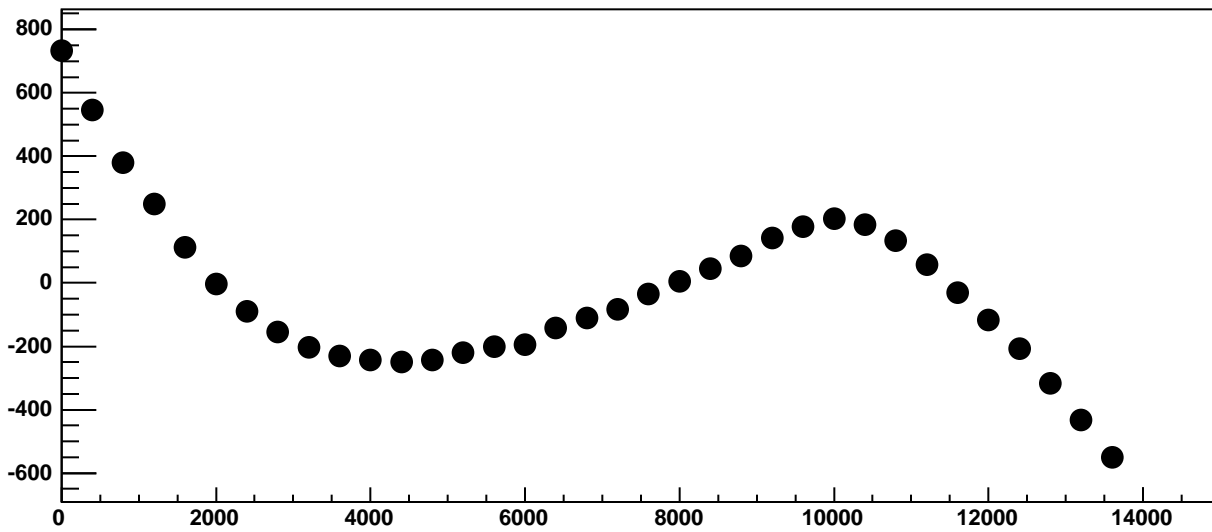
$\chi^2 / \text{ndf}$  4.845e+04 / 23  
p0 -935.1 ± 1.41  
p1 0.4711 ± 0.0002577



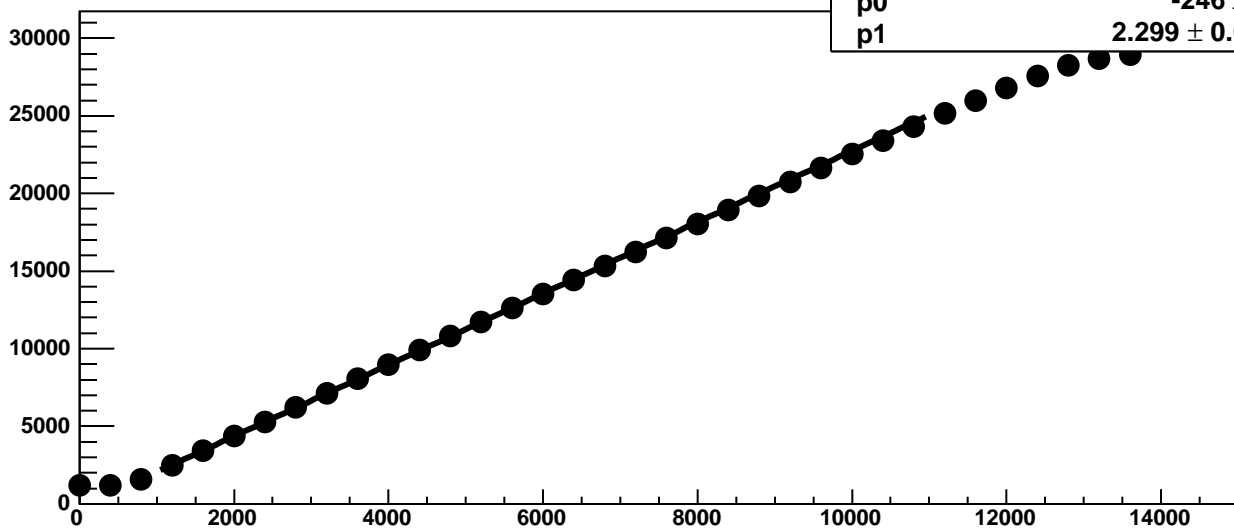
Chip 7, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

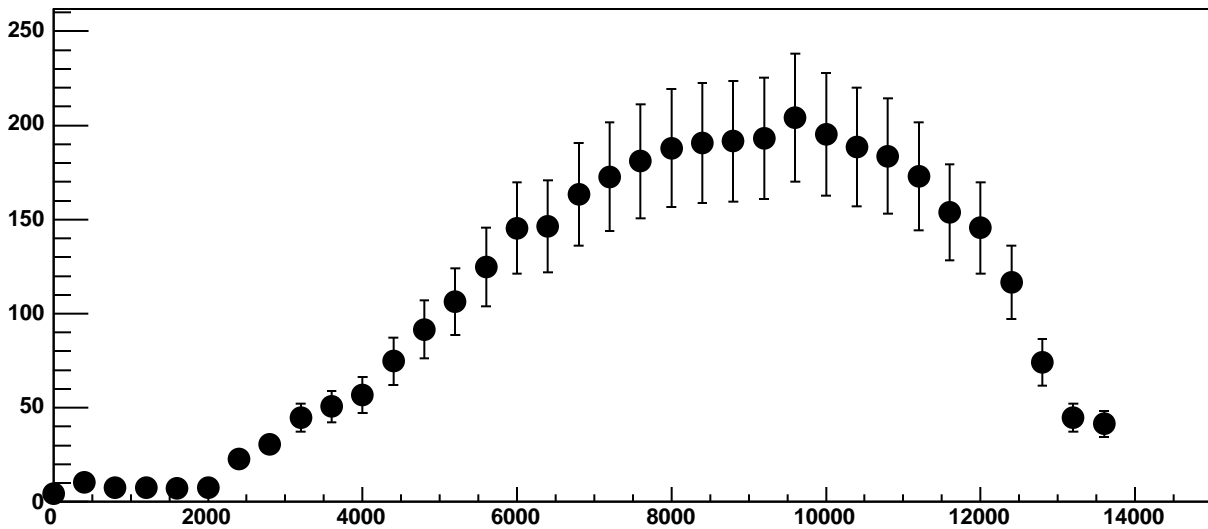


Chip 7, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

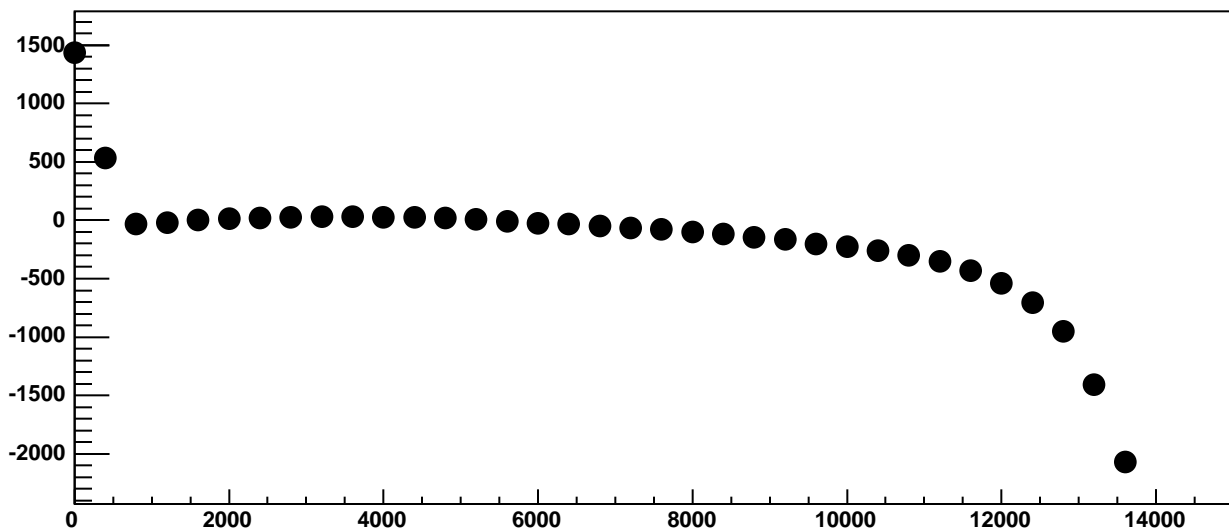


$\chi^2 / \text{ndf}$  426.6 / 23  
p0 -246 ± 2.349  
p1 2.299 ± 0.001226

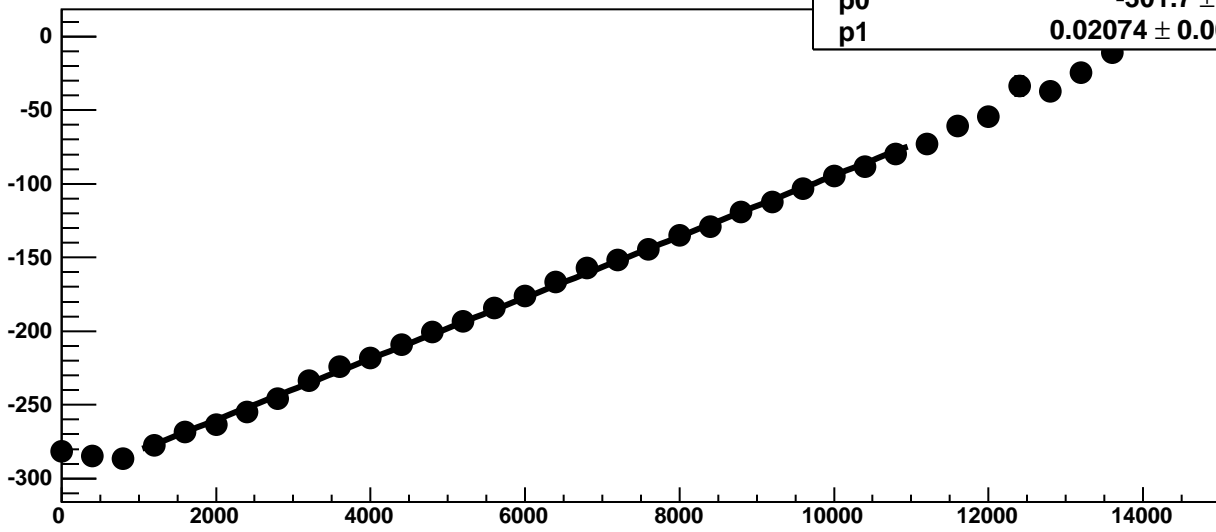
Chip 7, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



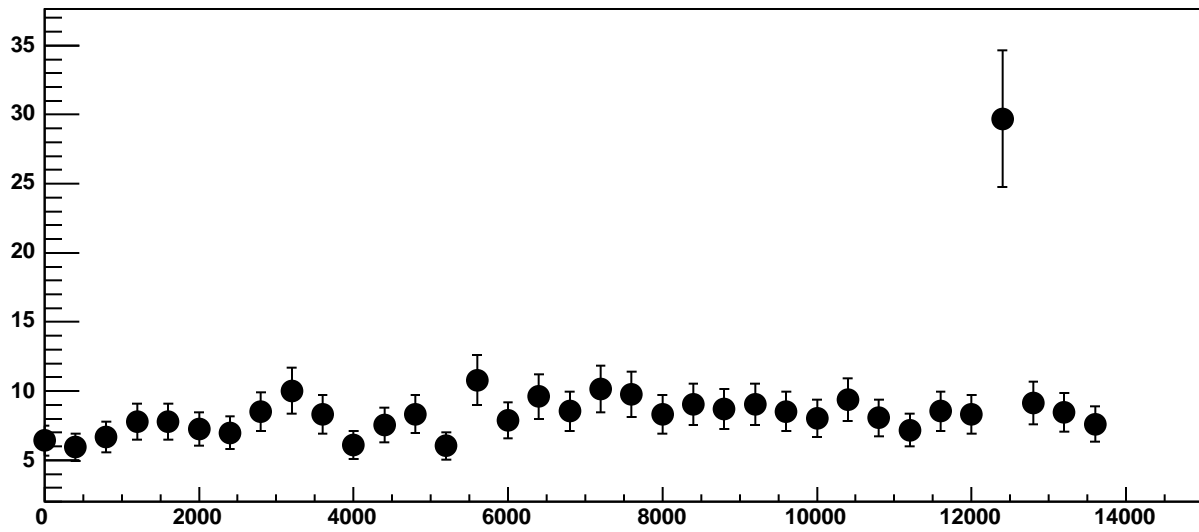
Chip 7, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



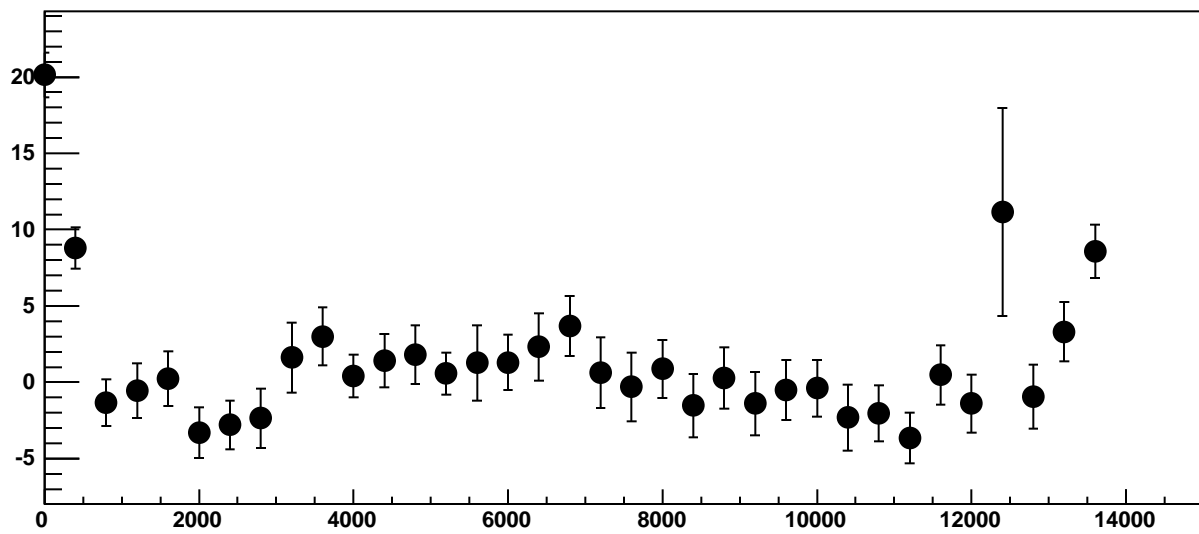
Chip 7, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



Chip 7, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

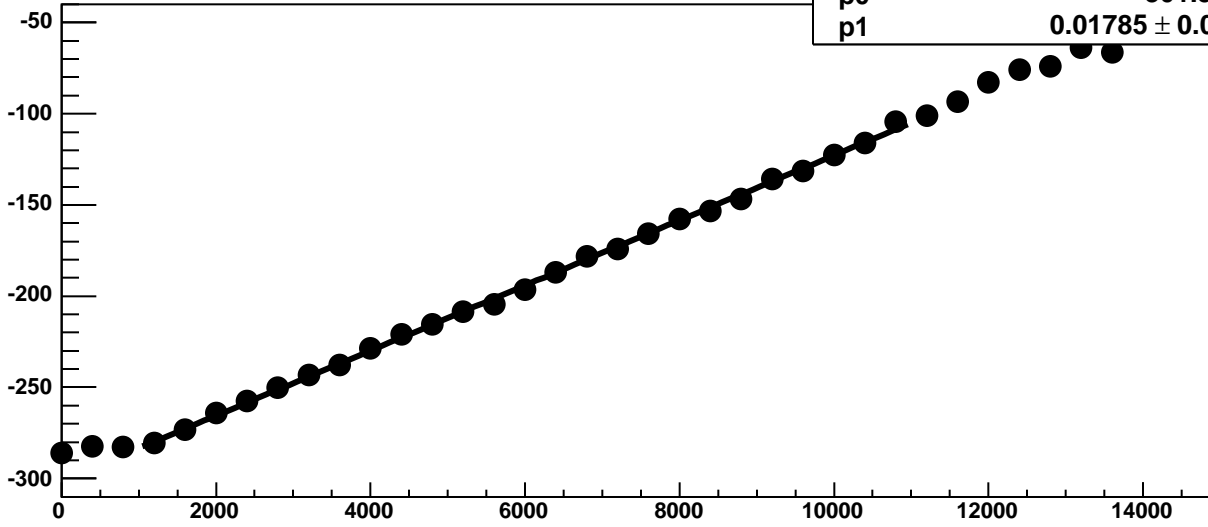


Chip 7, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



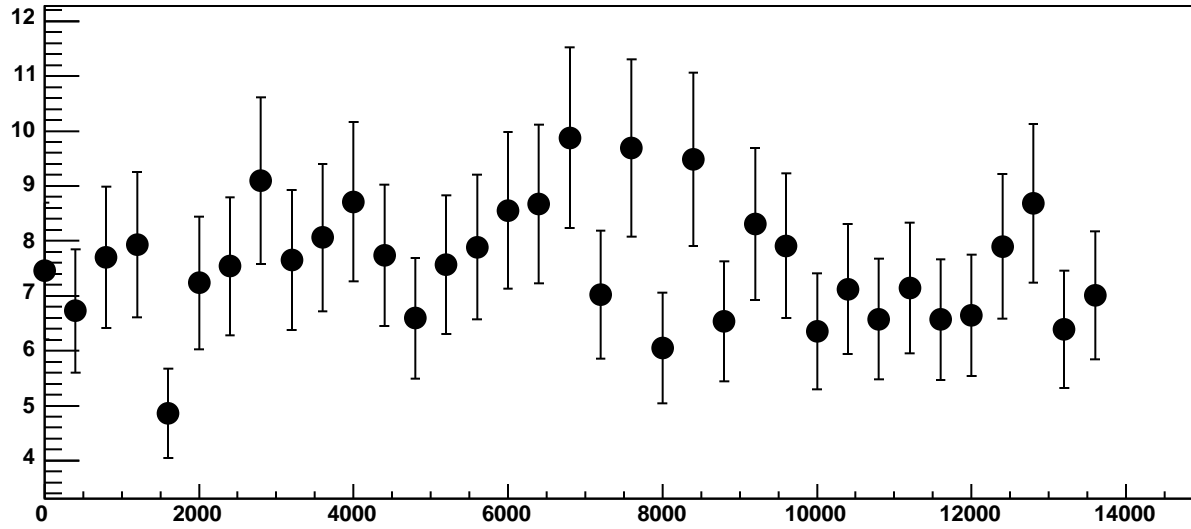


Chip 7, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

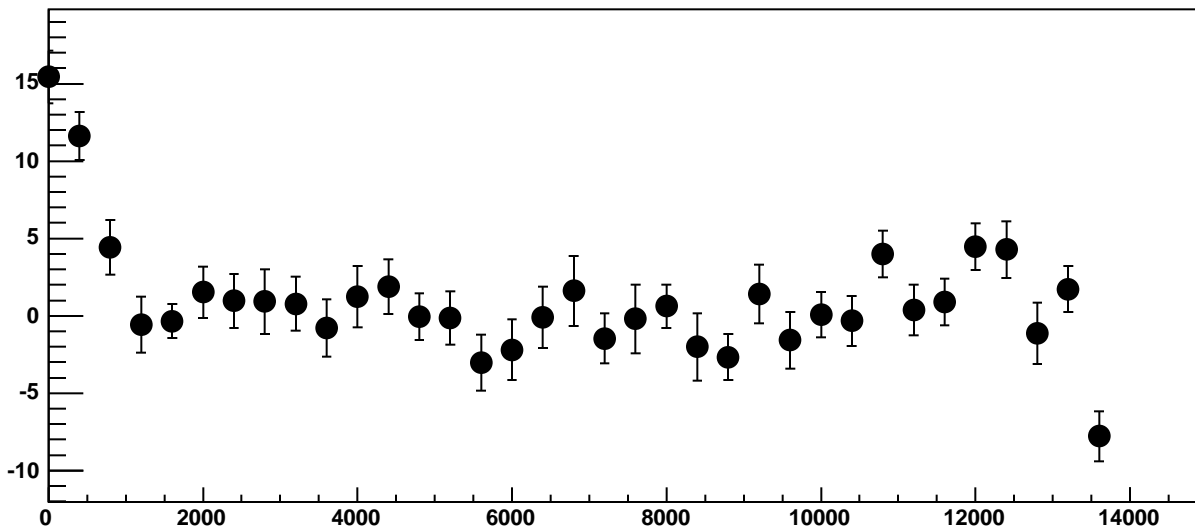


$\chi^2 / \text{ndf}$  21.38 / 23  
p0  $-301.3 \pm 0.741$   
p1  $0.01785 \pm 0.0001107$

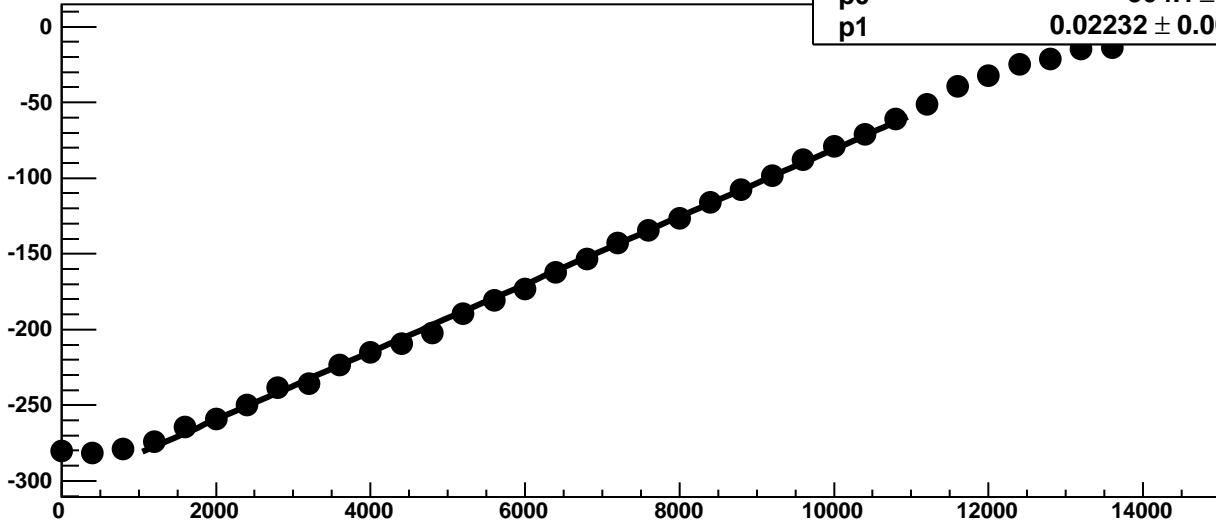
Chip 7, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



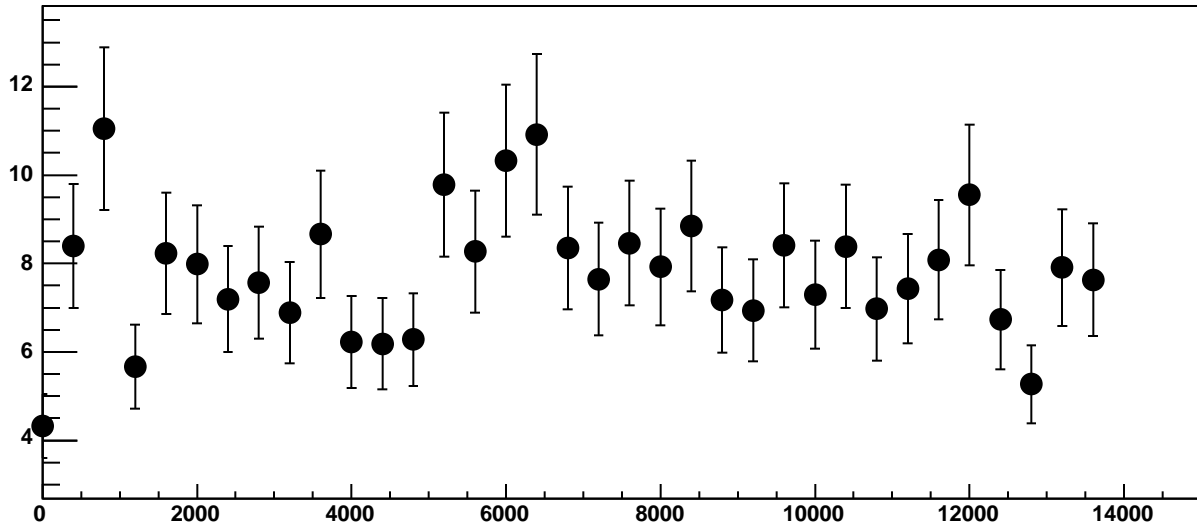
Chip 7, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



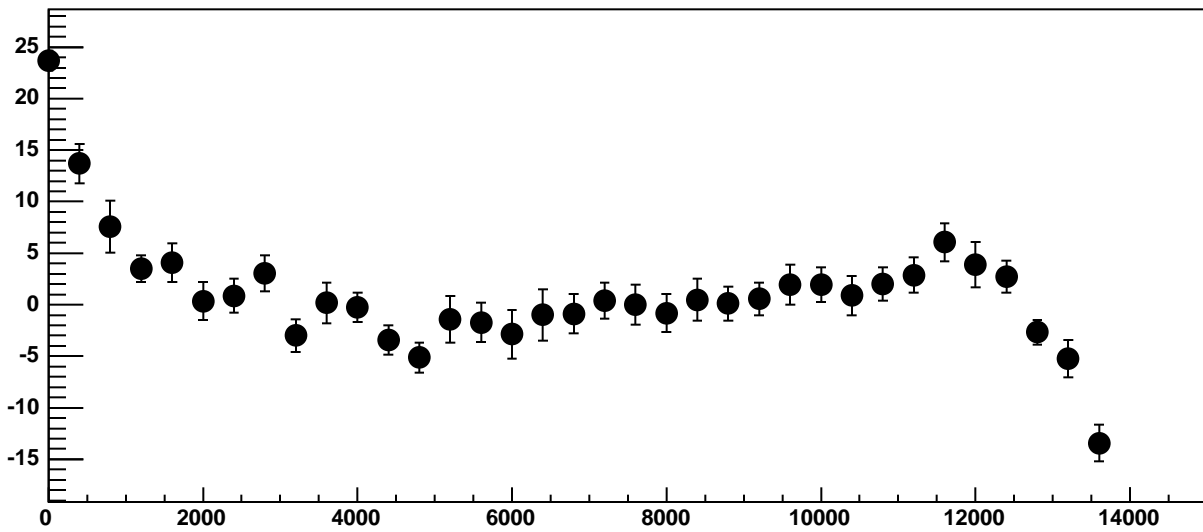
Chip 7, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



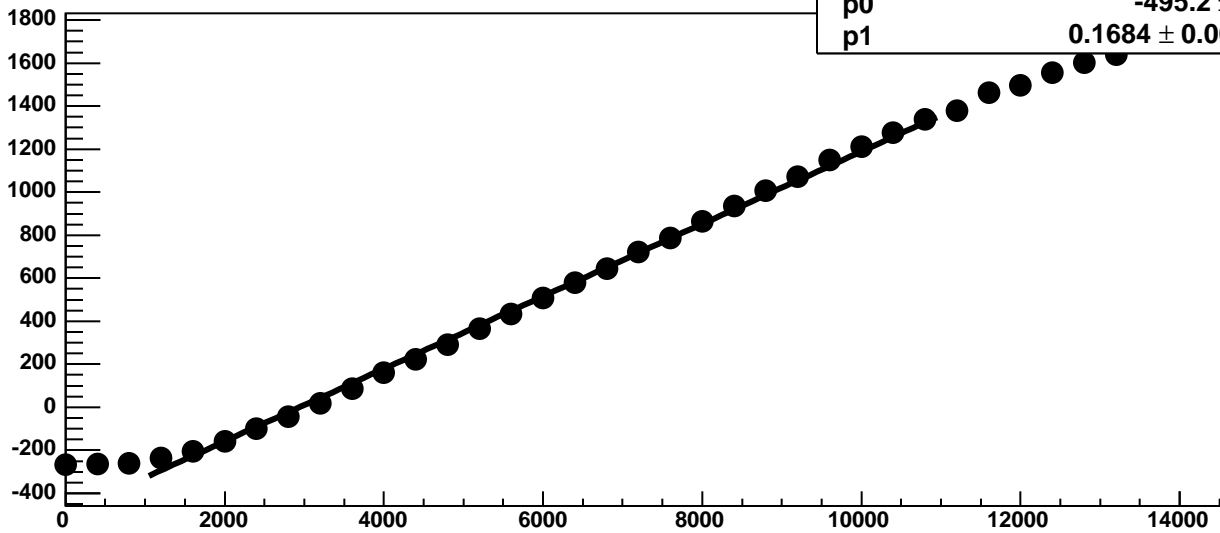
Chip 7, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



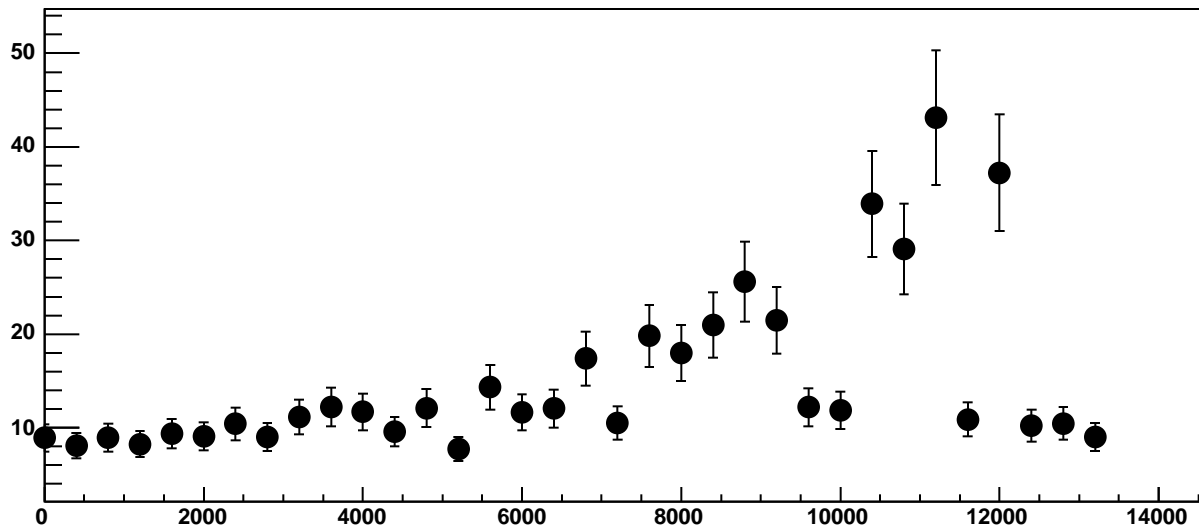
Chip 7, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



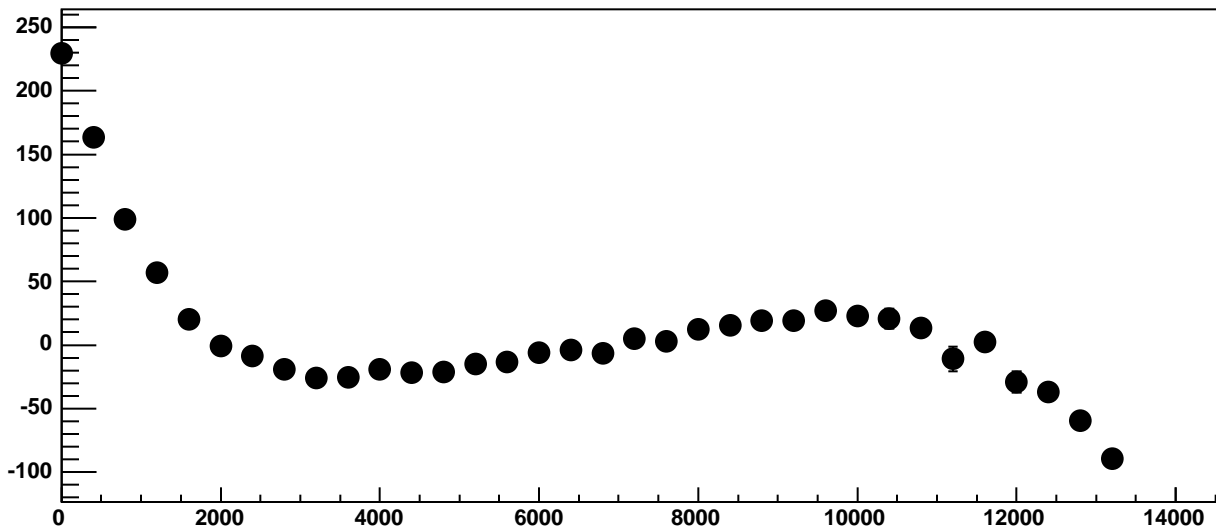
Chip 7, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



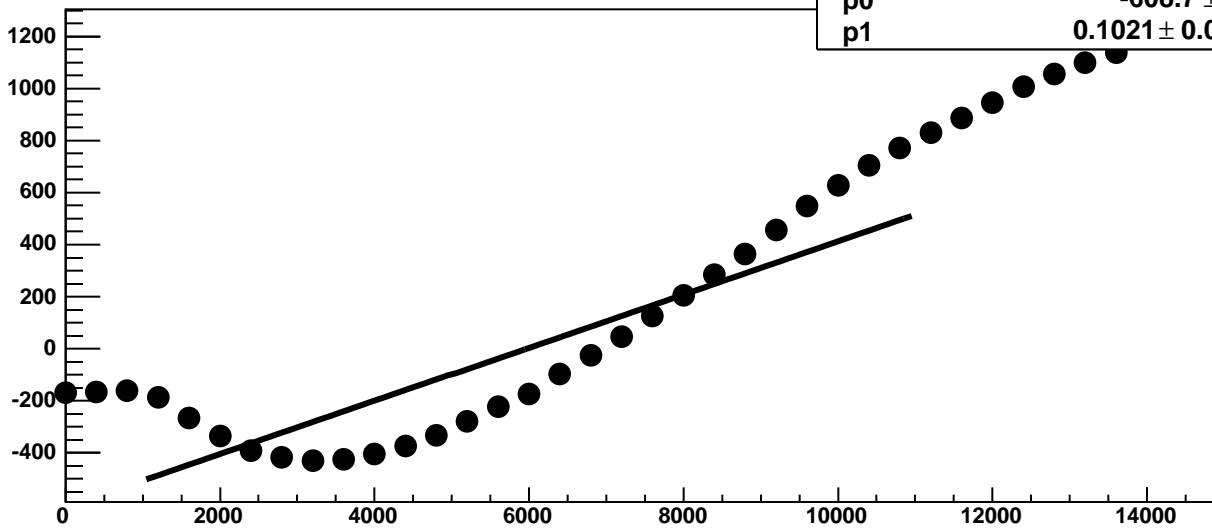
Chip 7, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



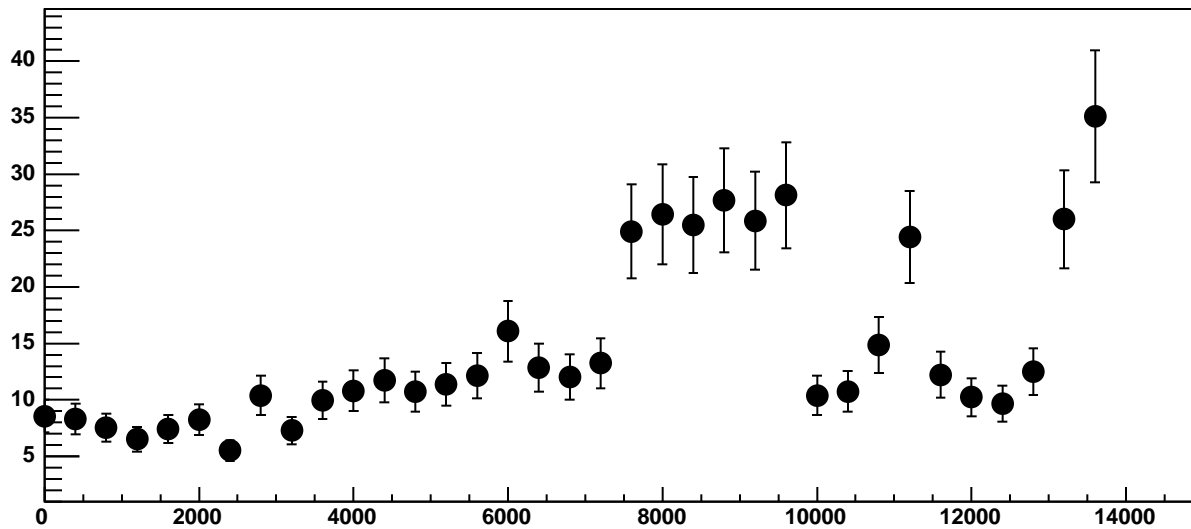
Chip 7, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



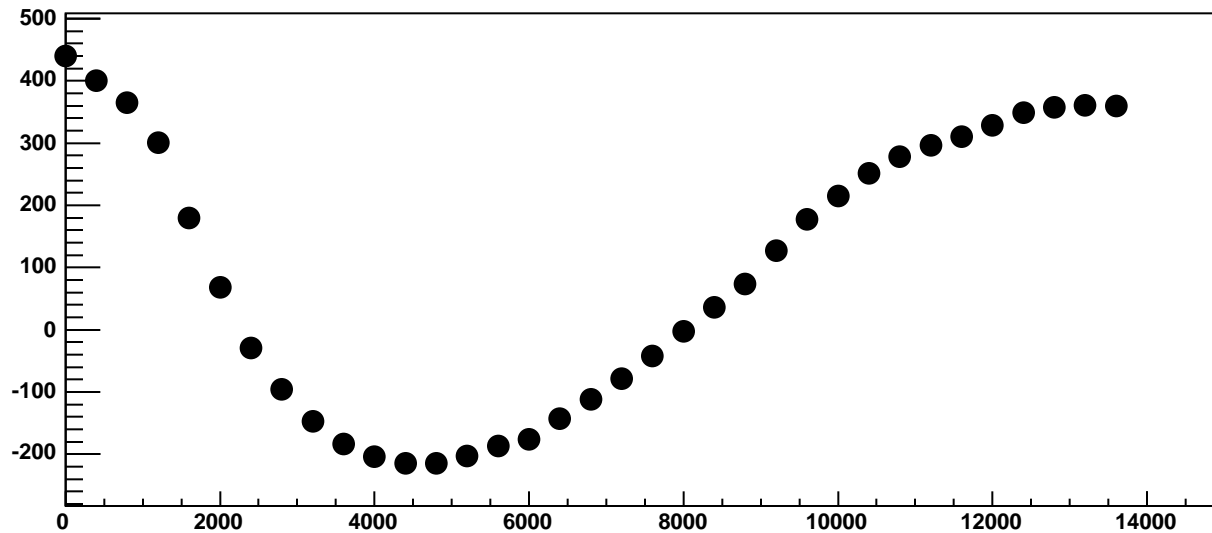
Chip 7, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



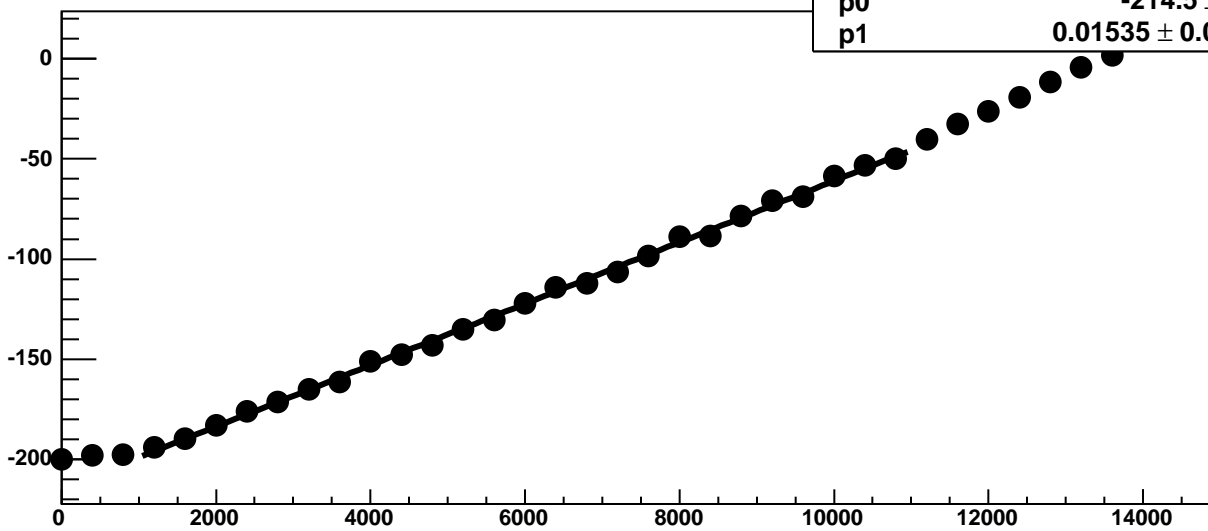
Chip 7, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

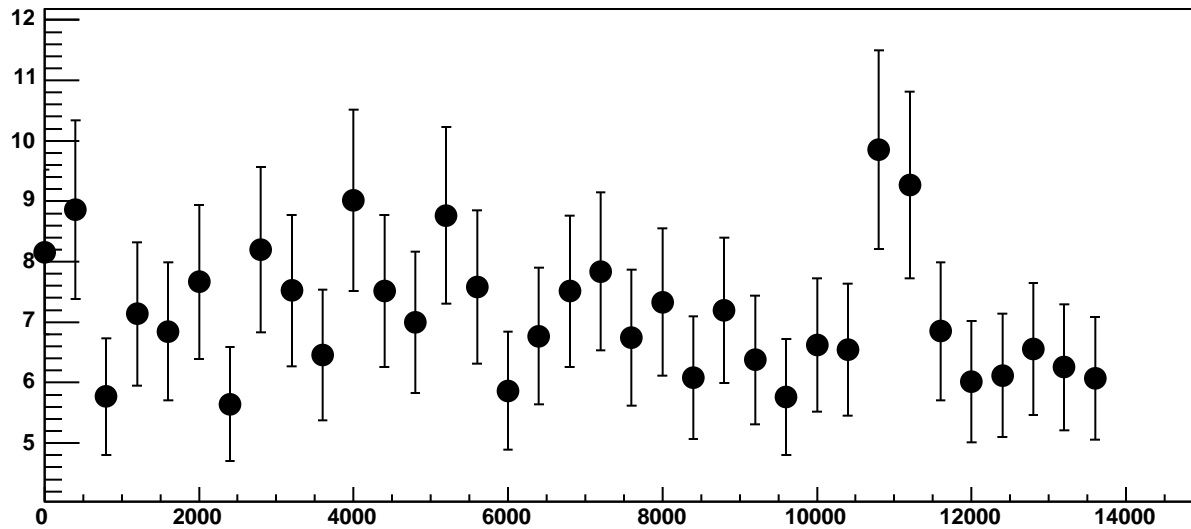


Chip 7, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

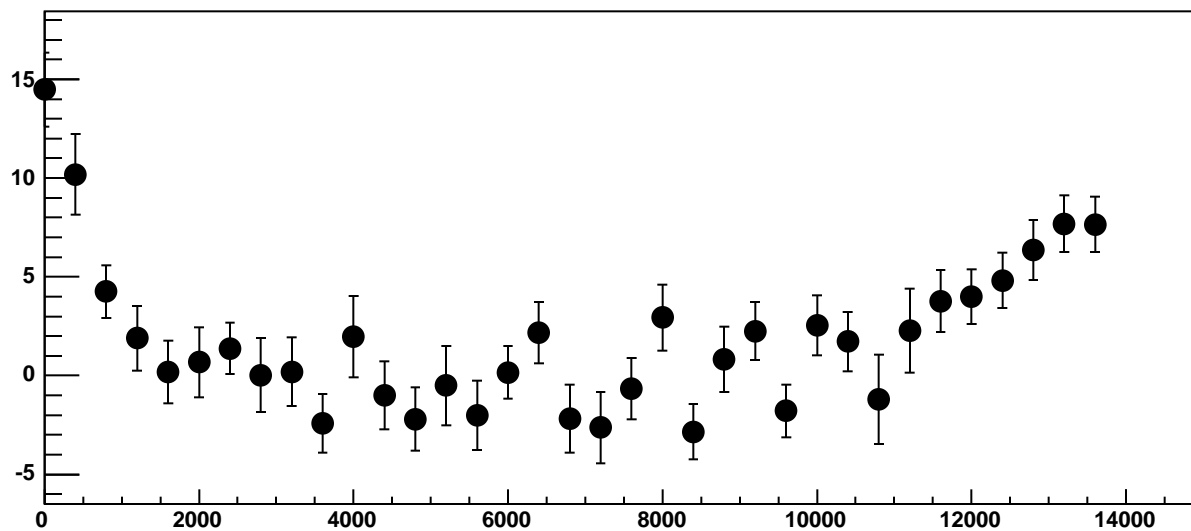


$\chi^2 / \text{ndf}$  31.75 / 23  
p0 -214.5 ± 0.7481  
p1 0.01535 ± 0.0001109

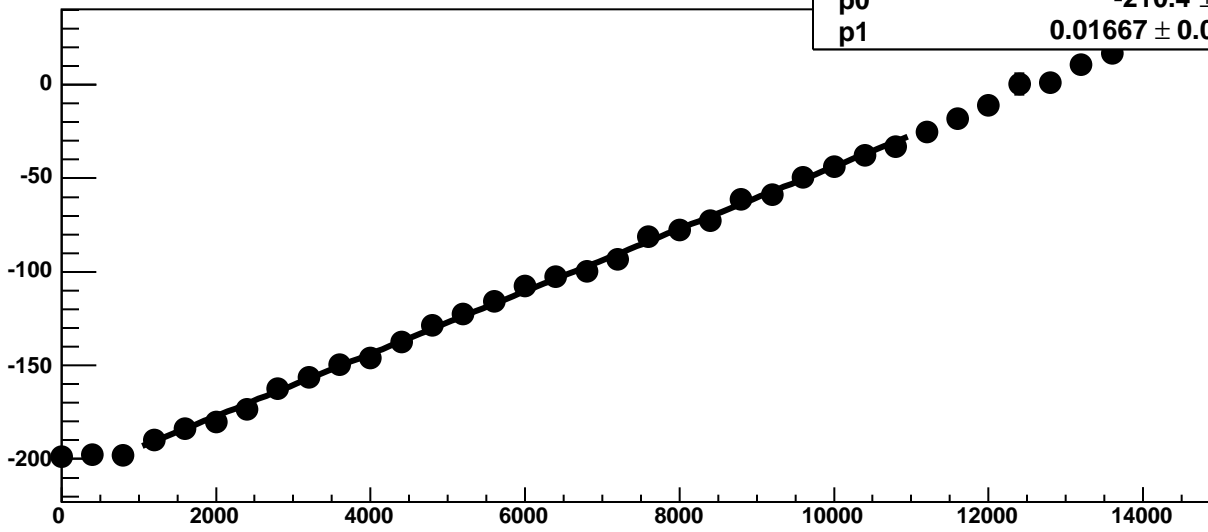
Chip 7, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



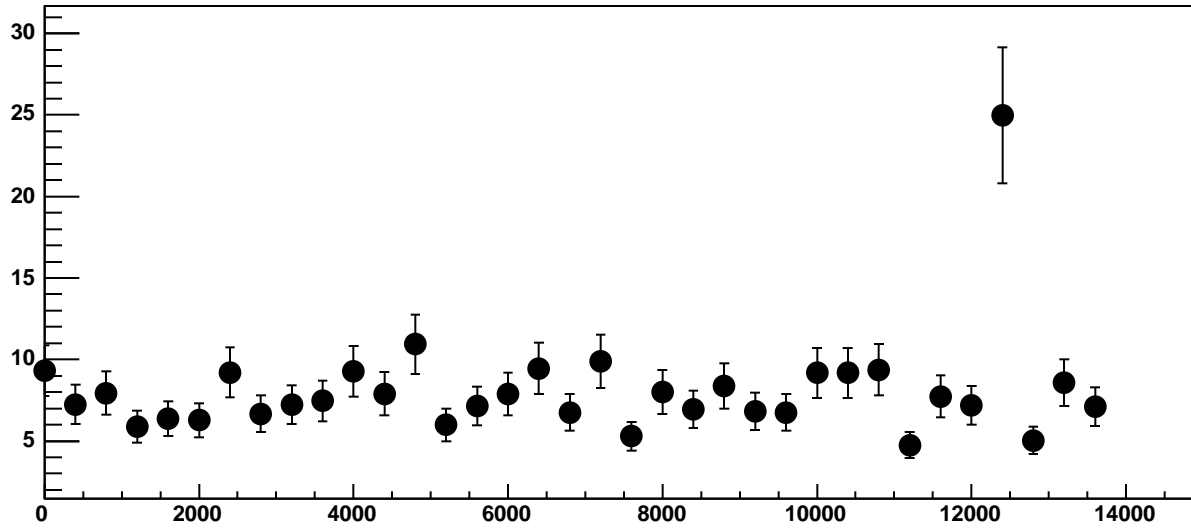
Chip 7, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



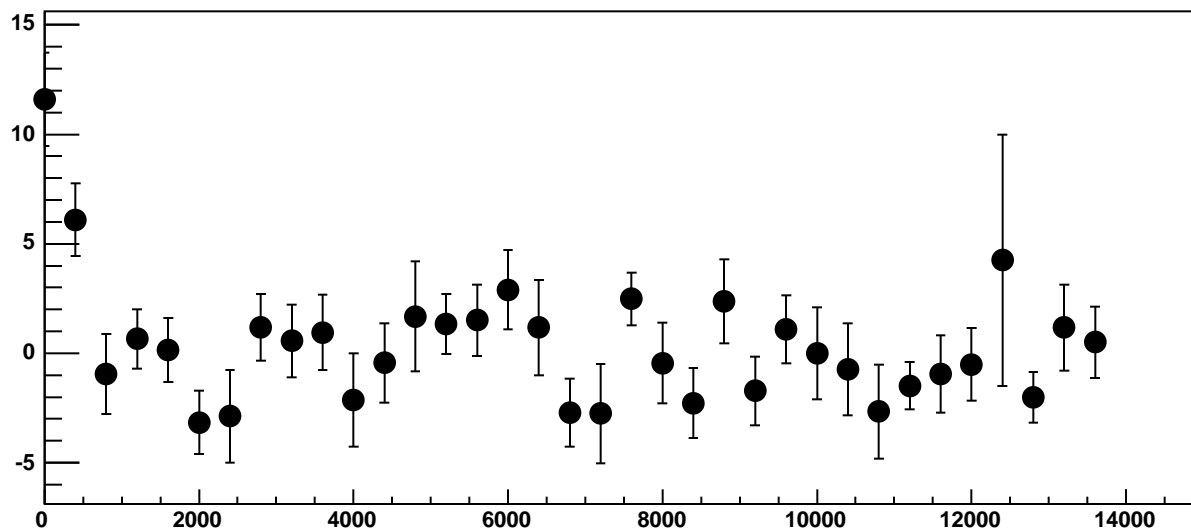
Chip 7, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



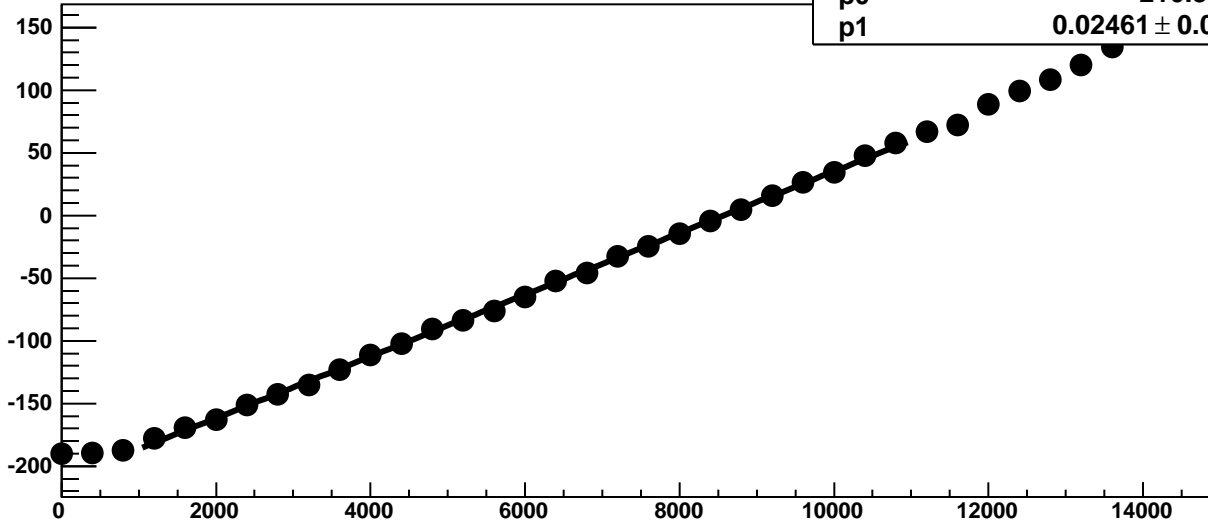
Chip 7, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

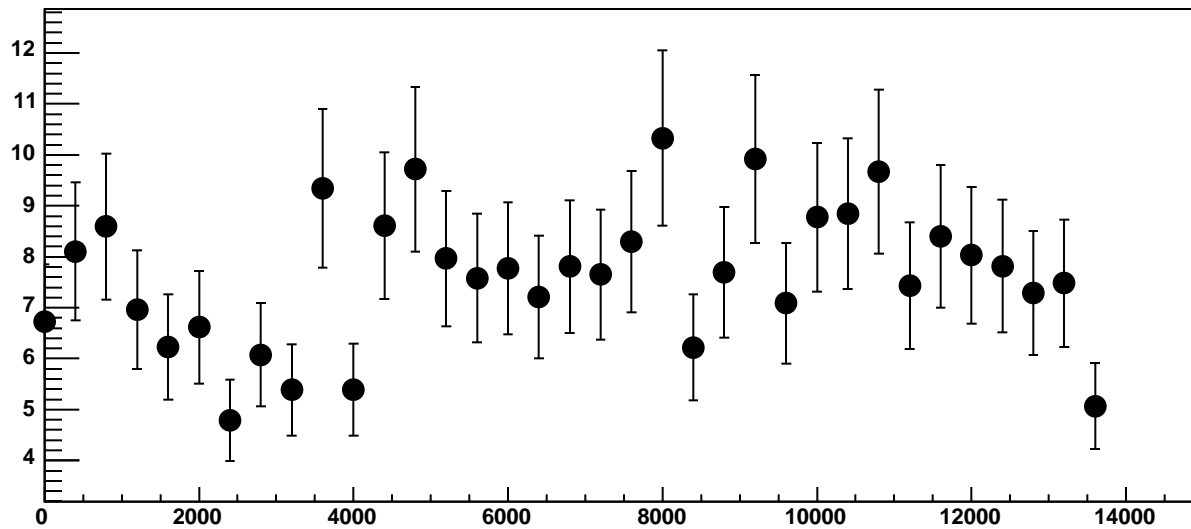


Chip 7, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

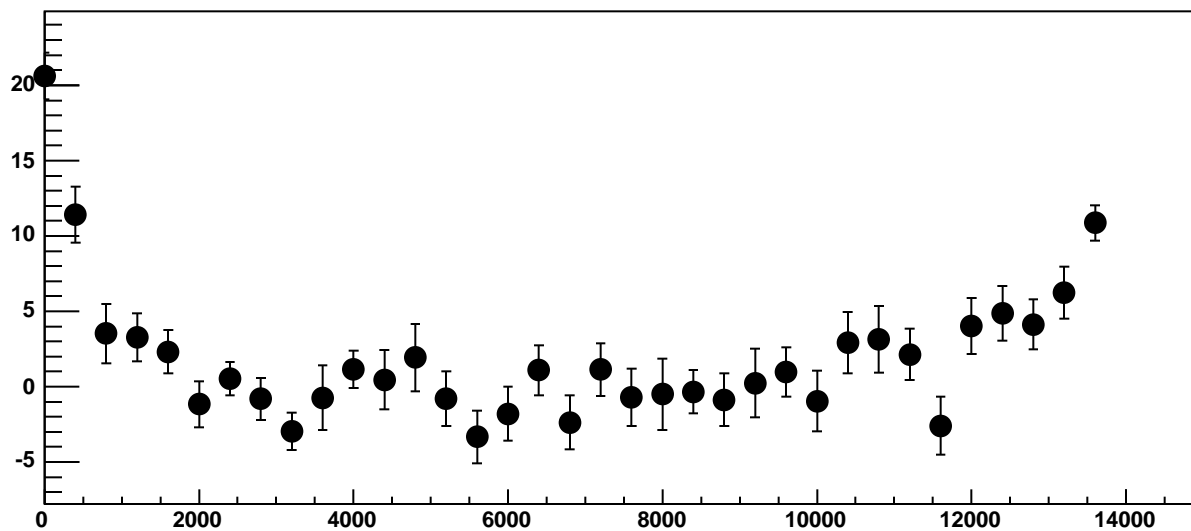


$\chi^2 / \text{ndf}$  28.09 / 23  
p0  $-210.8 \pm 0.696$   
p1  $0.02461 \pm 0.0001156$

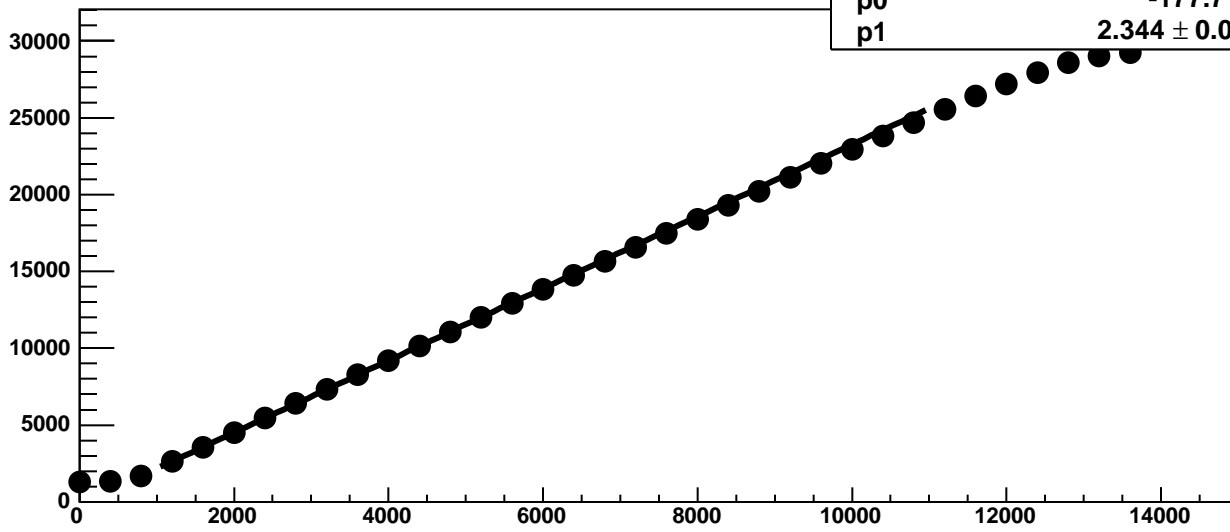
Chip 7, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

705 / 23

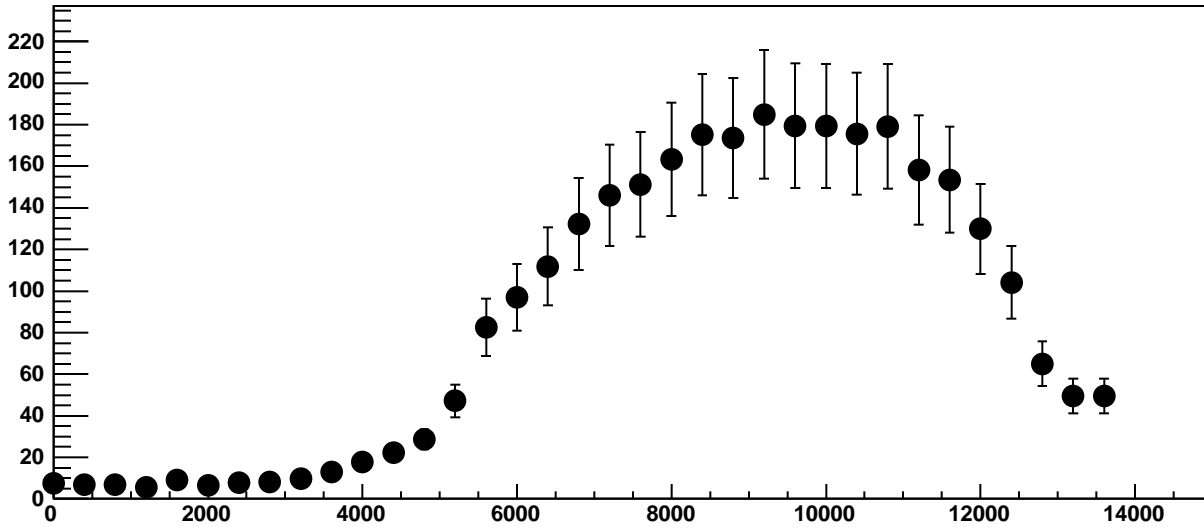
p0

$-177.7 \pm 1.638$

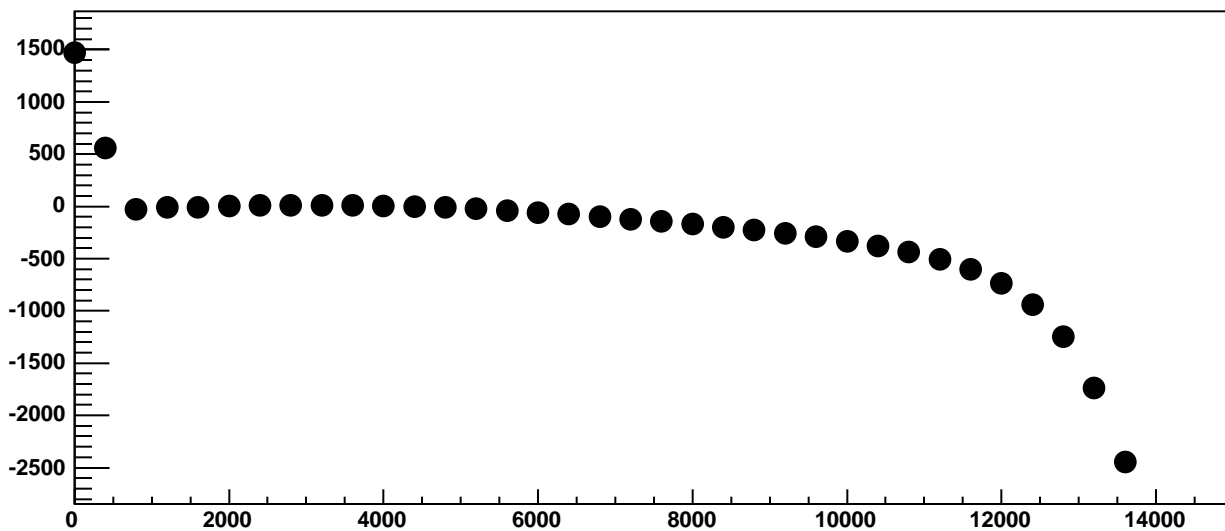
p1

$2.344 \pm 0.0006667$

Chip 7, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

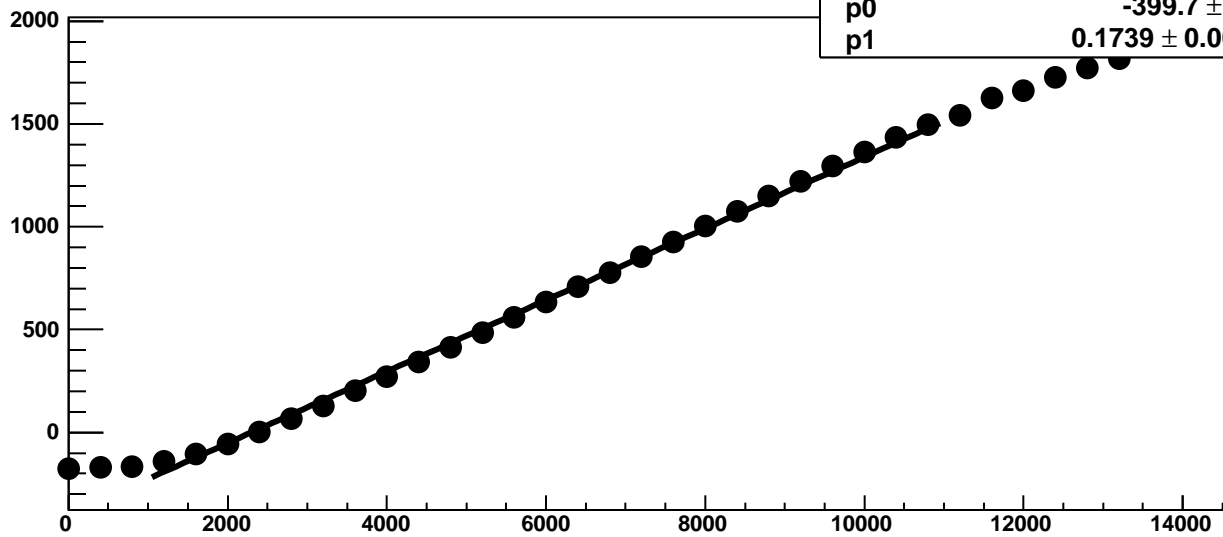


Chip 7, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

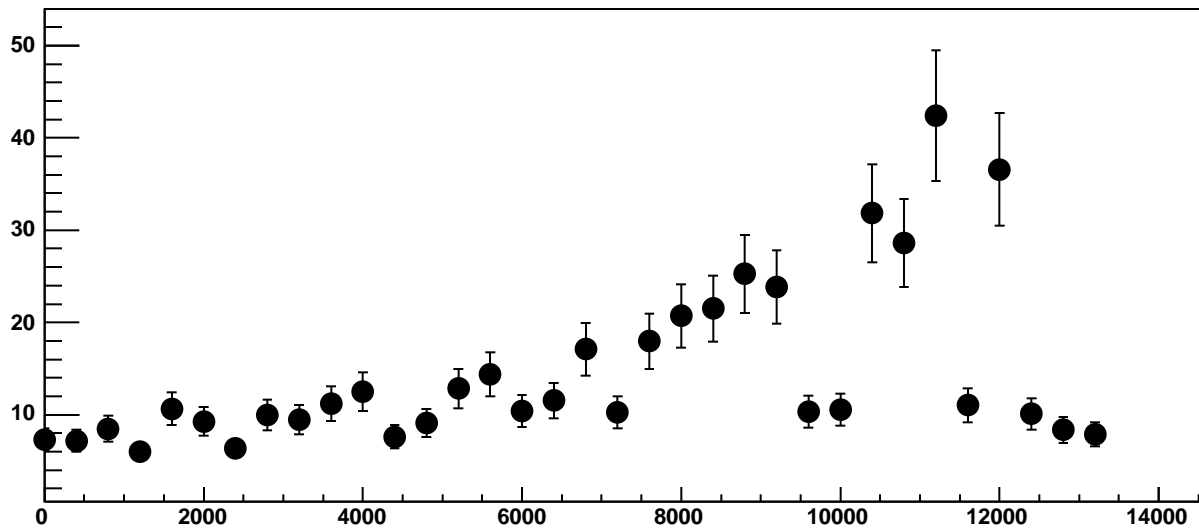




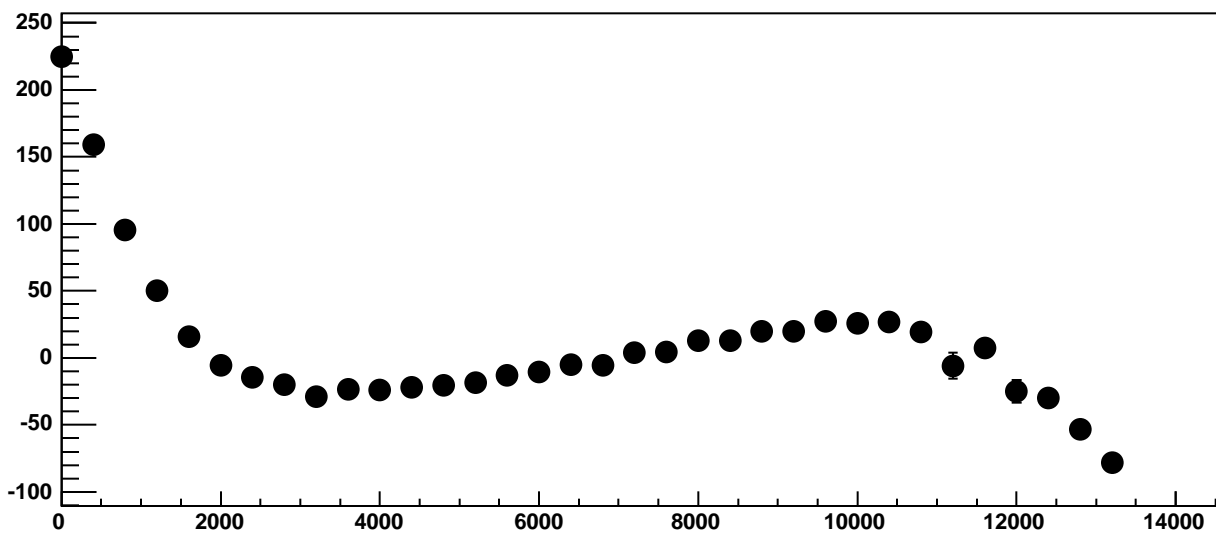
Chip 7, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



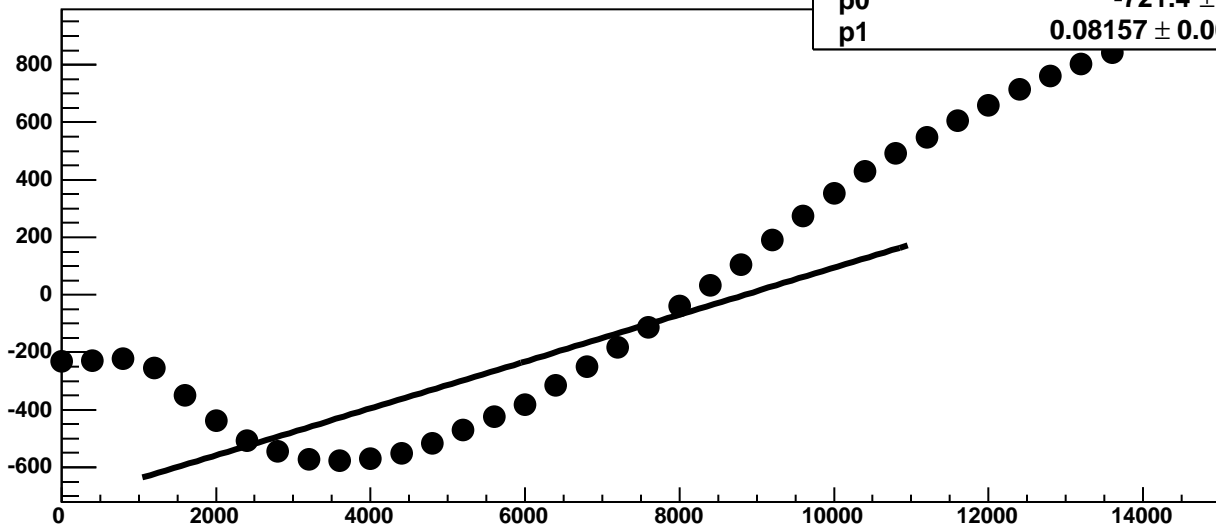
Chip 7, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



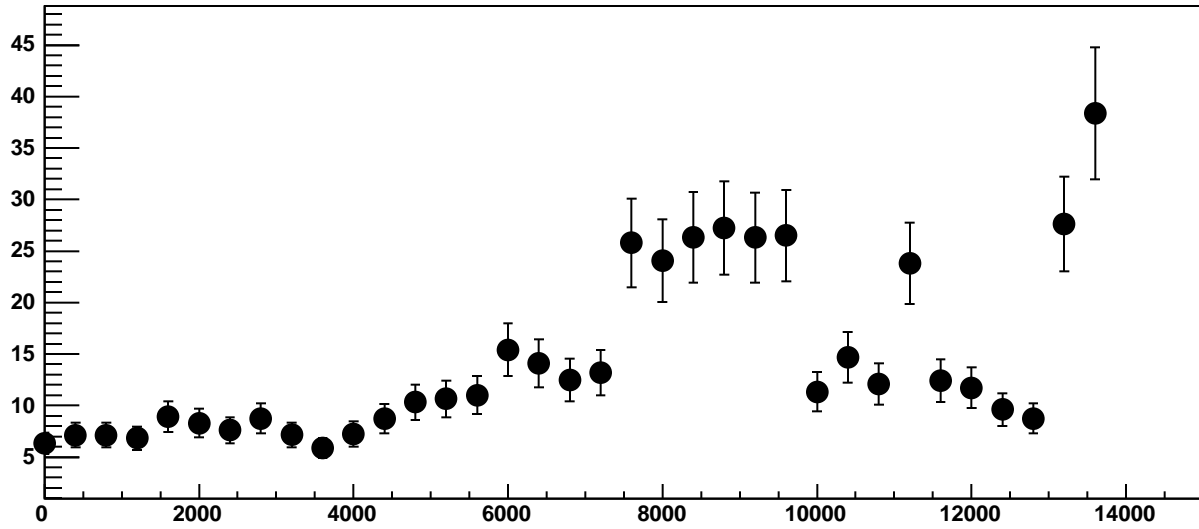
Chip 7, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



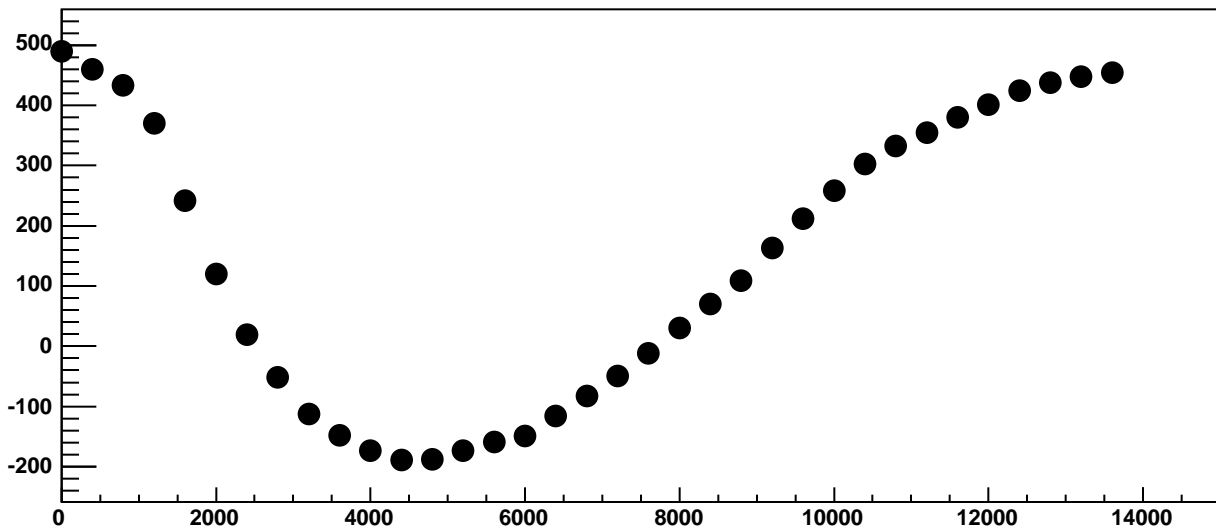
Chip 7, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



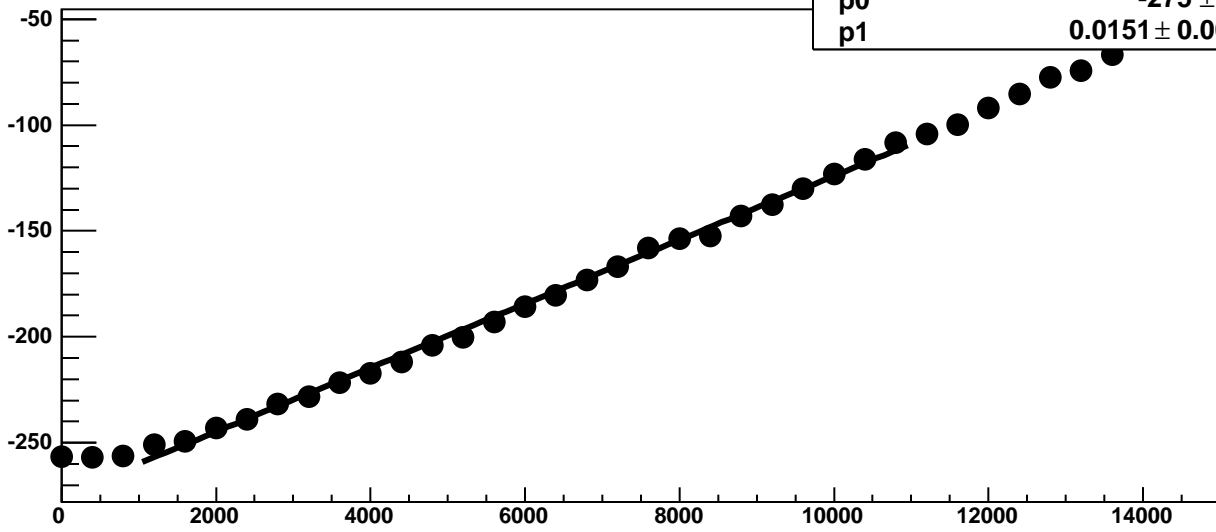
Chip 7, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

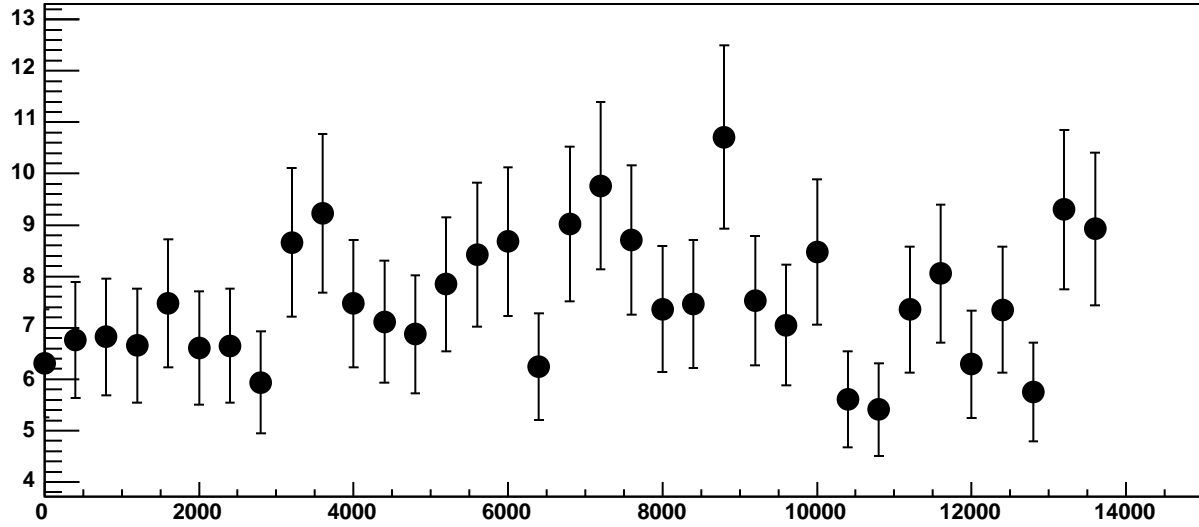


Chip 7, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

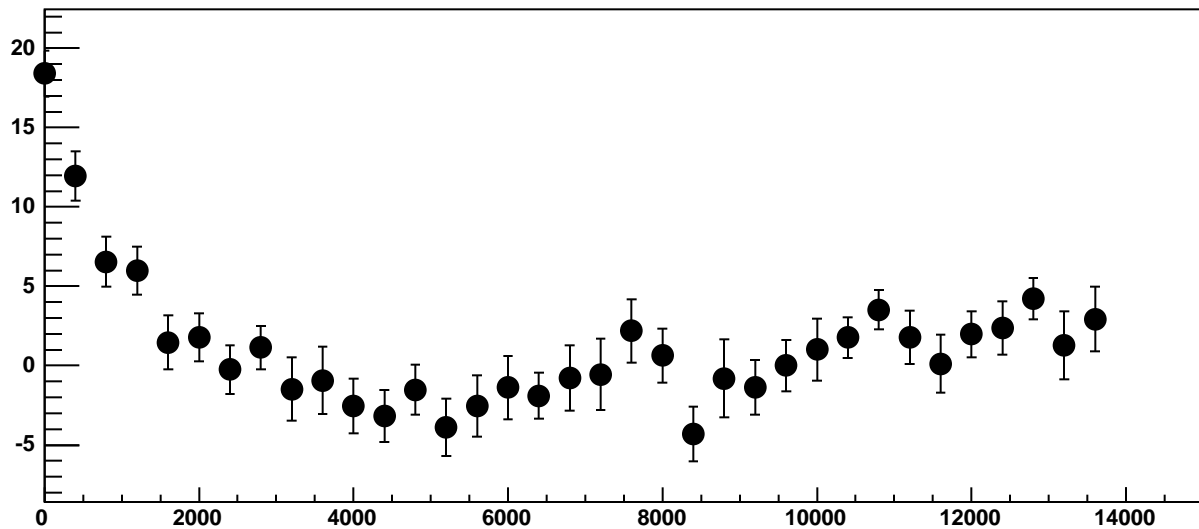


$\chi^2 / \text{ndf}$  53.33 / 23  
p0  $-275 \pm 0.7329$   
p1  $0.0151 \pm 0.0001078$

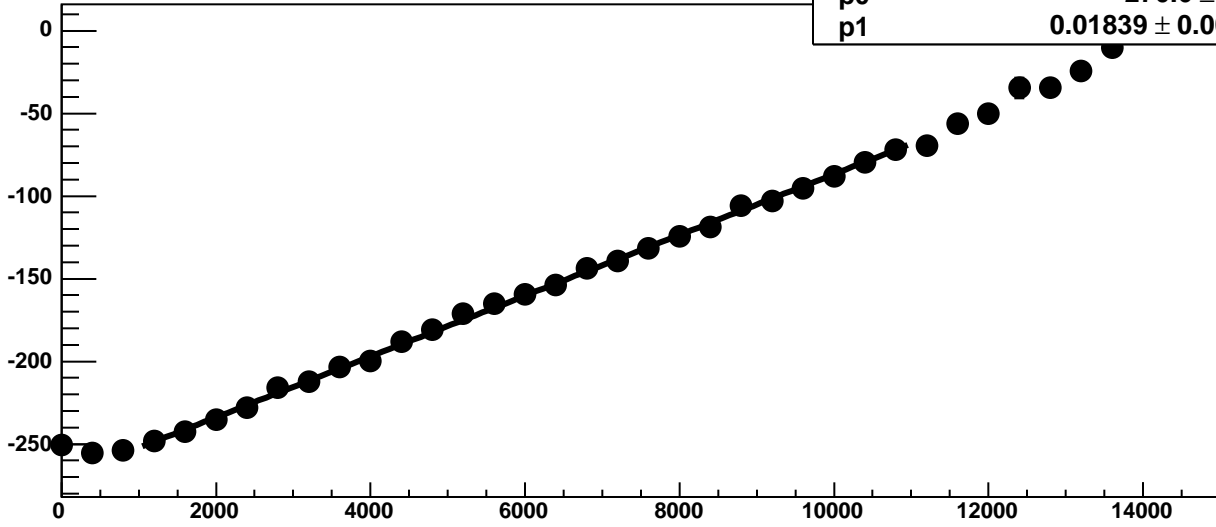
Chip 7, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



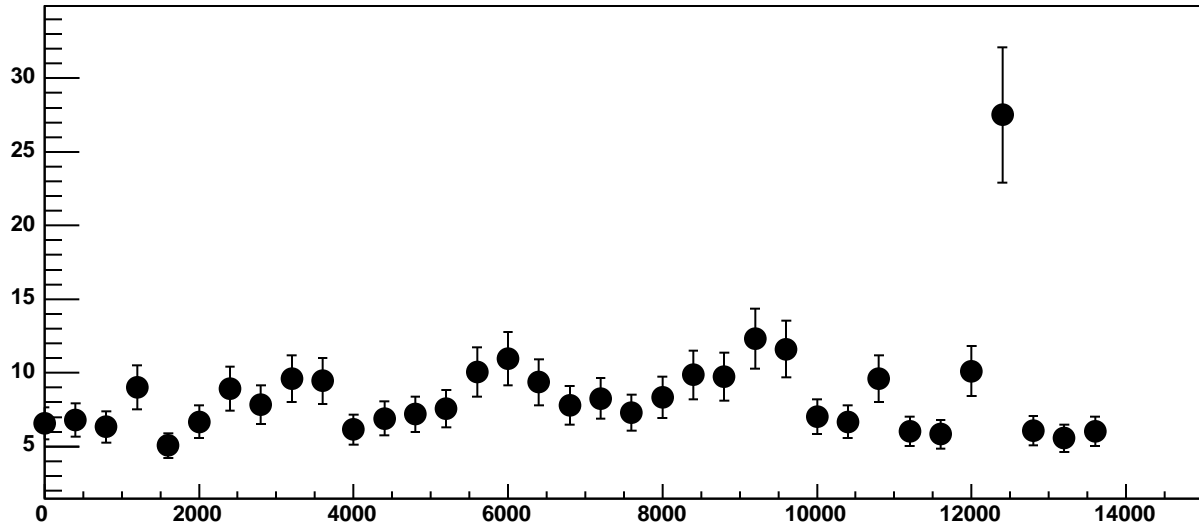
Chip 7, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



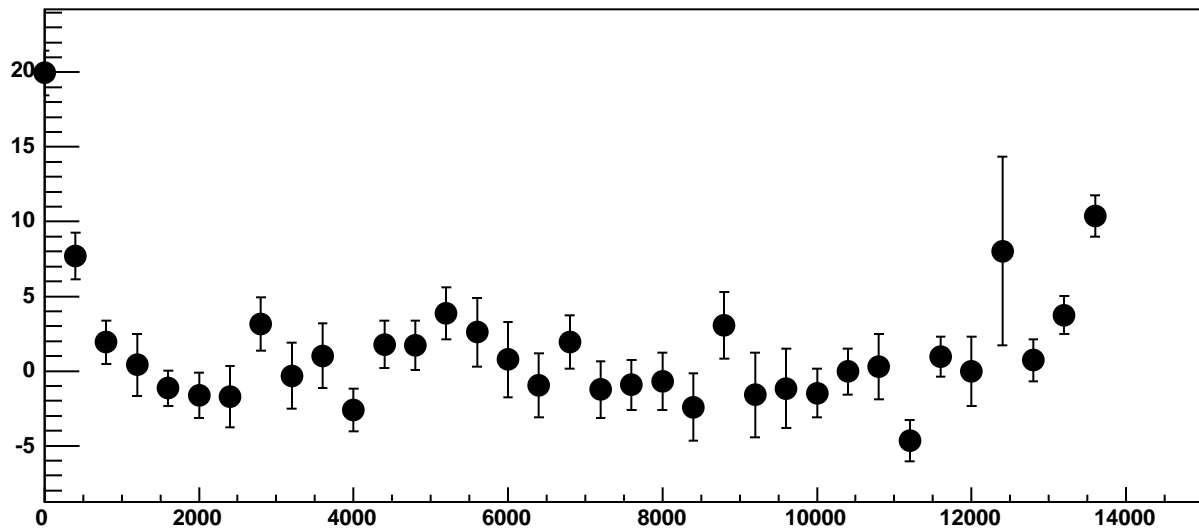
Chip 7, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



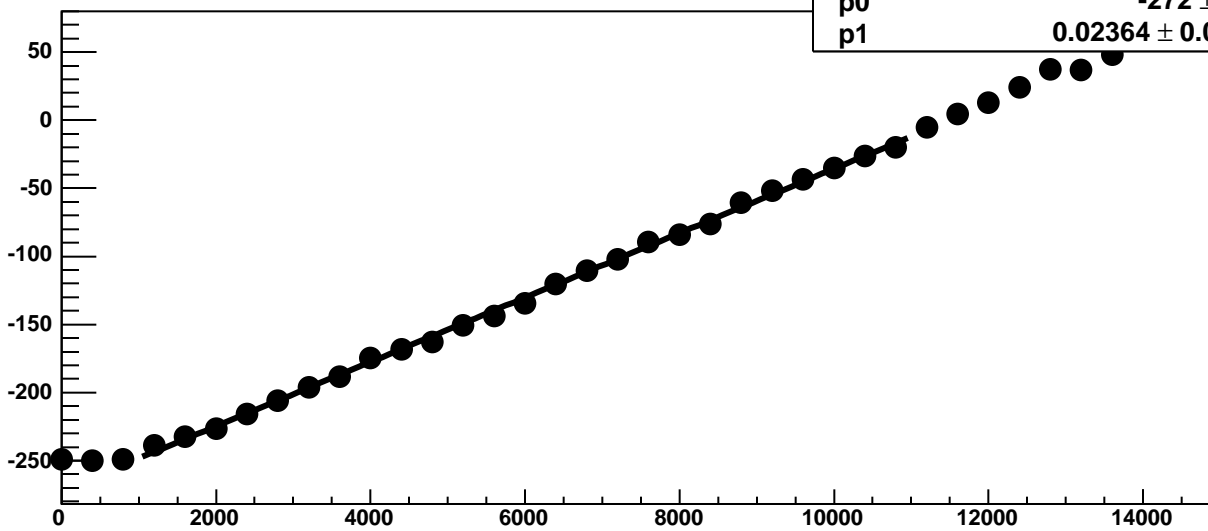
Chip 7, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

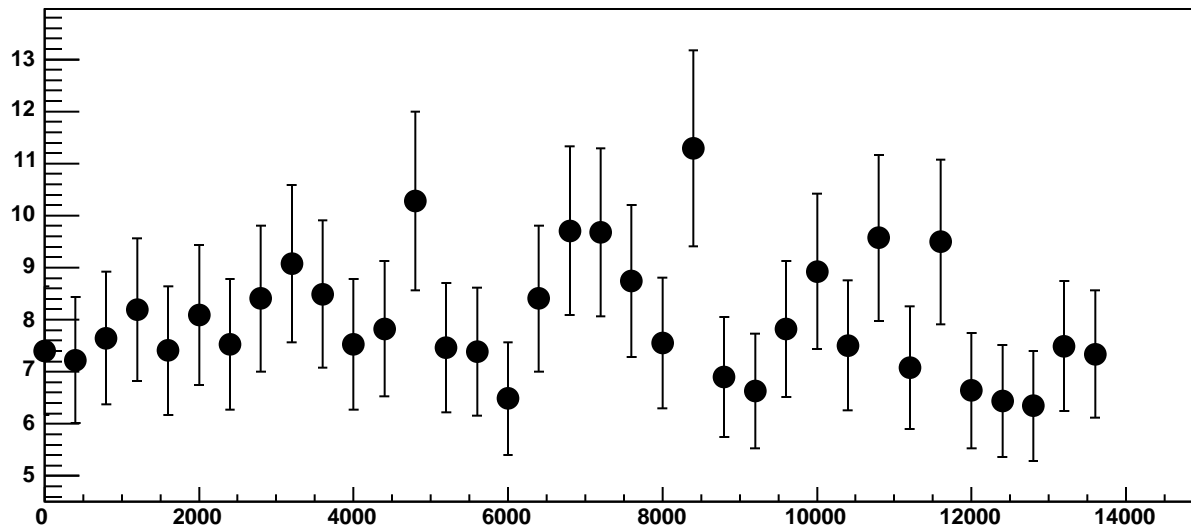


Chip 7, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

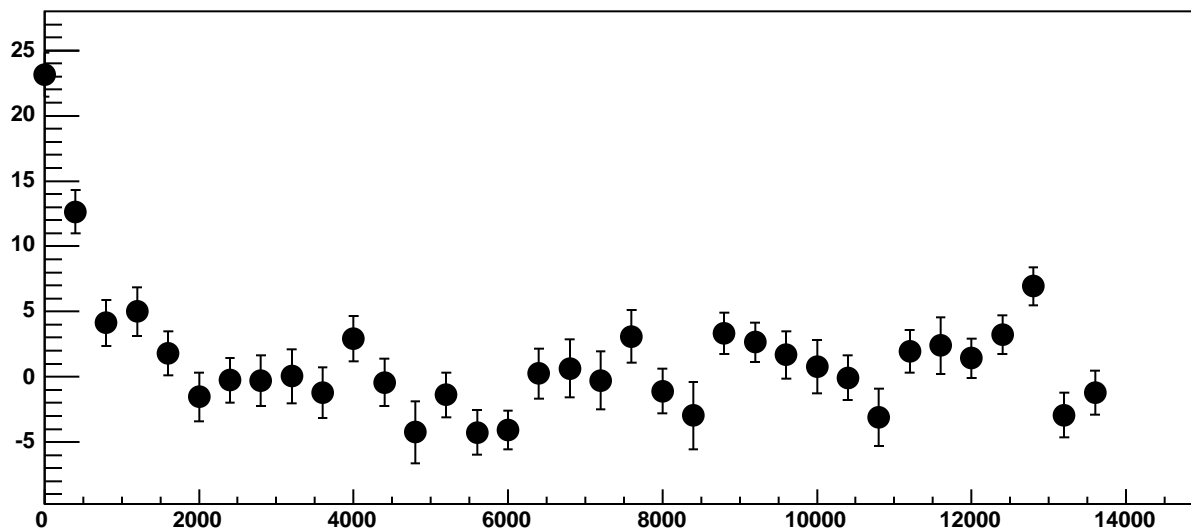


$\chi^2 / \text{ndf}$  44.66 / 23  
p0 -272 ± 0.8498  
p1 0.02364 ± 0.0001281

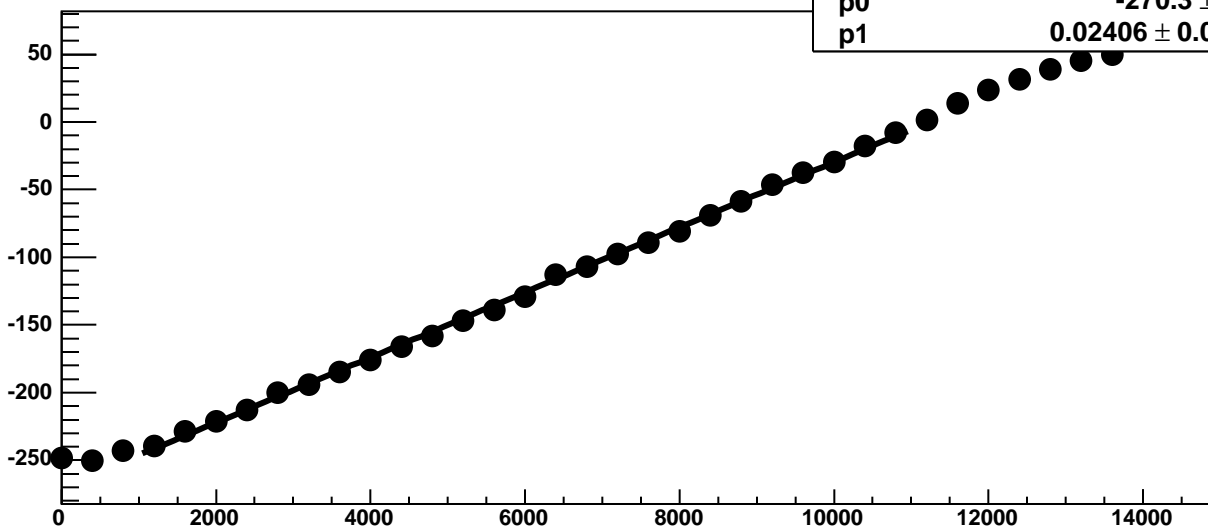
Chip 7, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

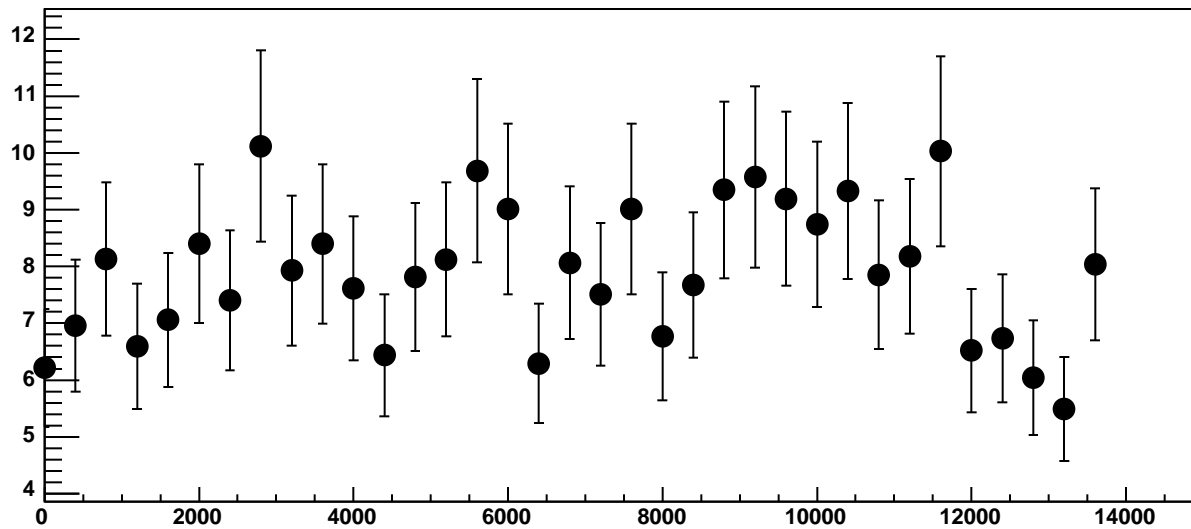


Chip 7, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

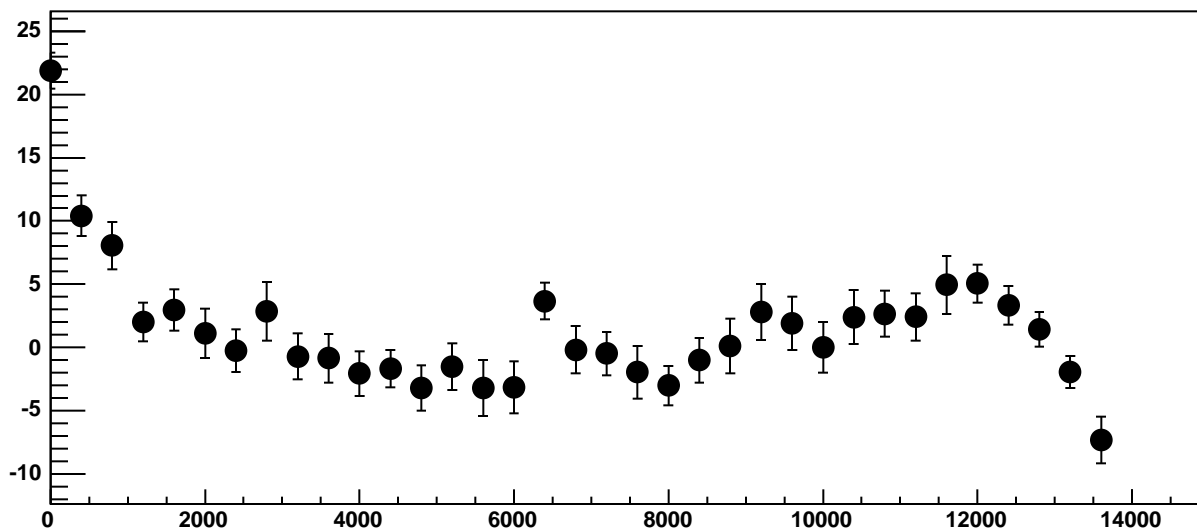


$\chi^2 / \text{ndf}$  35.63 / 23  
p0 -270.3 ± 0.8173  
p1 0.02406 ± 0.0001274

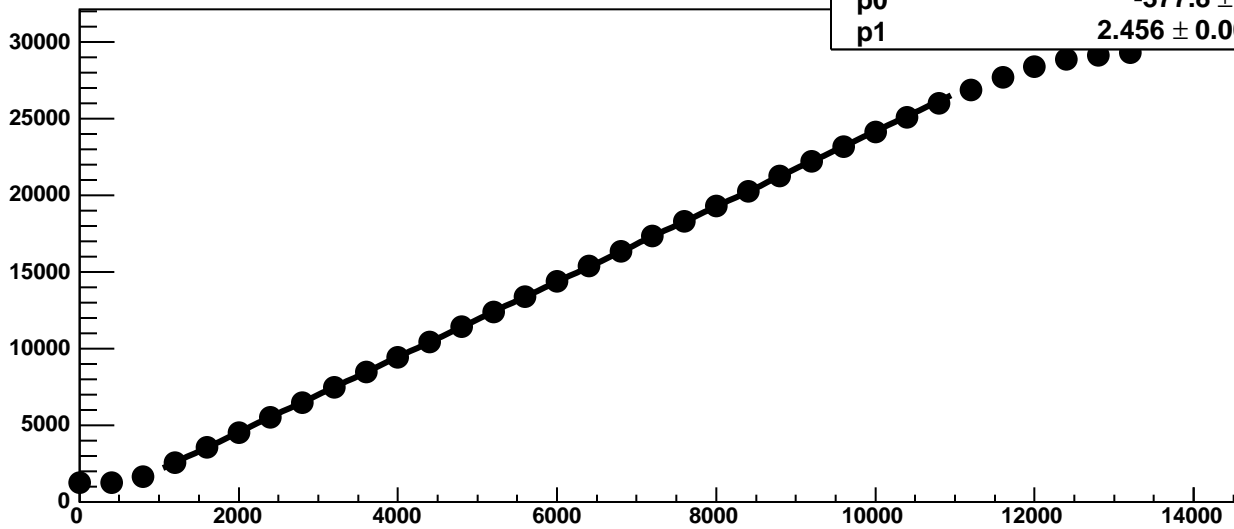
Chip 7, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

2129 / 23

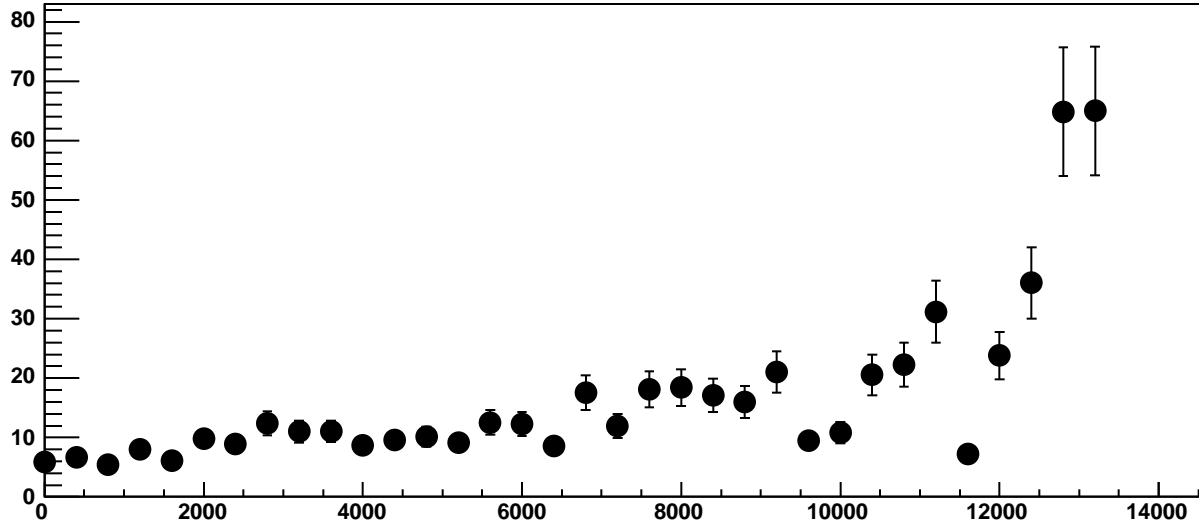
p0

$-377.8 \pm 0.9852$

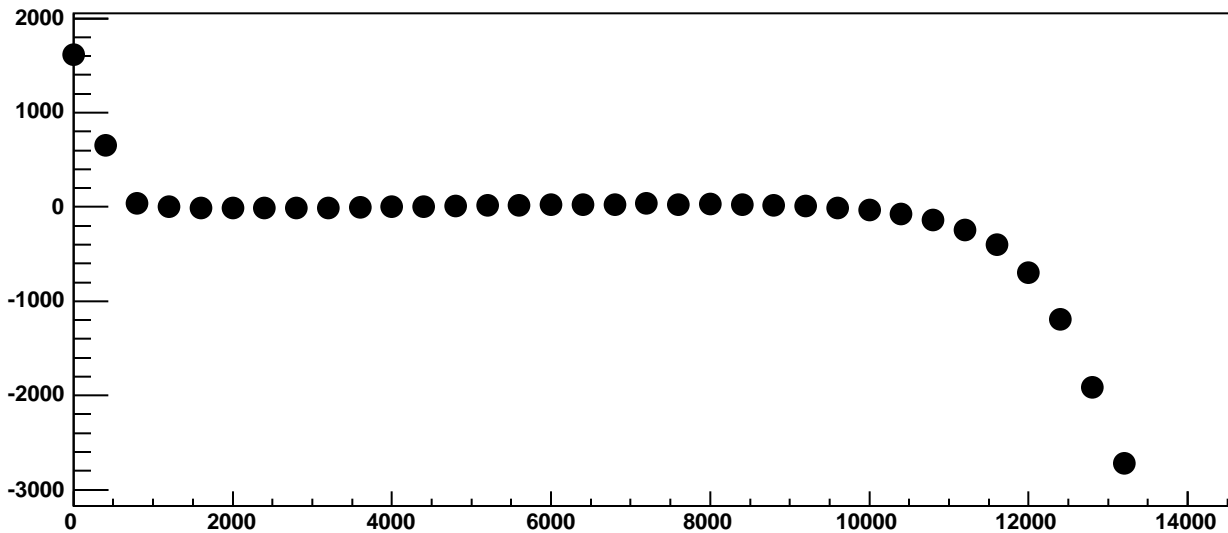
p1

$2.456 \pm 0.0001789$

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC

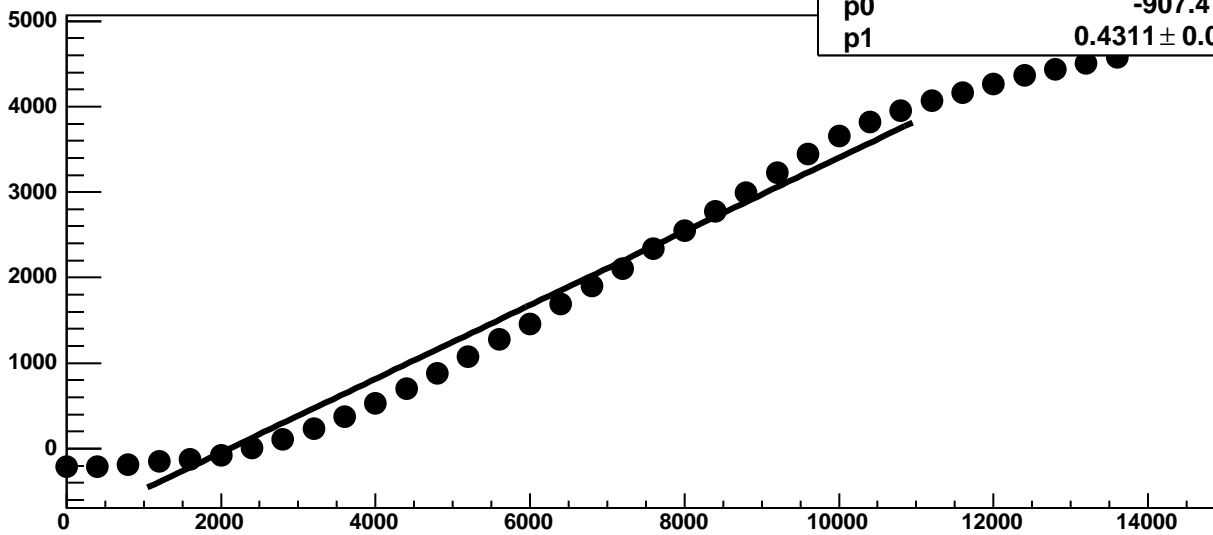


Chip 7, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

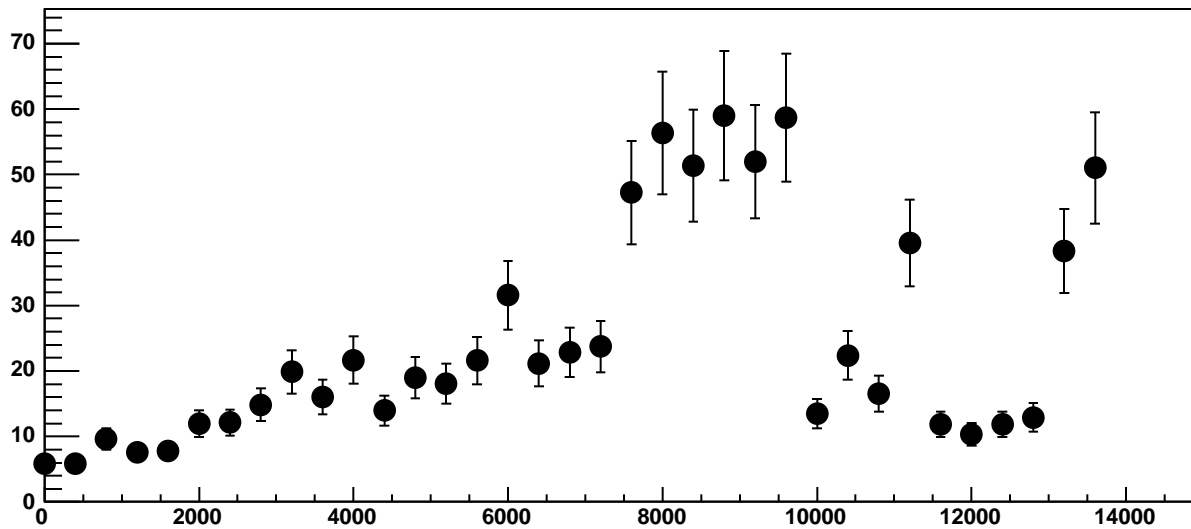


Chip 7, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

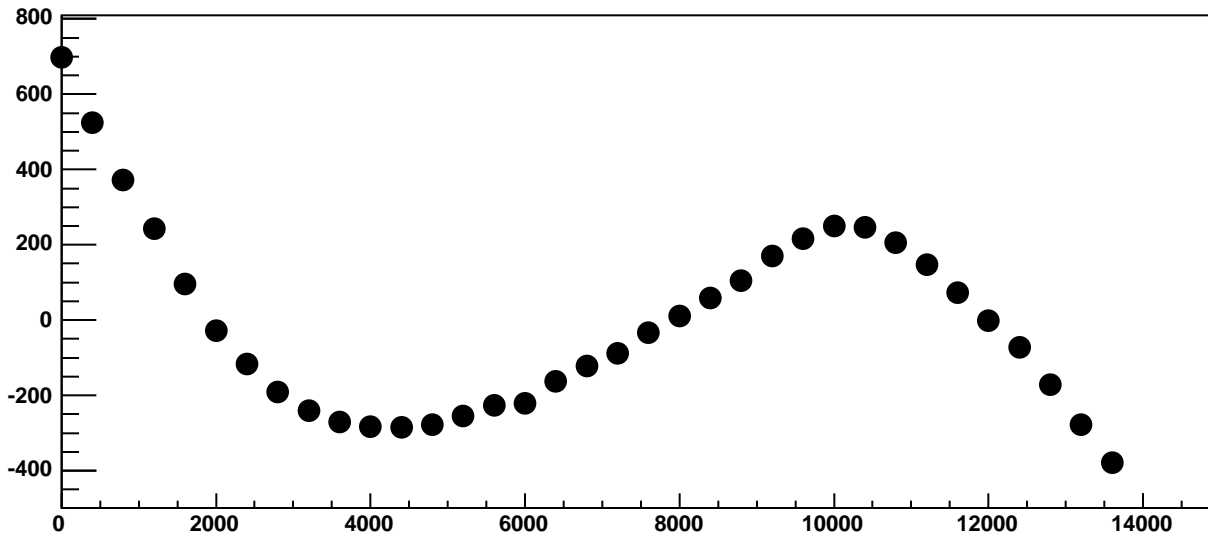
$\chi^2 / \text{ndf}$  7.183e+04 / 23  
p0 -907.4 ± 1.225  
p1 0.4311 ± 0.0002506



Chip 7, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

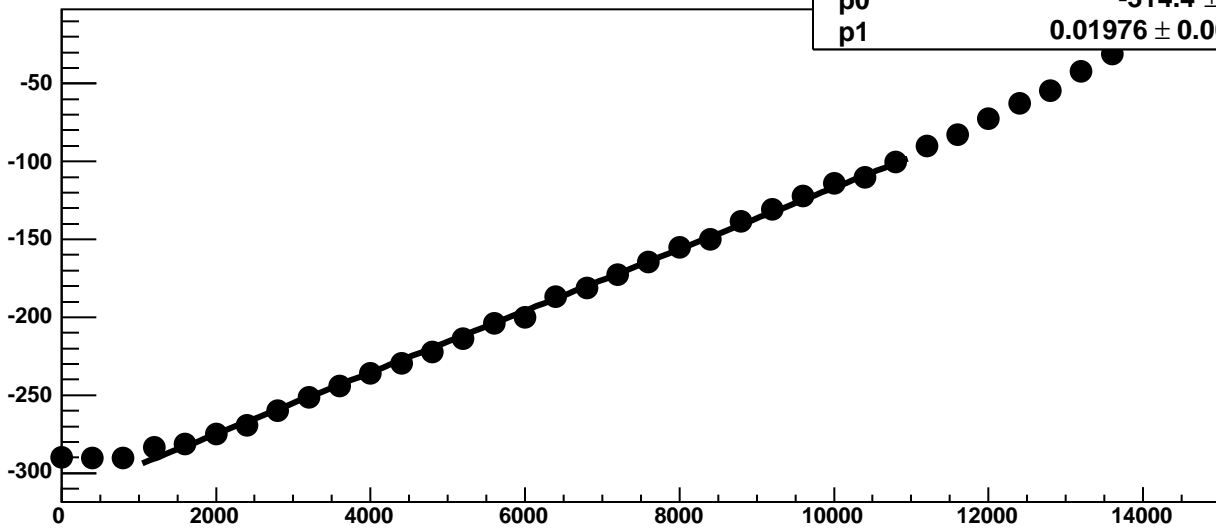


Chip 7, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



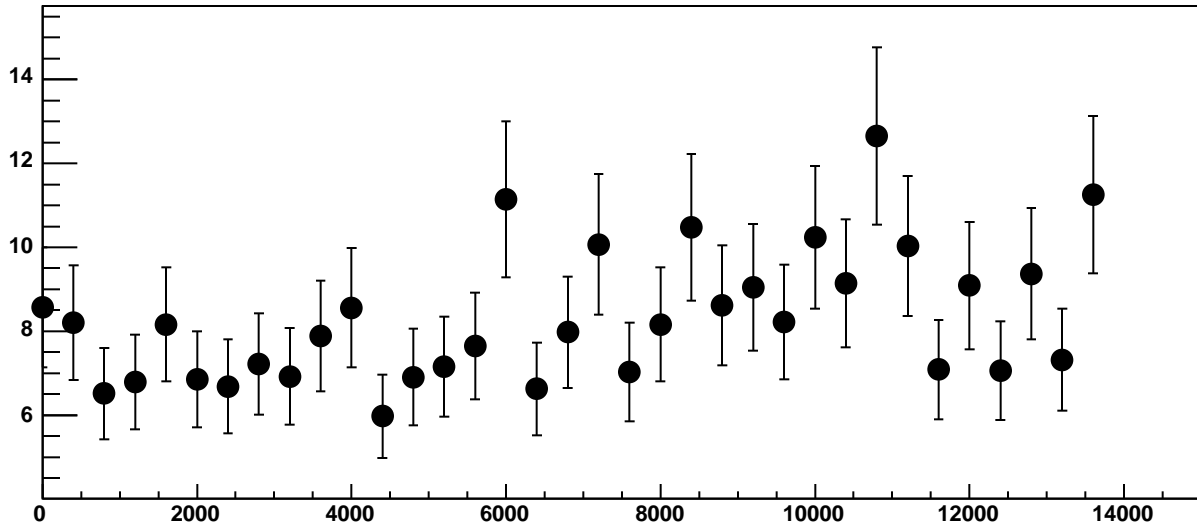


Chip 7, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

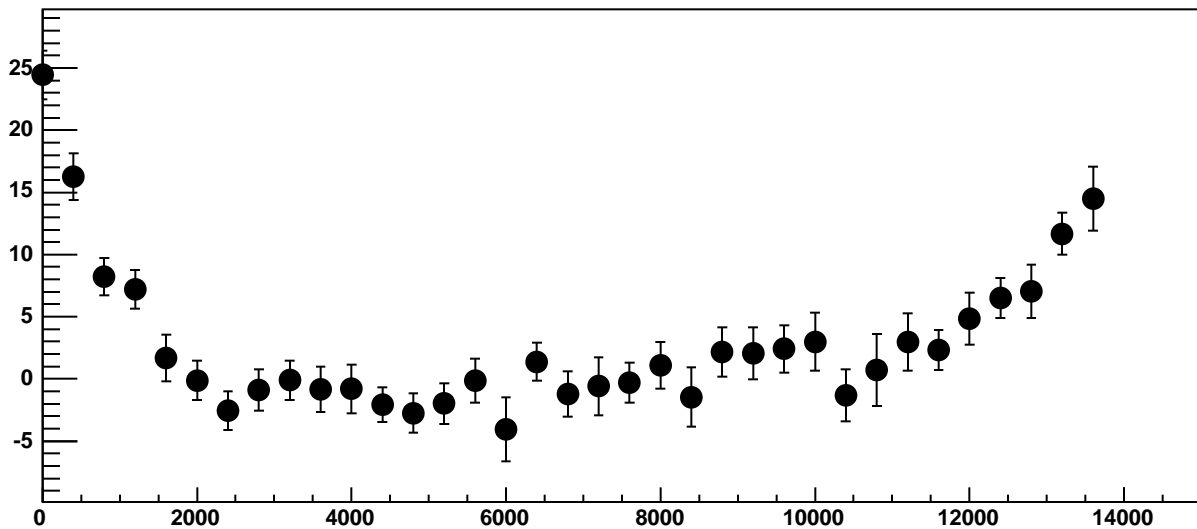


$\chi^2 / \text{ndf}$  42.87 / 23  
p0 -314.4 ± 0.7961  
p1 0.01976 ± 0.0001316

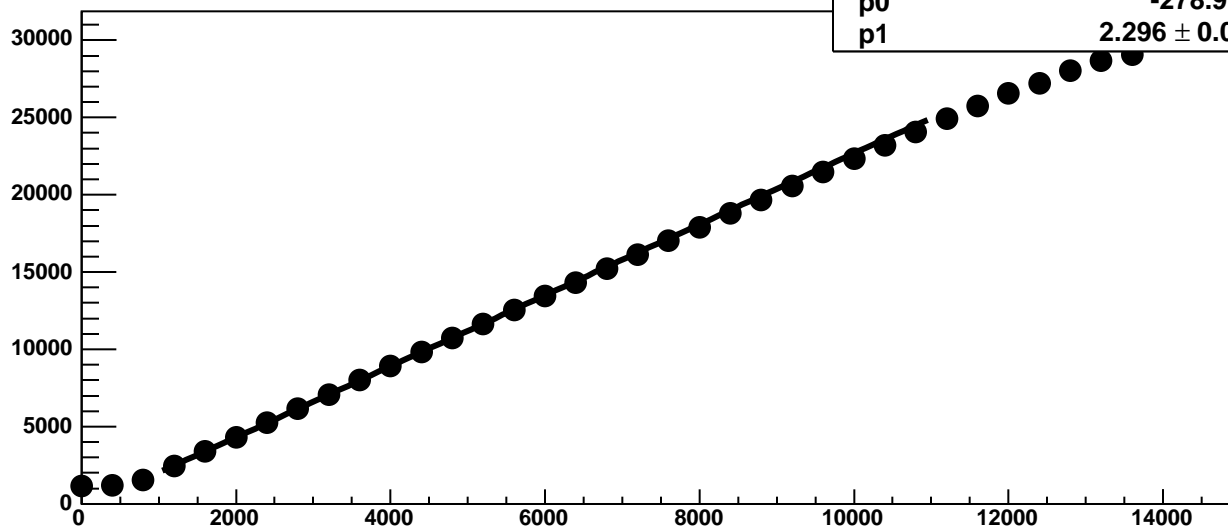
Chip 7, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



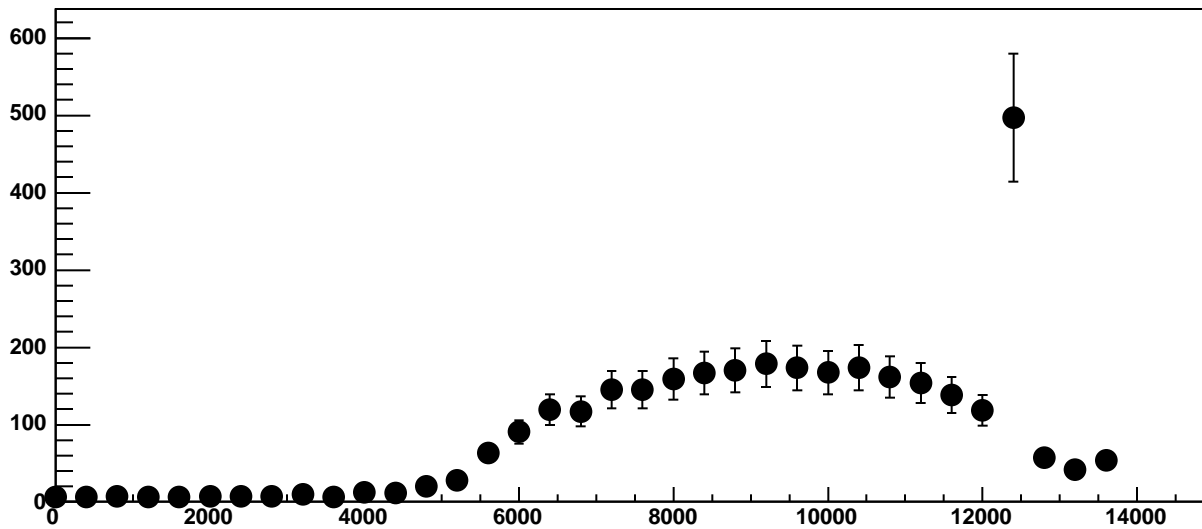
Chip 7, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



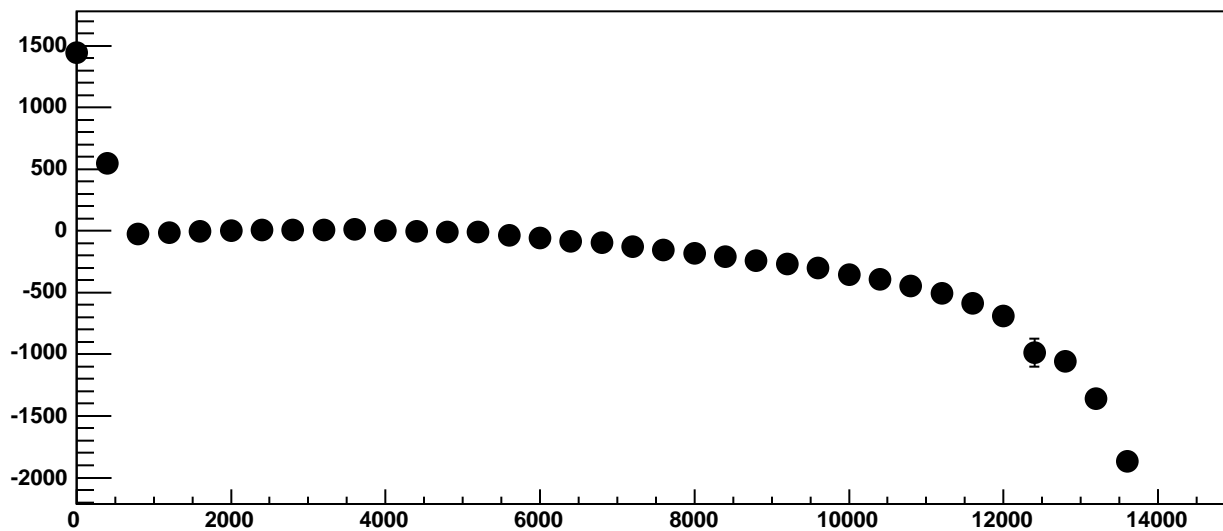
Chip 7, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



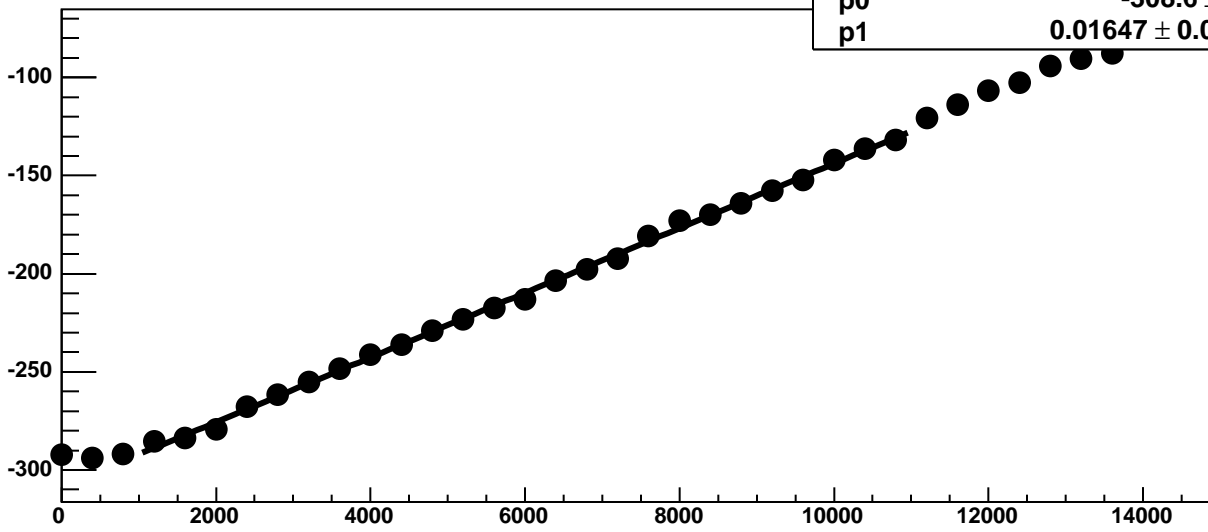
Chip 7, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

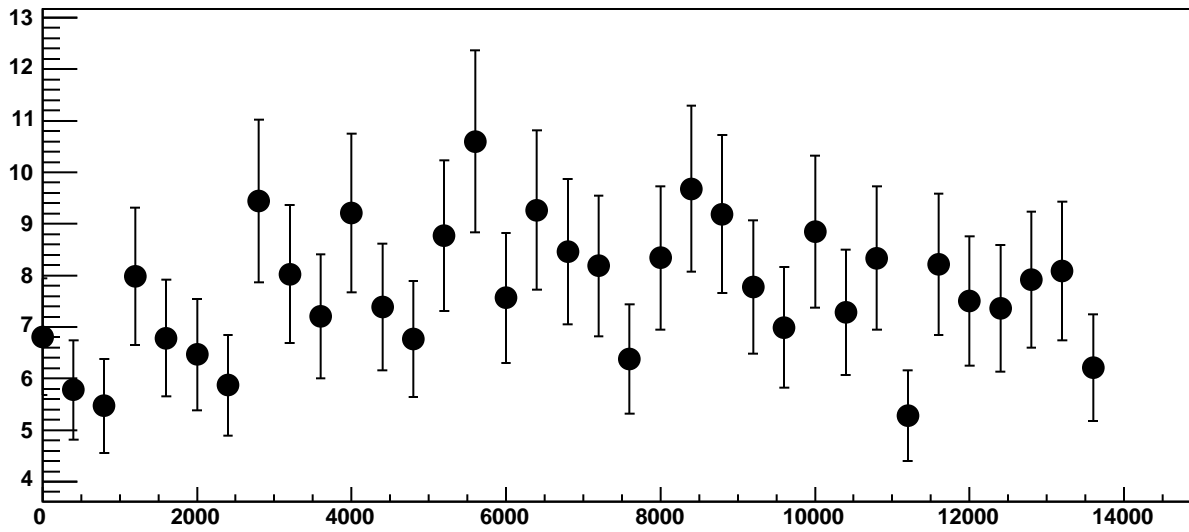


Chip 7, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

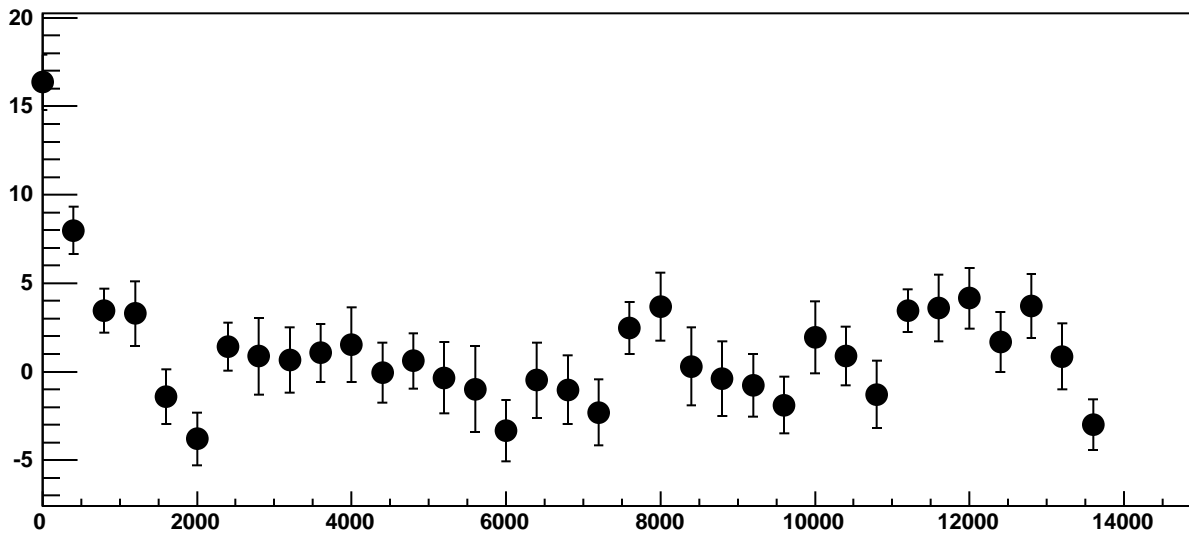


$\chi^2 / \text{ndf}$  28.56 / 23  
p0  $-308.6 \pm 0.7791$   
p1  $0.01647 \pm 0.0001202$

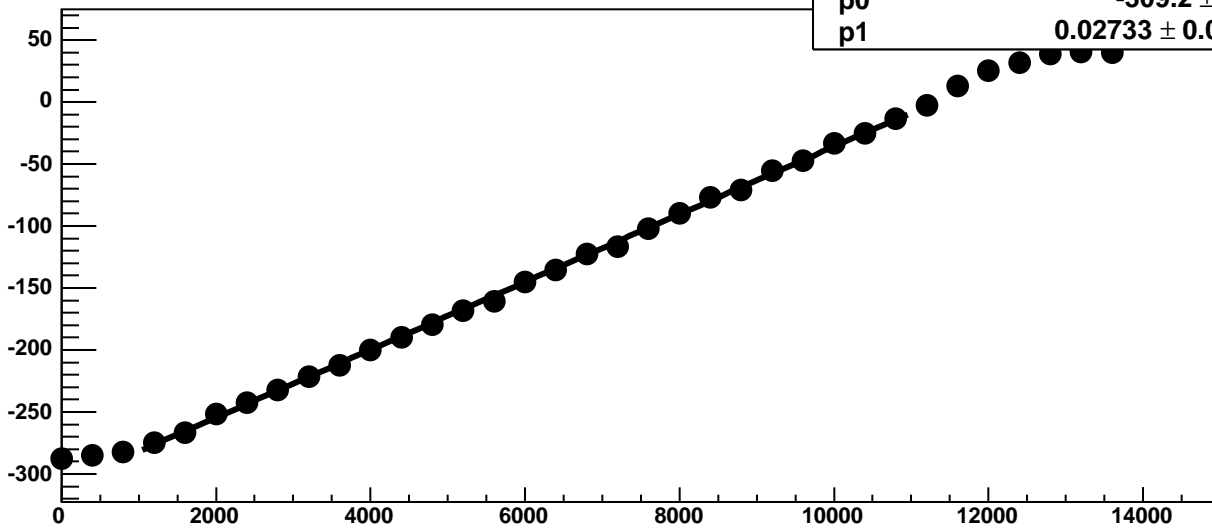
Chip 7, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



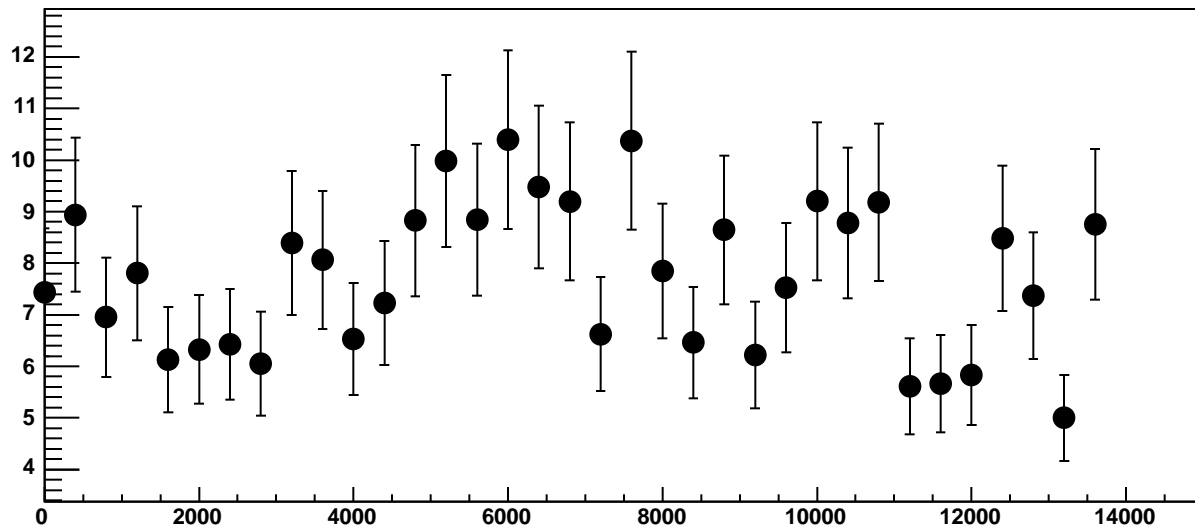
Chip 7, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



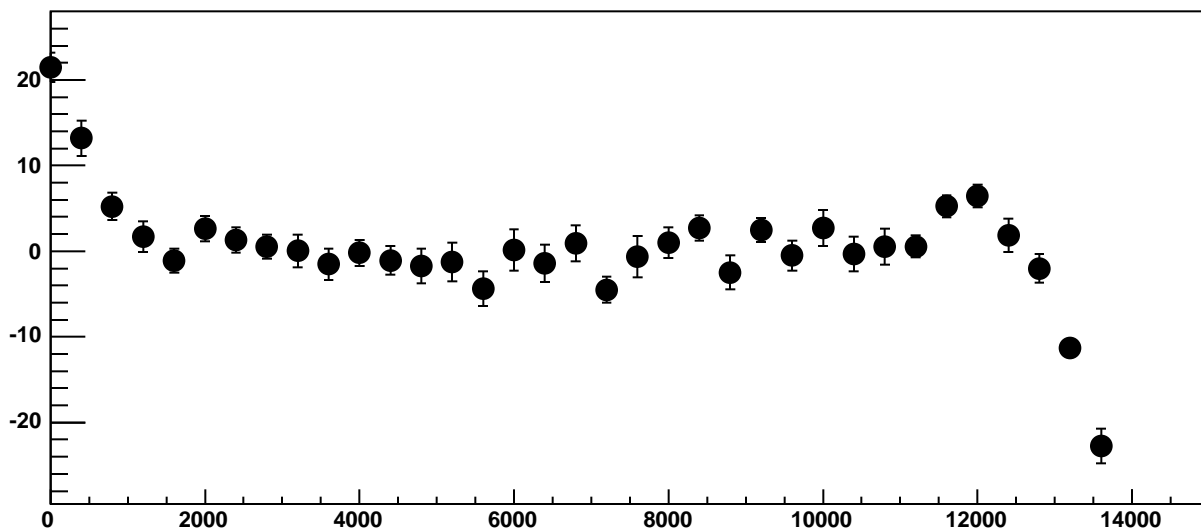
Chip 7, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



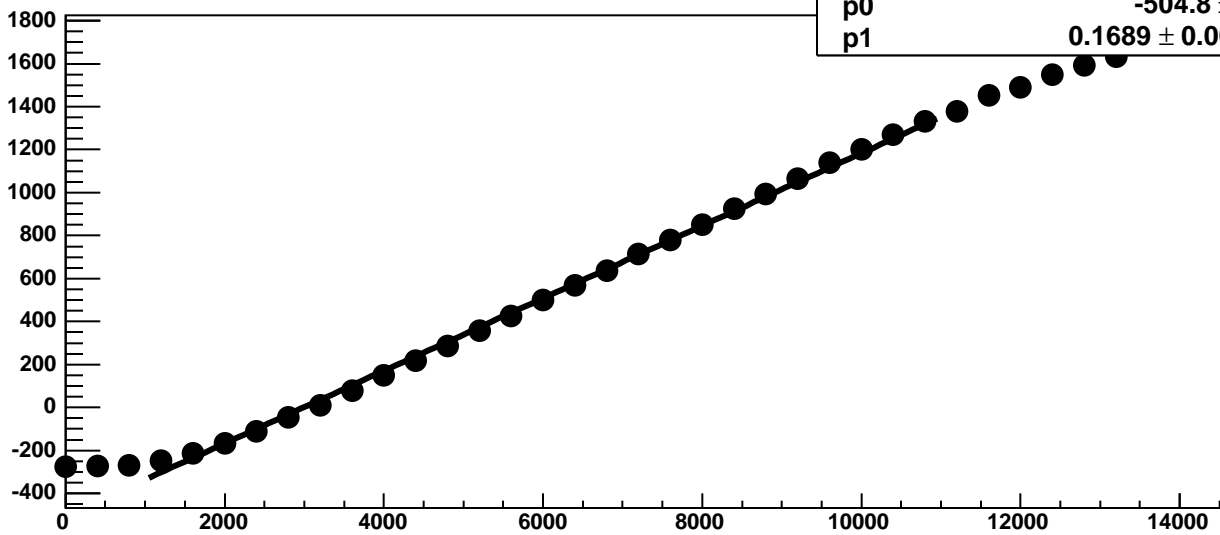
Chip 7, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



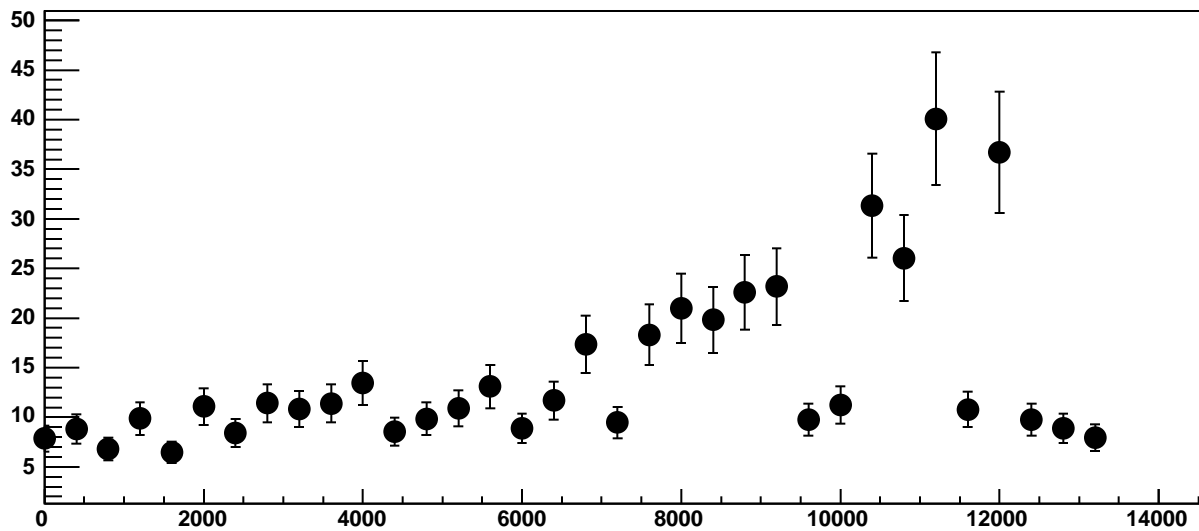
Chip 7, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



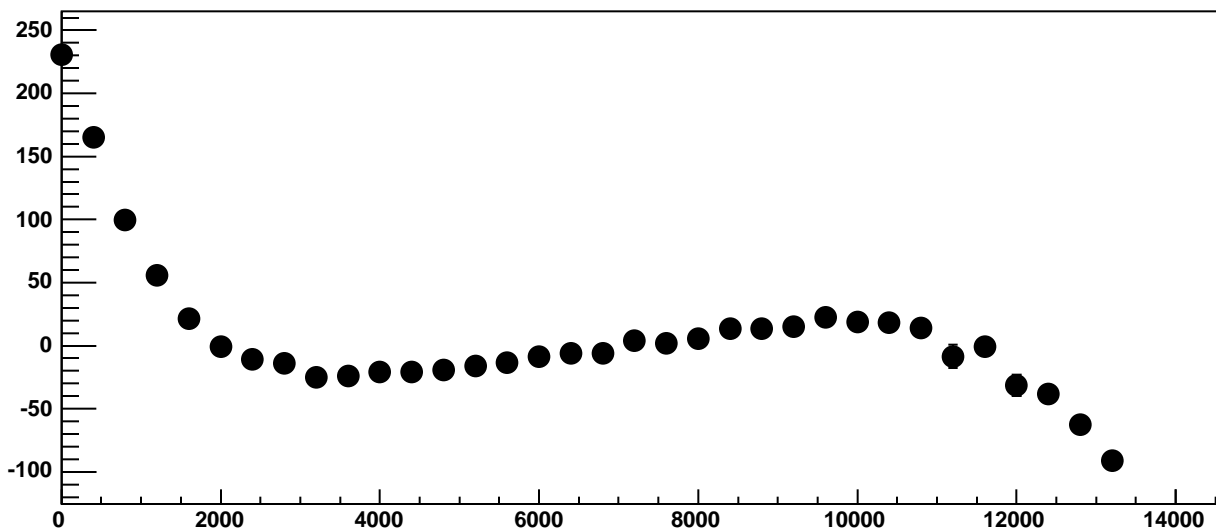
Chip 7, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



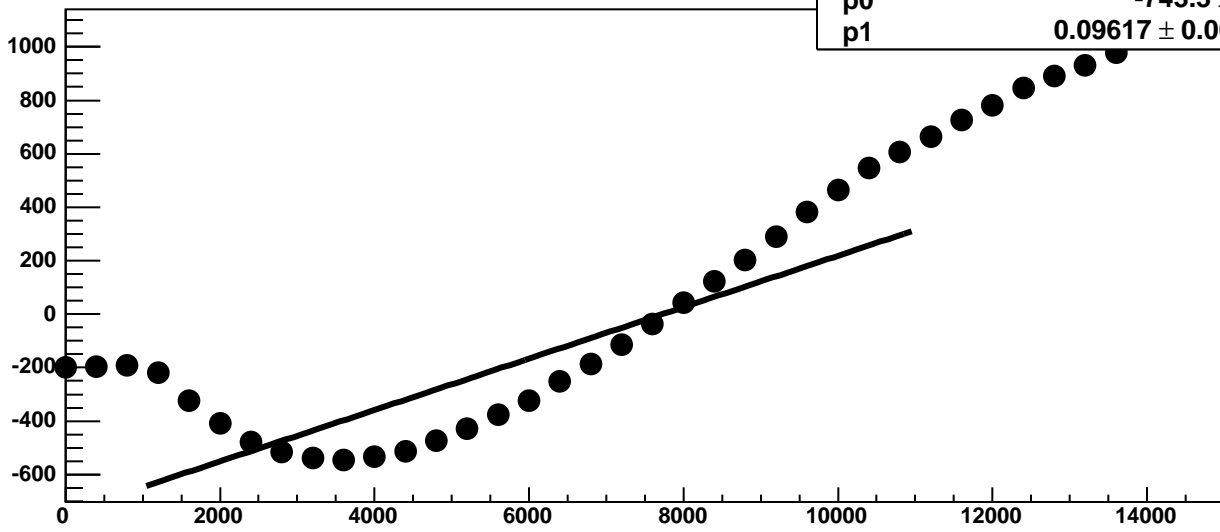
Chip 7, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



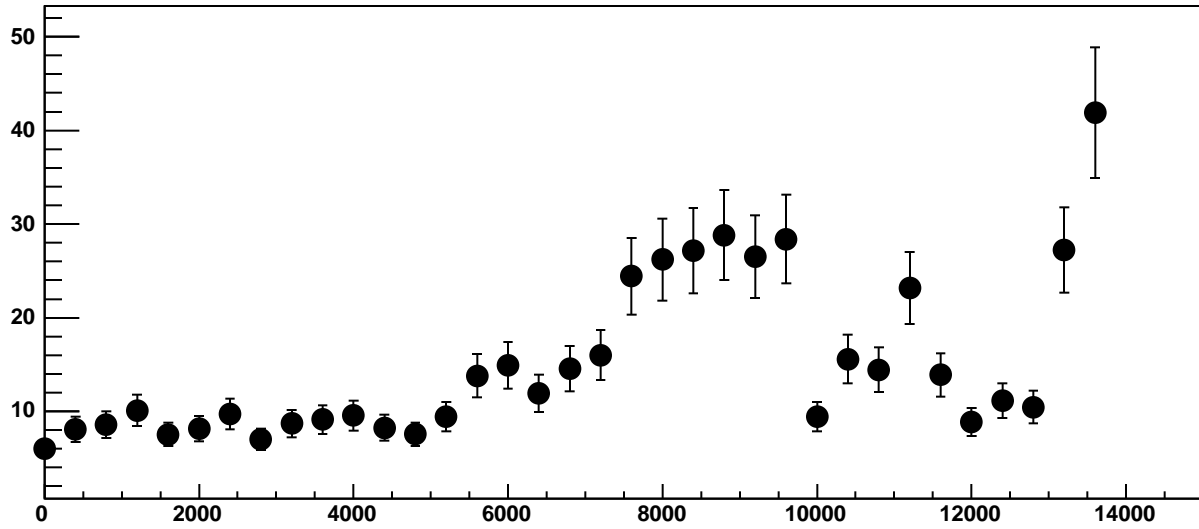
Chip 7, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC



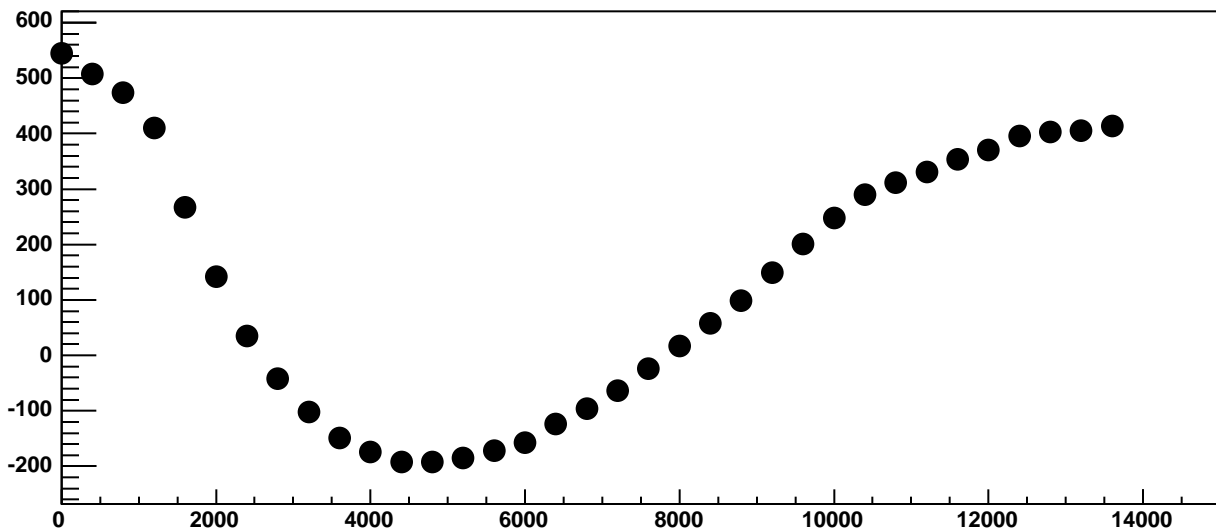
Chip 7, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



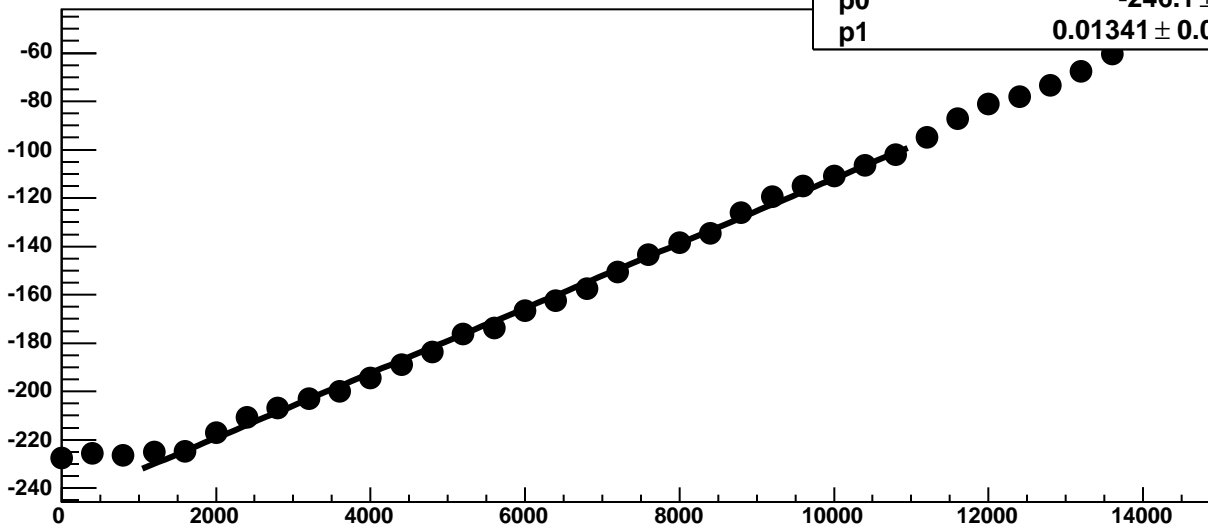
Chip 7, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

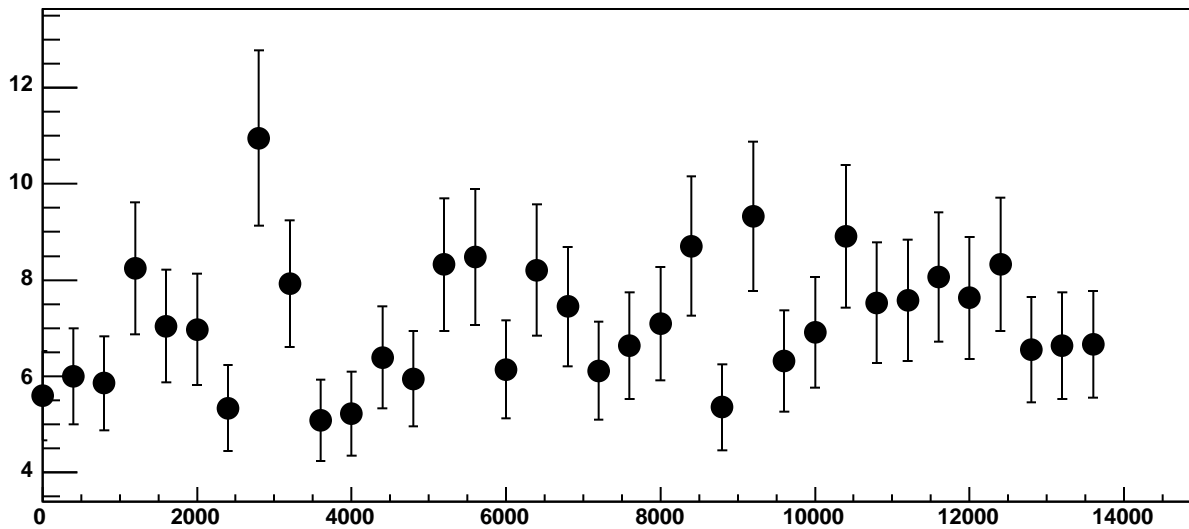


Chip 7, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

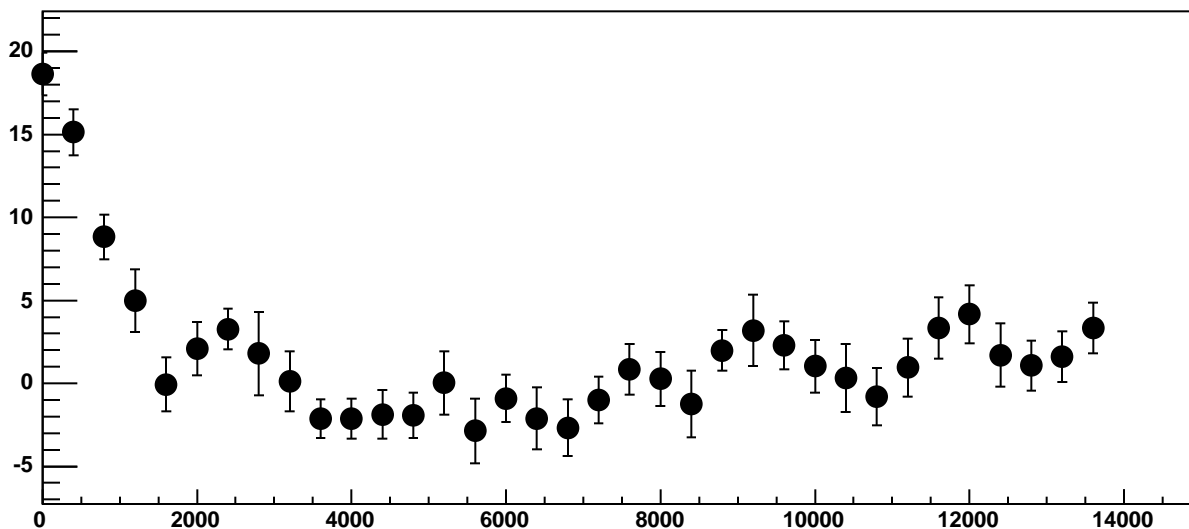


$\chi^2 / \text{ndf}$  41.97 / 23  
p0 -246.1 ± 0.7273  
p1 0.01341 ± 0.0001124

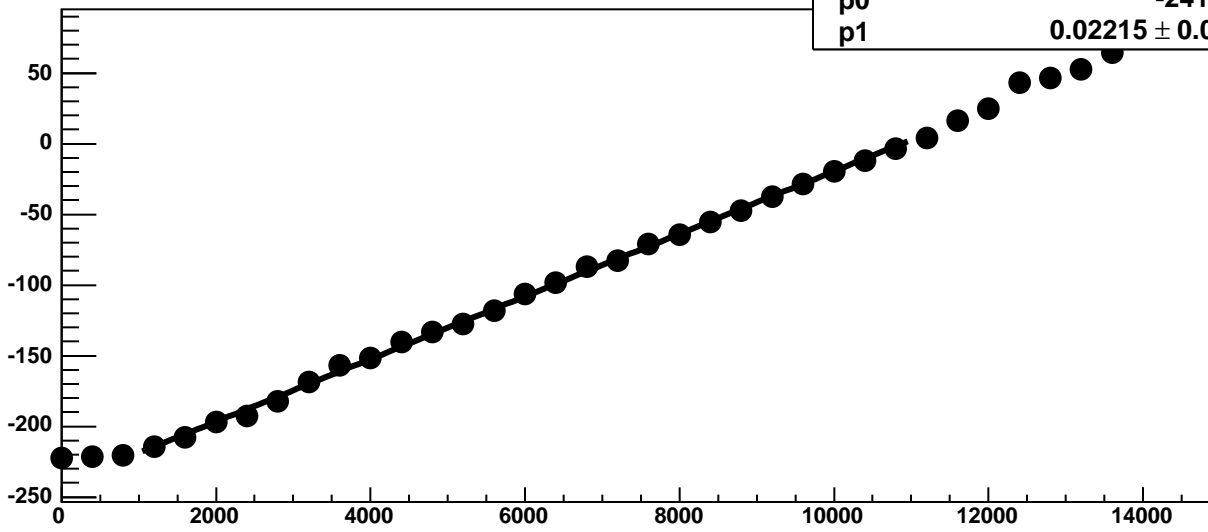
Chip 7, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



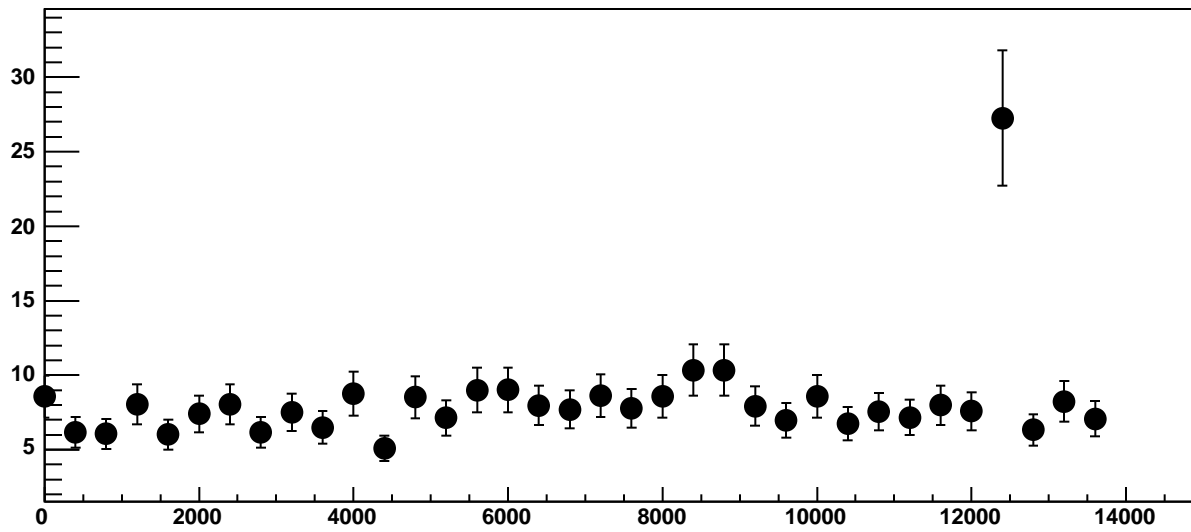
Chip 7, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



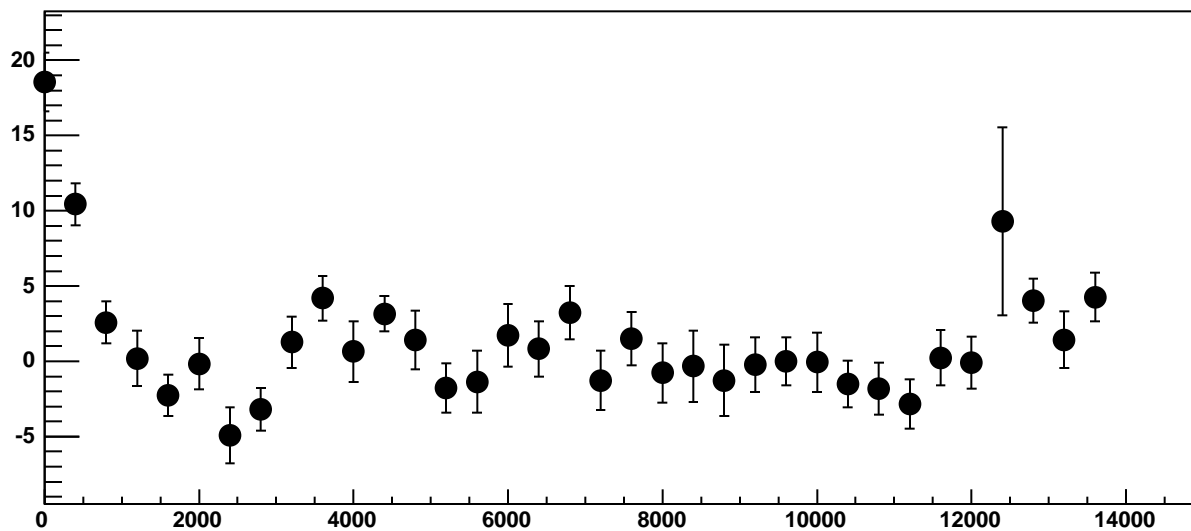
Chip 7, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



Chip 7, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

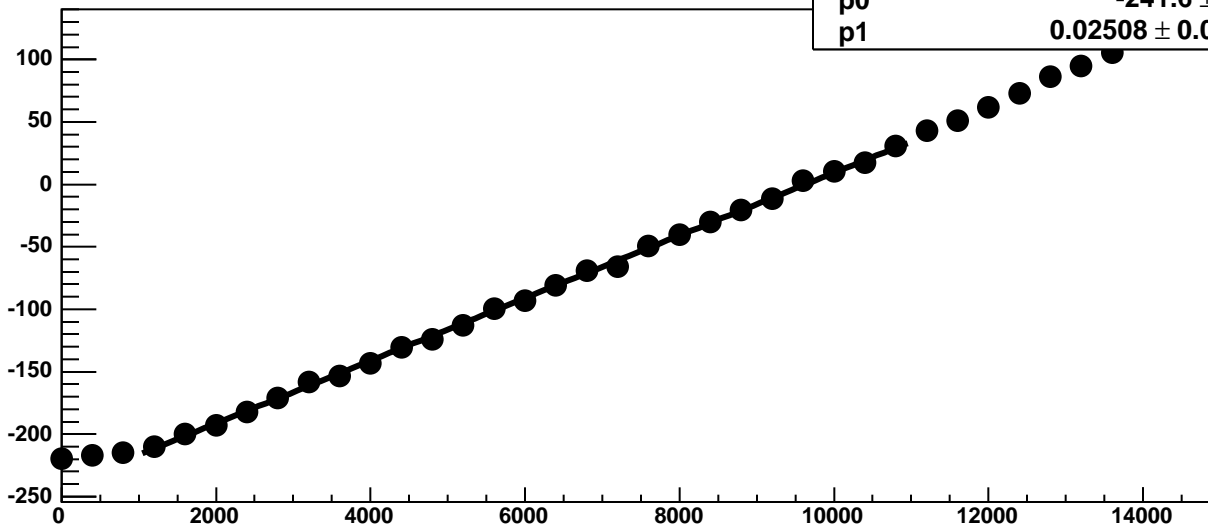


Chip 7, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

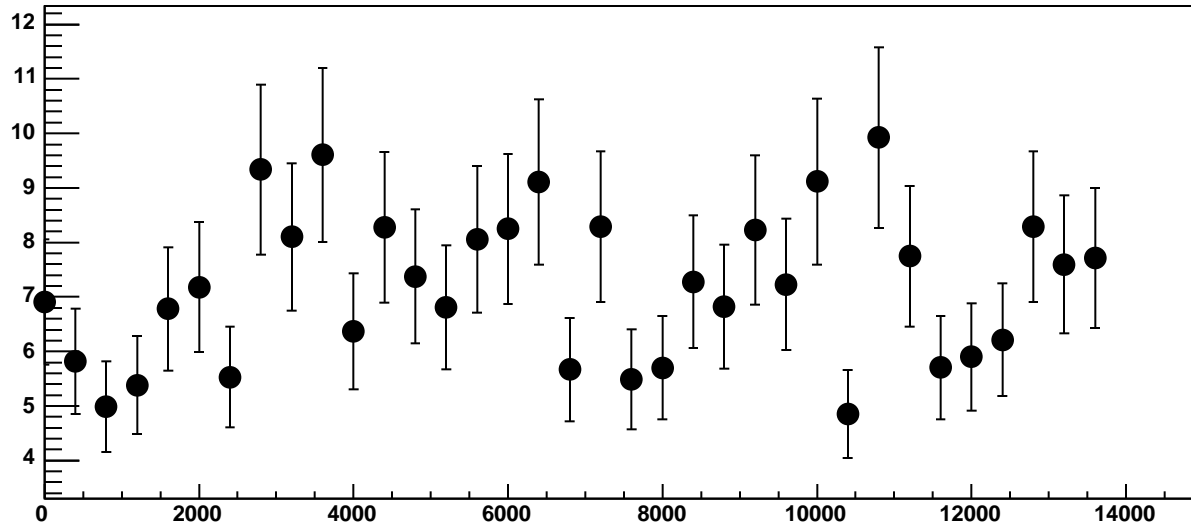




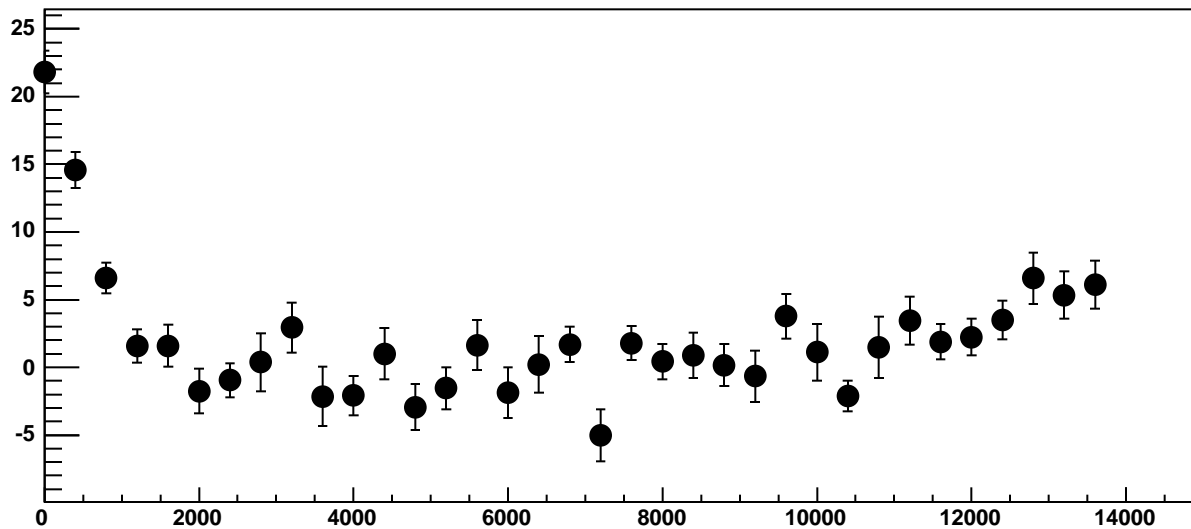
Chip 7, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



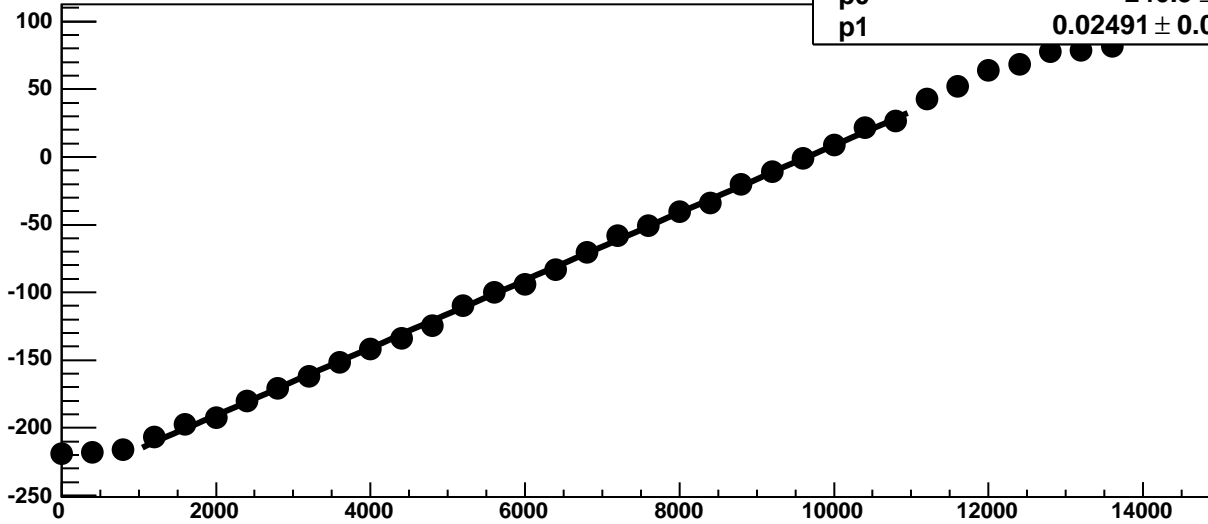
Chip 7, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



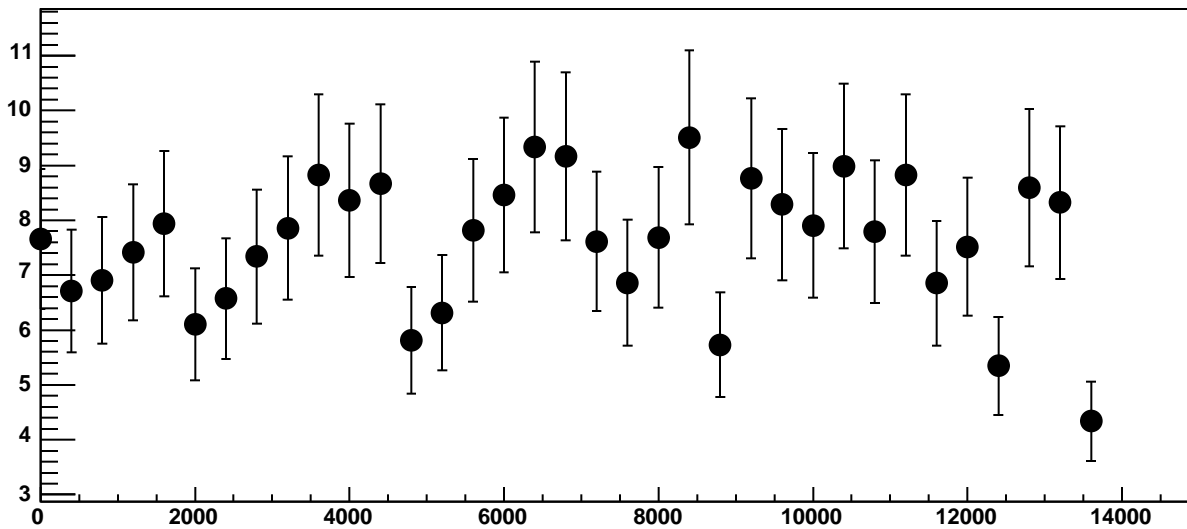
Chip 7, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



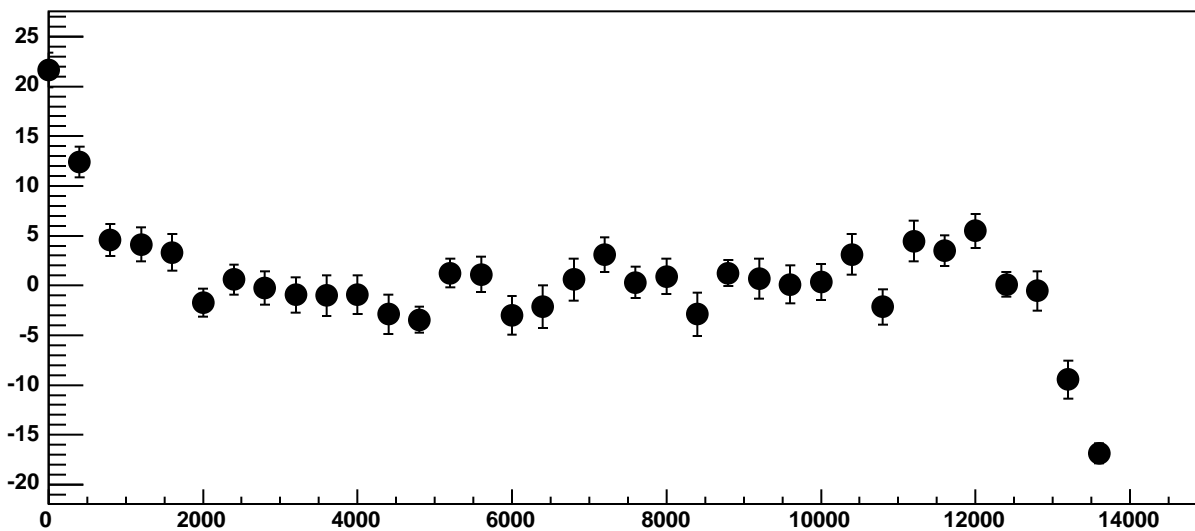
Chip 7, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



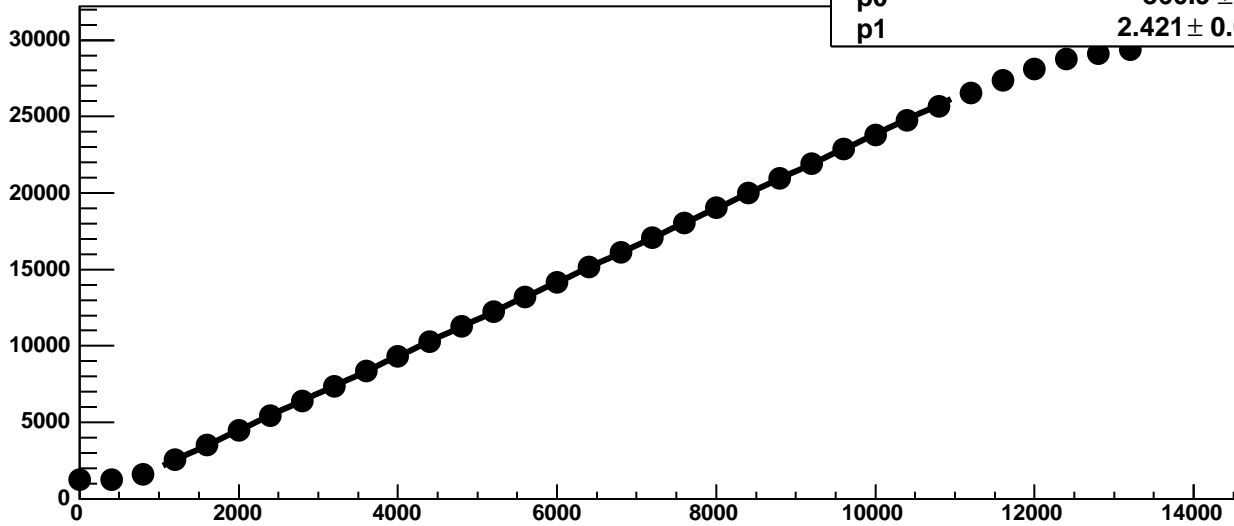
Chip 7, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



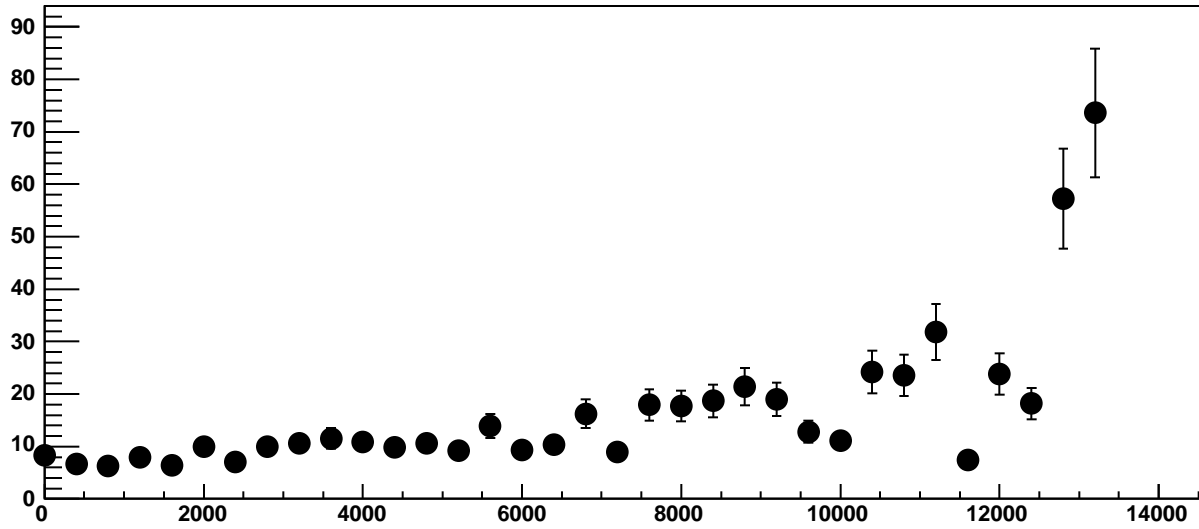
Chip 7, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



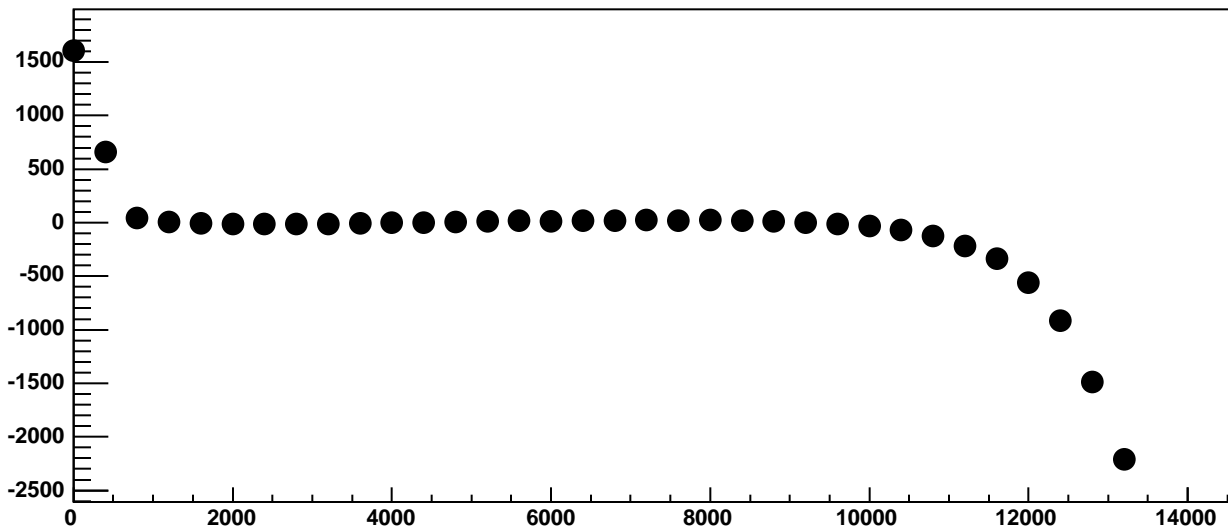
Chip 7, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 7, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC

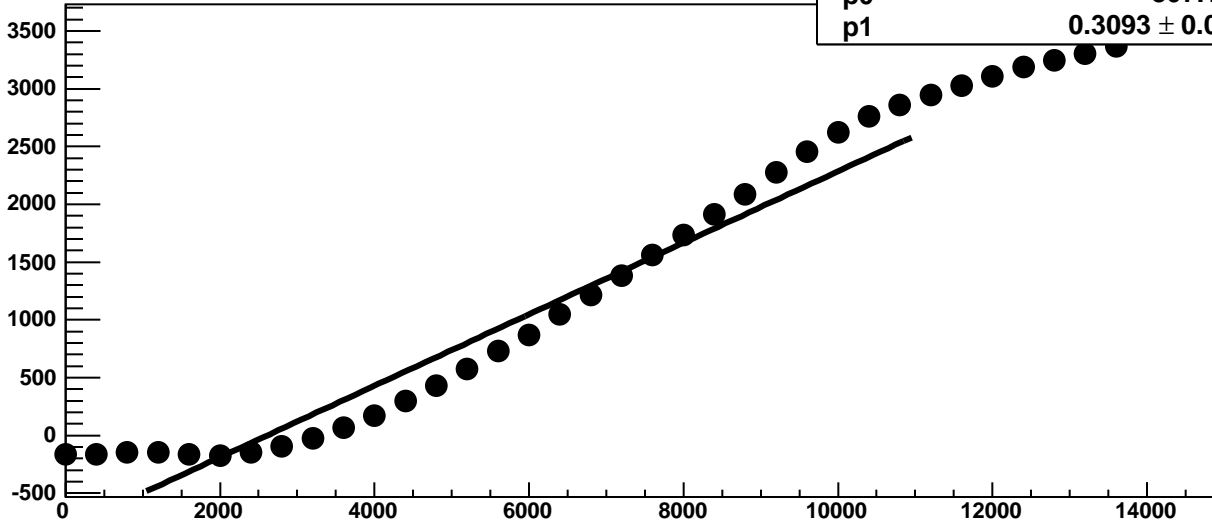


Chip 7, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

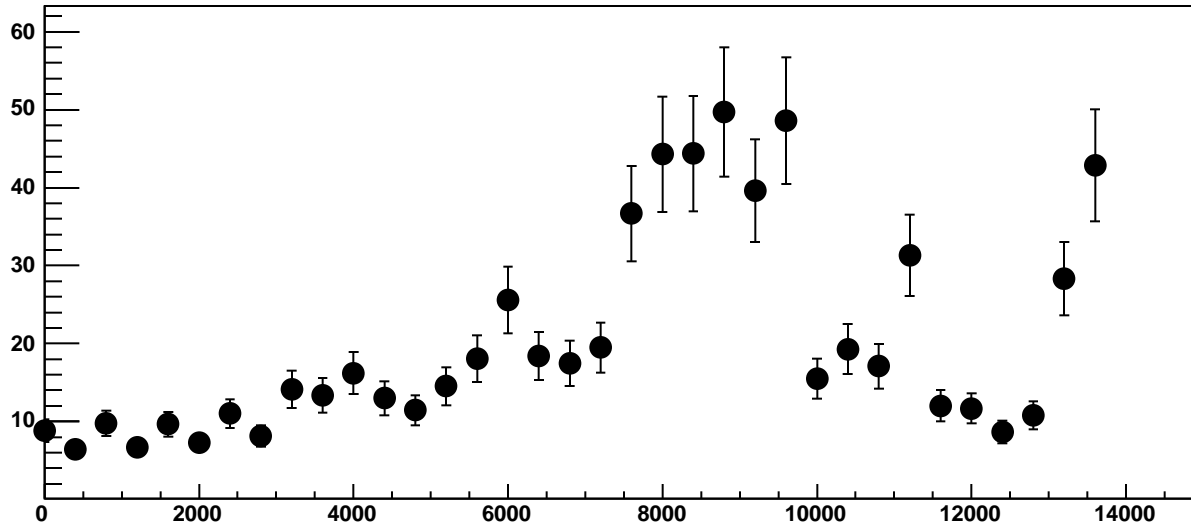


Chip 7, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

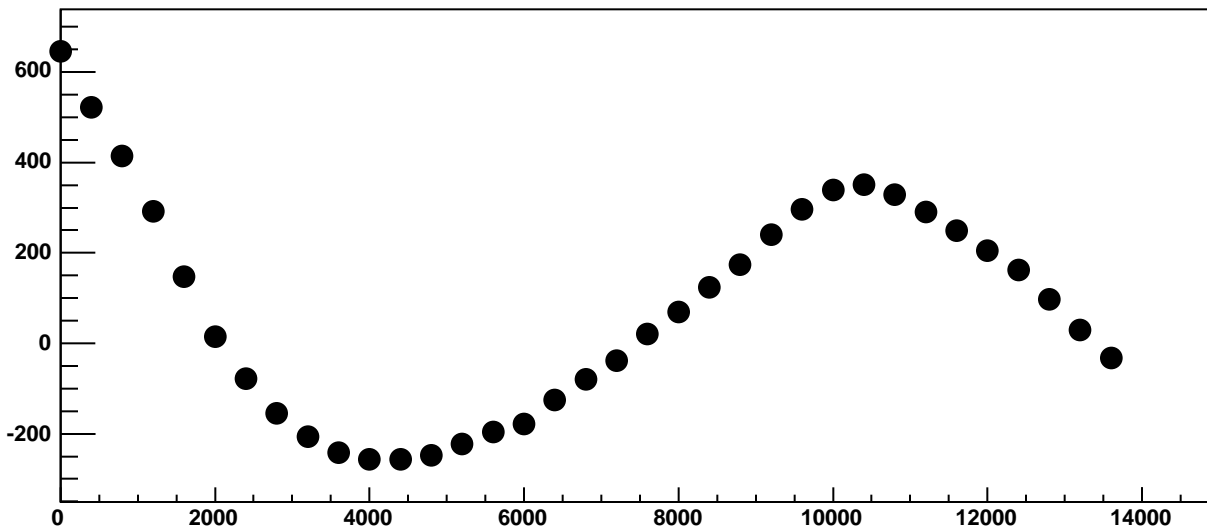
$\chi^2 / \text{ndf}$  1.136e+05 / 23  
p0 -807.1 ± 1.081  
p1 0.3093 ± 0.0002374



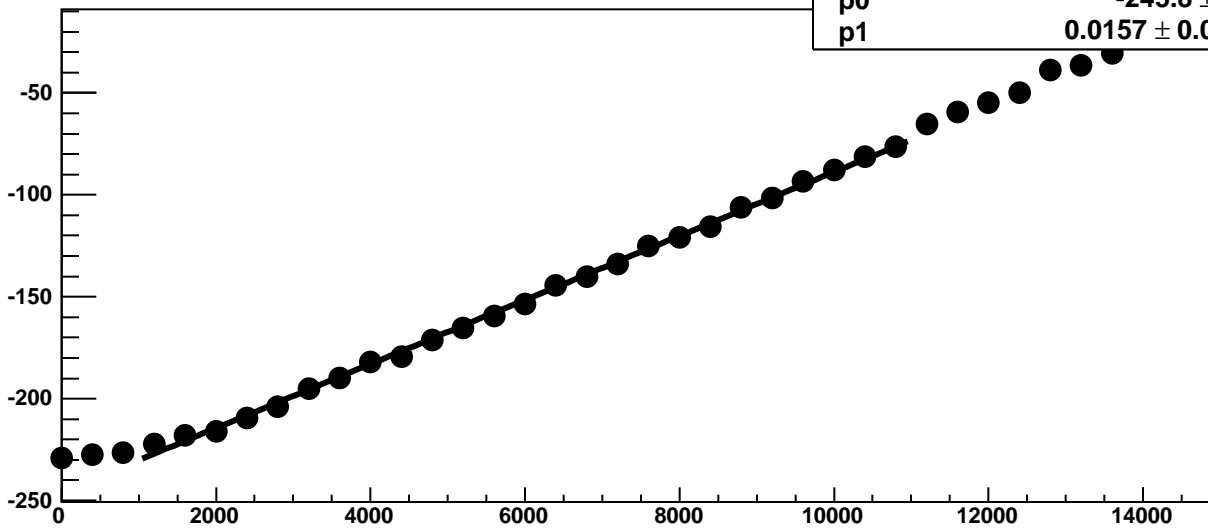
Chip 7, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

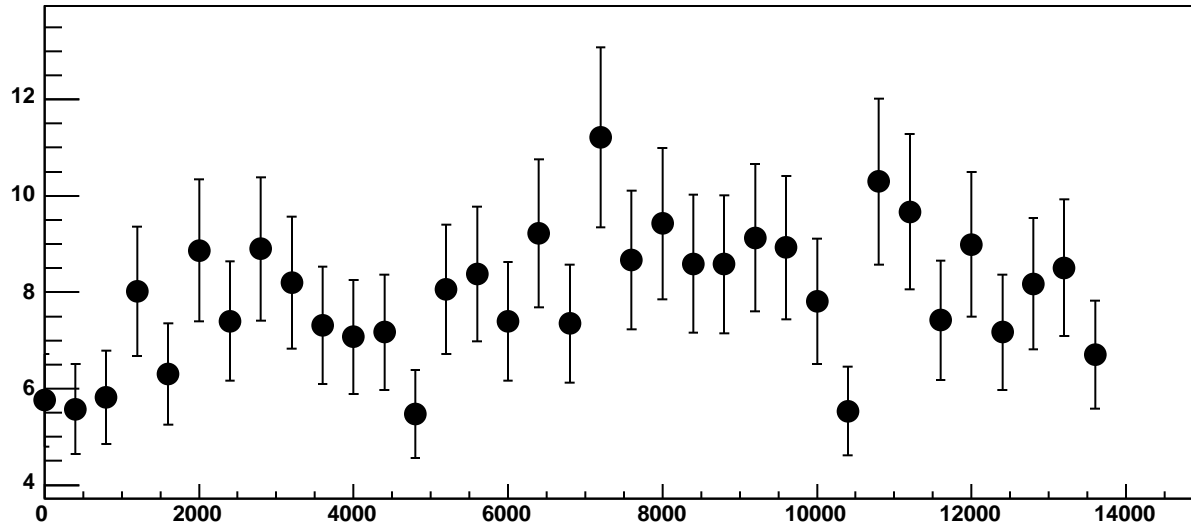


Chip 7, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

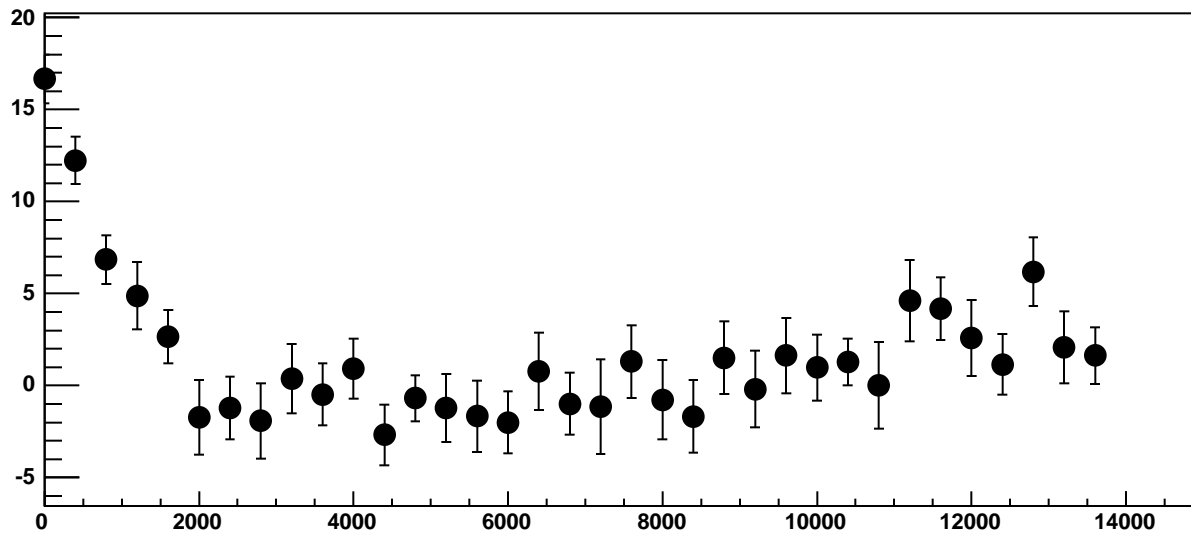


$\chi^2 / \text{ndf}$  23.04 / 23  
p0  $-245.8 \pm 0.7976$   
p1  $0.0157 \pm 0.0001226$

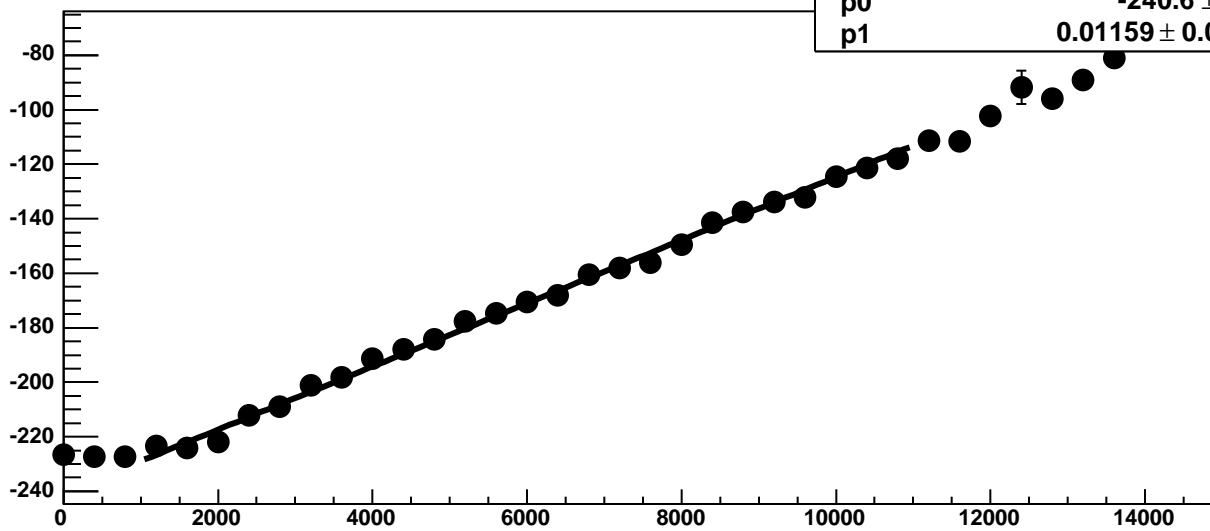
Chip 7, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

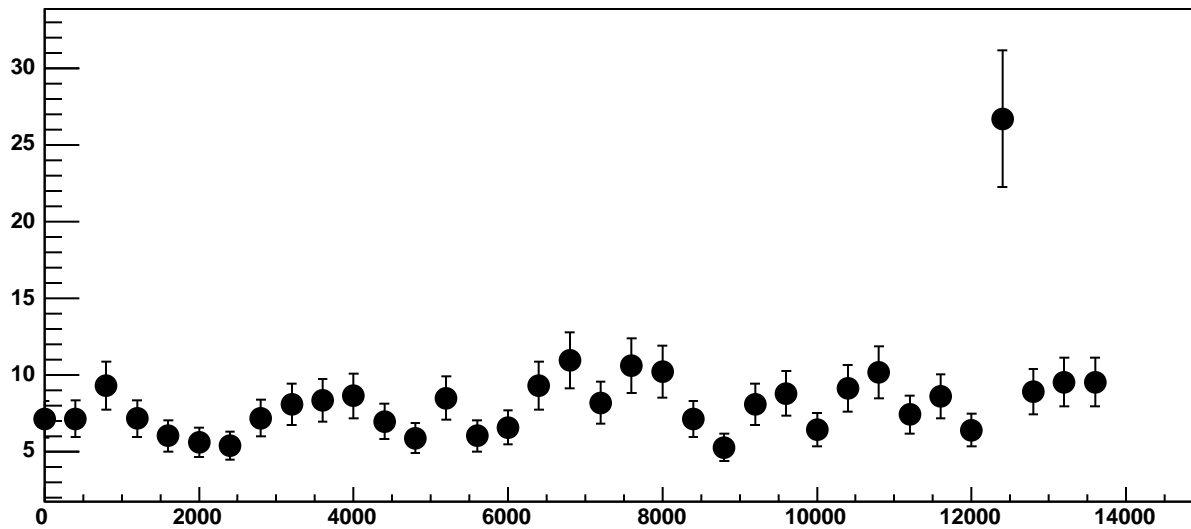


Chip 7, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

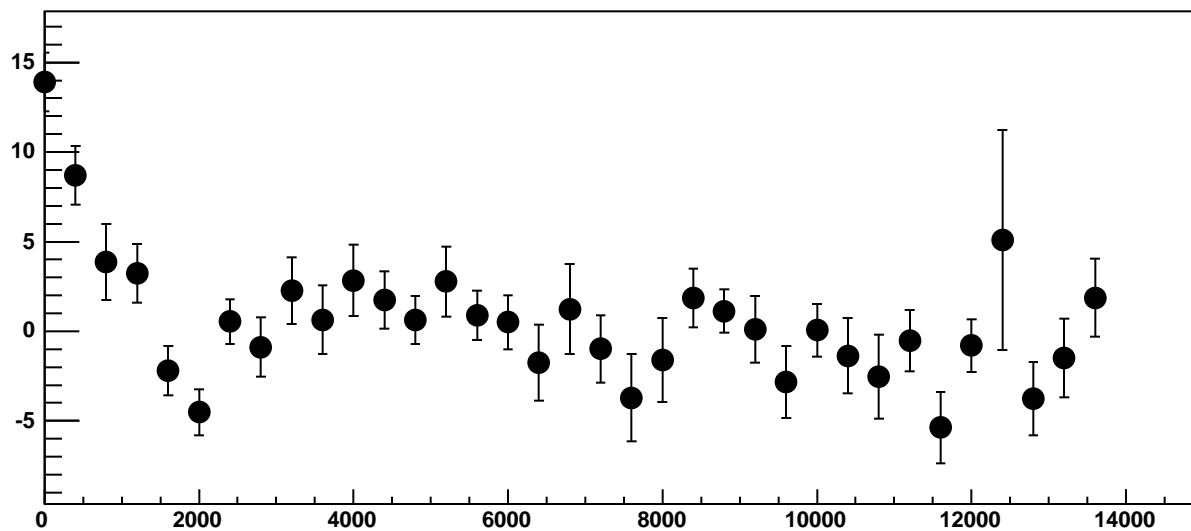


$\chi^2 / \text{ndf}$  36.41 / 23  
p0  $-240.6 \pm 0.7149$   
p1  $0.01159 \pm 0.0001142$

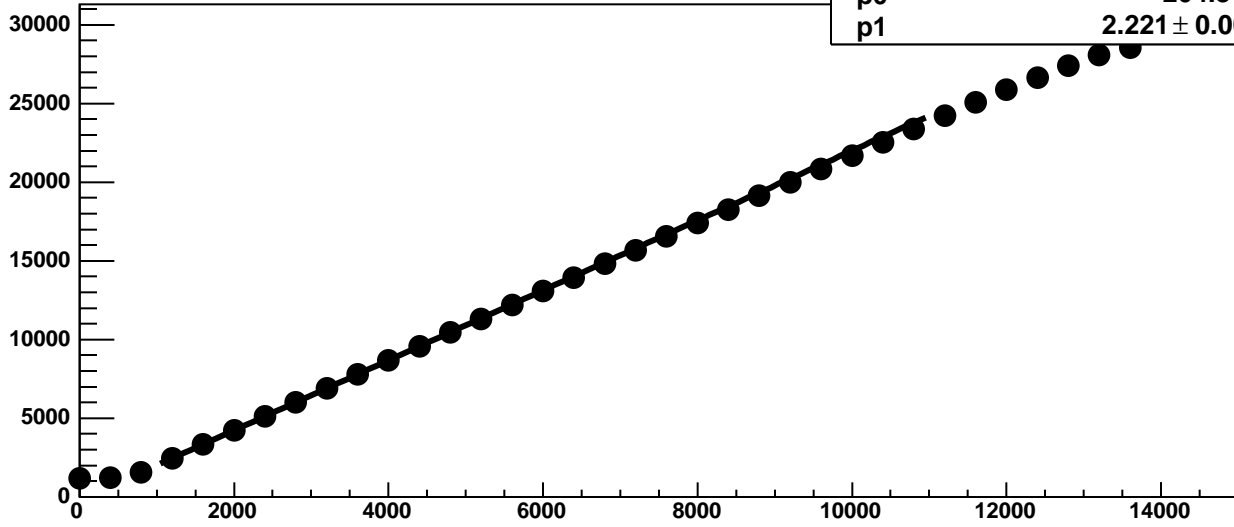
Chip 7, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



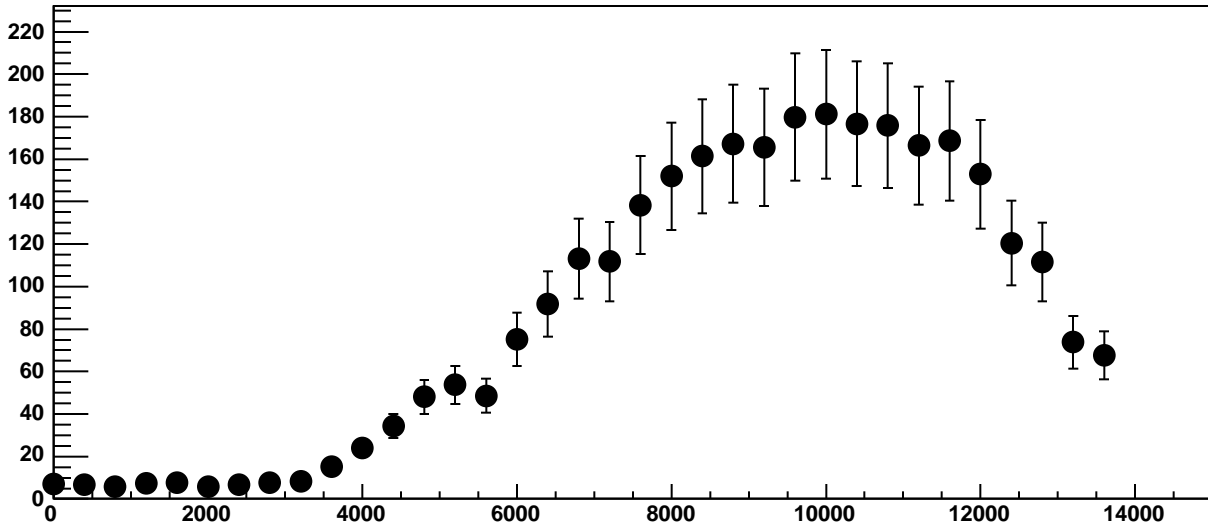
Chip 7, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



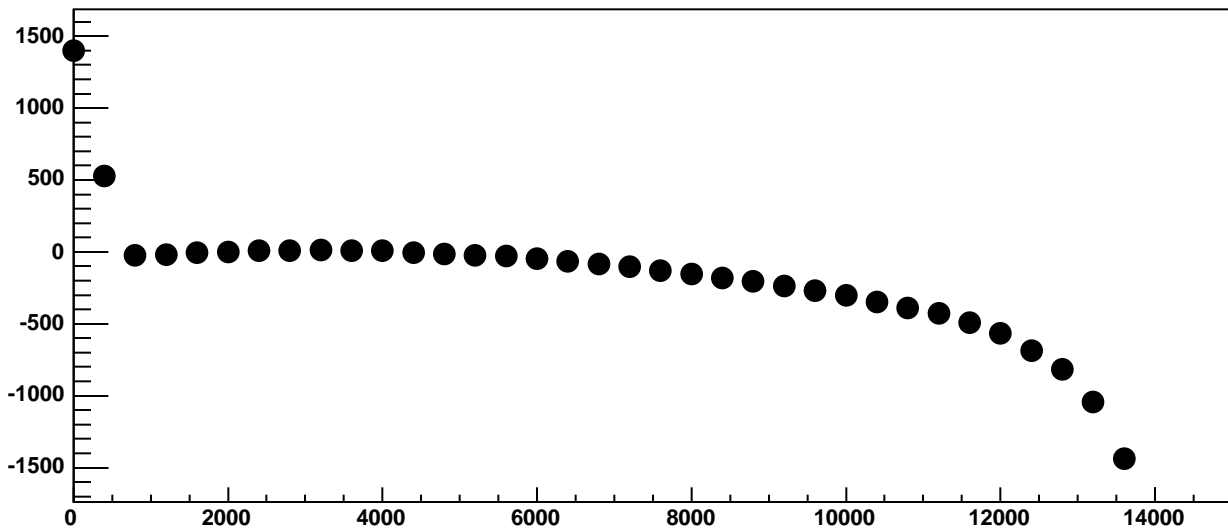
Chip 7, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



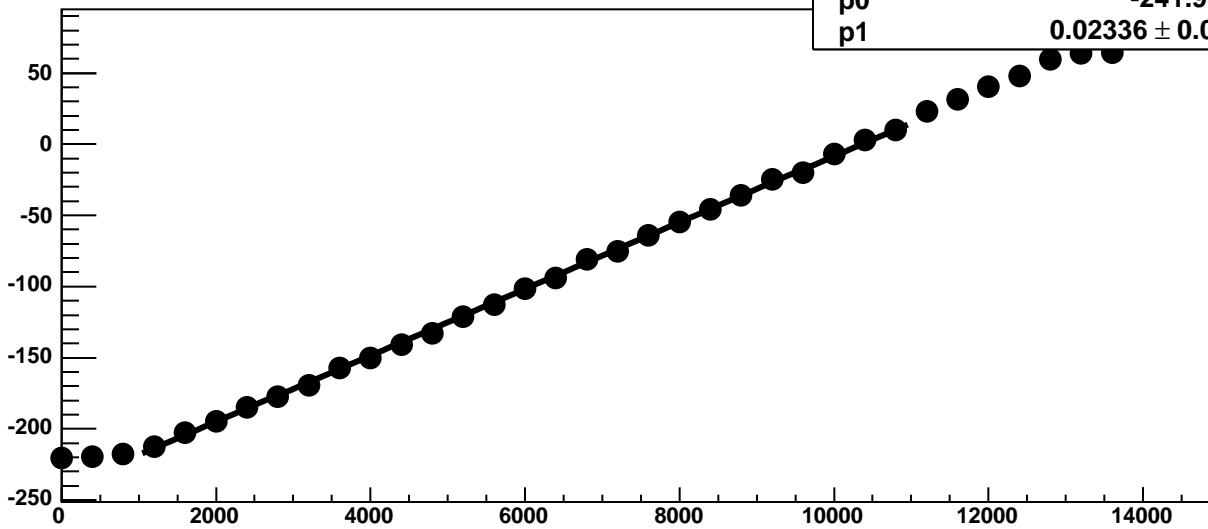
Chip 7, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

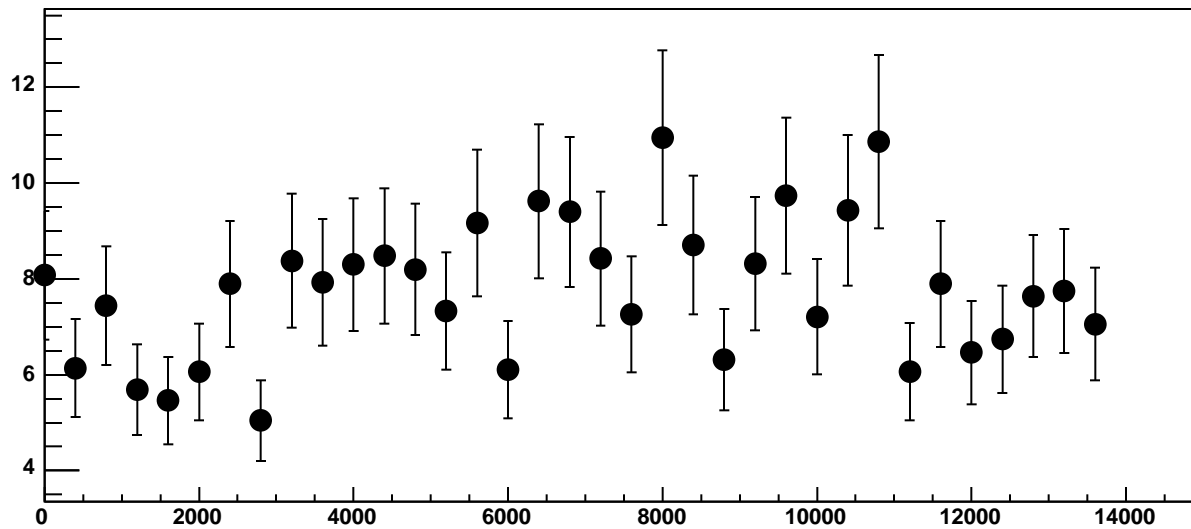


Chip 7, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

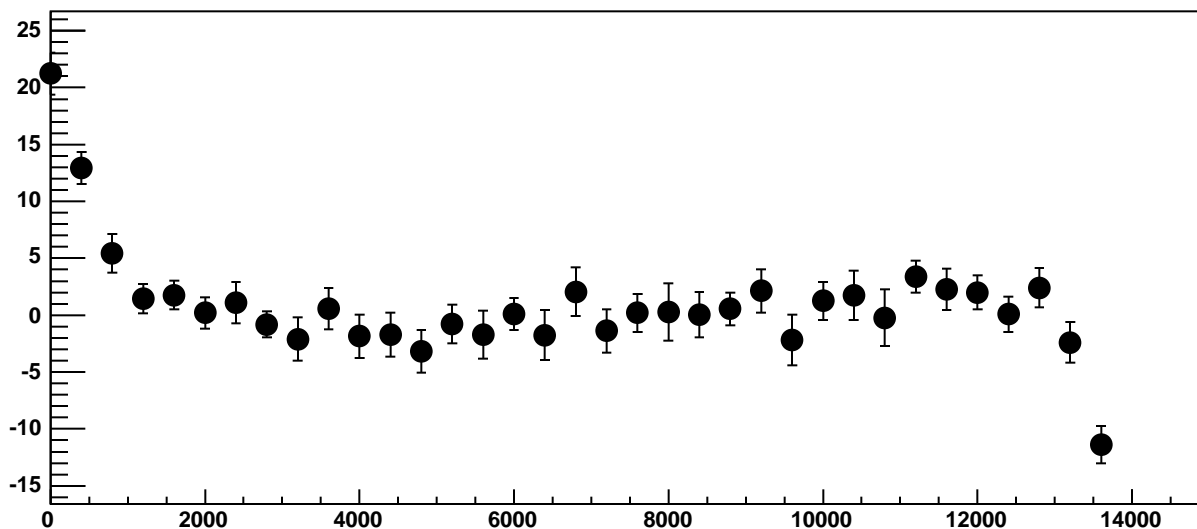


$\chi^2 / \text{ndf}$  16.5 / 23  
p0  $-241.9 \pm 0.703$   
p1  $0.02336 \pm 0.0001163$

Chip 7, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

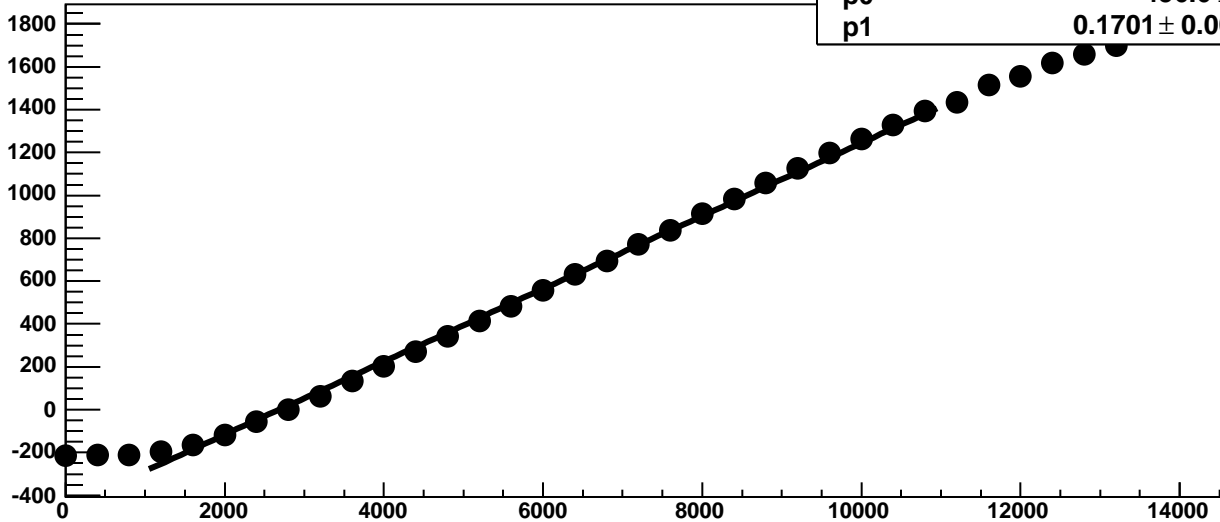


Chip 7, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

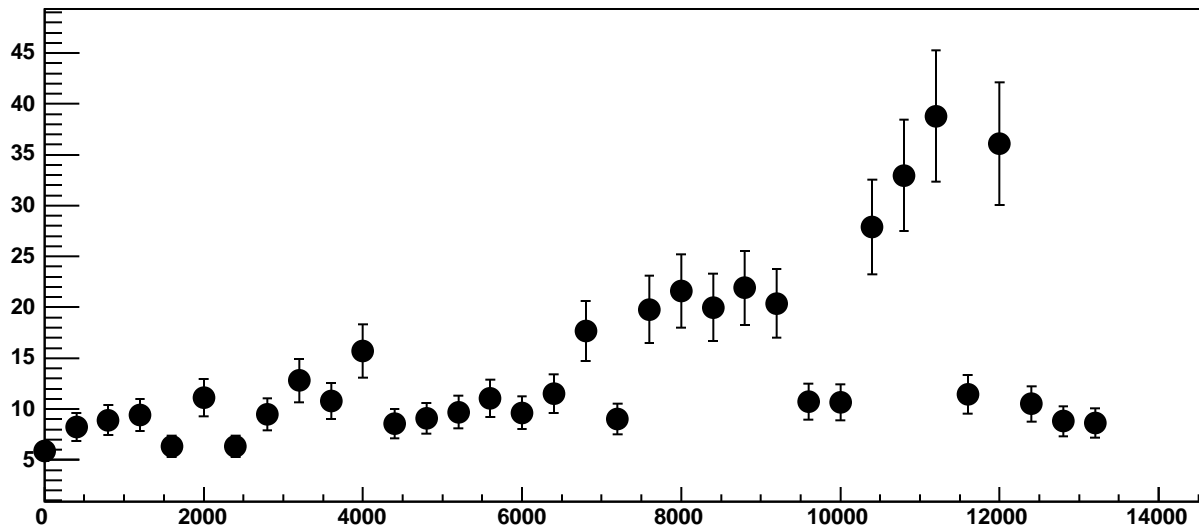




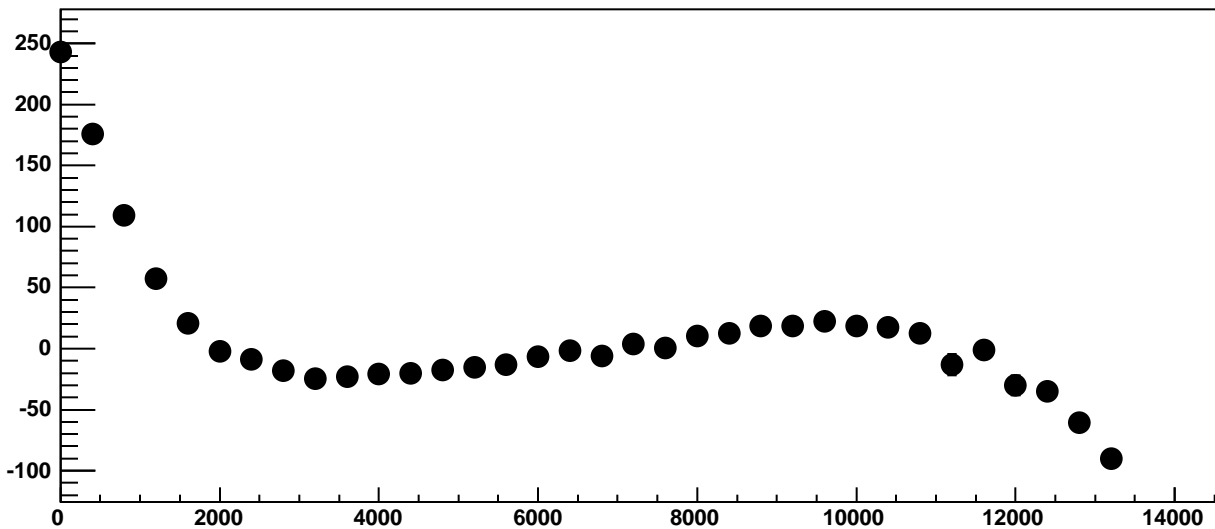
Chip 7, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



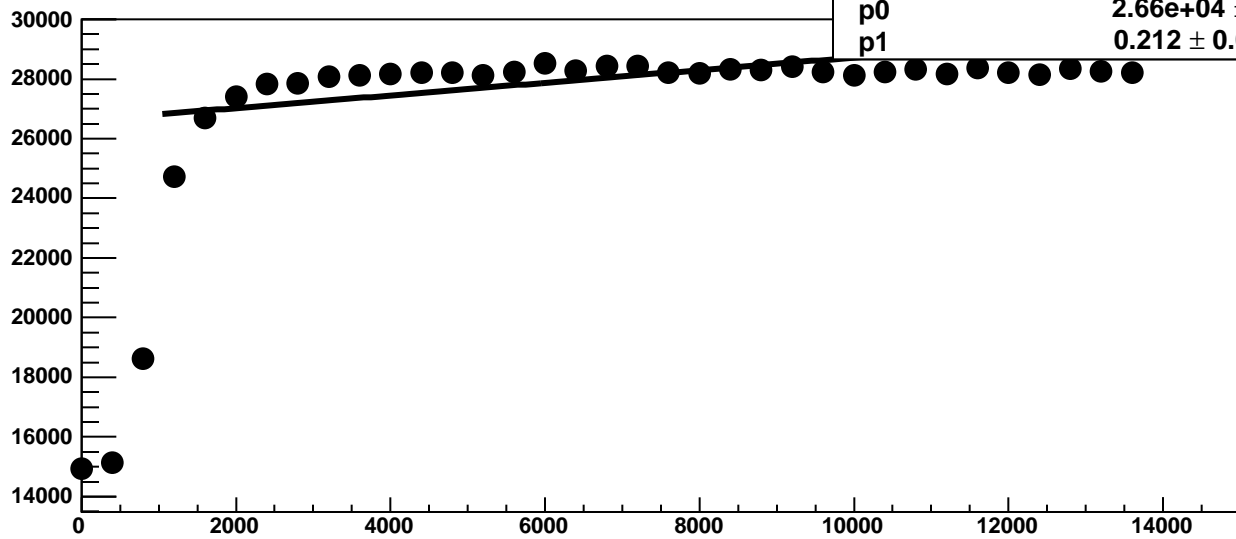
Chip 7, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



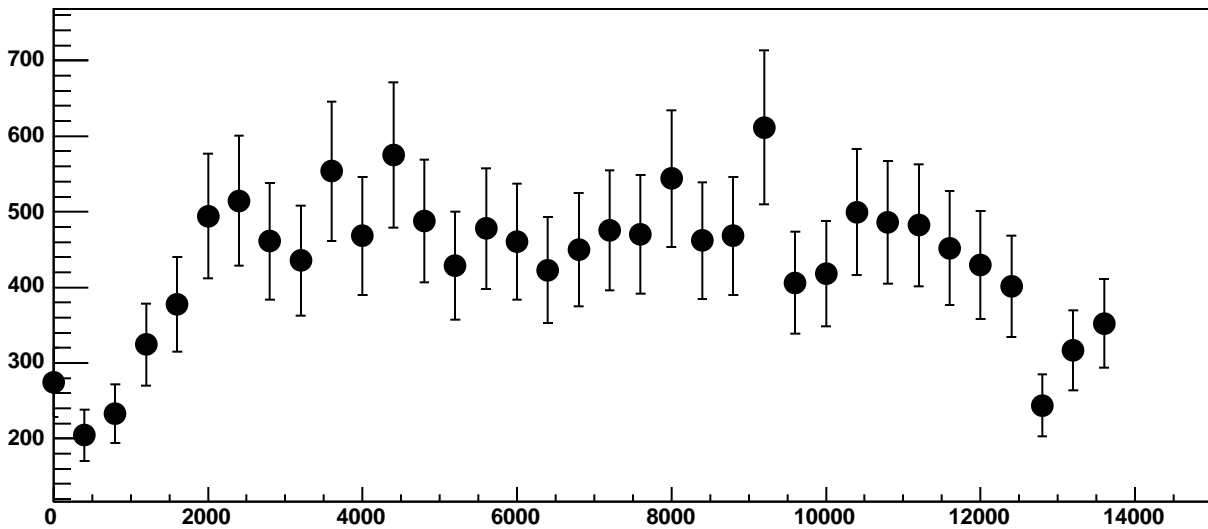
Chip 7, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



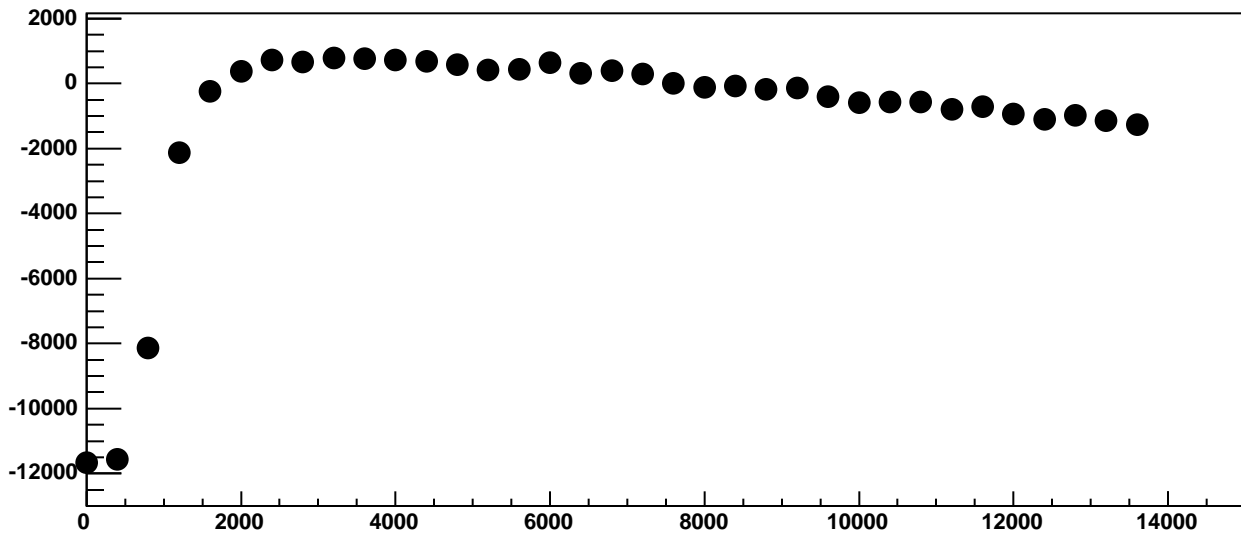
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC



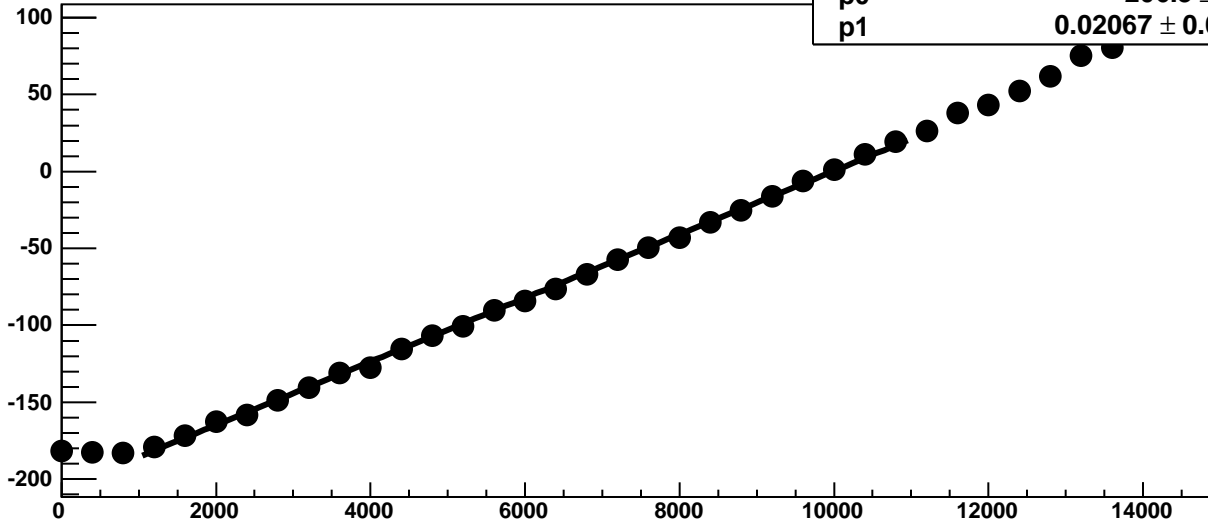
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

21.22 / 23

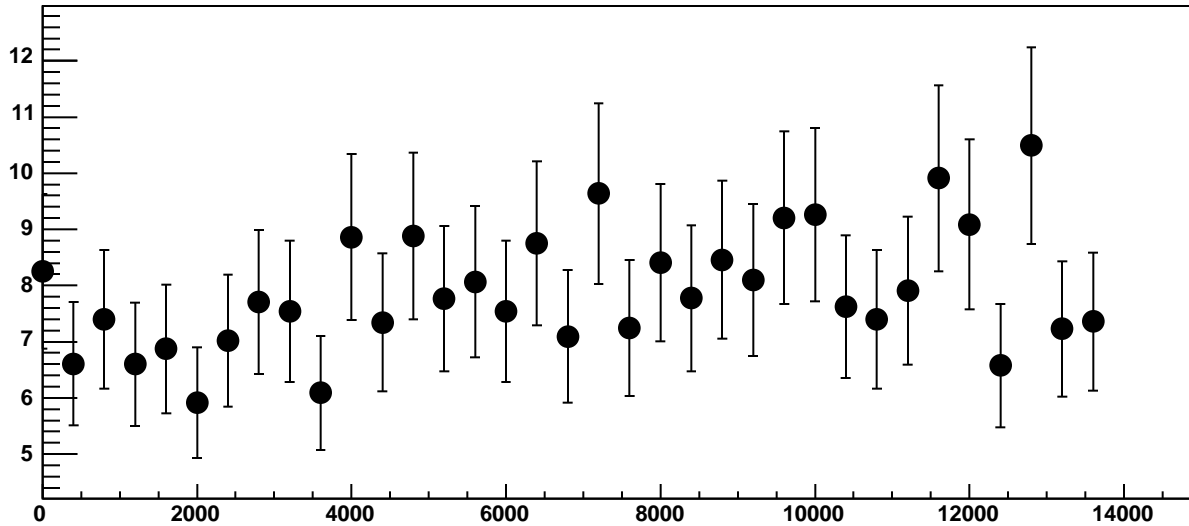
p0

$-206.3 \pm 0.7476$

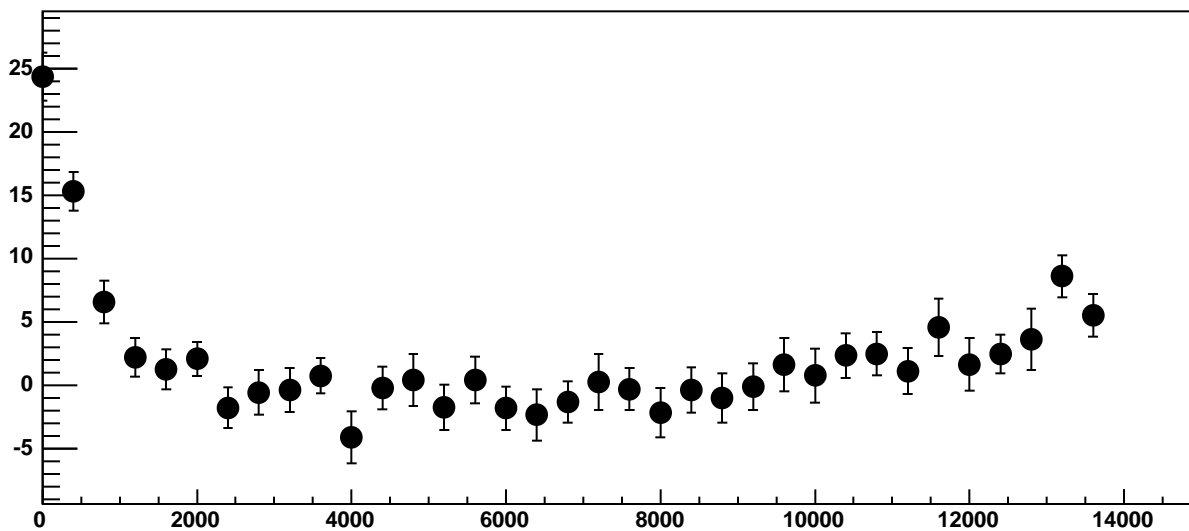
p1

$0.02067 \pm 0.0001181$

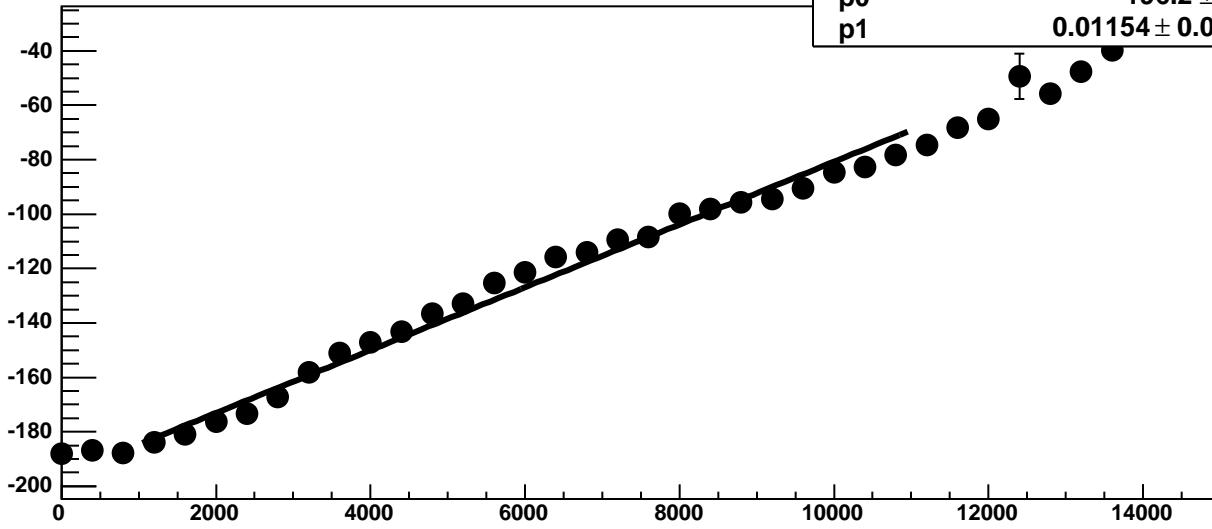
Chip 7, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

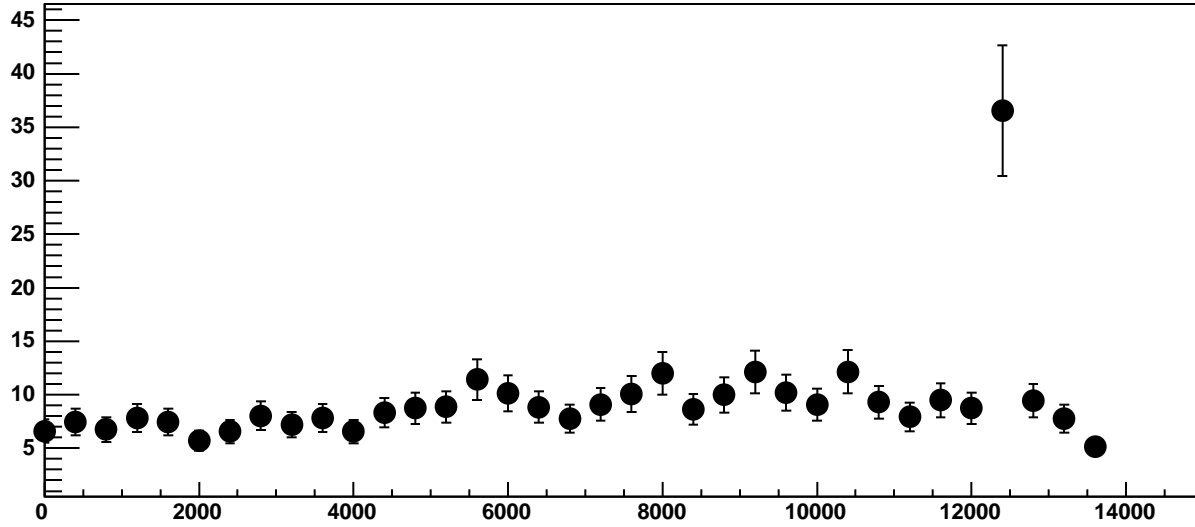


Chip 7, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

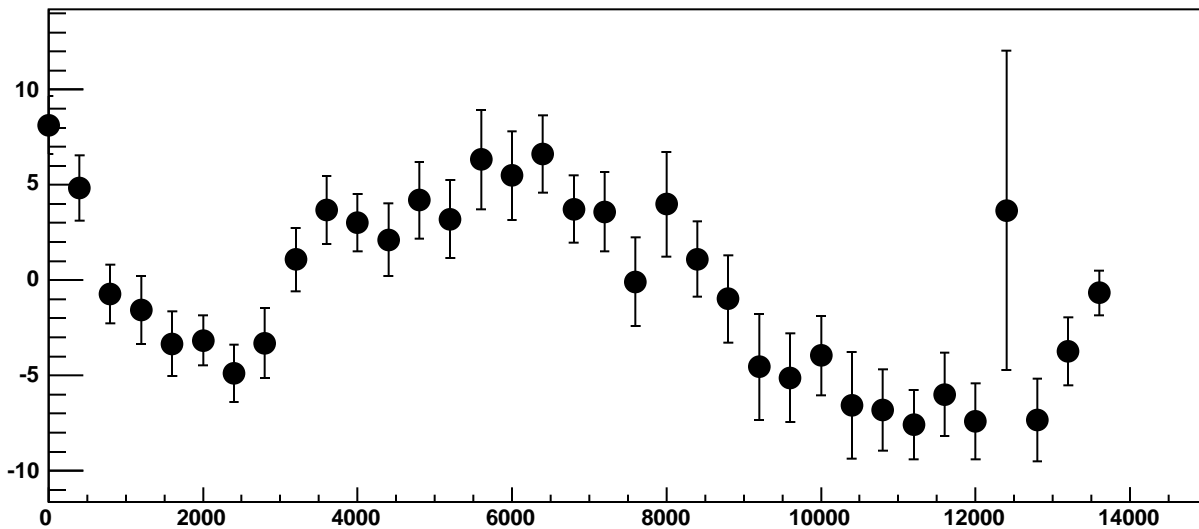


$\chi^2 / \text{ndf}$  99.88 / 23  
p0  $-196.2 \pm 0.7997$   
p1  $0.01154 \pm 0.0001355$

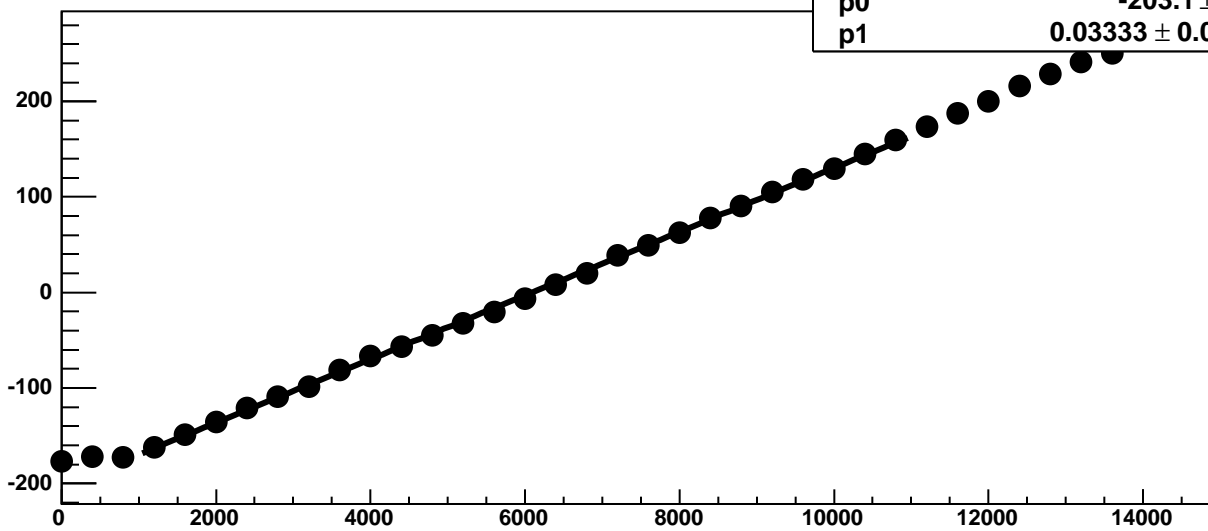
Chip 7, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

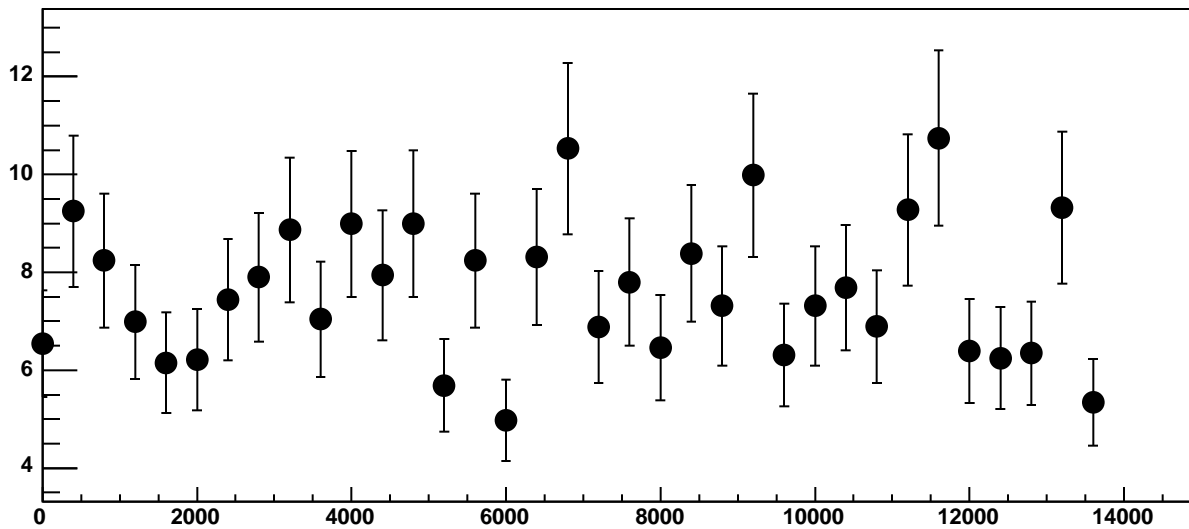


Chip 7, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

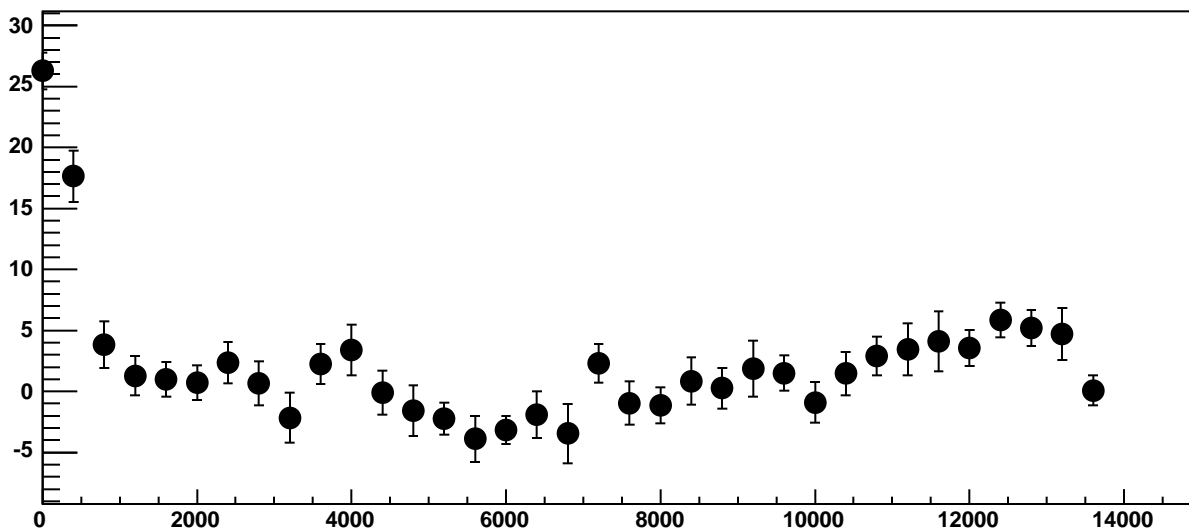


$\chi^2 / \text{ndf}$  37.01 / 23  
p0  $-203.1 \pm 0.7492$   
p1  $0.03333 \pm 0.0001136$

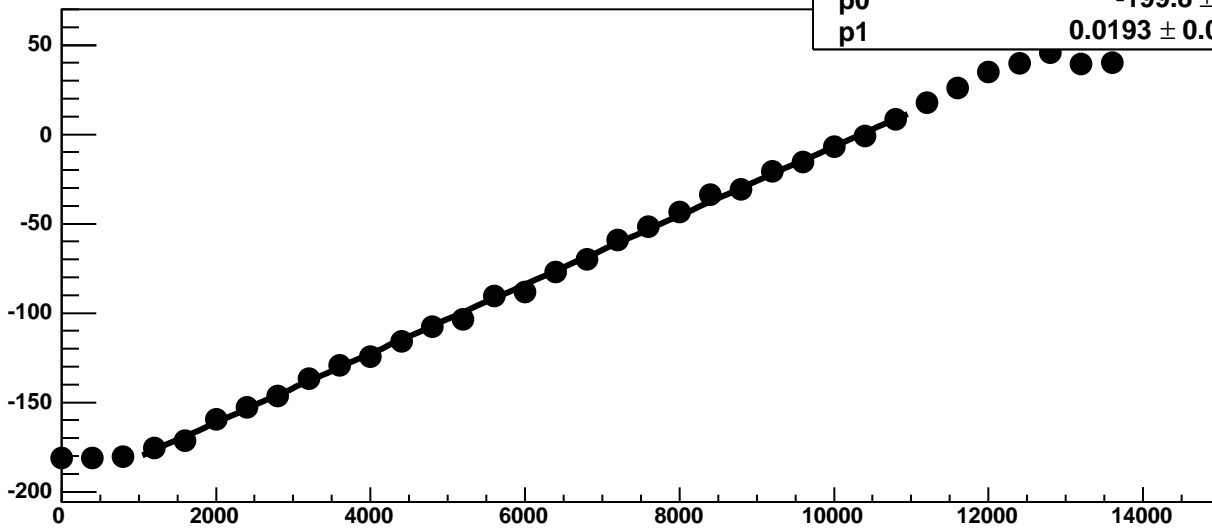
Chip 7, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



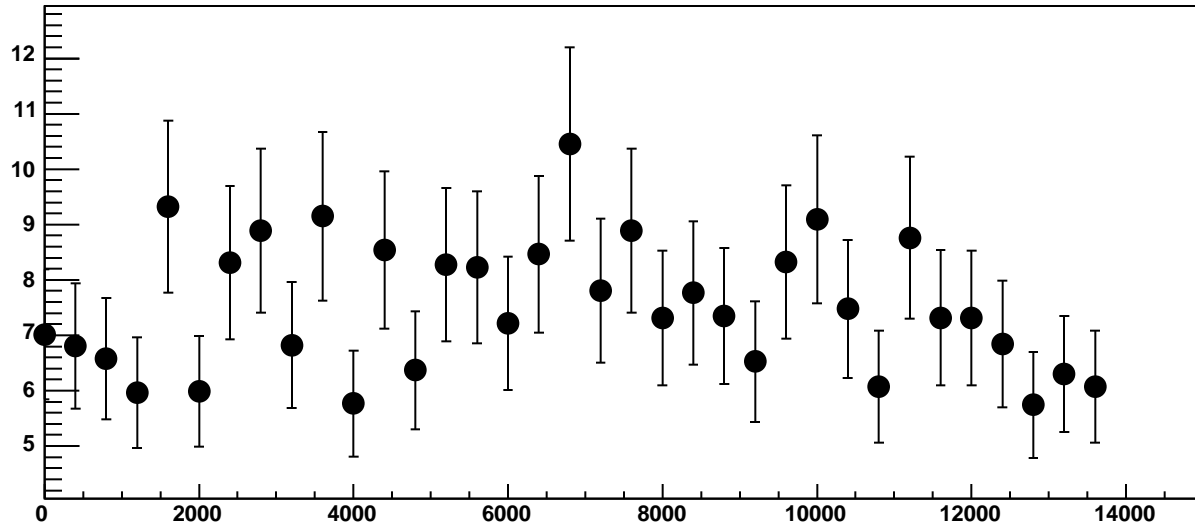
Chip 7, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



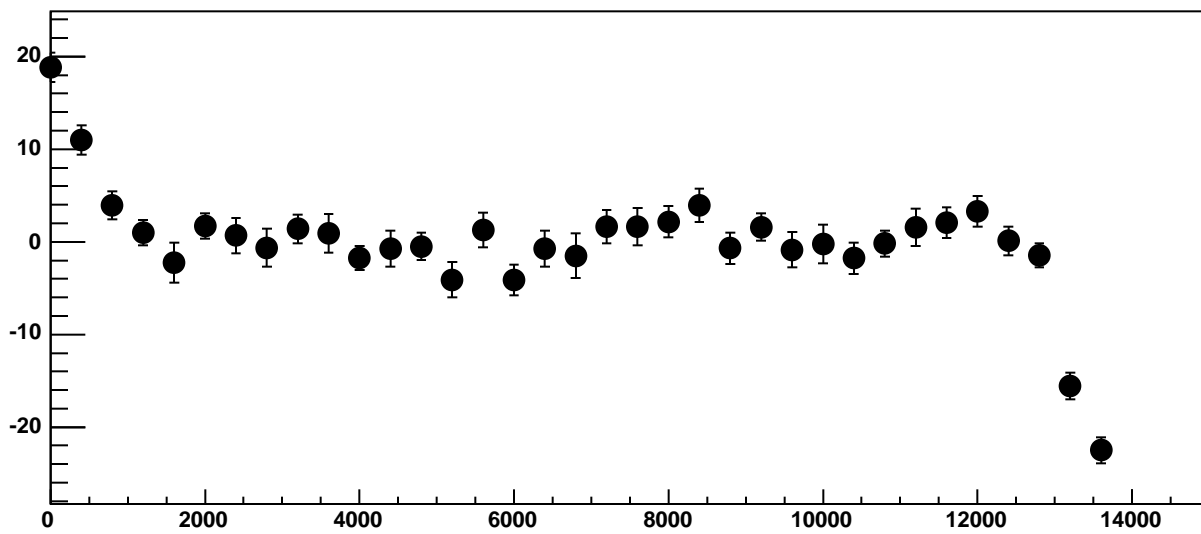
Chip 7, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



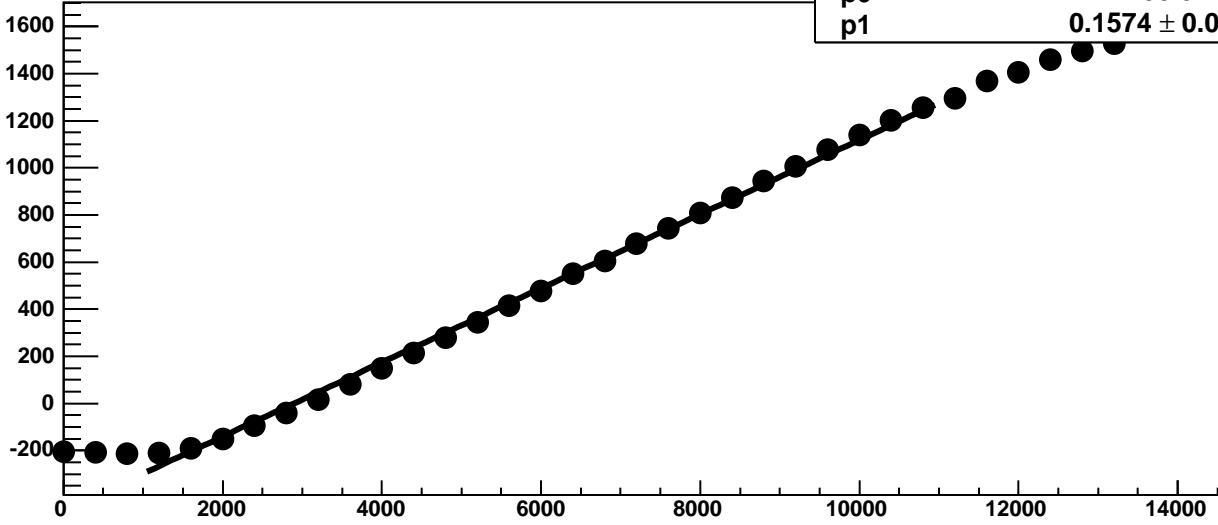
Chip 7, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



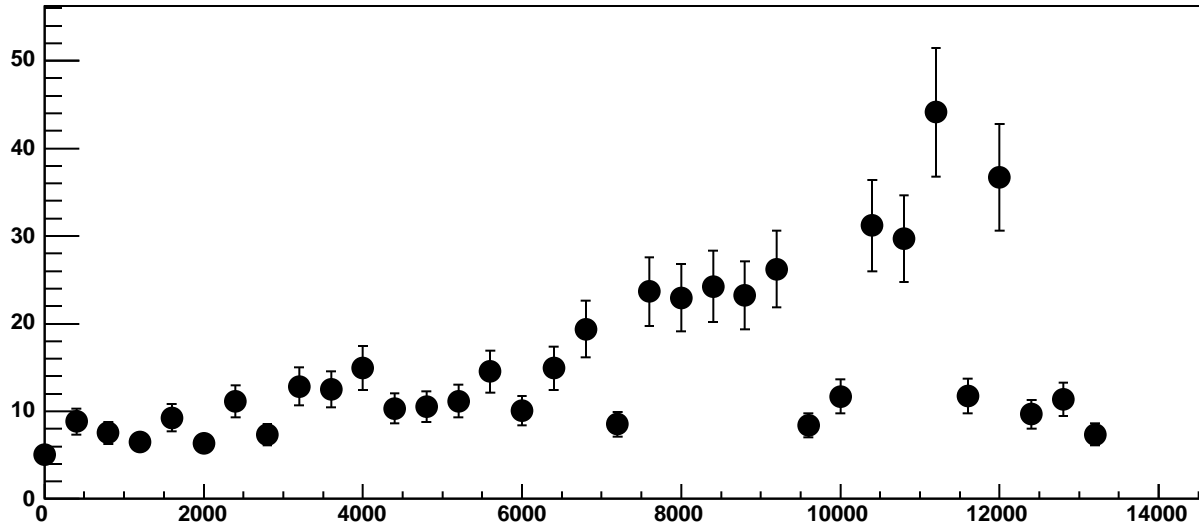
Chip 7, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



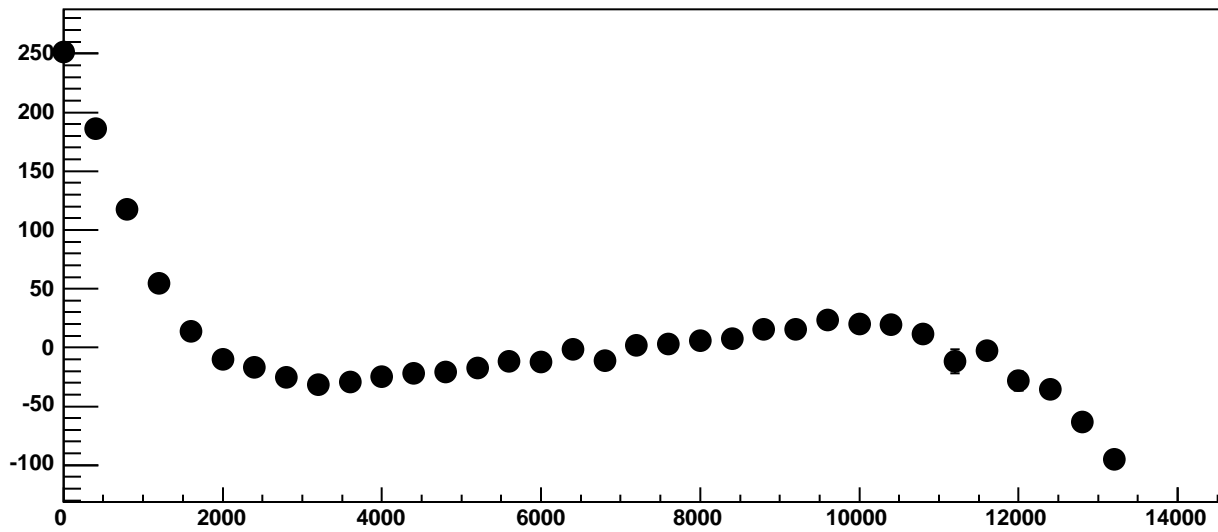
Chip 7, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 7, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

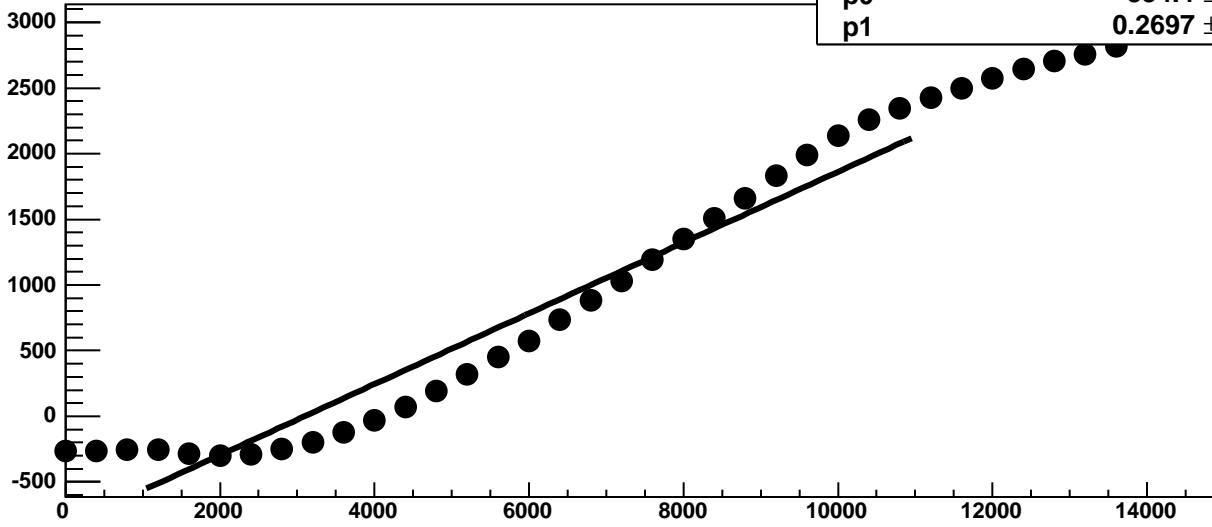


Chip 7, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

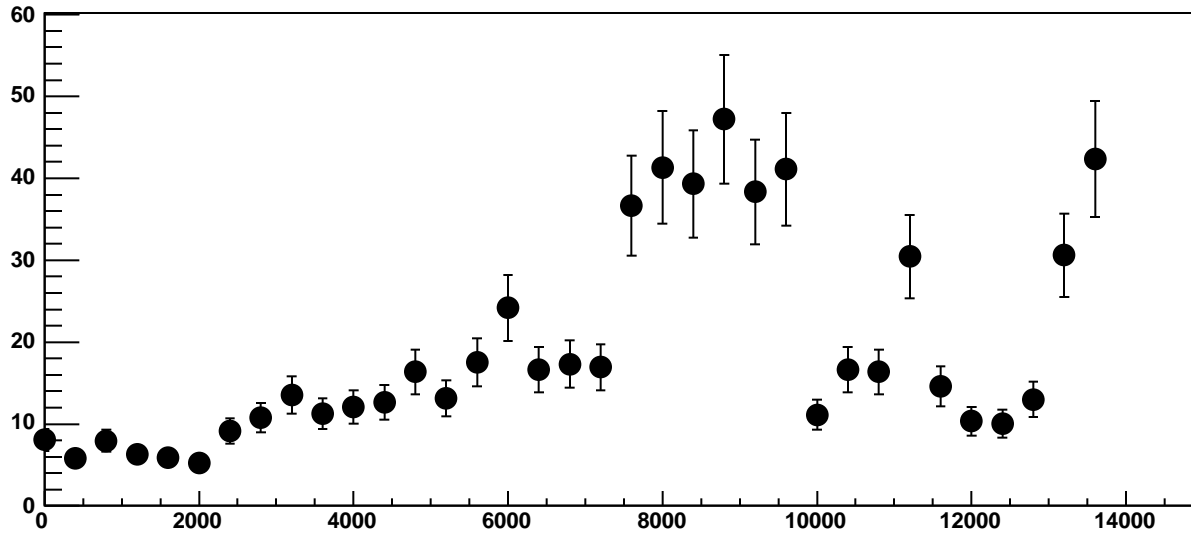


Chip 7, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

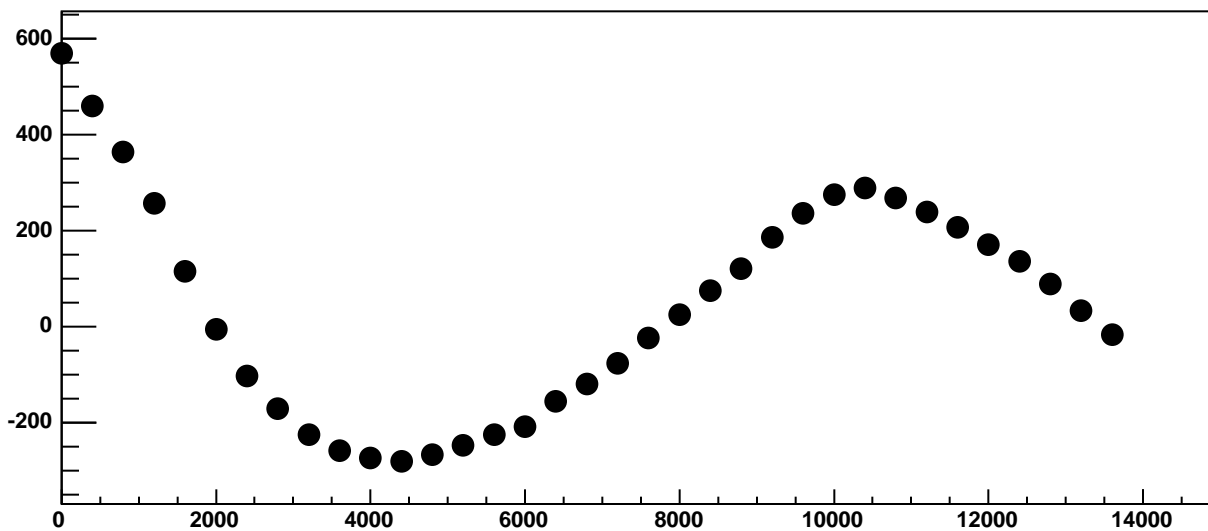
$\chi^2 / \text{ndf}$  1.233e+05 / 23  
p0 -834.4 ± 0.8872  
p1 0.2697 ± 0.0002



Chip 7, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

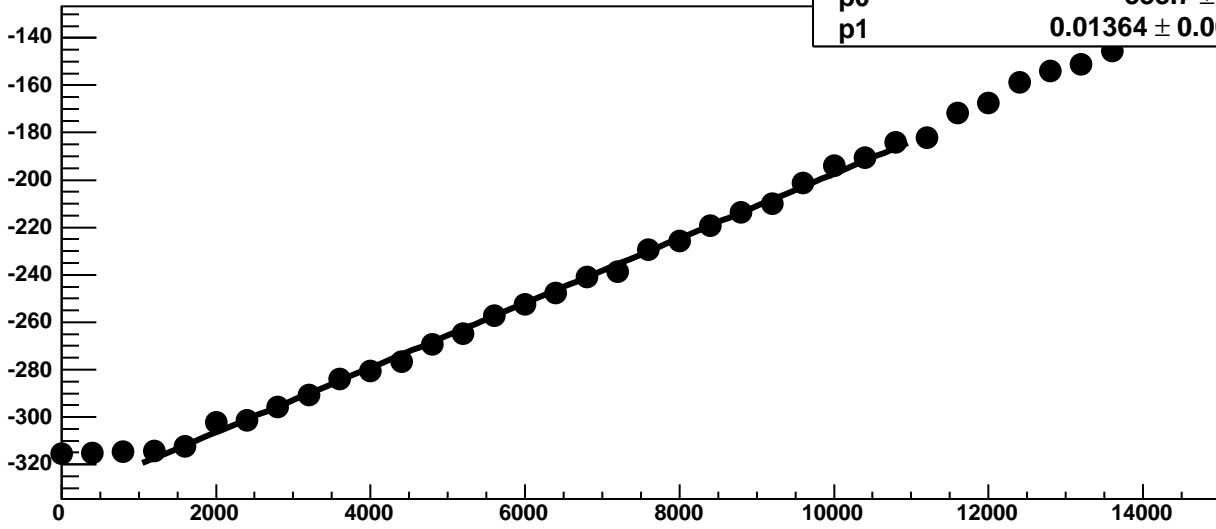


Chip 7, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

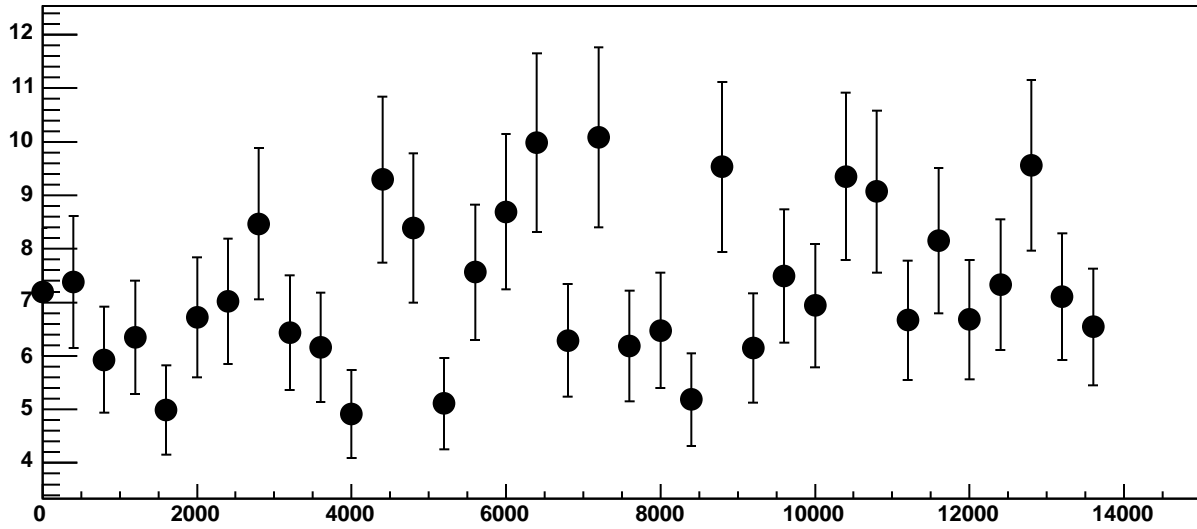




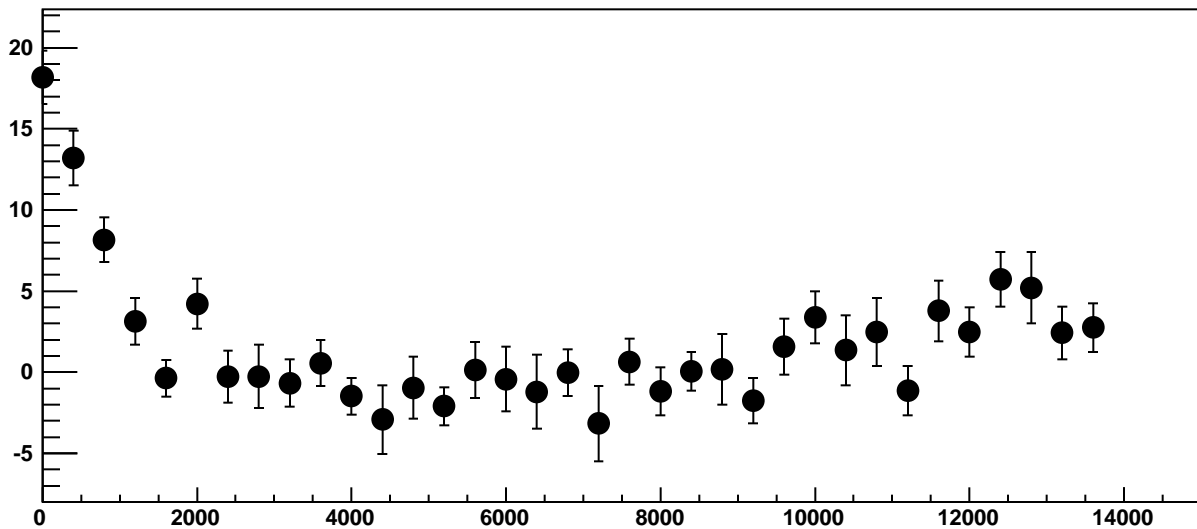
Chip 7, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



Chip 7, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

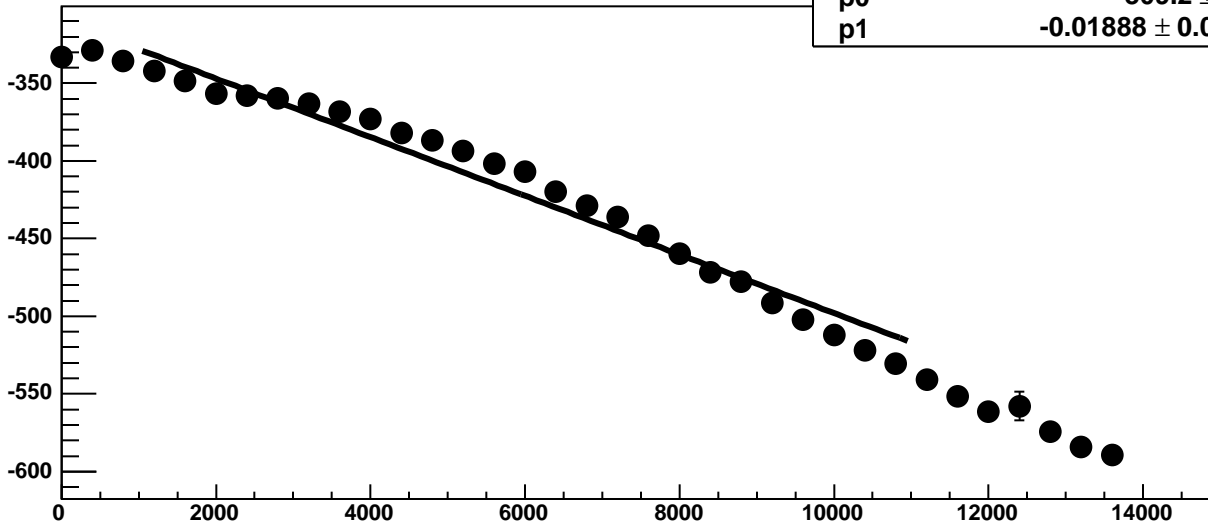


Chip 7, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

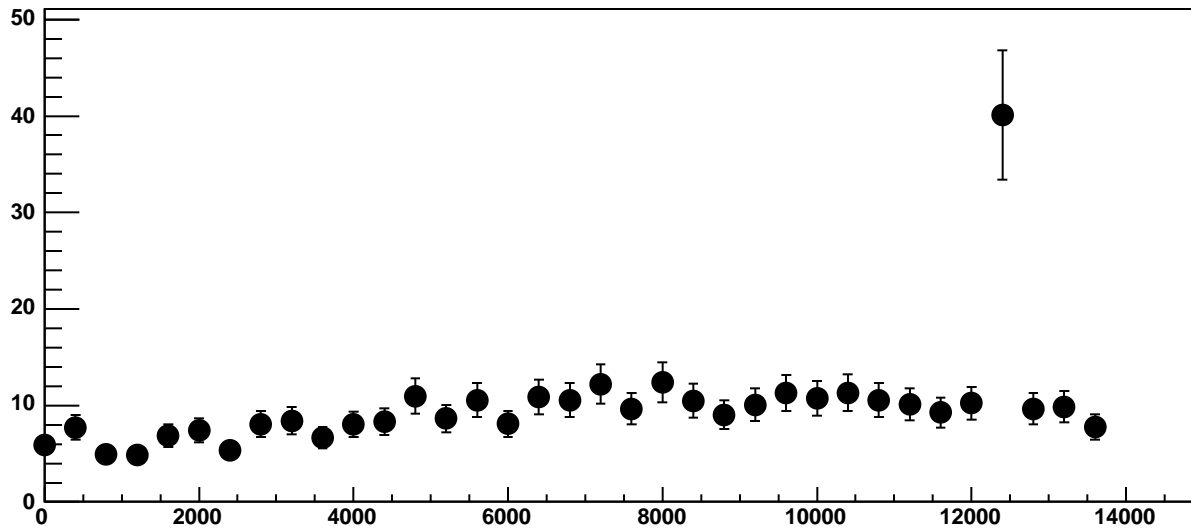


Chip 7, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

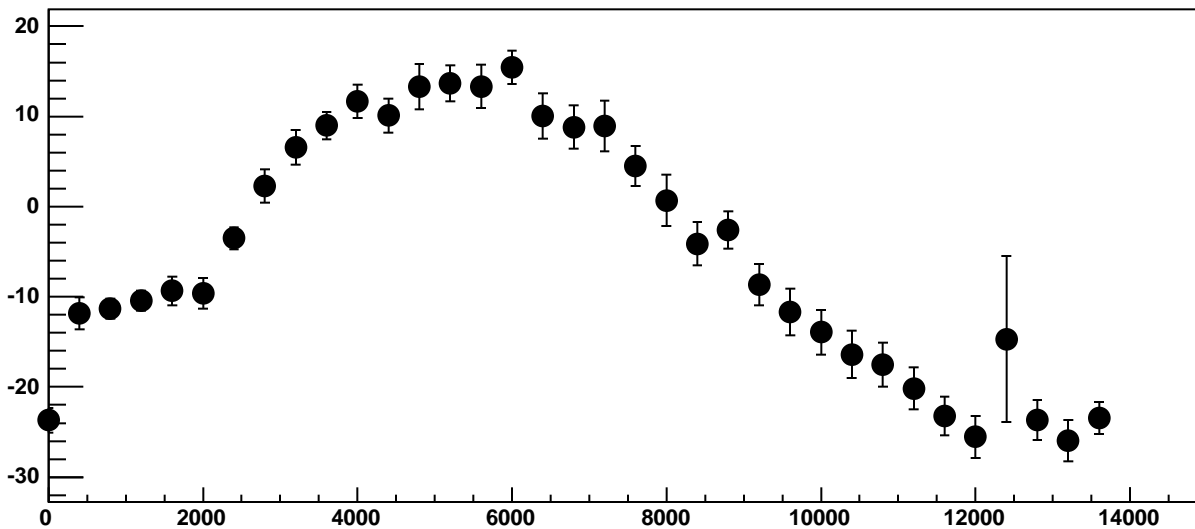
$\chi^2 / \text{ndf}$  658.6 / 23  
p0  $-309.2 \pm 0.7385$   
p1  $-0.01888 \pm 0.0001323$



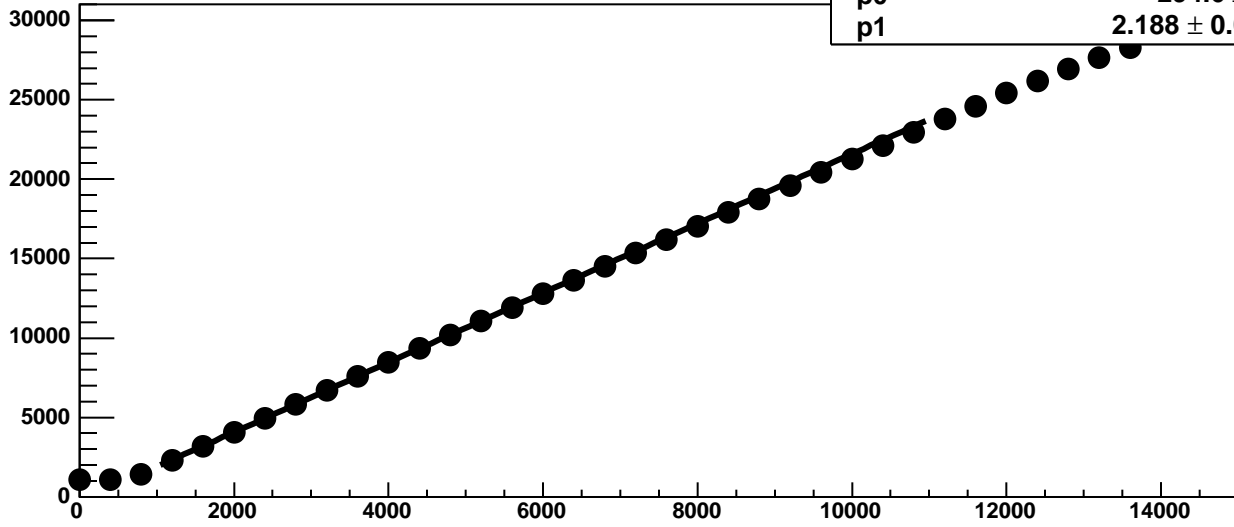
Chip 7, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



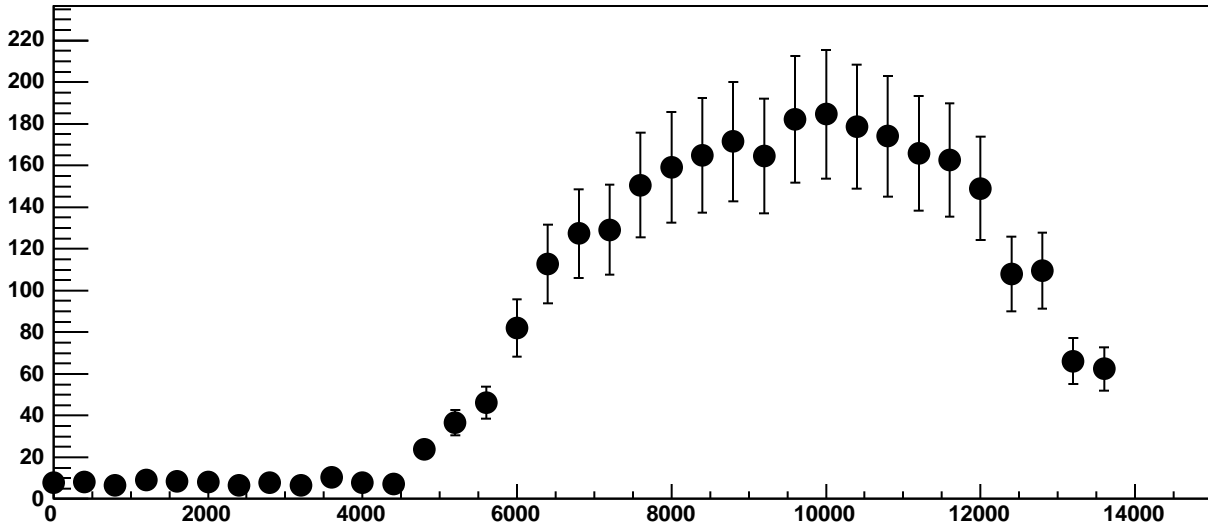
Chip 7, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



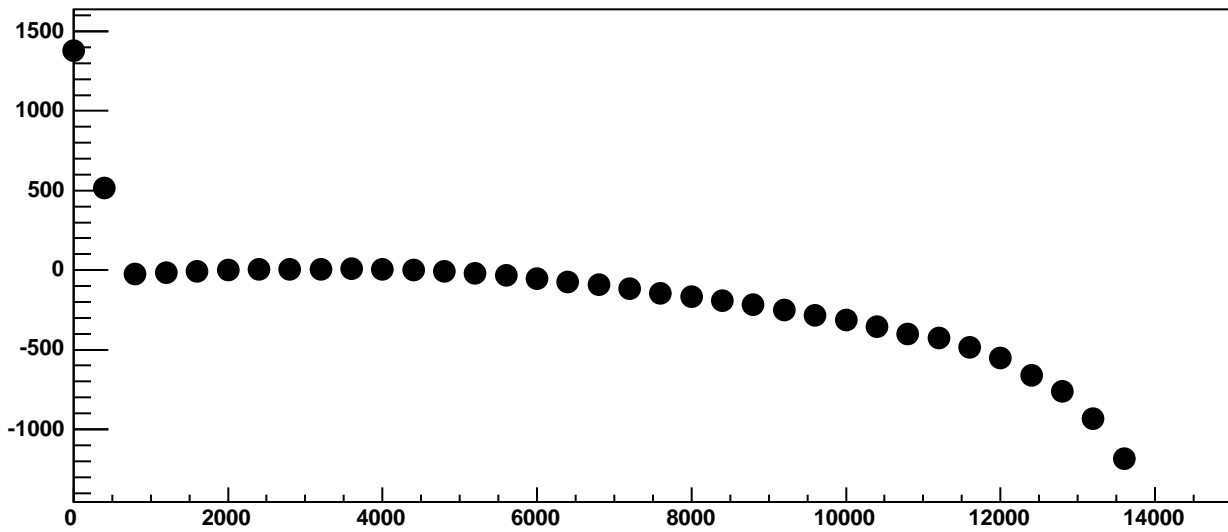
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



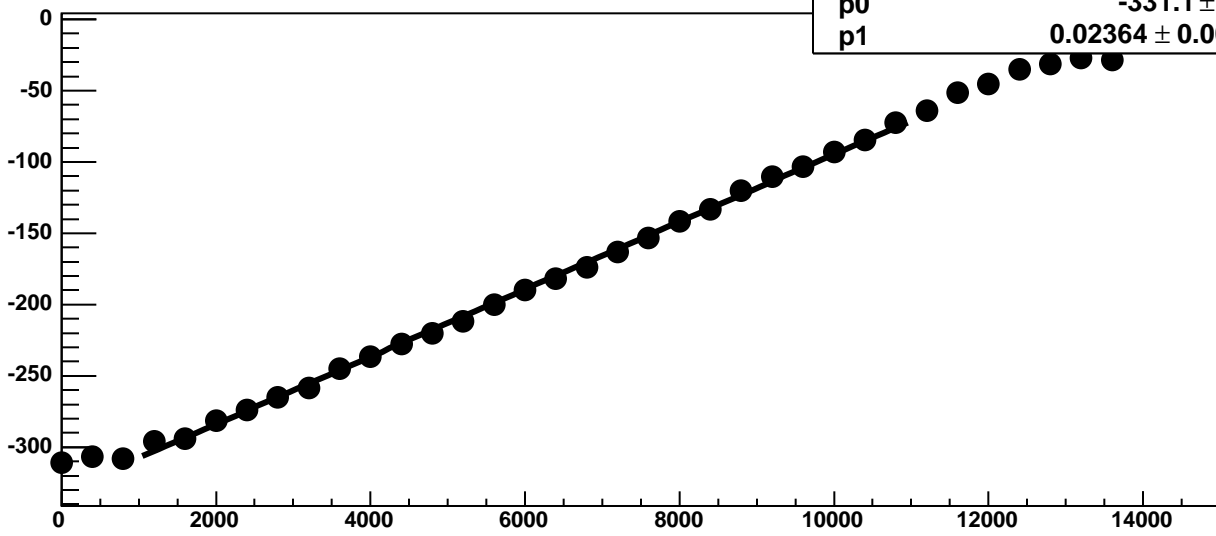
Chip 7, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

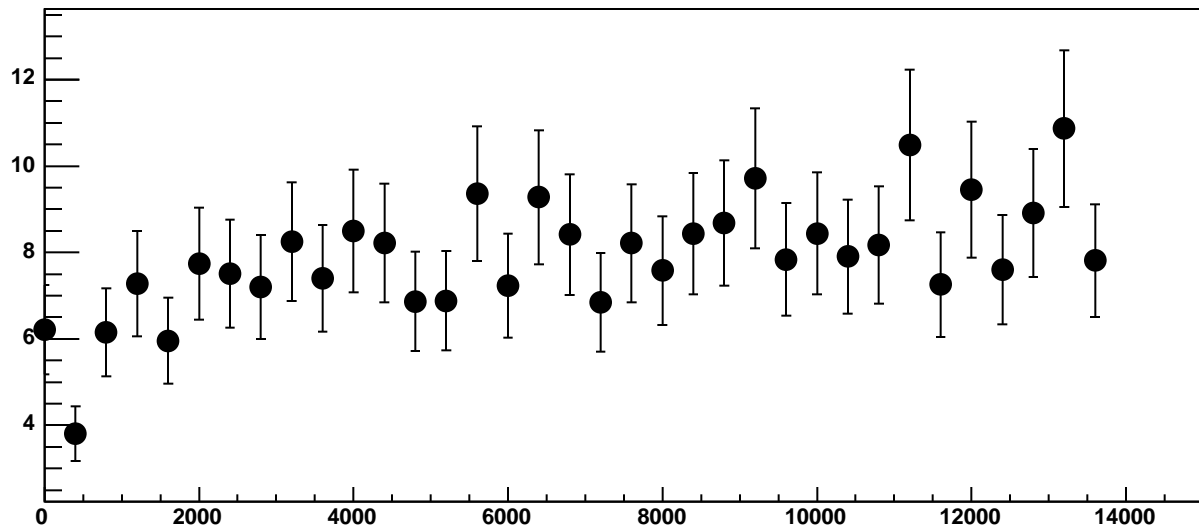


Chip 7, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

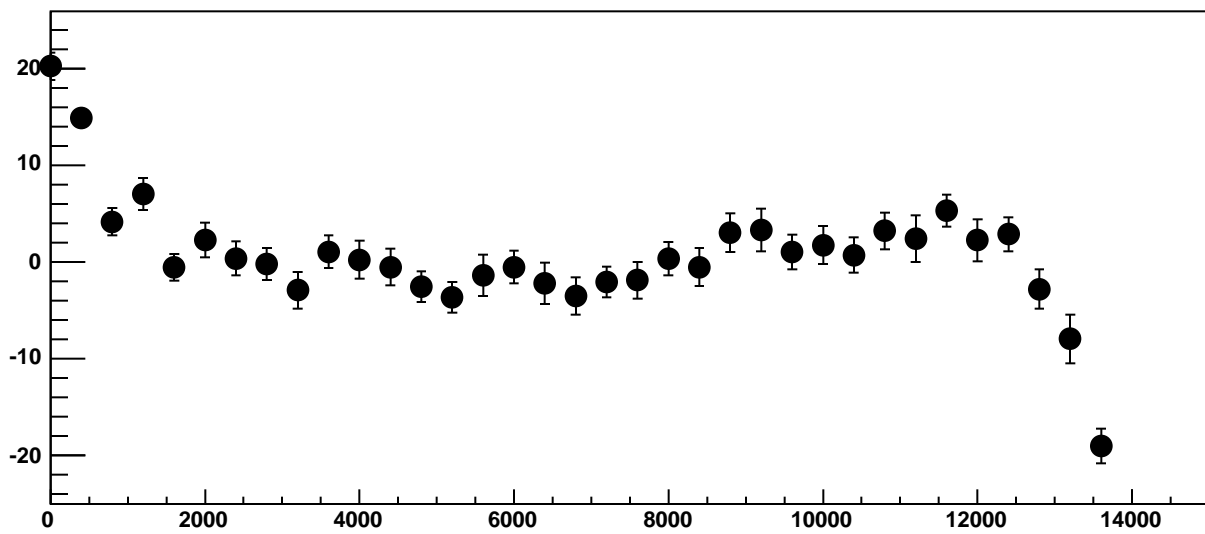


$\chi^2 / \text{ndf}$  46.72 / 23  
p0 -331.1 ± 0.7812  
p1 0.02364 ± 0.0001222

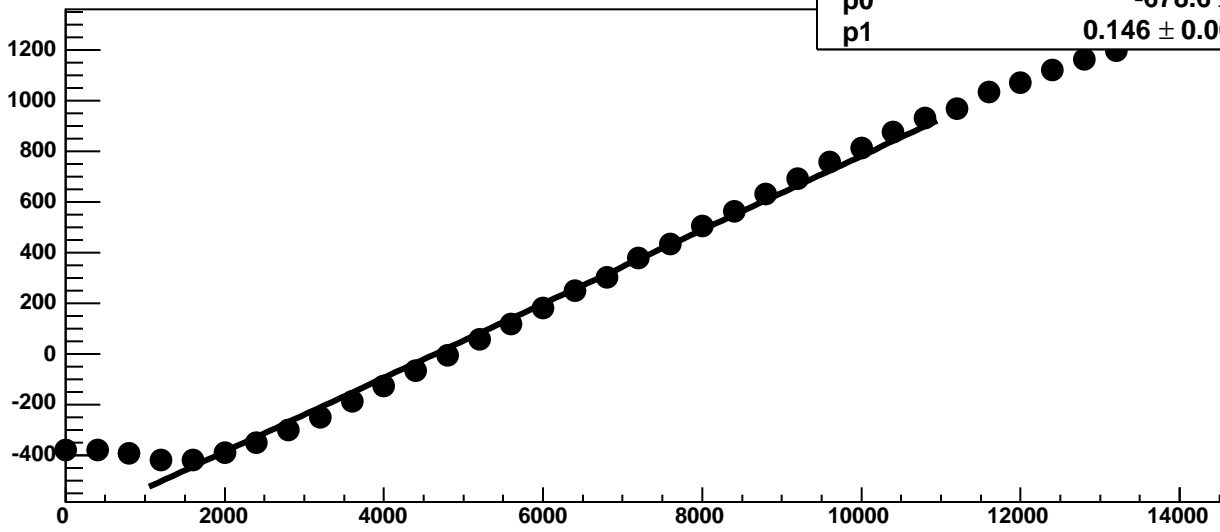
Chip 7, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



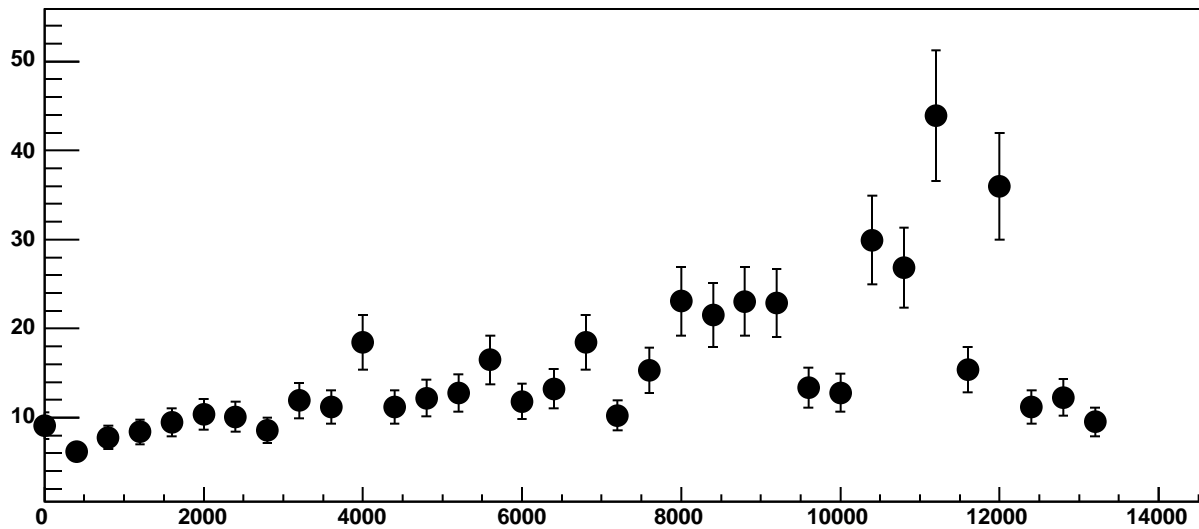
Chip 7, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



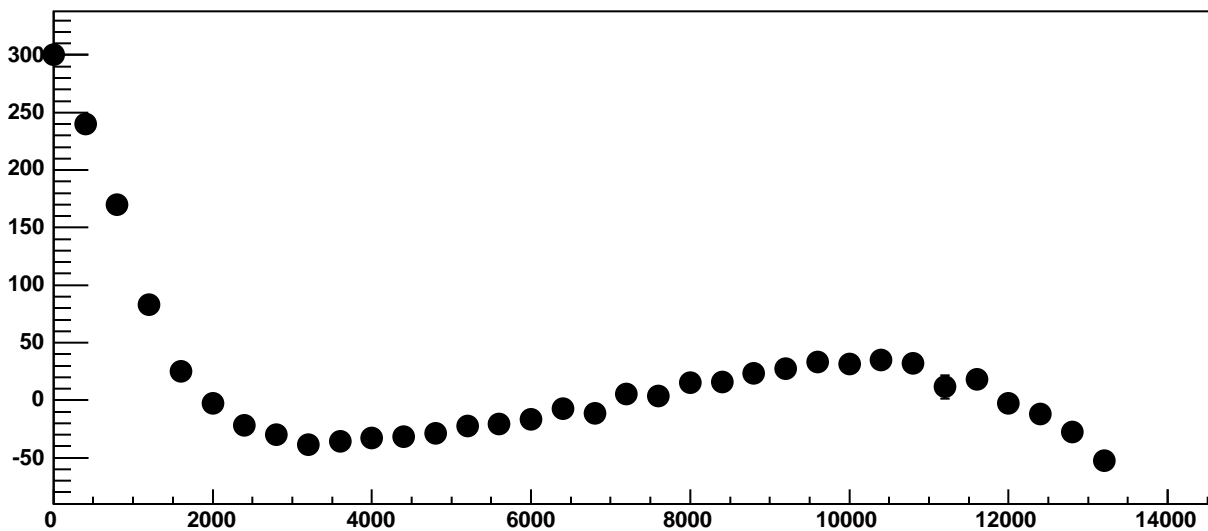
Chip 7, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



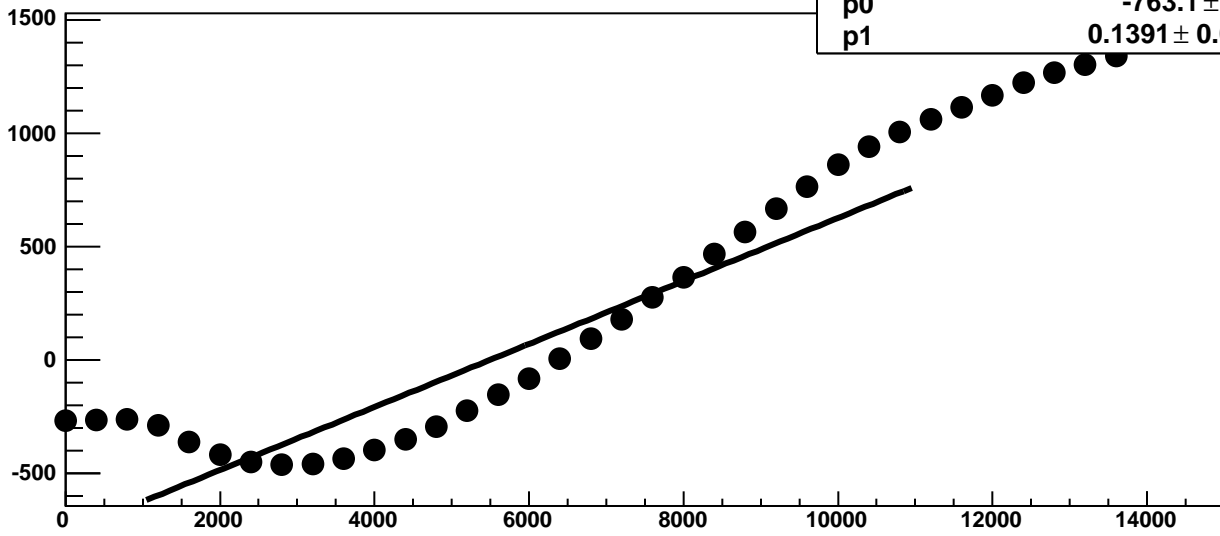
Chip 7, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

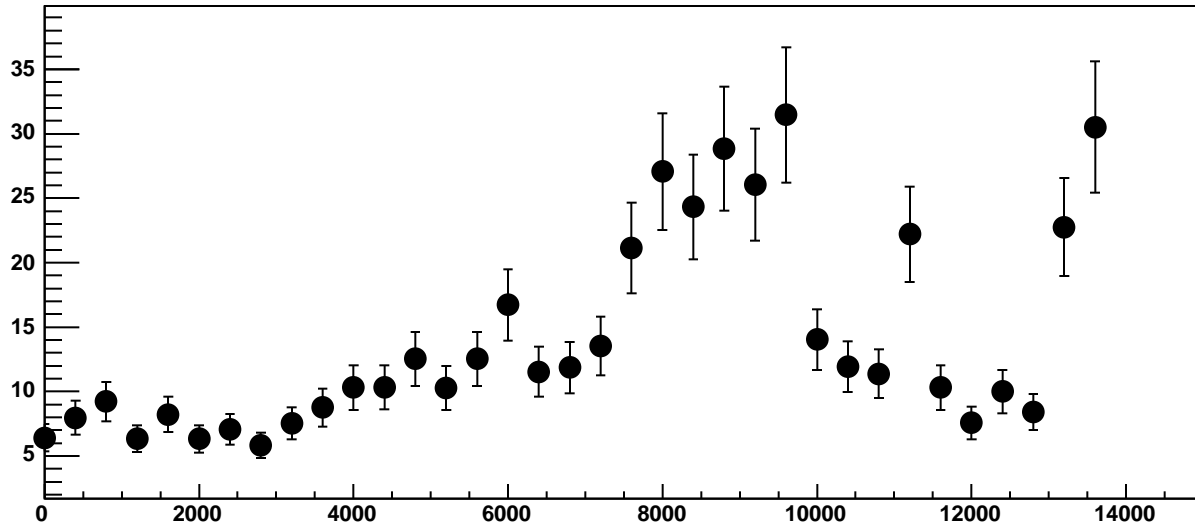


Chip 7, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

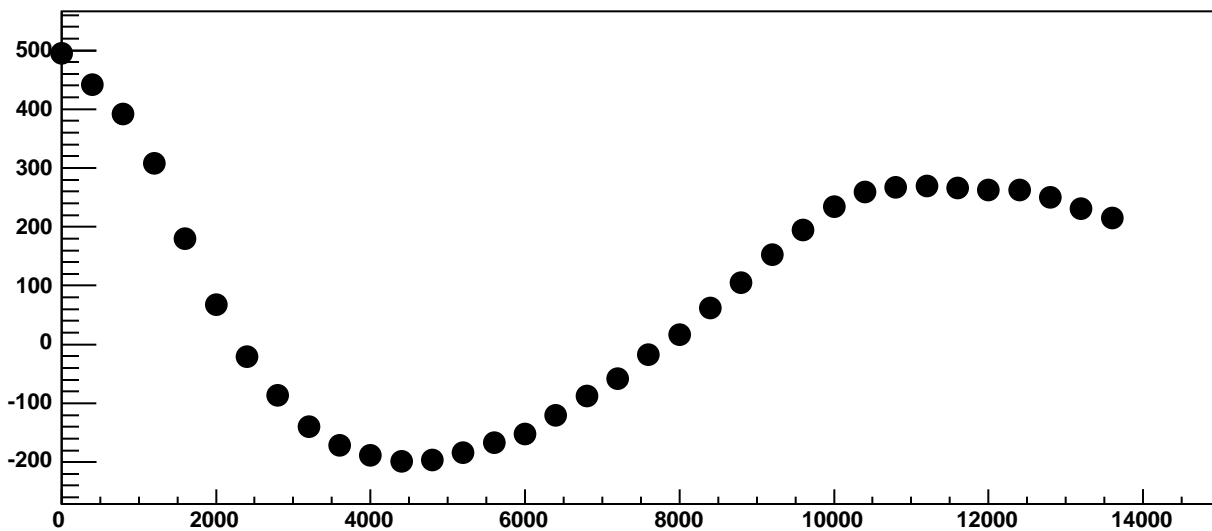


$\chi^2 / \text{ndf}$  1.331e+05 / 23  
p0 -763.1 ± 0.8586  
p1 0.1391 ± 0.000176

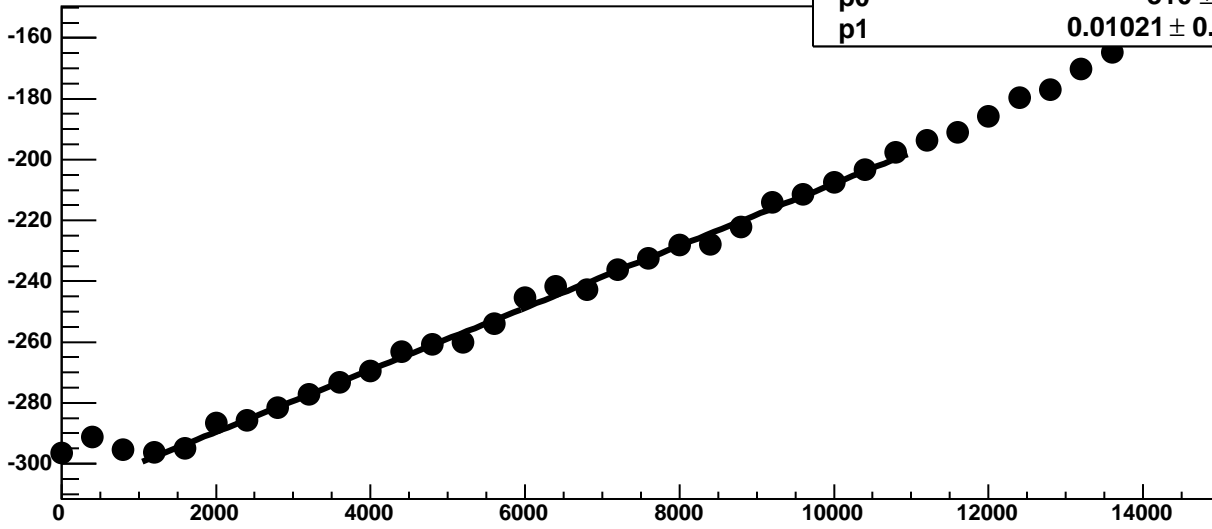
Chip 7, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

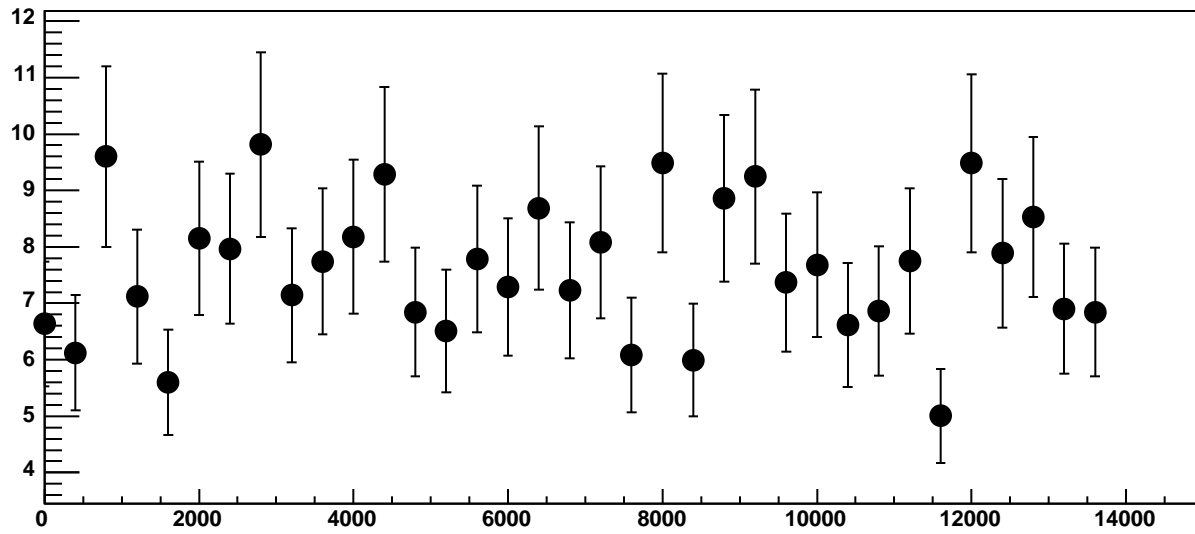


Chip 7, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

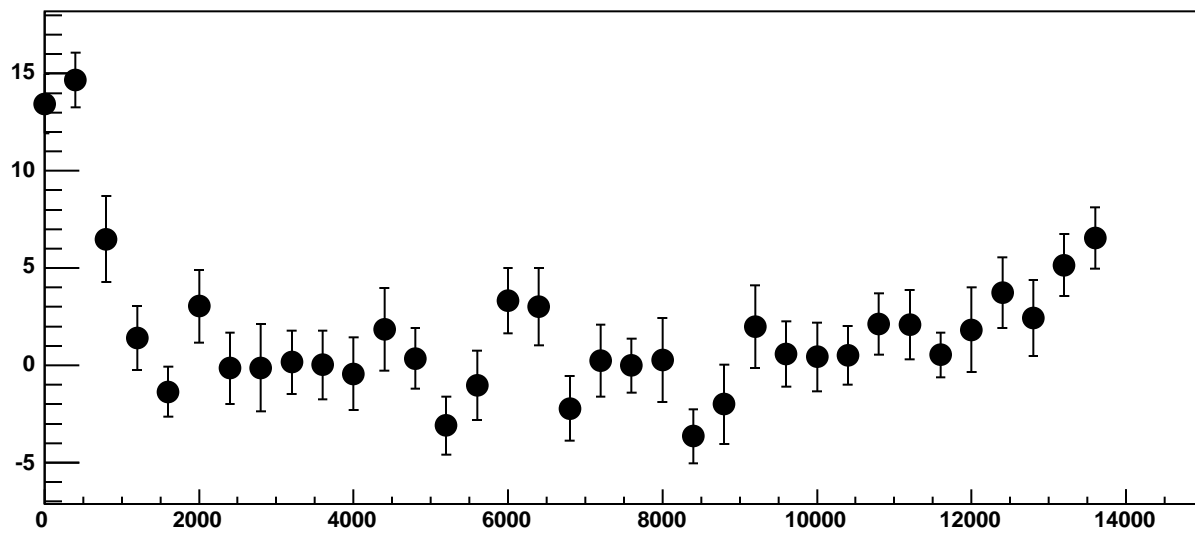


$\chi^2 / \text{ndf}$  29.05 / 23  
p0  $-310 \pm 0.7695$   
p1  $0.01021 \pm 0.000115$

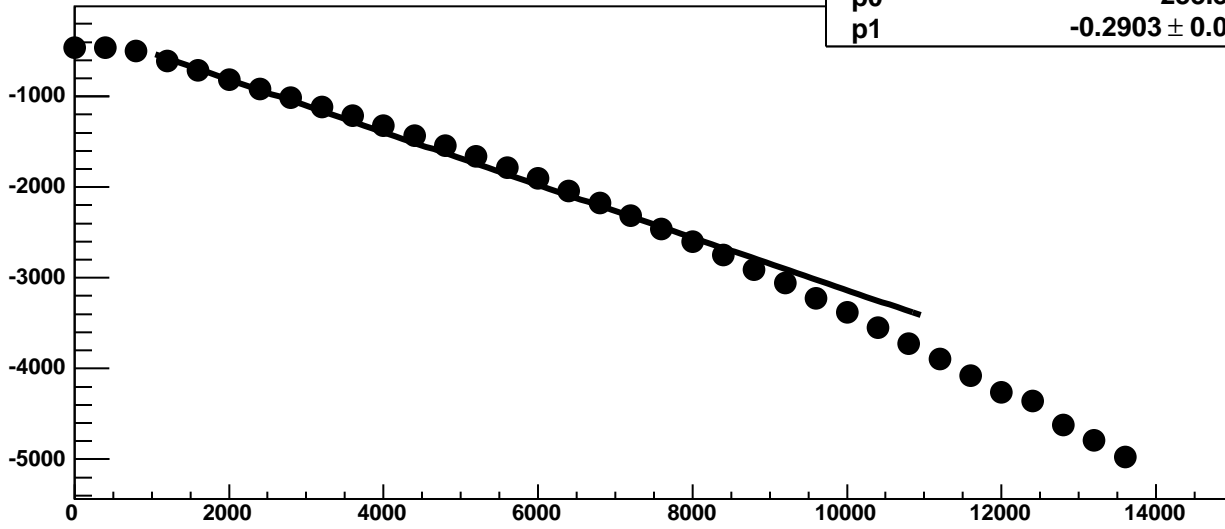
Chip 7, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

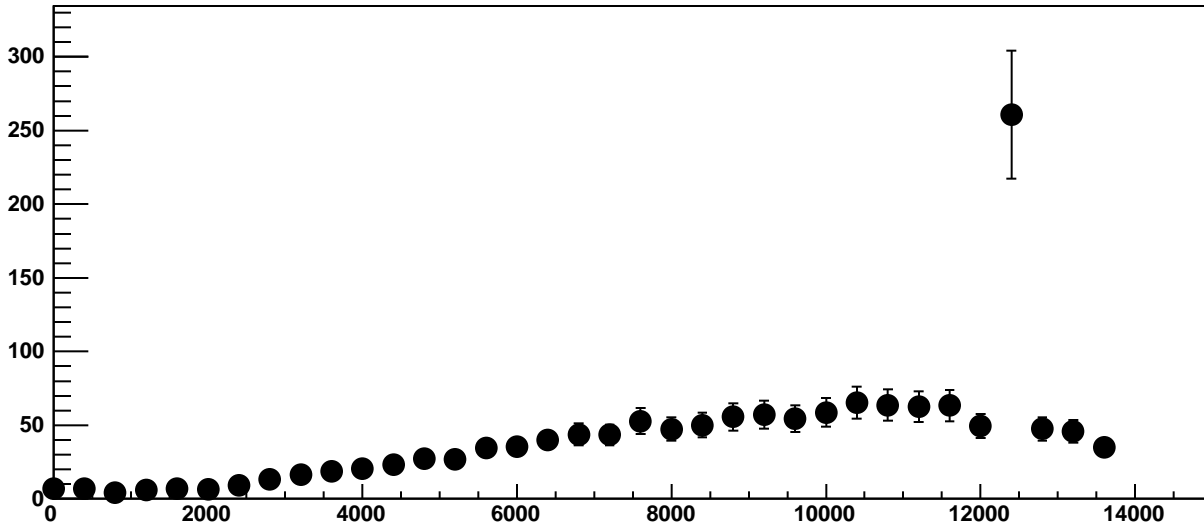


Chip 7, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

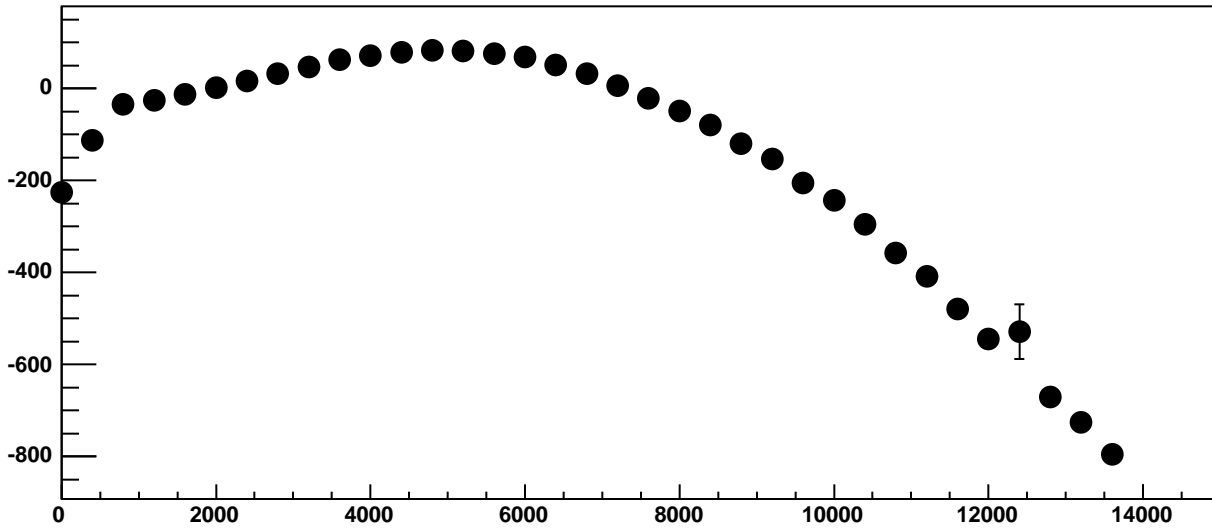


$\chi^2 / \text{ndf}$  3817 / 23  
p0  $-233.3 \pm 1.23$   
p1  $-0.2903 \pm 0.0004326$

Chip 7, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

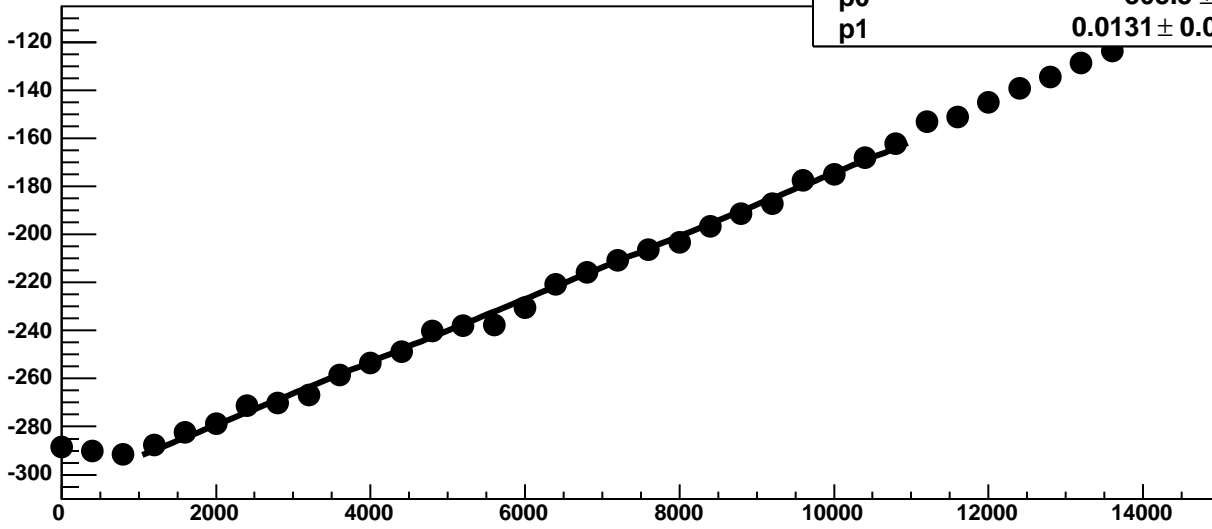


Chip 7, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC



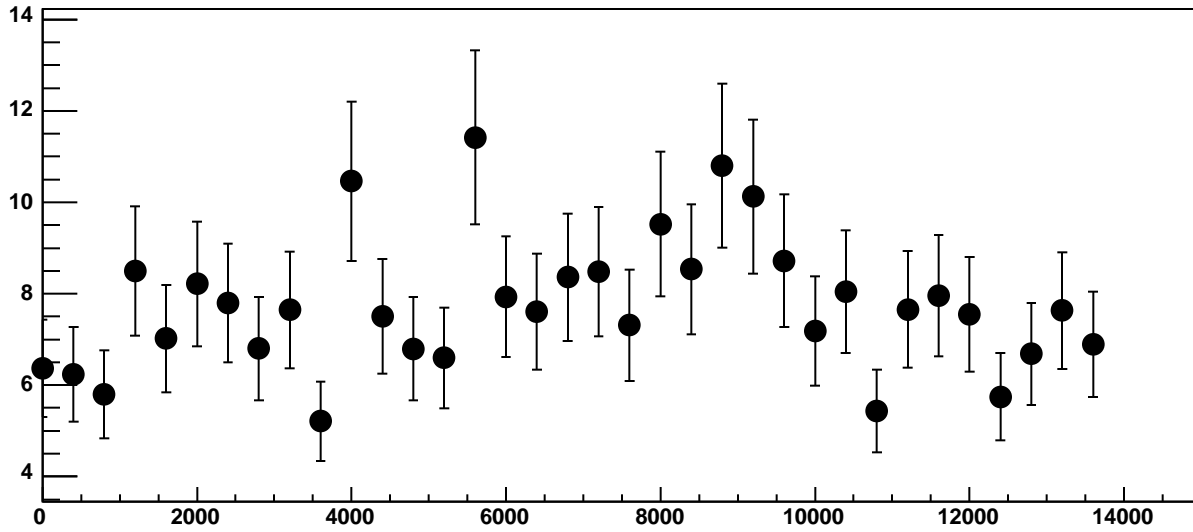


Chip 7, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

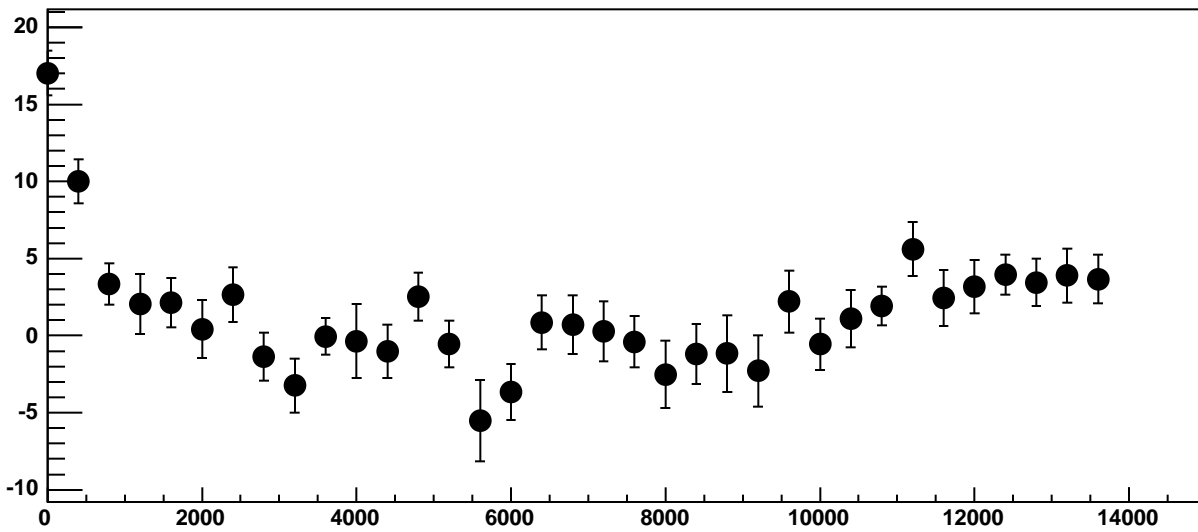


$\chi^2 / \text{ndf}$  28.29 / 23  
p0  $-305.5 \pm 0.7794$   
p1  $0.0131 \pm 0.0001179$

Chip 7, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

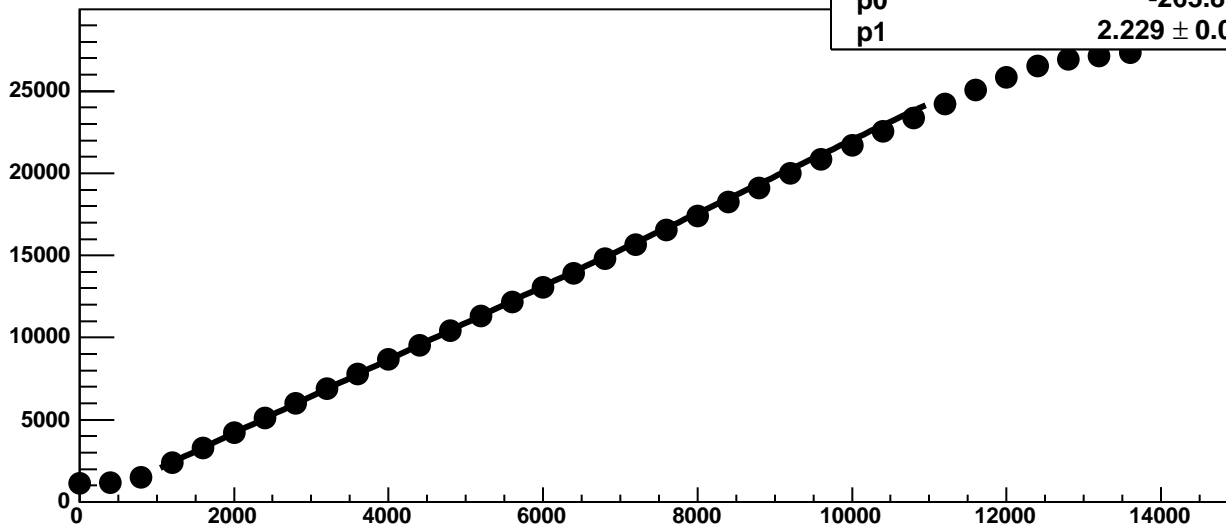


Chip 7, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

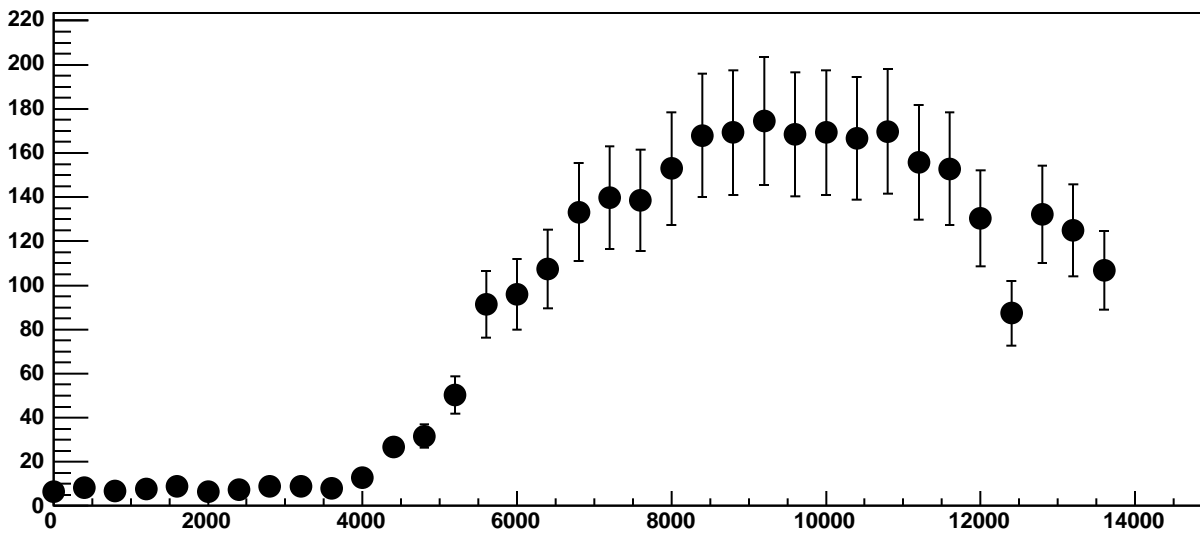


Chip 7, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

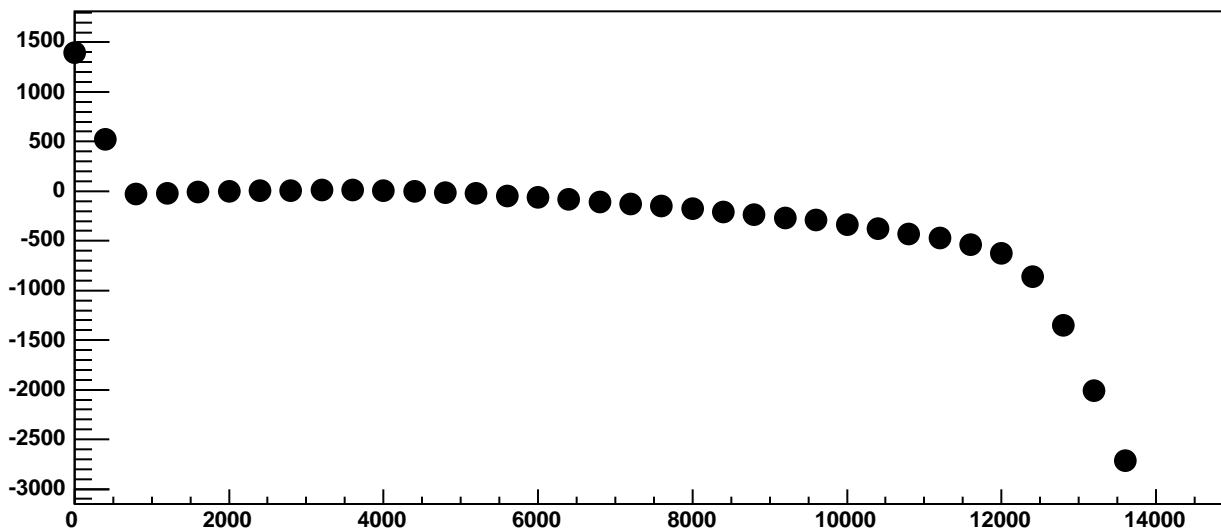
$\chi^2 / \text{ndf}$  775.9 / 23  
p0  $-263.8 \pm 1.786$   
p1  $2.229 \pm 0.0006615$



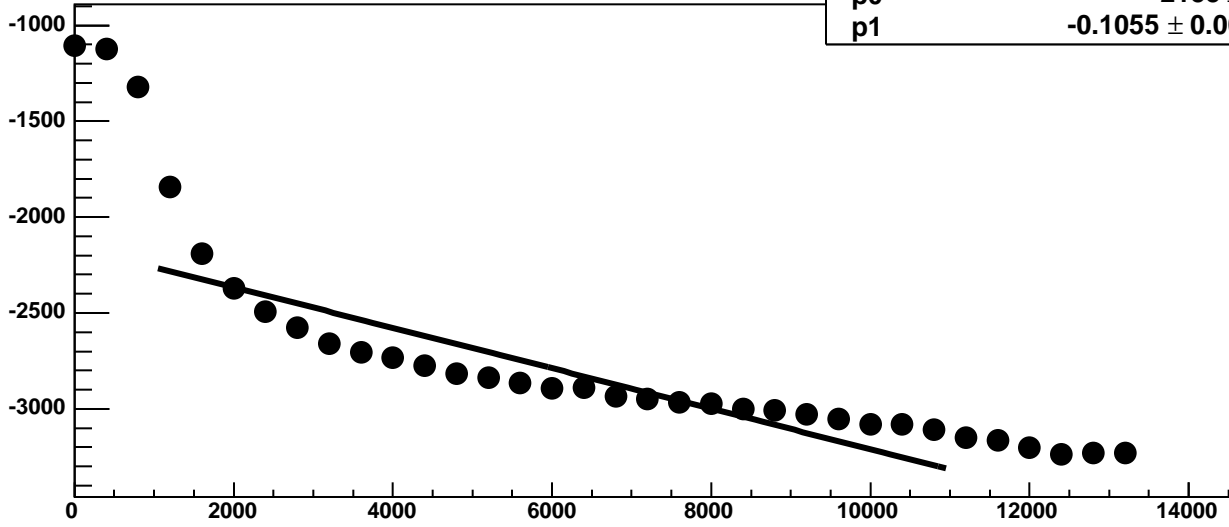
Chip 7, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

9284 / 23

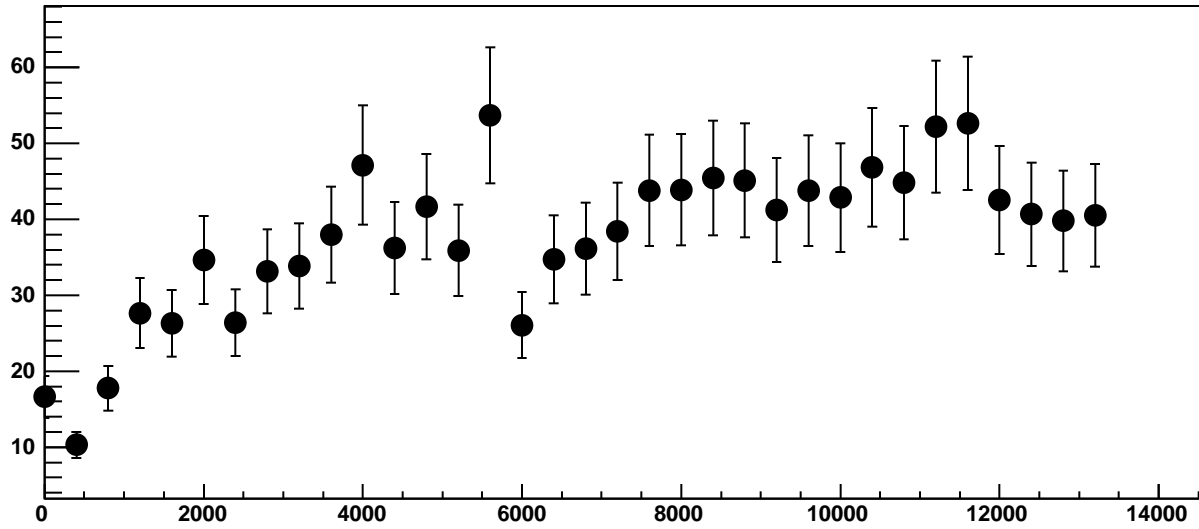
p0

$-2155 \pm 3.446$

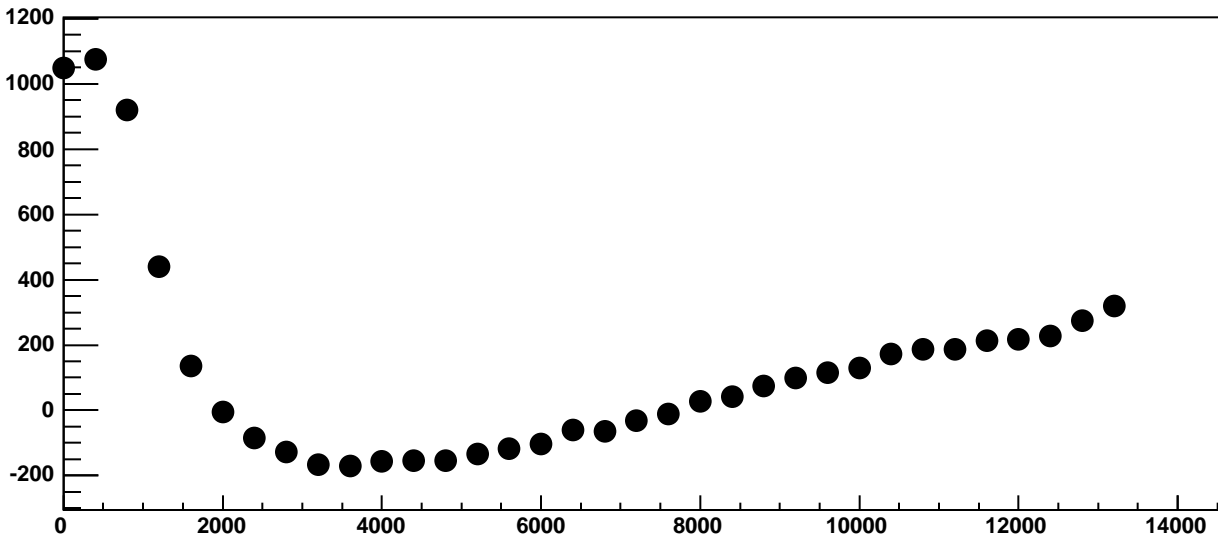
p1

$-0.1055 \pm 0.0005803$

Chip 7, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

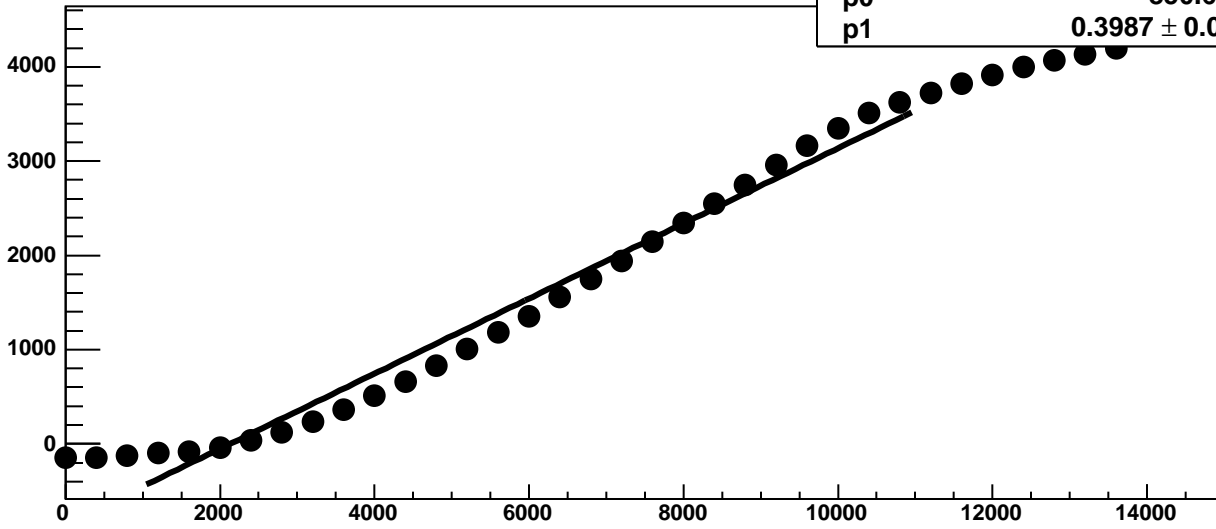


Chip 7, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

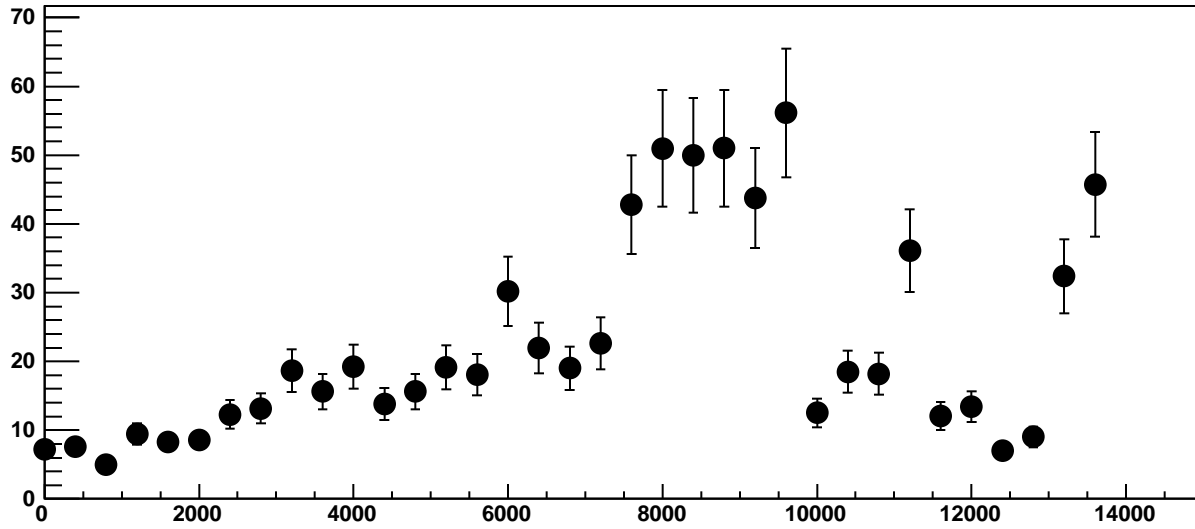


Chip 7, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

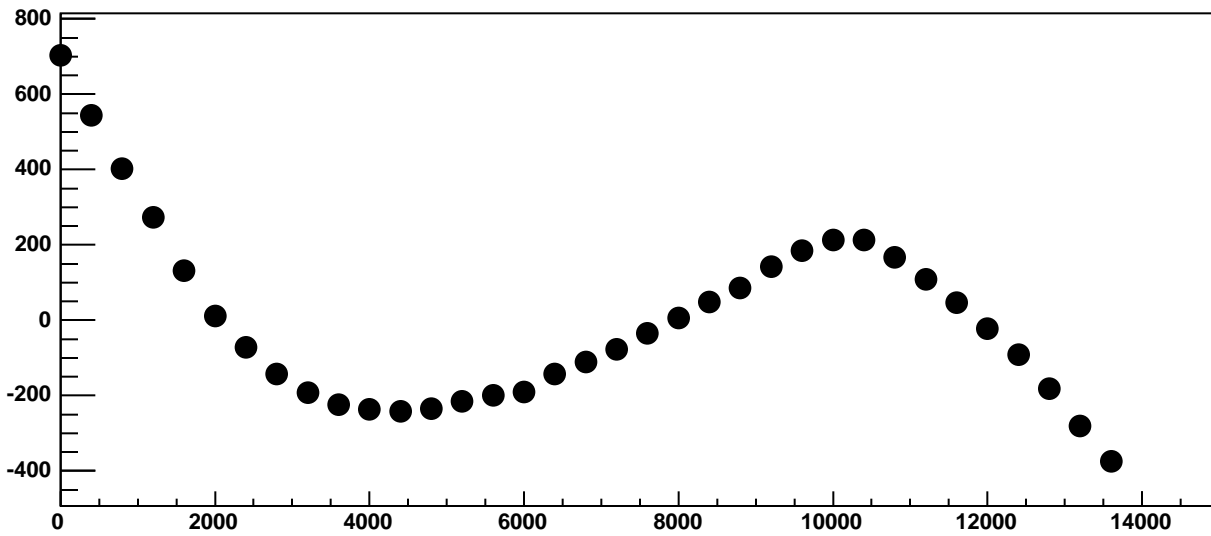
$\chi^2 / \text{ndf}$  5.983e+04 / 23  
p0 -850.6 ± 1.241  
p1 0.3987 ± 0.0002461



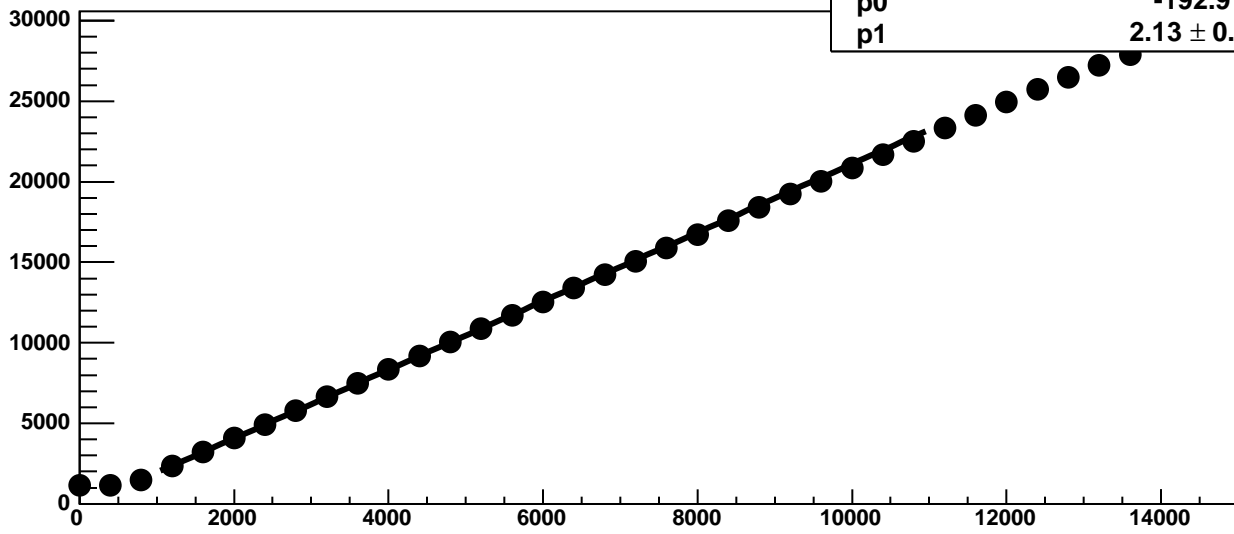
Chip 7, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

432 / 23

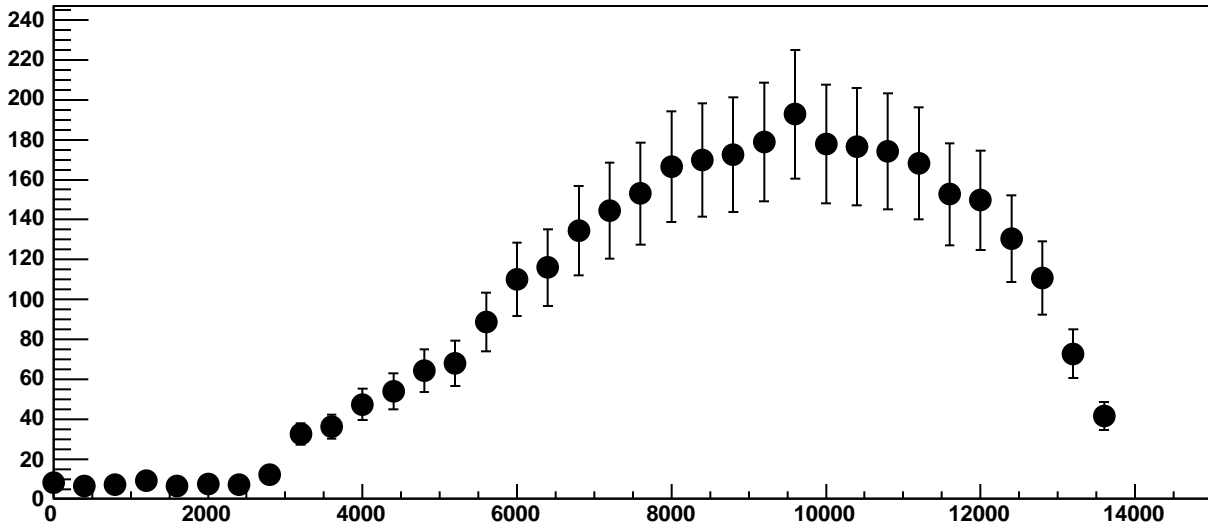
p0

$-192.9 \pm 2.191$

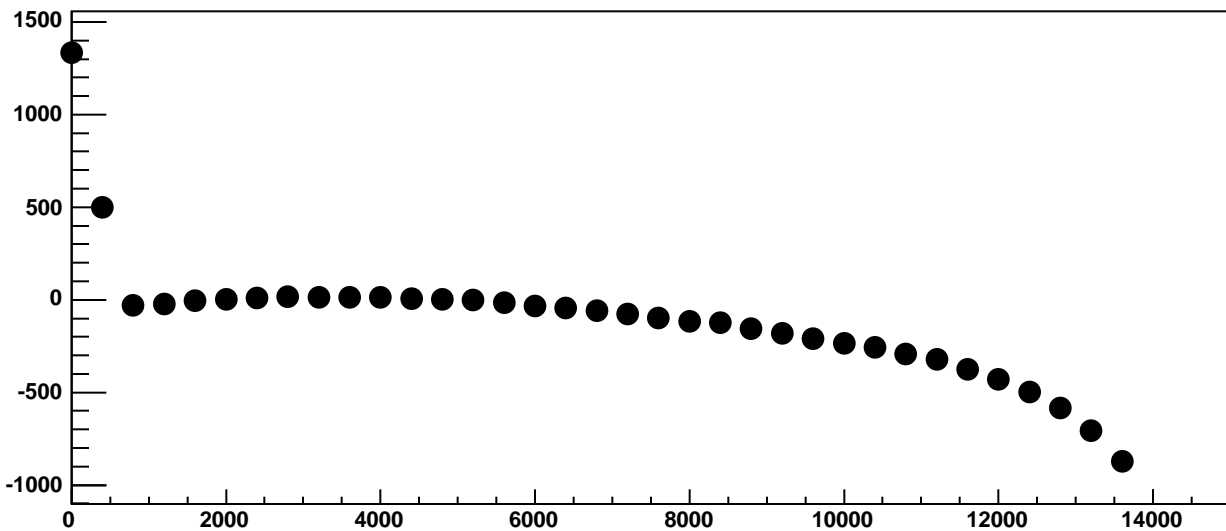
p1

$2.13 \pm 0.001001$

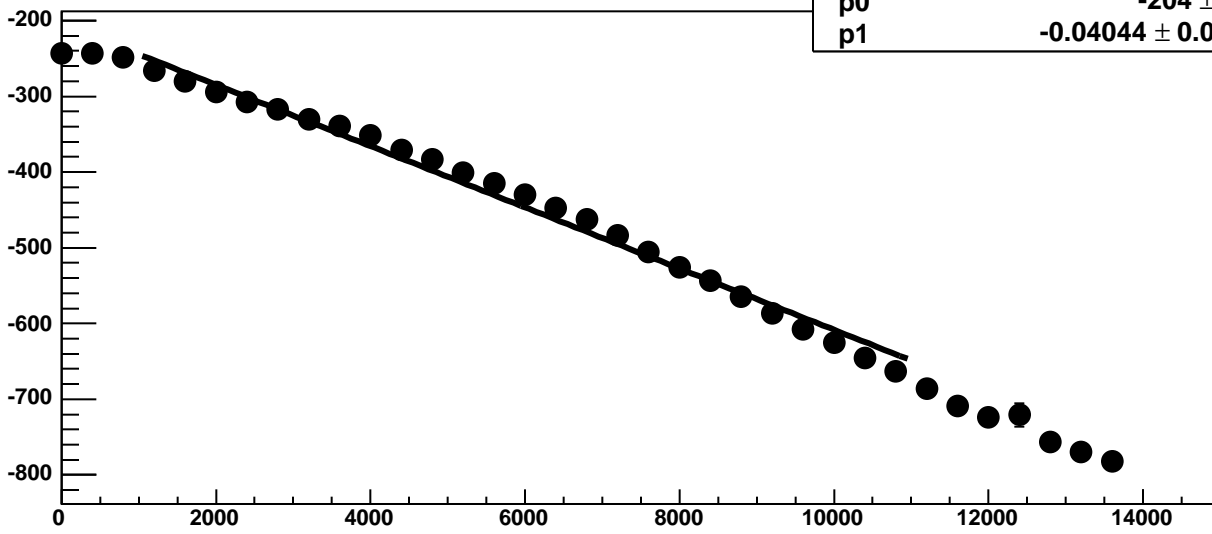
Chip 7, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

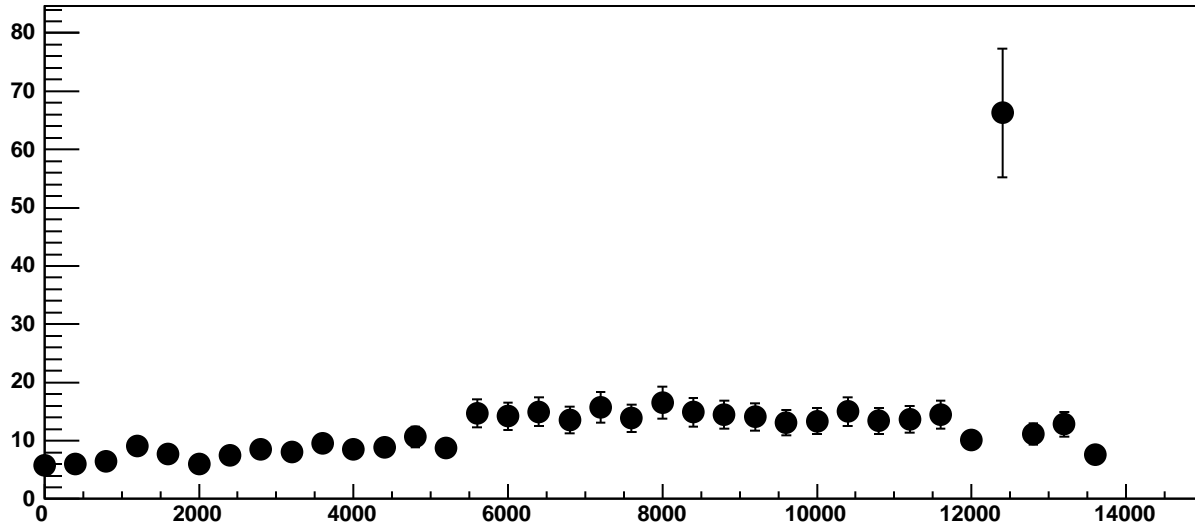


Chip 7, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

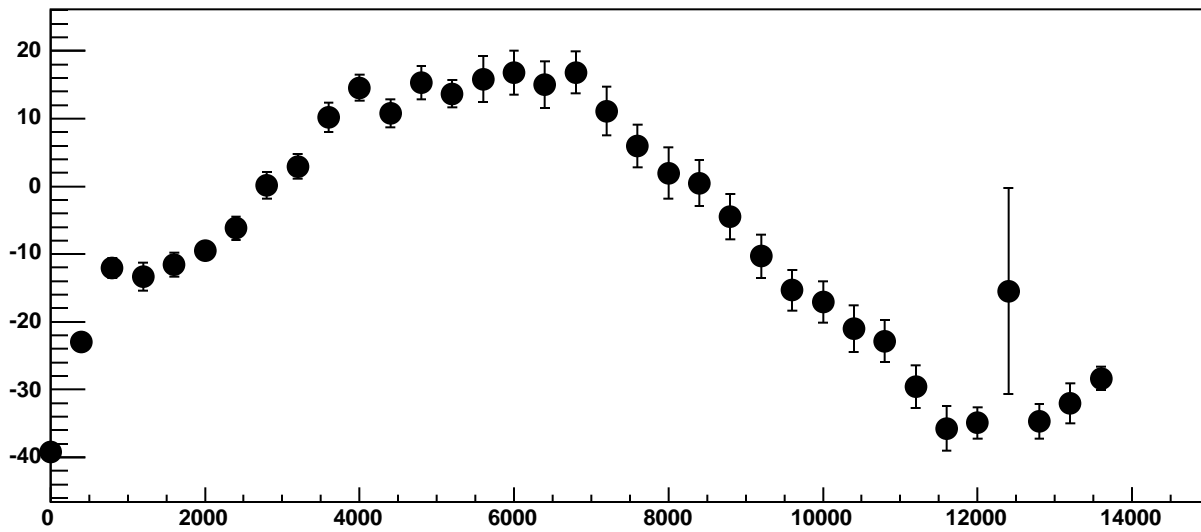


$\chi^2 / \text{ndf}$  608.2 / 23  
p0 -204 ± 0.9196  
p1 -0.04044 ± 0.0001729

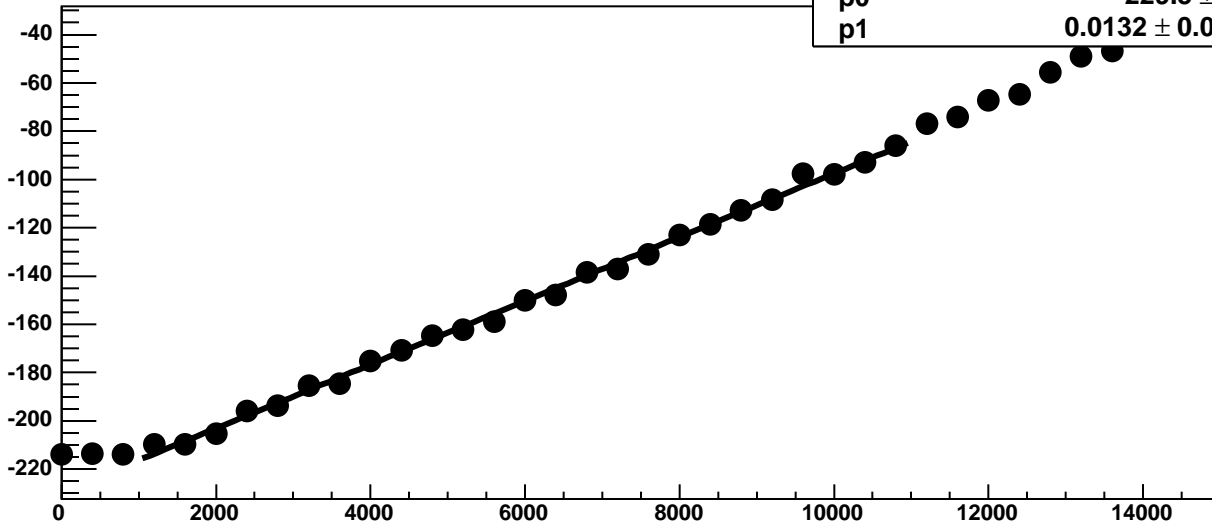
Chip 7, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



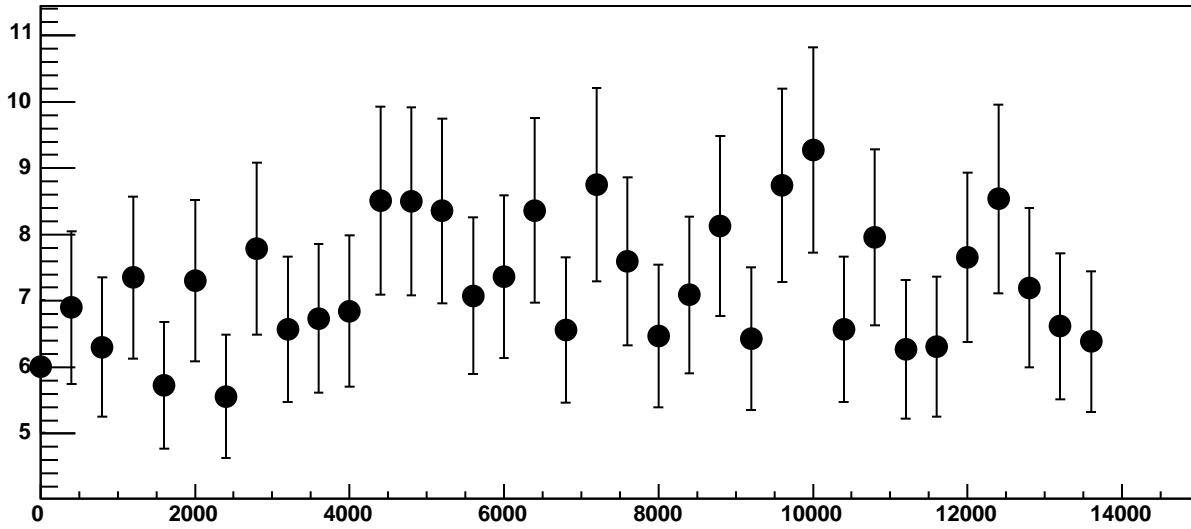
Chip 7, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



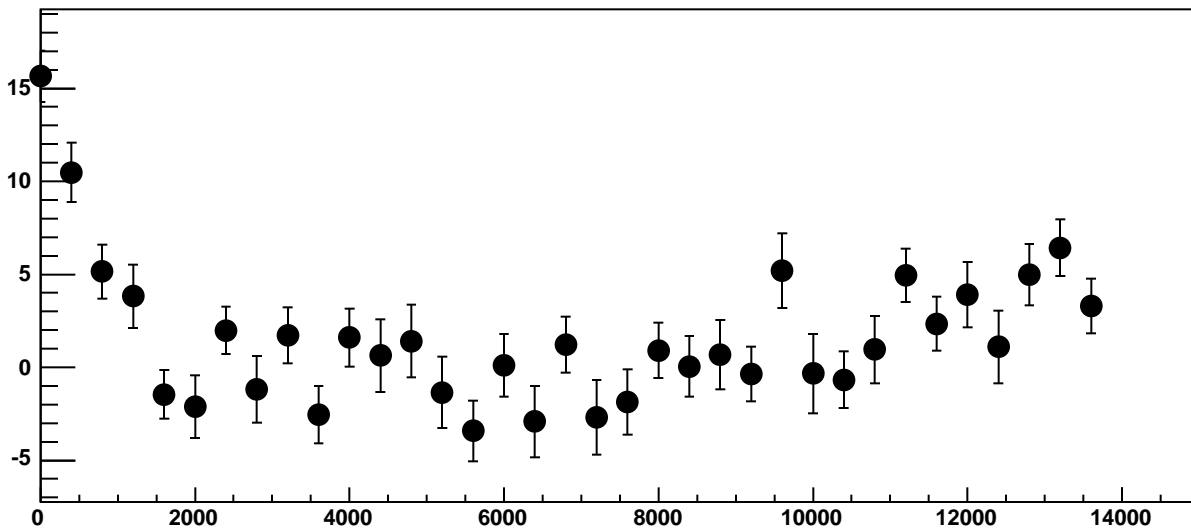
Chip 7, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



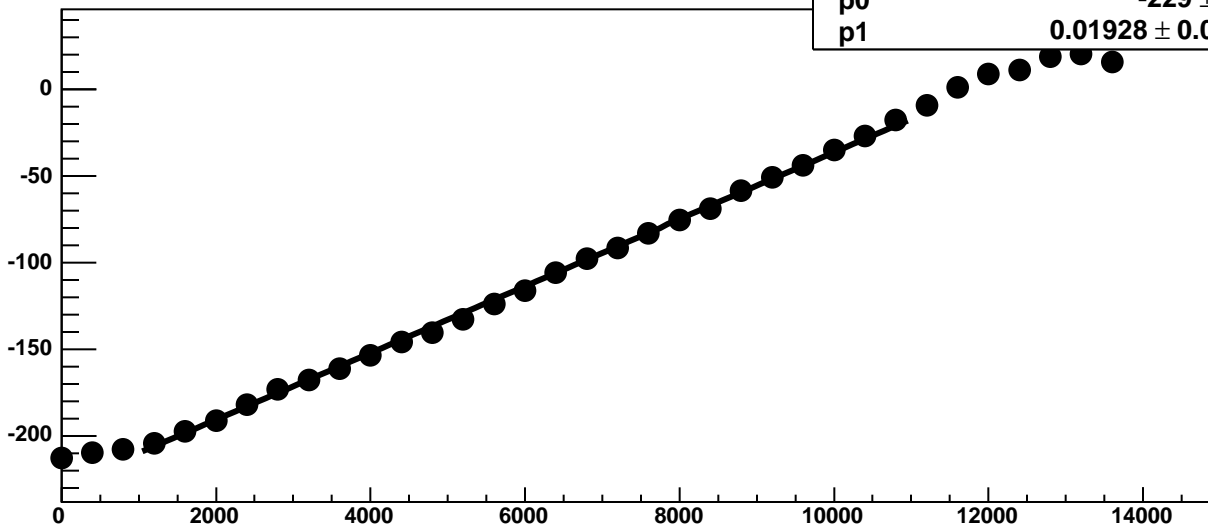
Chip 7, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



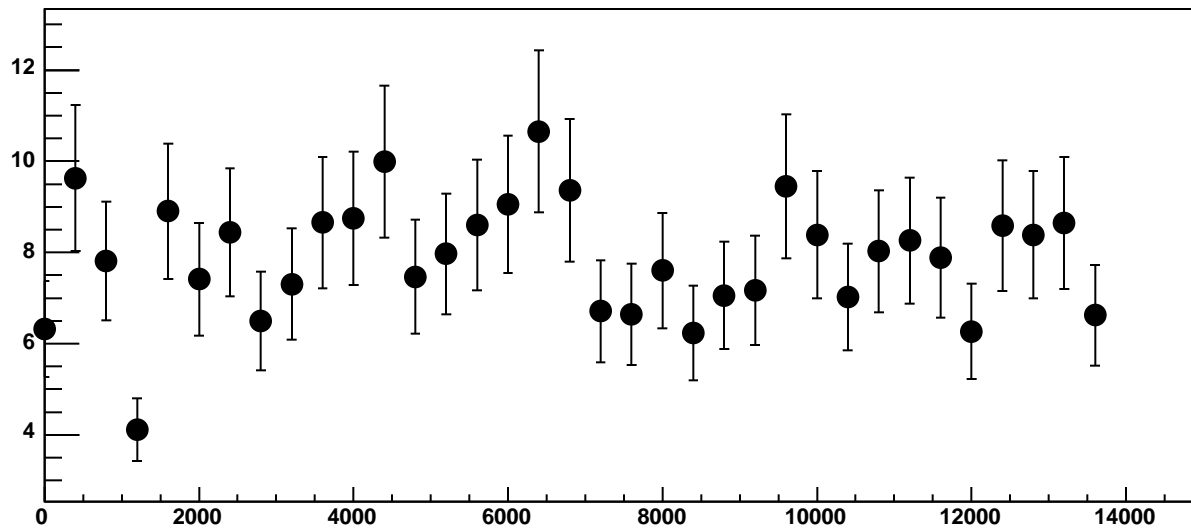
Chip 7, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



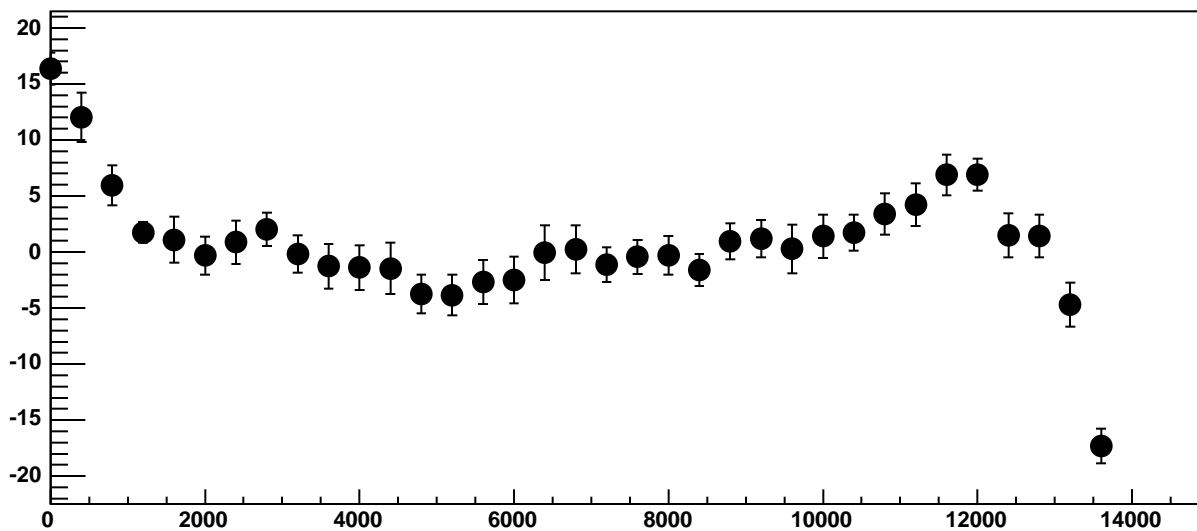
Chip 7, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



Chip 7, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

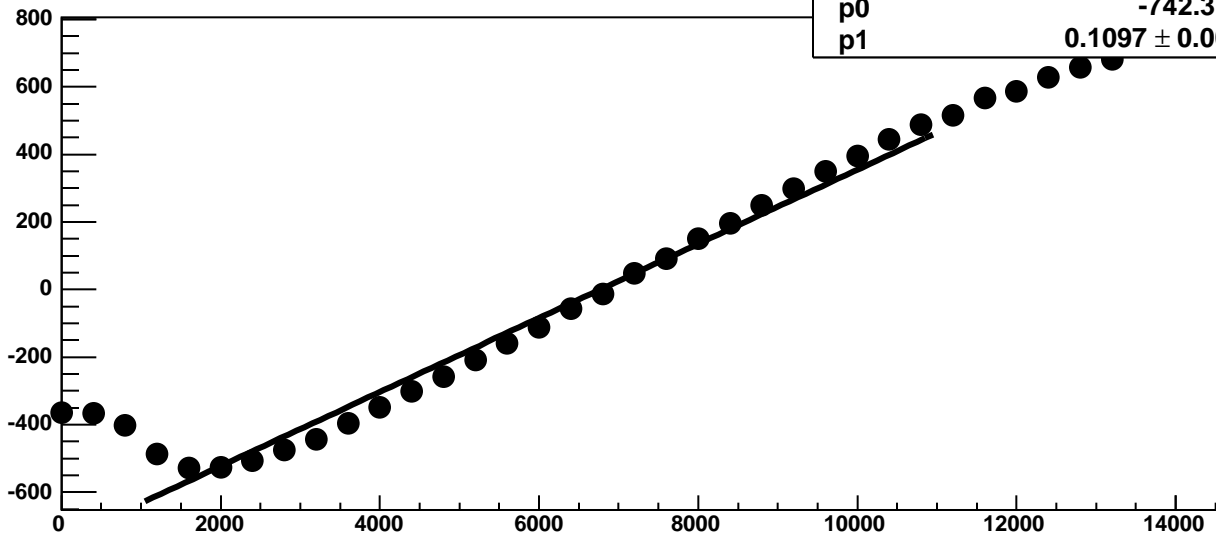


Chip 7, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

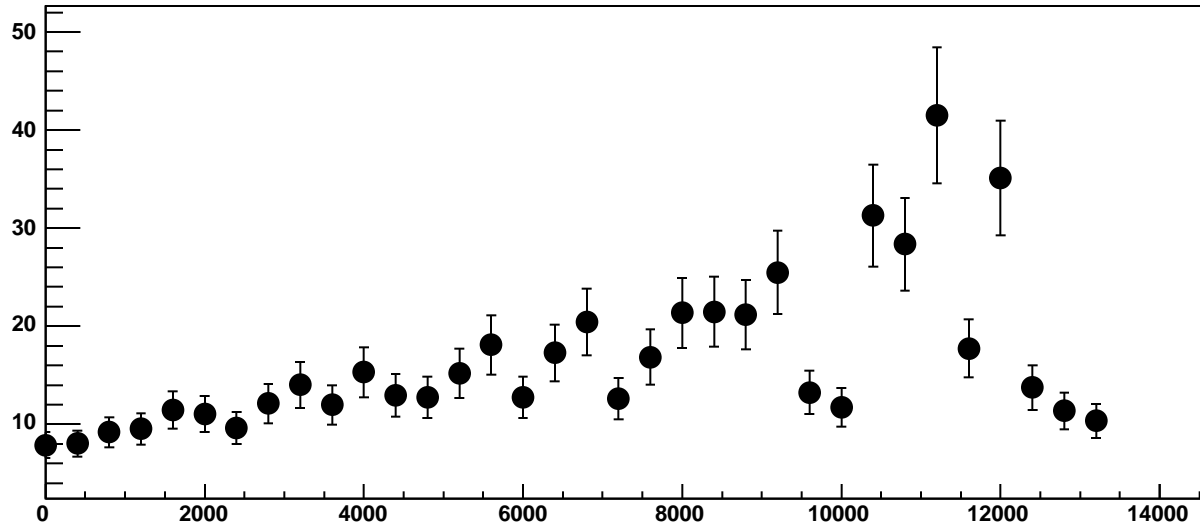




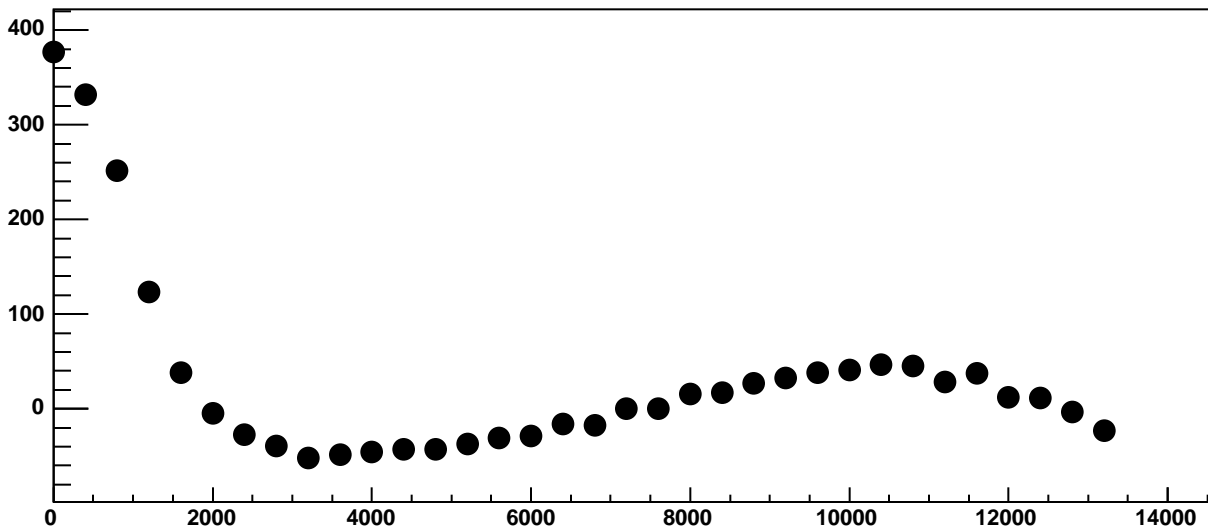
Chip 7, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



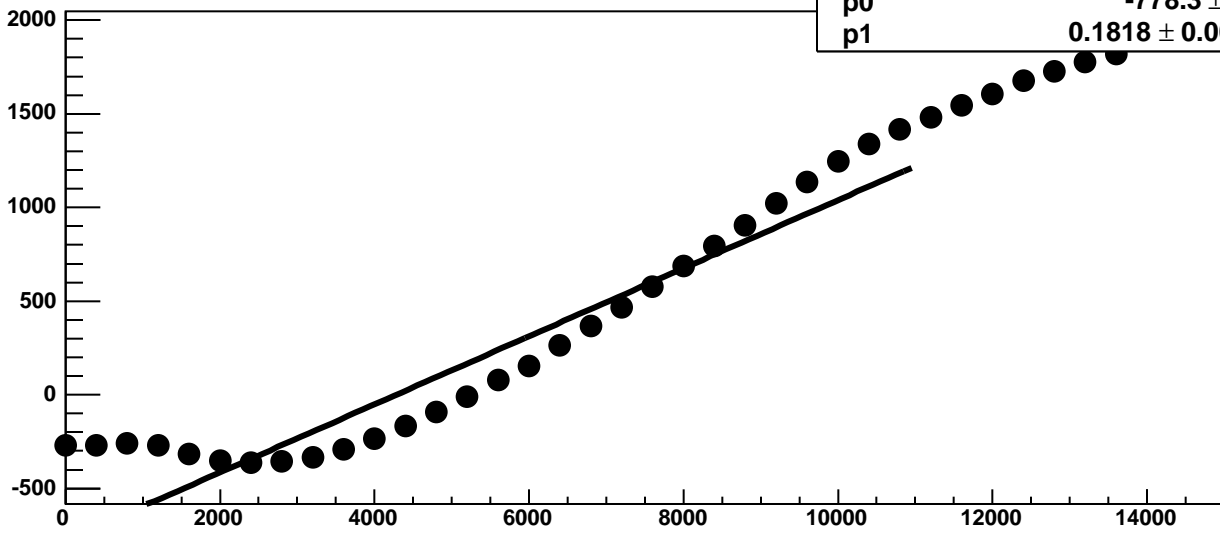
Chip 7, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

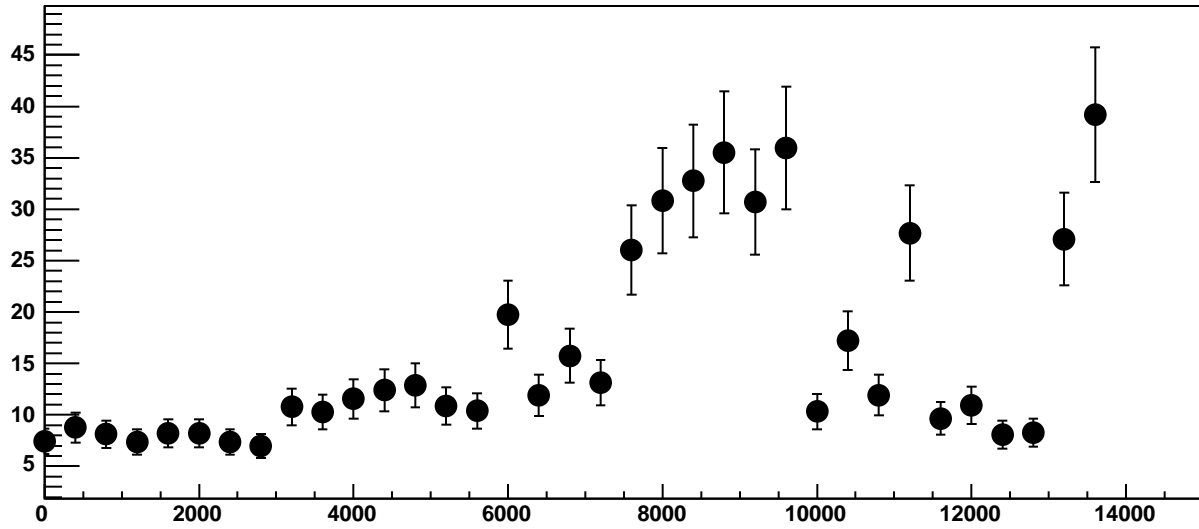


Chip 7, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

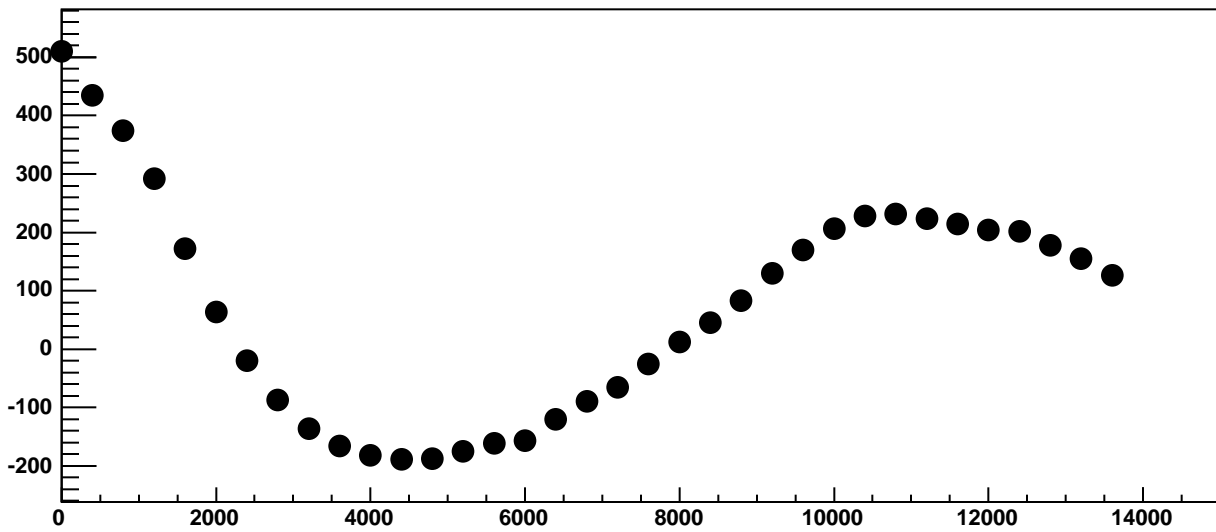


$\chi^2 / \text{ndf}$  9.643e+04 / 23  
p0 -778.3 ± 0.9701  
p1 0.1818 ± 0.0001895

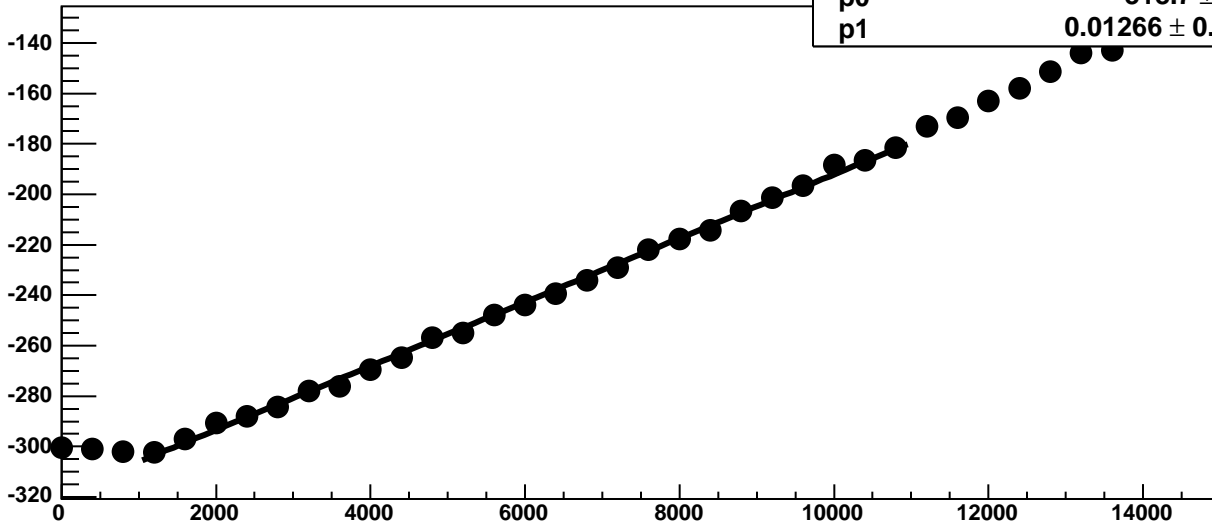
Chip 7, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

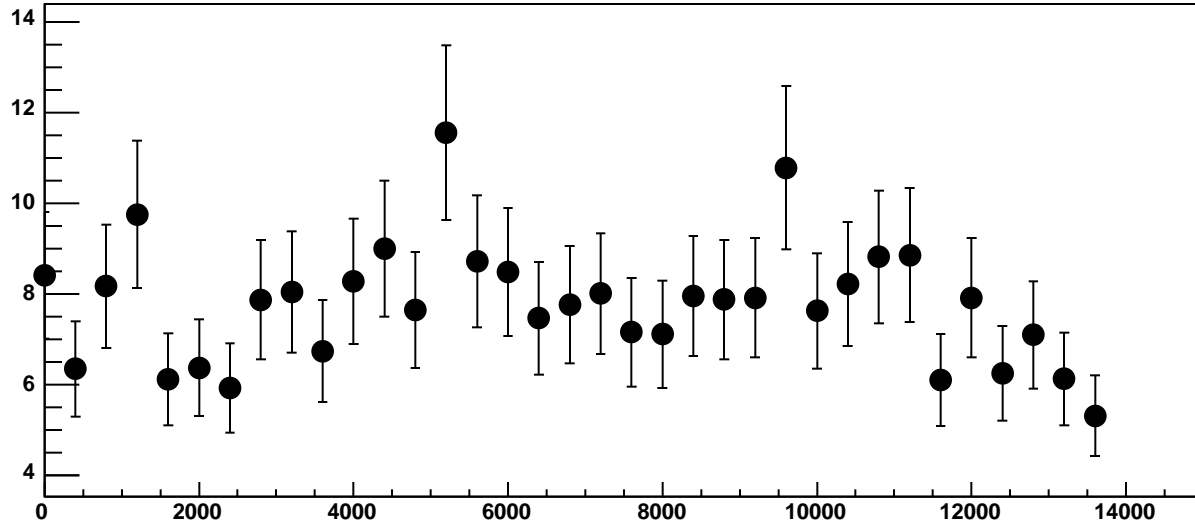


Chip 7, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

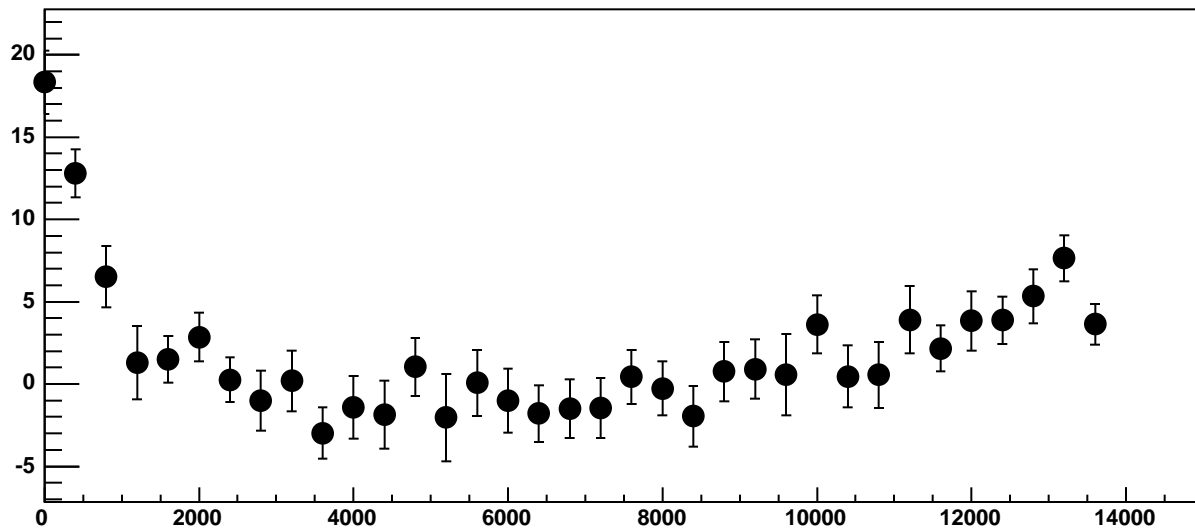


$\chi^2 / \text{ndf}$  20.51 / 23  
p0  $-318.7 \pm 0.7809$   
p1  $0.01266 \pm 0.000122$

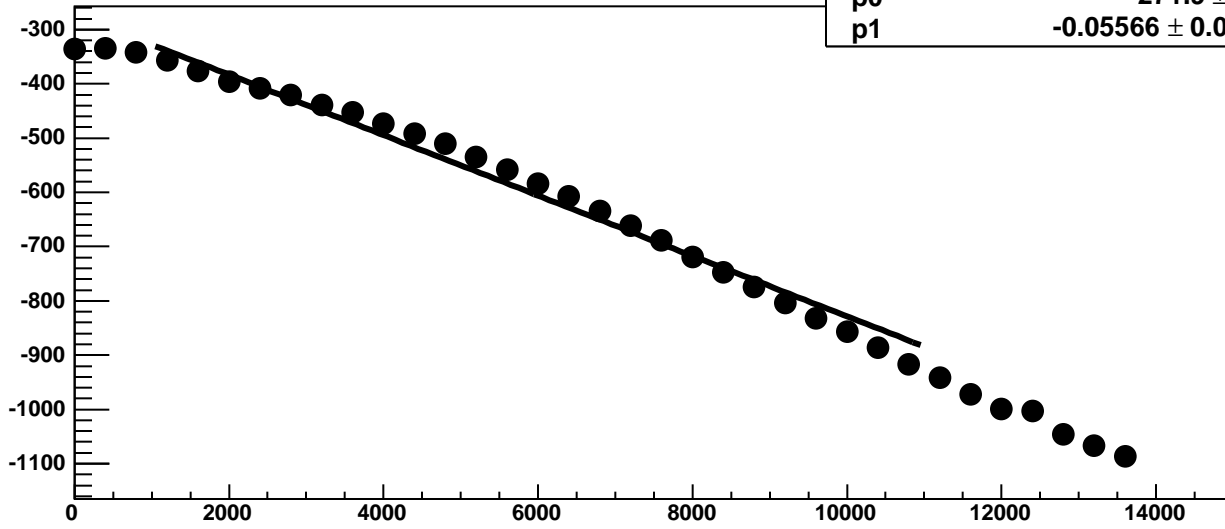
Chip 7, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 7, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1515 / 23

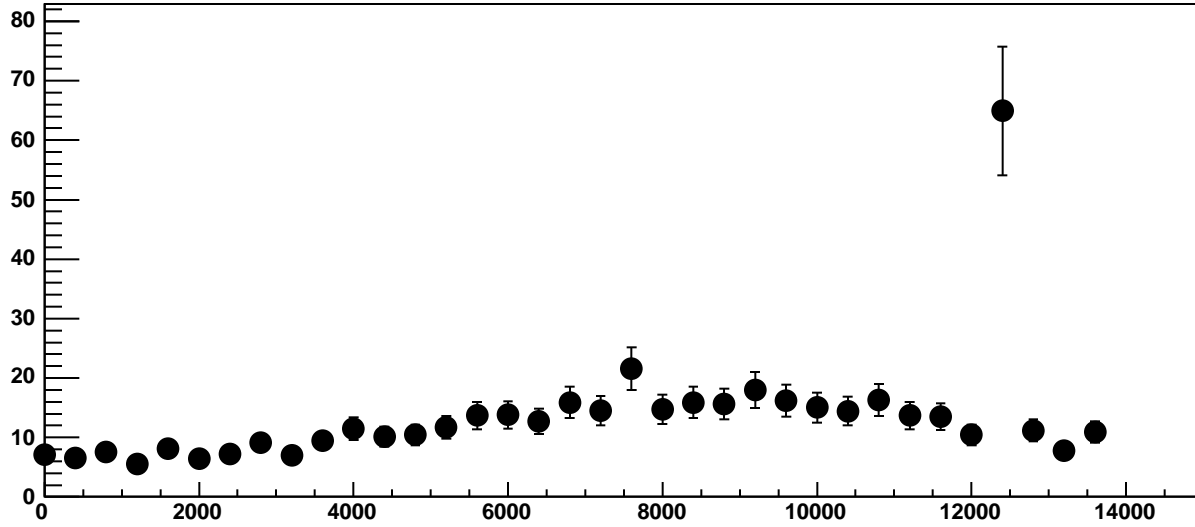
p0

$-271.9 \pm 0.8672$

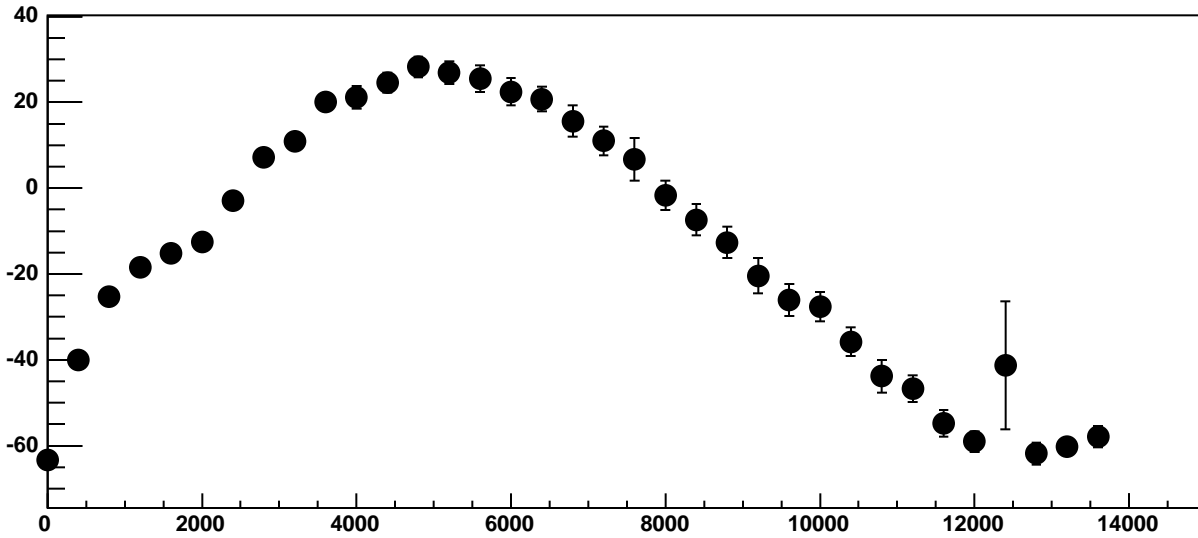
p1

$-0.05566 \pm 0.0001764$

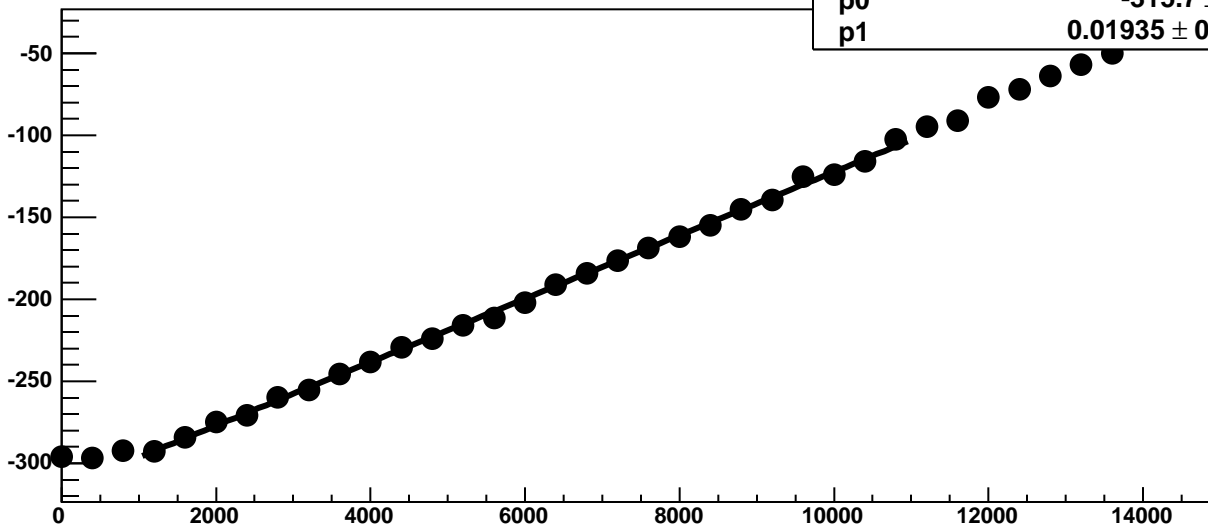
Chip 7, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

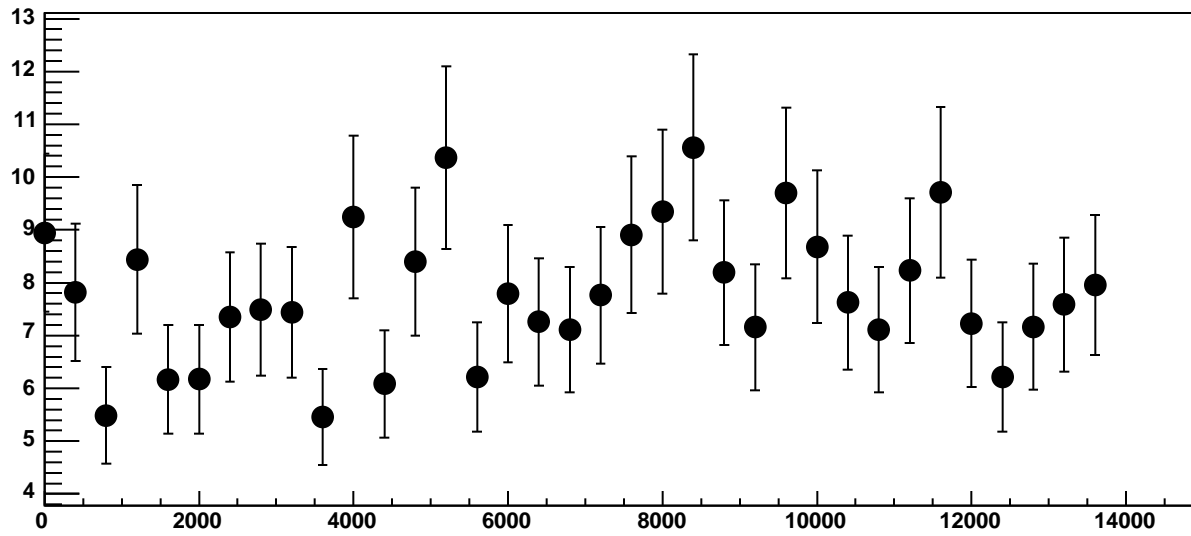


Chip 7, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

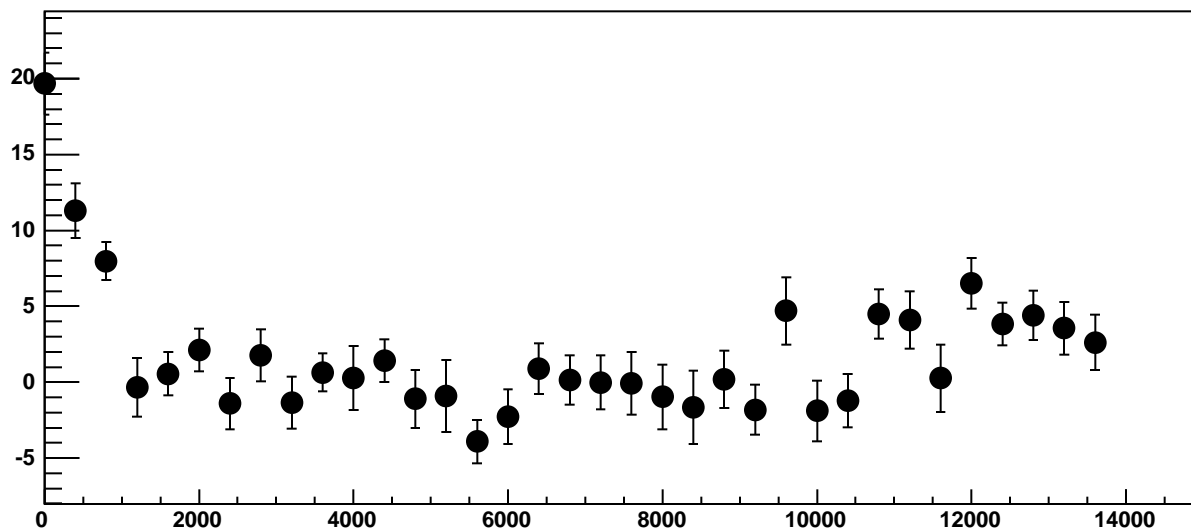


$\chi^2 / \text{ndf}$  31.27 / 23  
p0  $-315.7 \pm 0.7521$   
p1  $0.01935 \pm 0.000119$

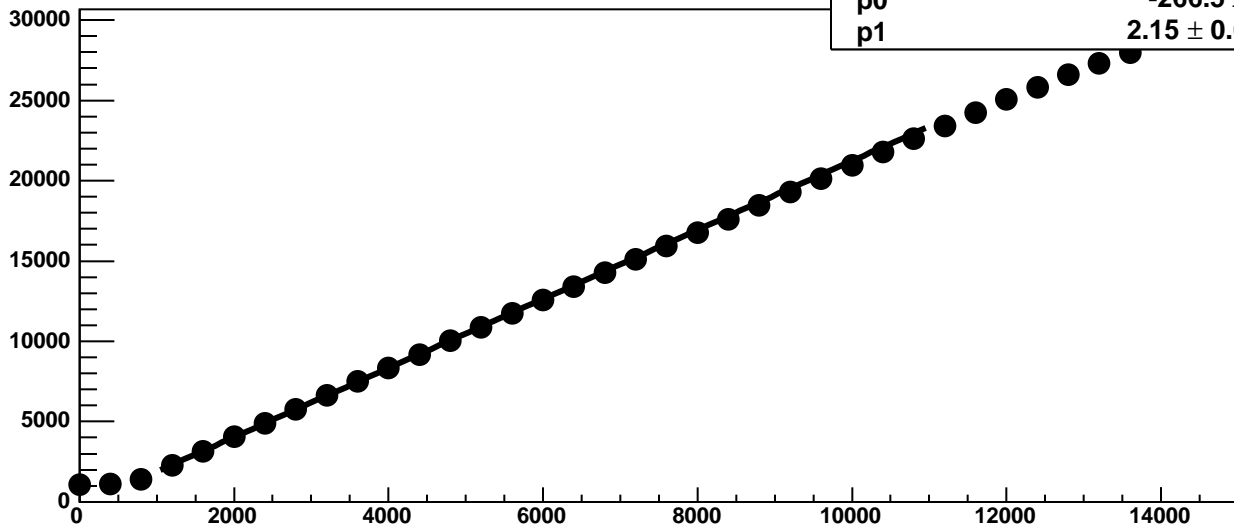
Chip 7, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



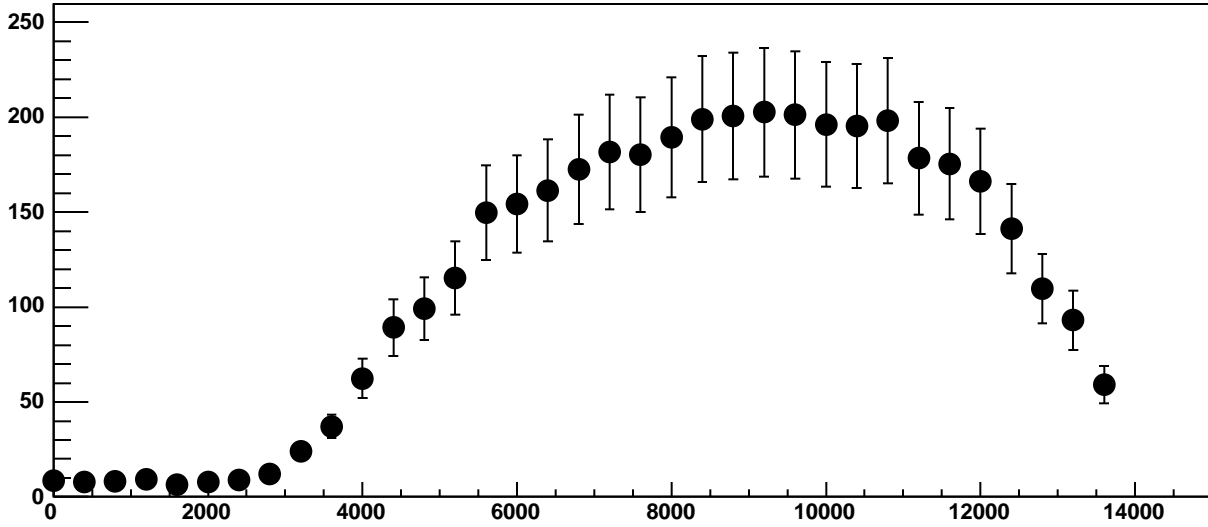
Chip 7, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



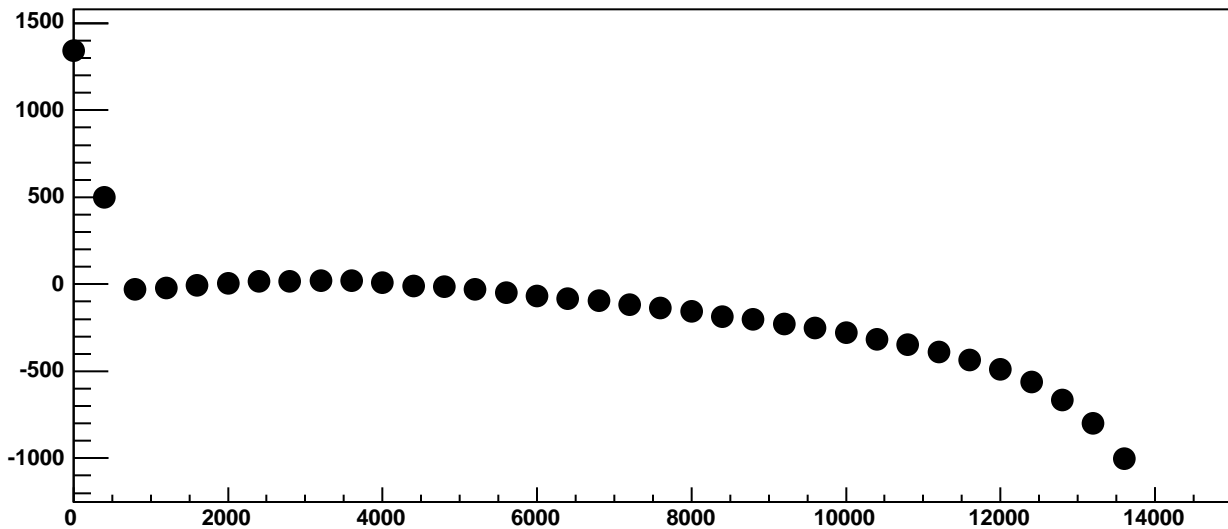
Chip 7, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC



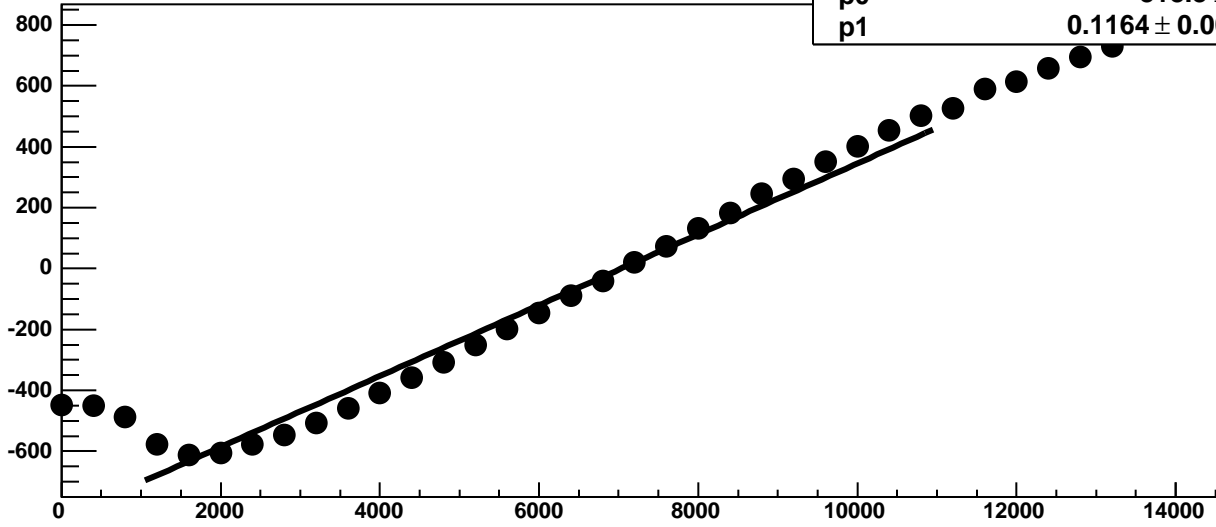
Chip 7, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



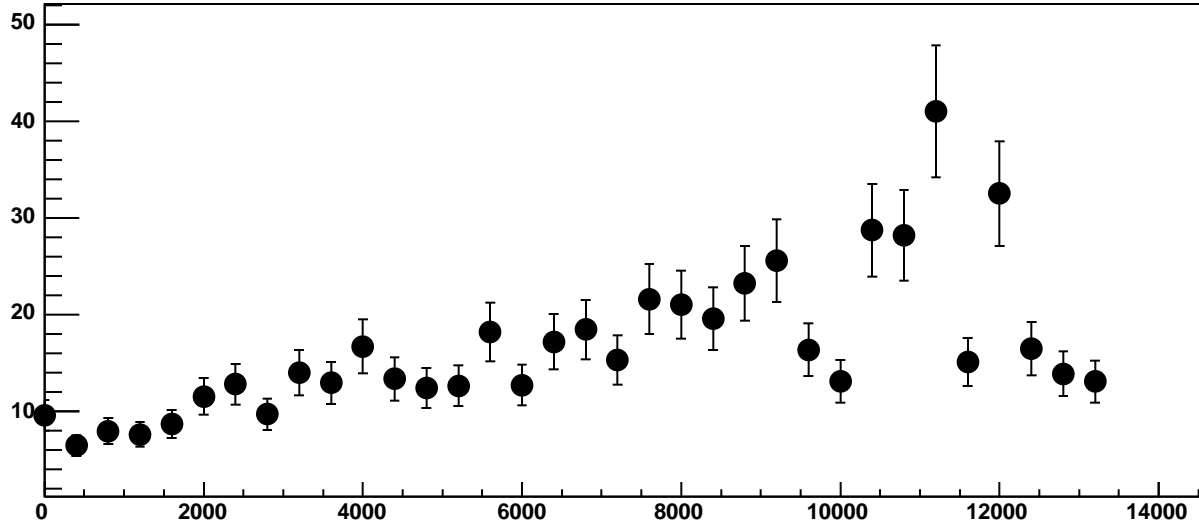
Chip 7, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



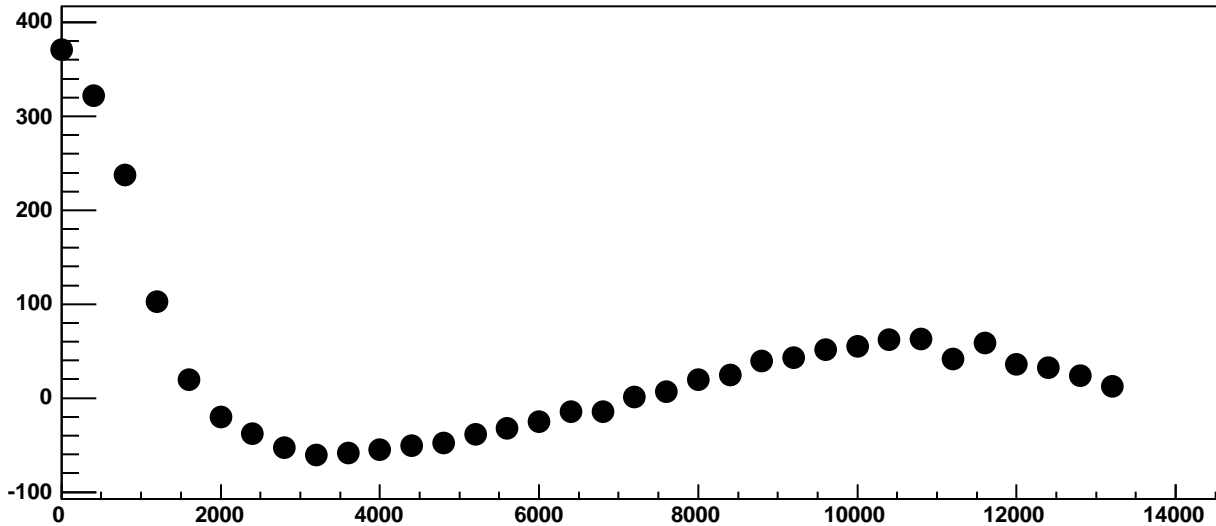
Chip 7, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



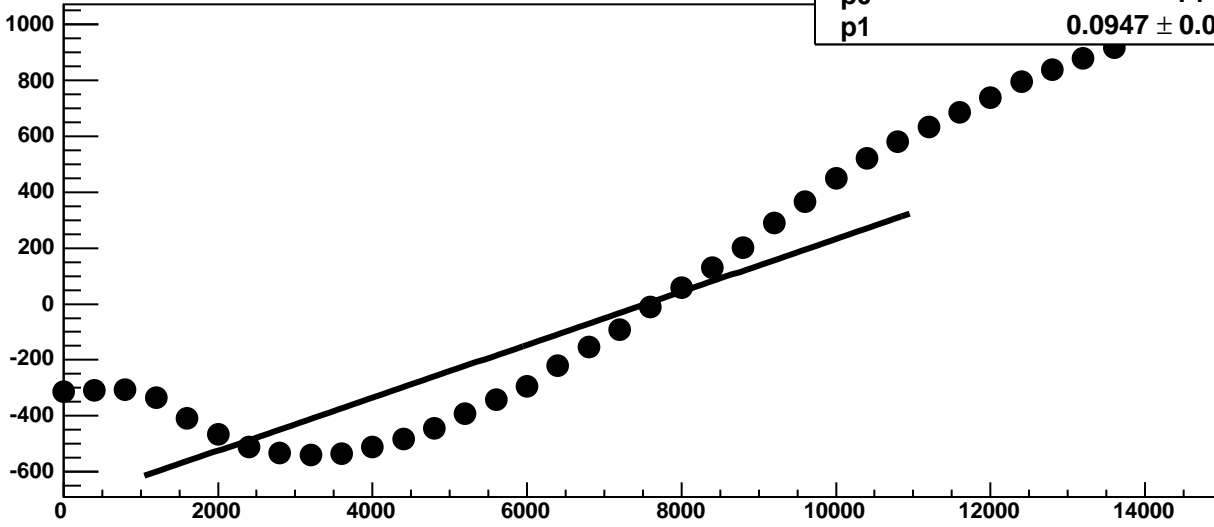
Chip 7, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



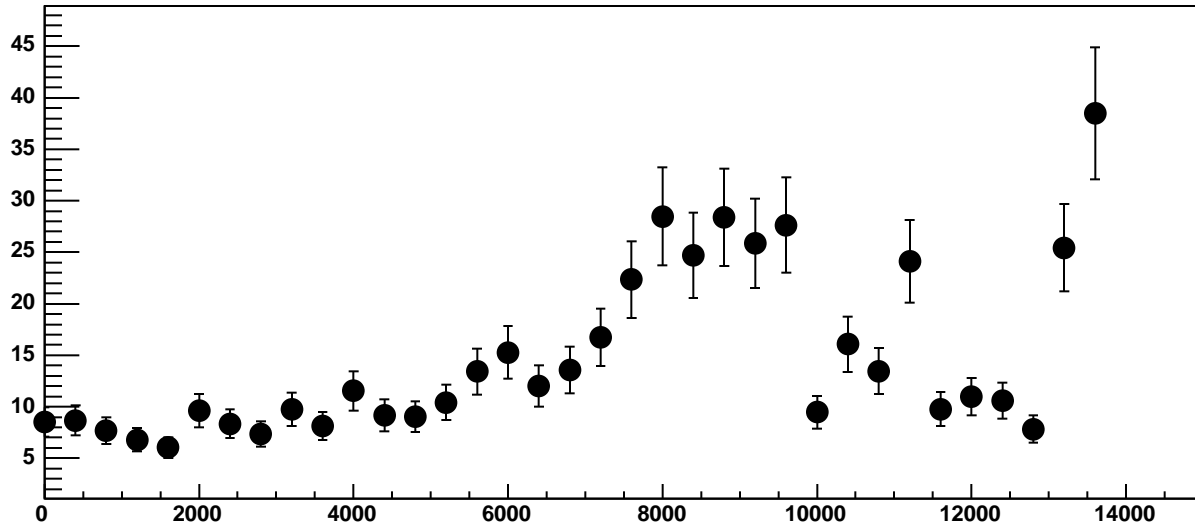
Chip 7, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



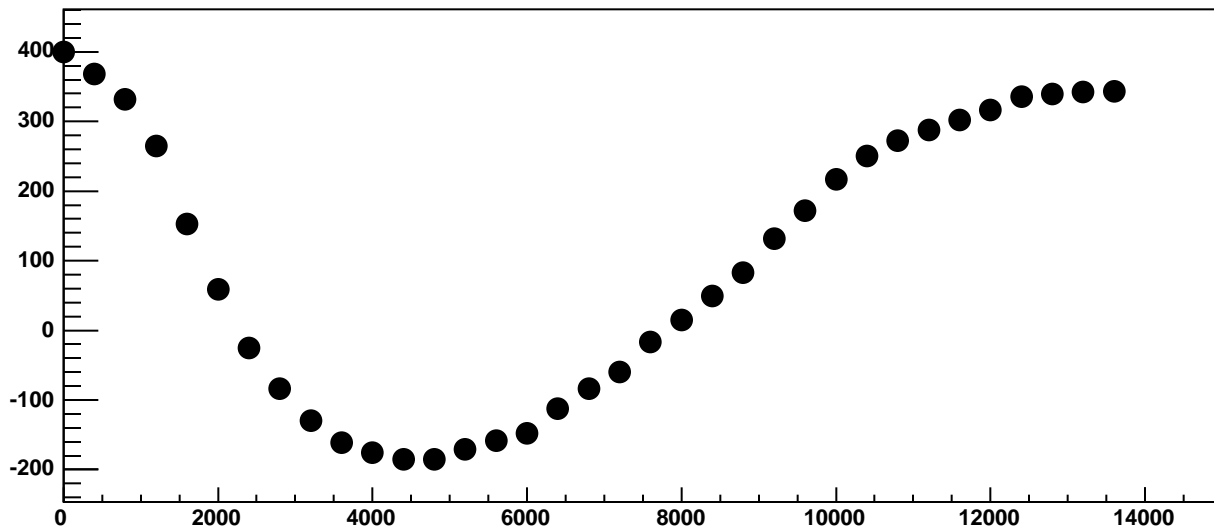
Chip 7, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



Chip 7, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

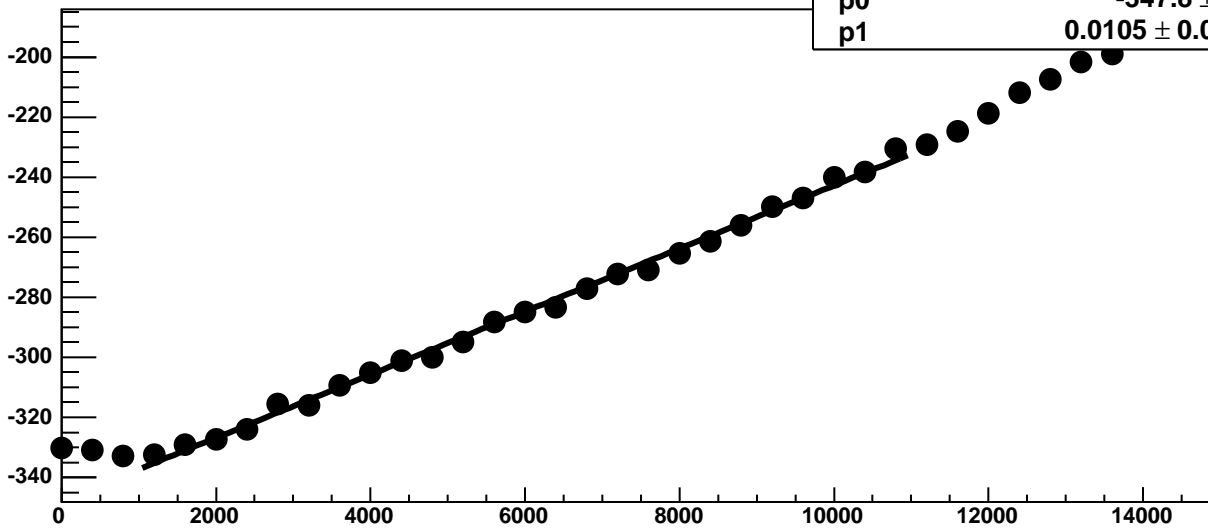


Chip 7, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



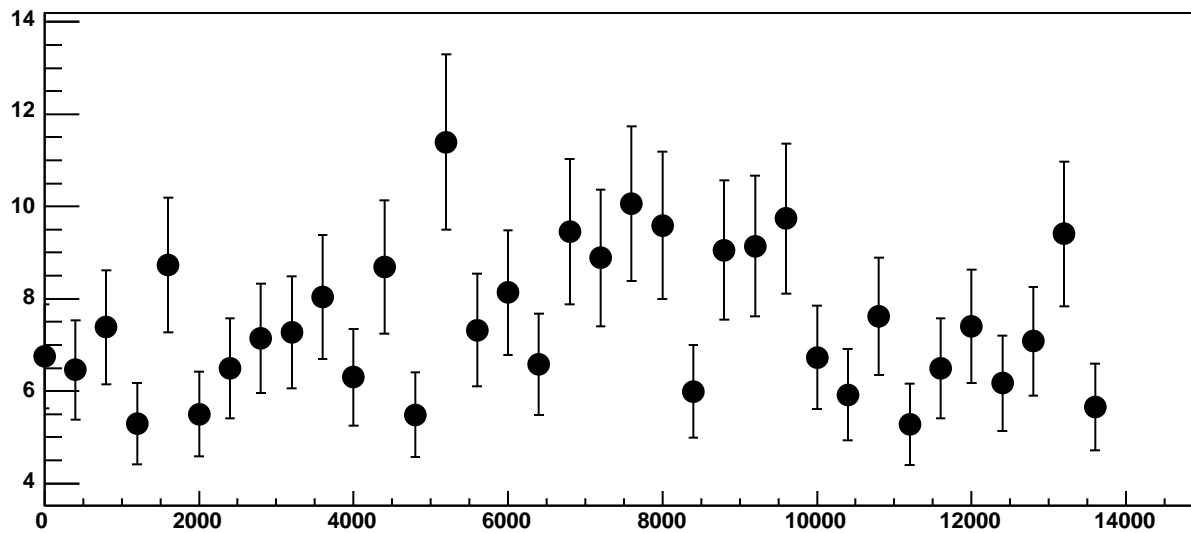


Chip 7, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

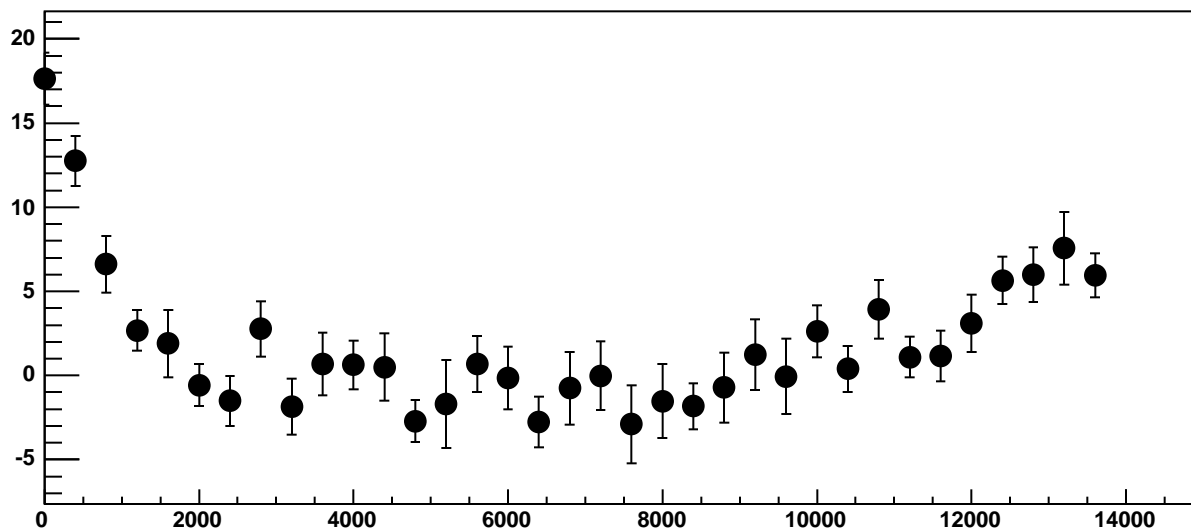


$\chi^2 / \text{ndf}$  32.47 / 23  
p0  $-347.8 \pm 0.7003$   
p1  $0.0105 \pm 0.0001095$

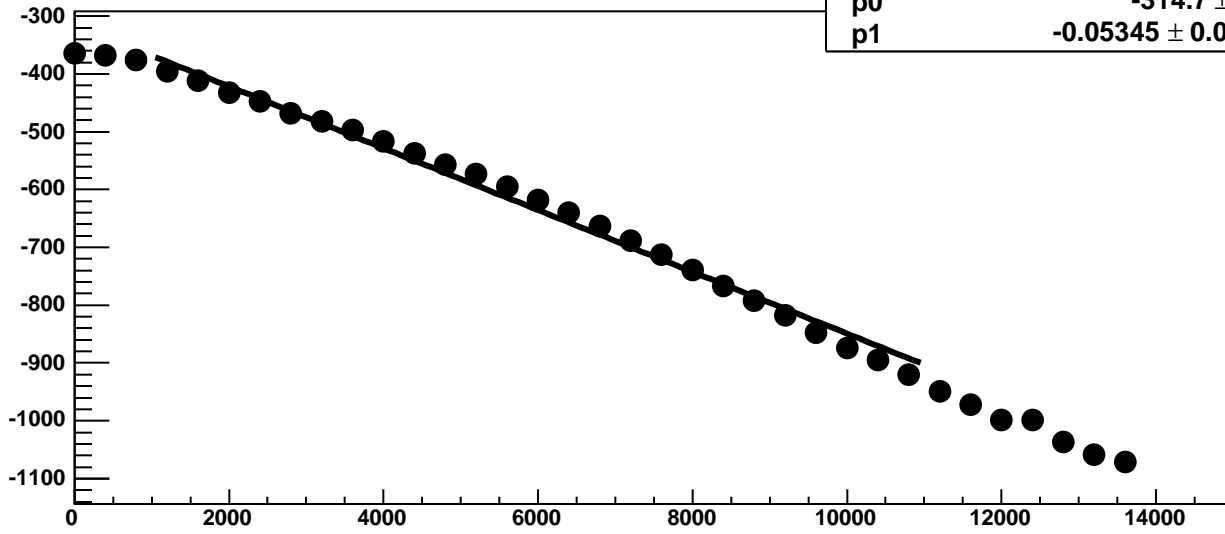
Chip 7, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

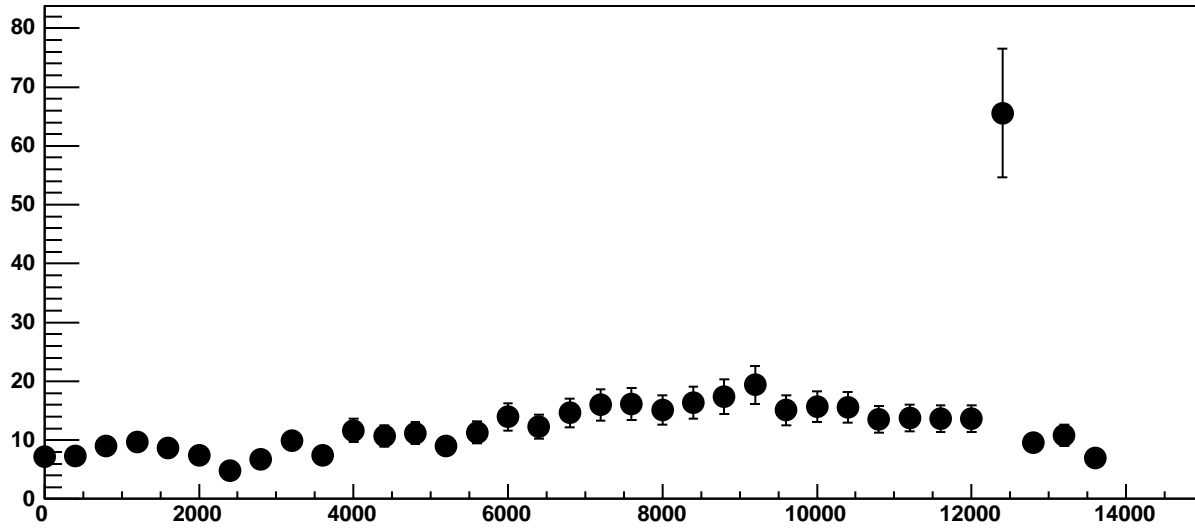


Chip 7, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

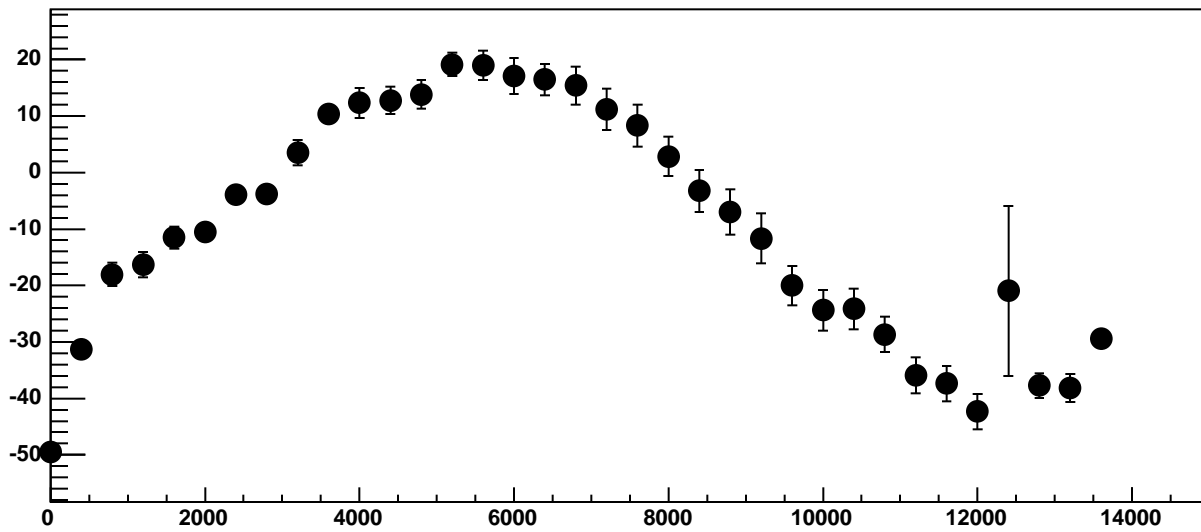


$\chi^2 / \text{ndf}$	722.1 / 23
p0	$-314.7 \pm 0.9116$
p1	$-0.05345 \pm 0.0001819$

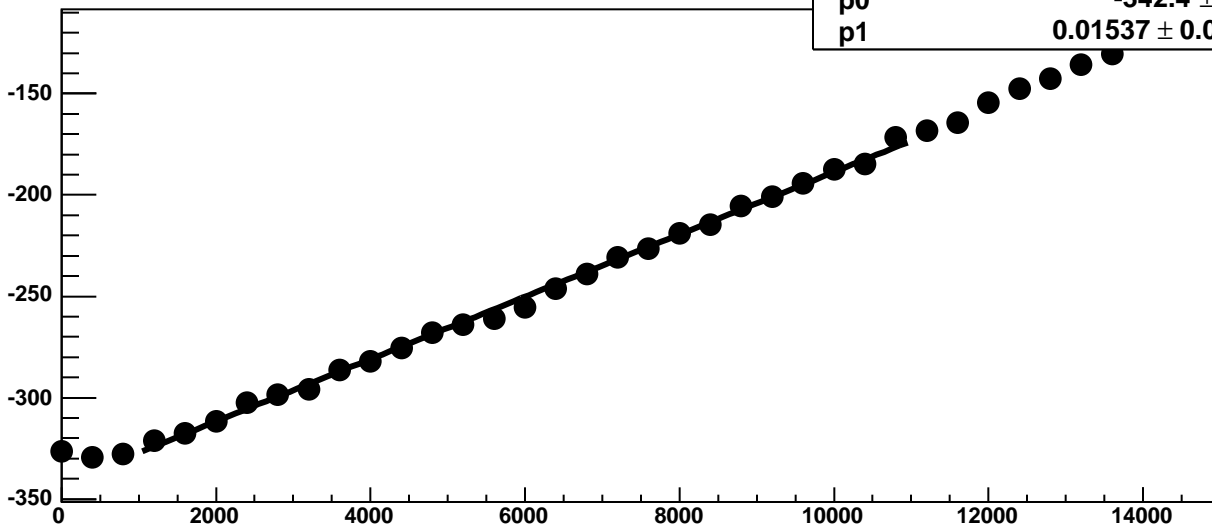
Chip 7, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

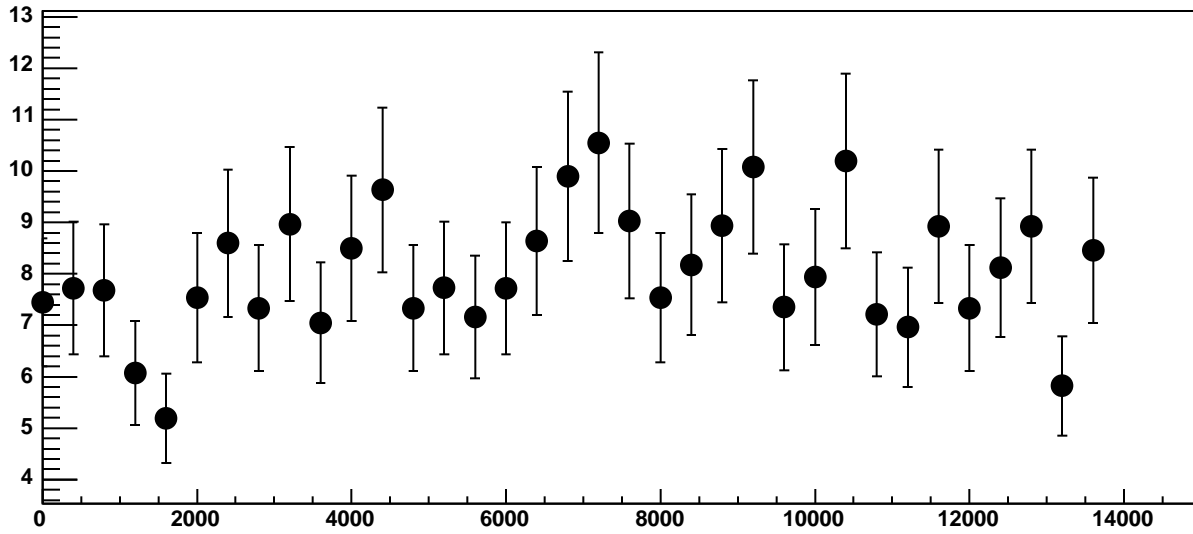


Chip 7, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

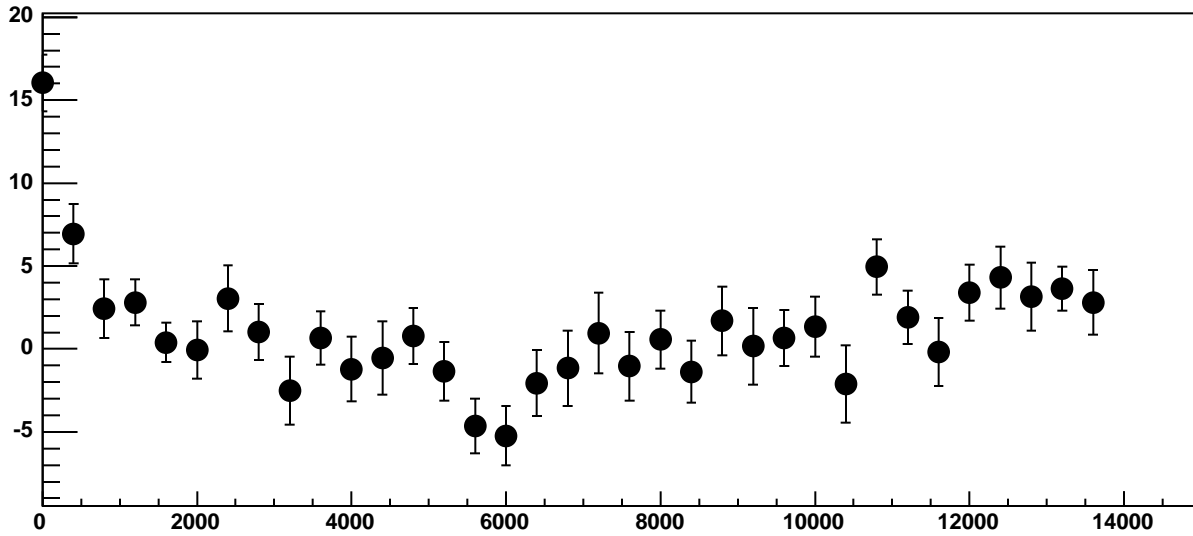


$\chi^2 / \text{ndf}$  40.13 / 23  
p0  $-342.4 \pm 0.7447$   
p1  $0.01537 \pm 0.0001182$

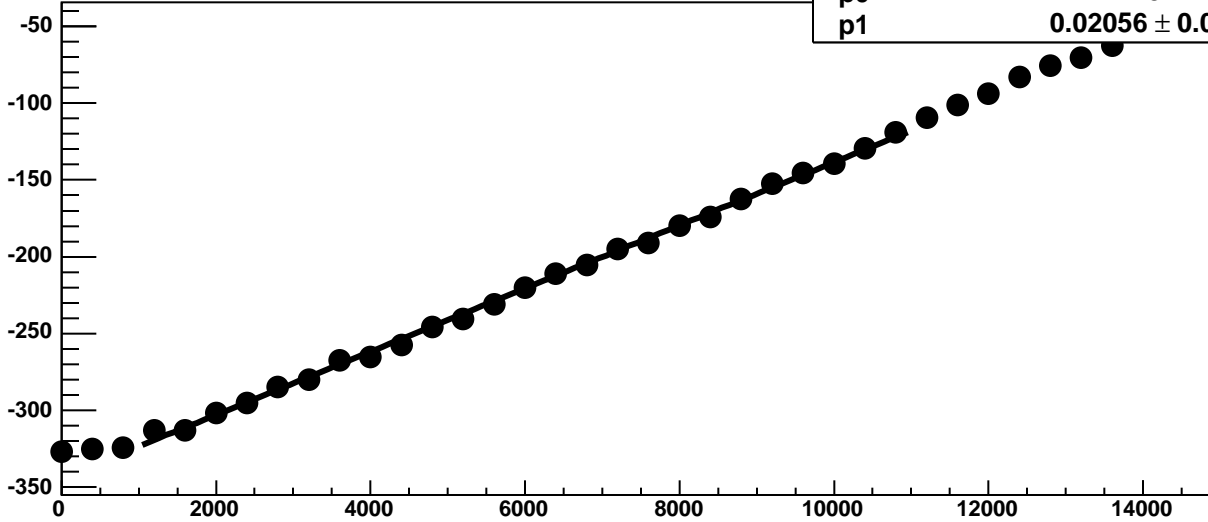
Chip 7, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

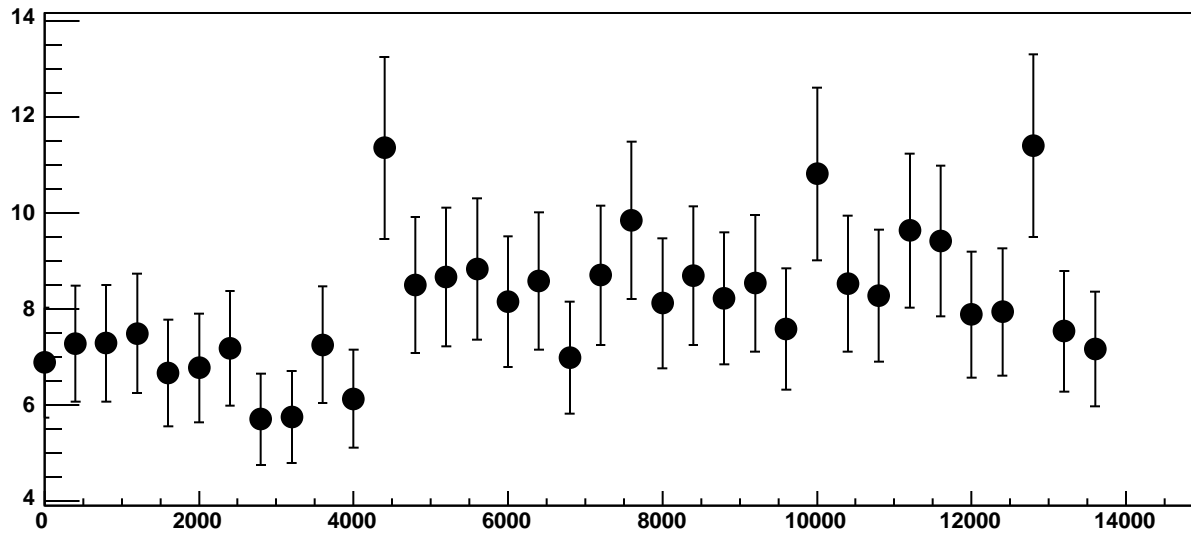


Chip 7, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

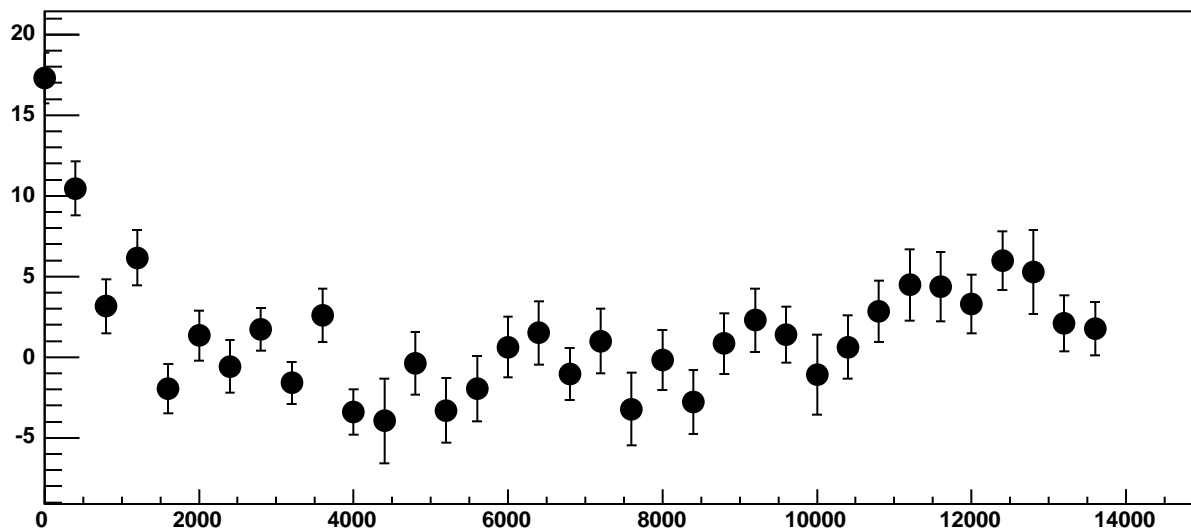


$\chi^2 / \text{ndf}$  42.94 / 23  
p0  $-344 \pm 0.7506$   
p1  $0.02056 \pm 0.0001219$

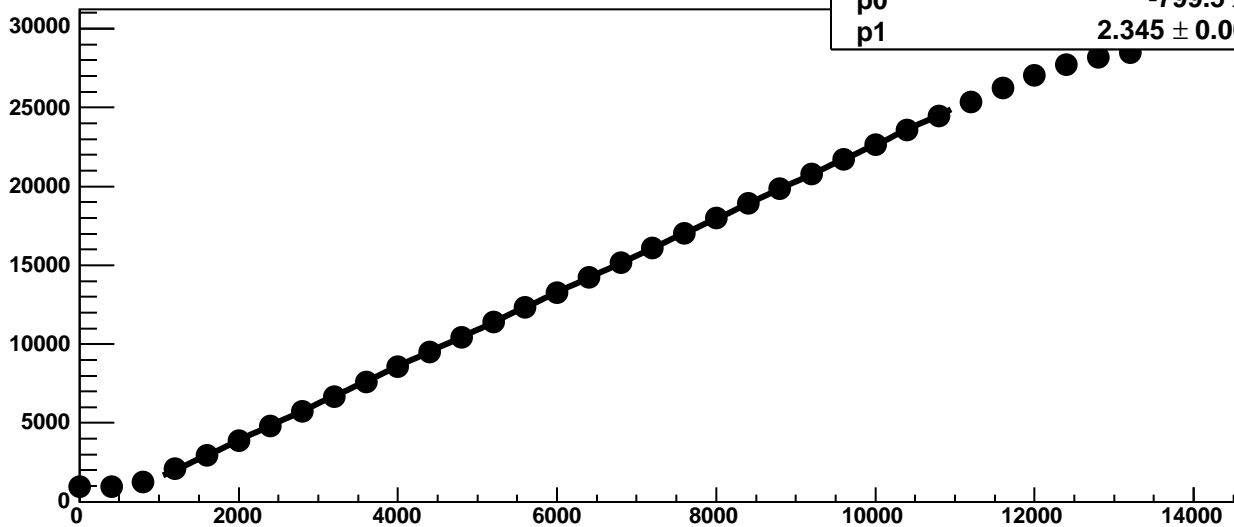
Chip 7, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 7, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

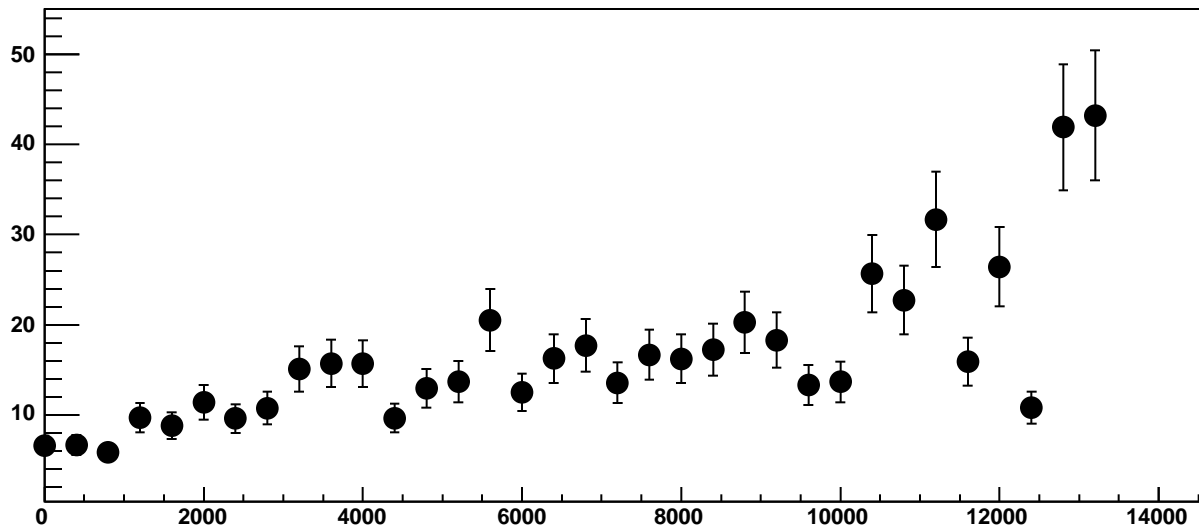


Chip 7, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

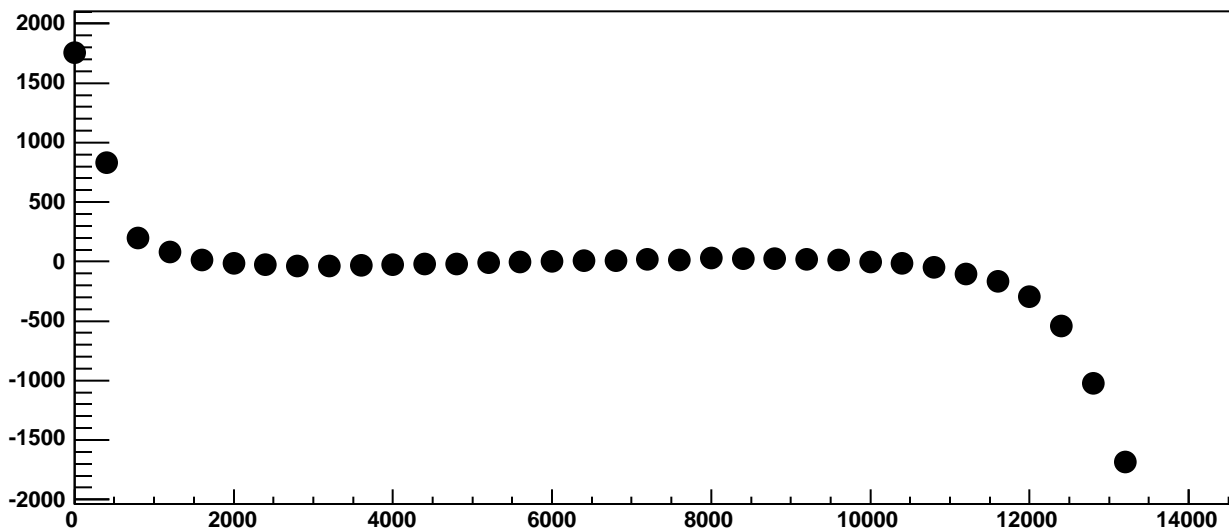


$\chi^2 / \text{ndf}$  2393 / 23  
p0  $-799.5 \pm 1.222$   
p1  $2.345 \pm 0.0002176$

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

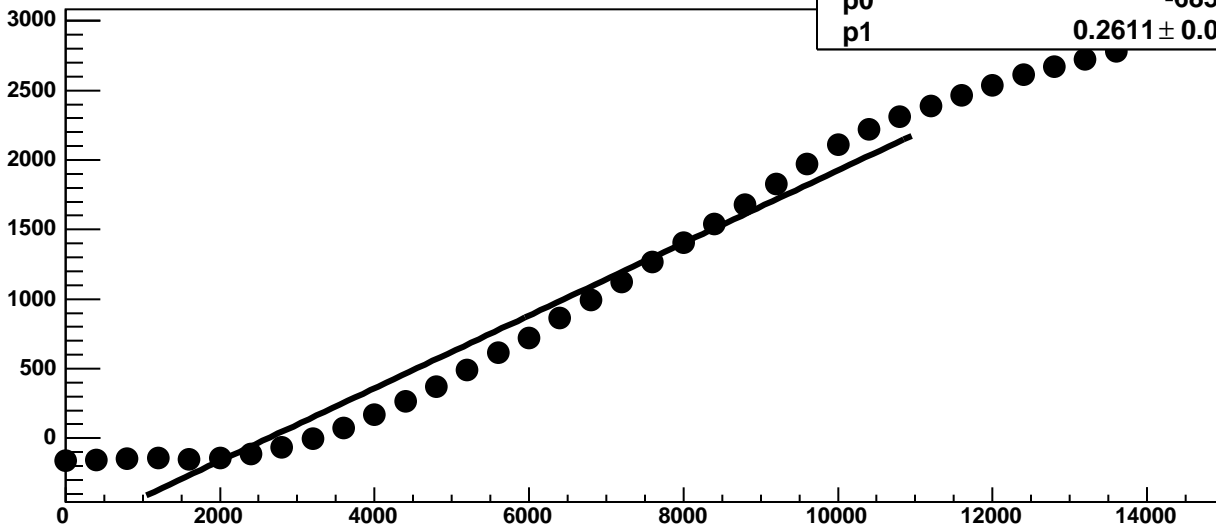


Chip 7, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

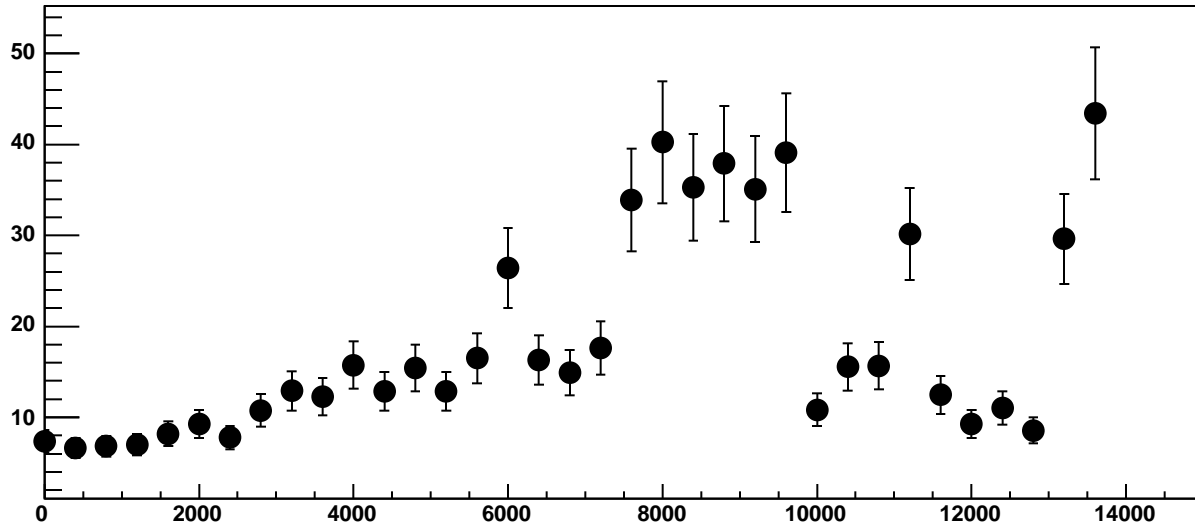


Chip 7, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

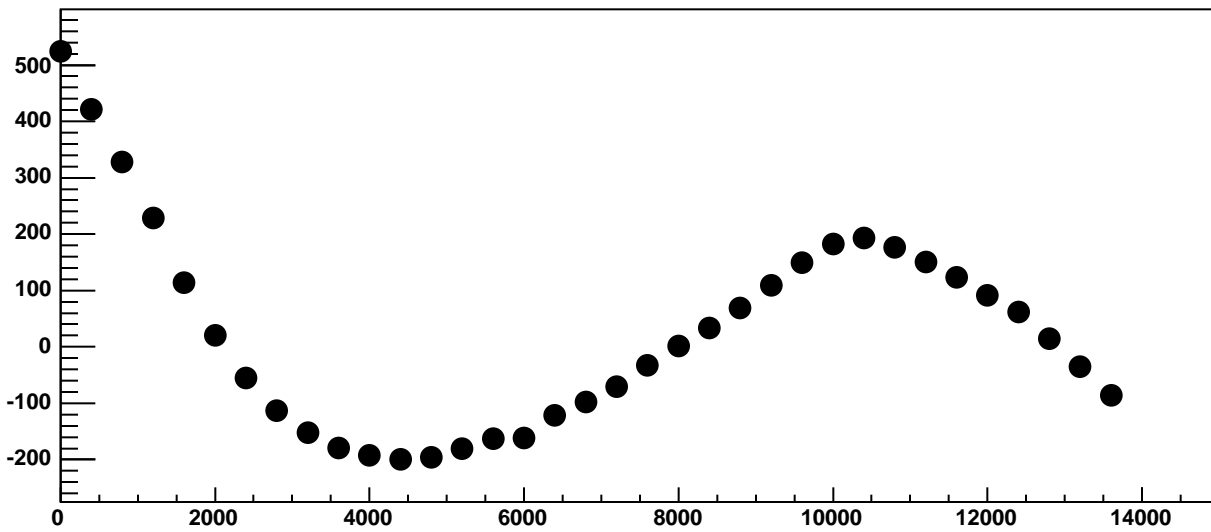
$\chi^2 / \text{ndf}$  6.397e+04 / 23  
p0 -685 ± 1.05  
p1 0.2611 ± 0.0002072



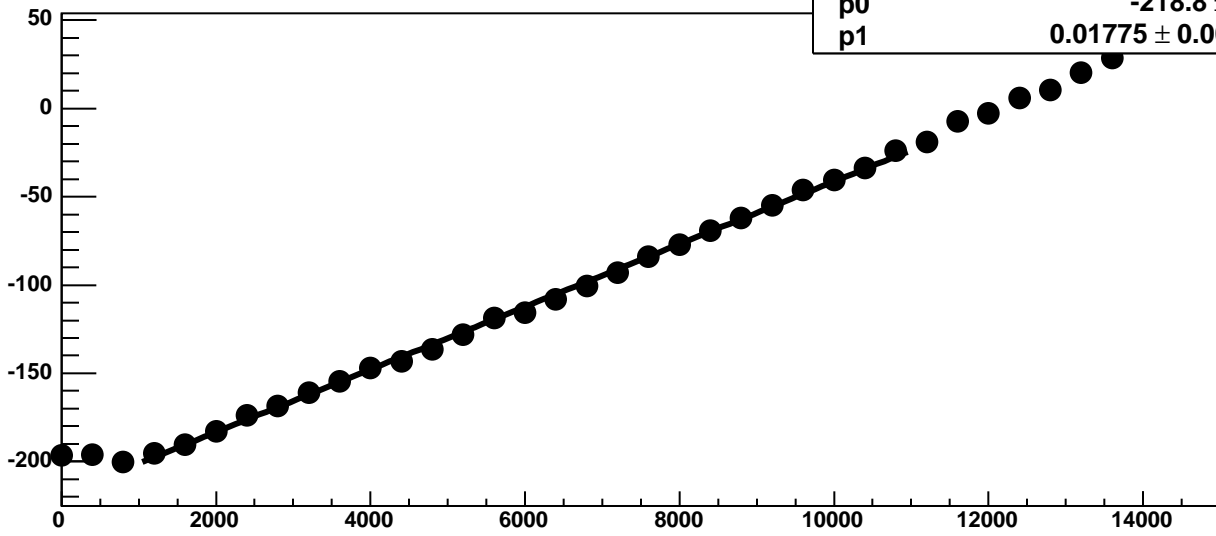
Chip 7, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



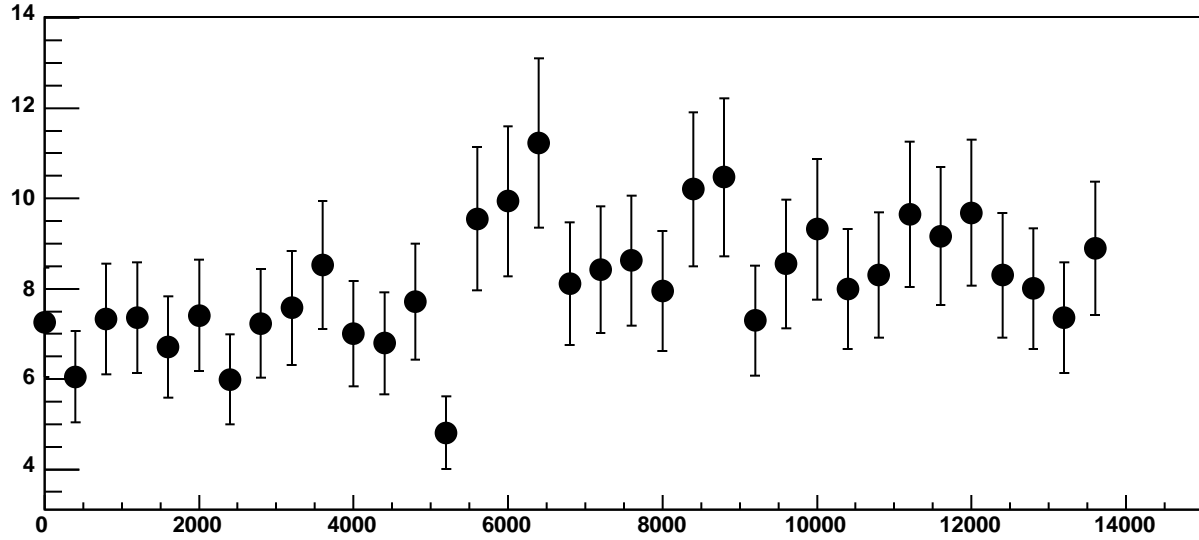
Chip 7, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



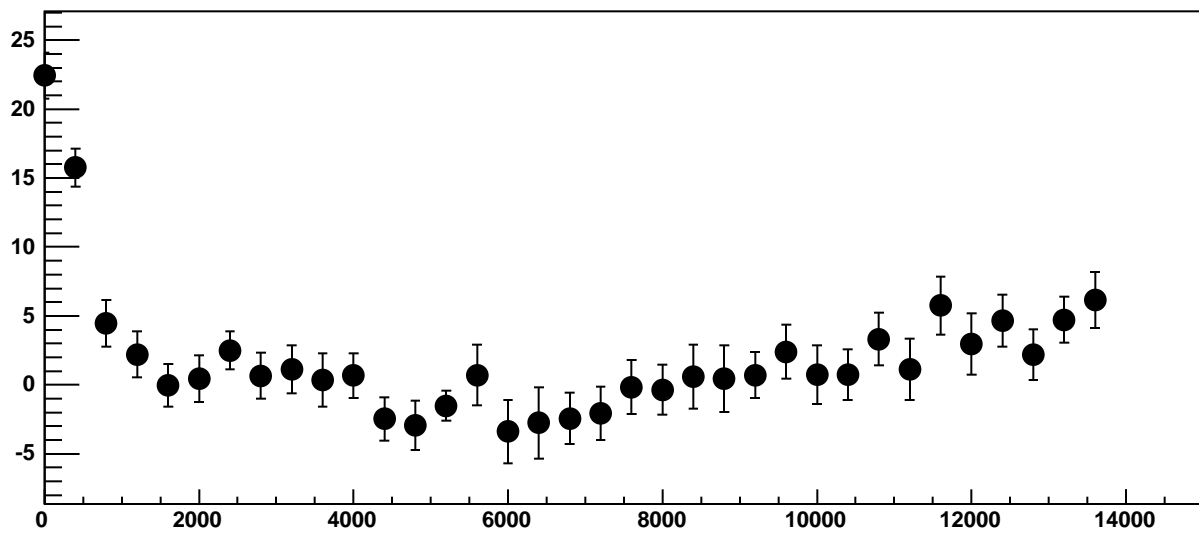
Chip 7, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



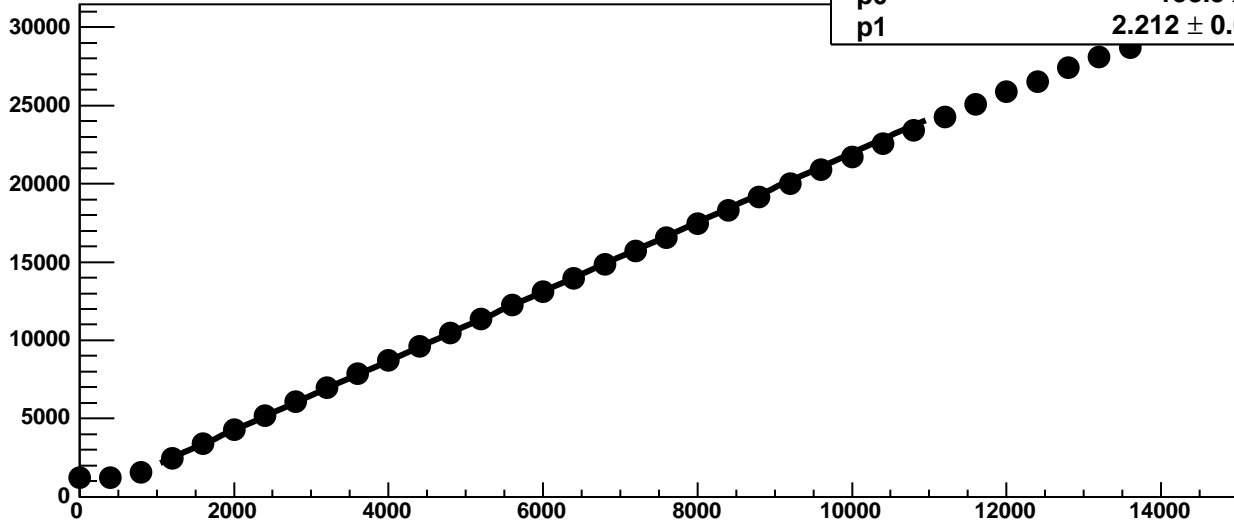
Chip 7, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



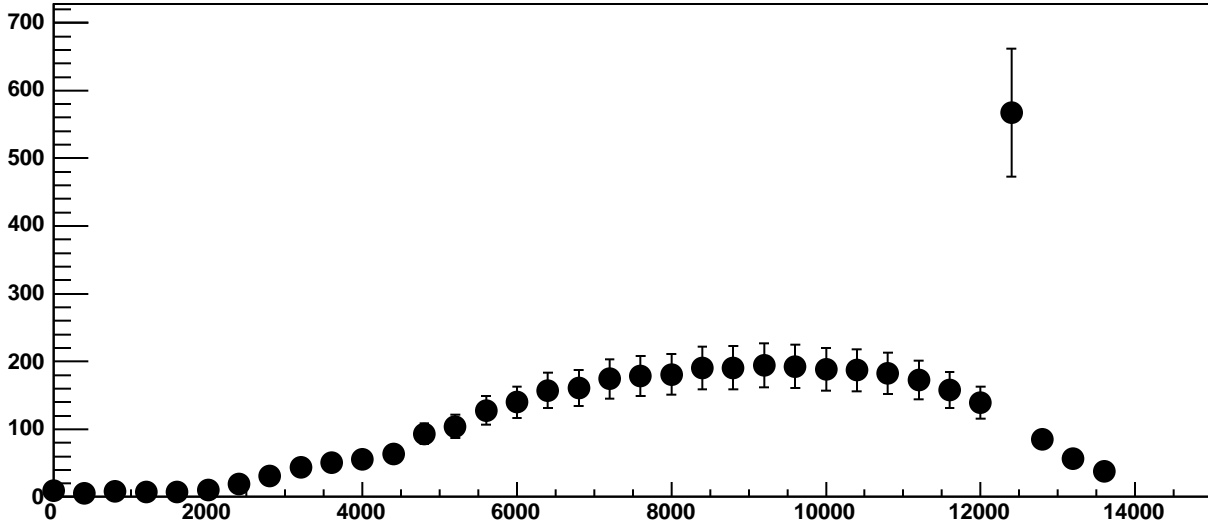
Chip 7, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



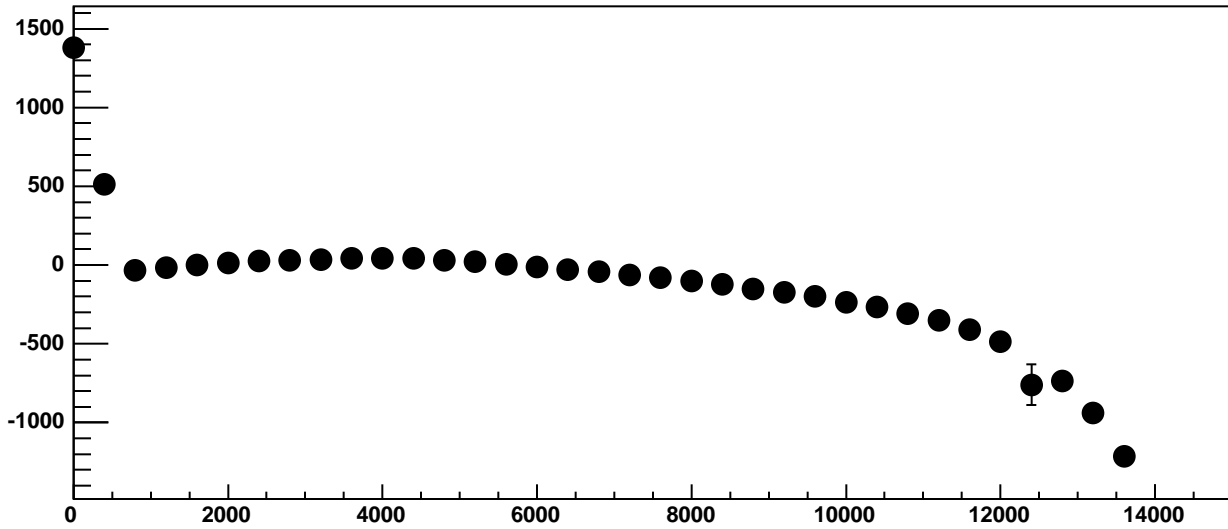
Chip 7, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



Chip 7, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

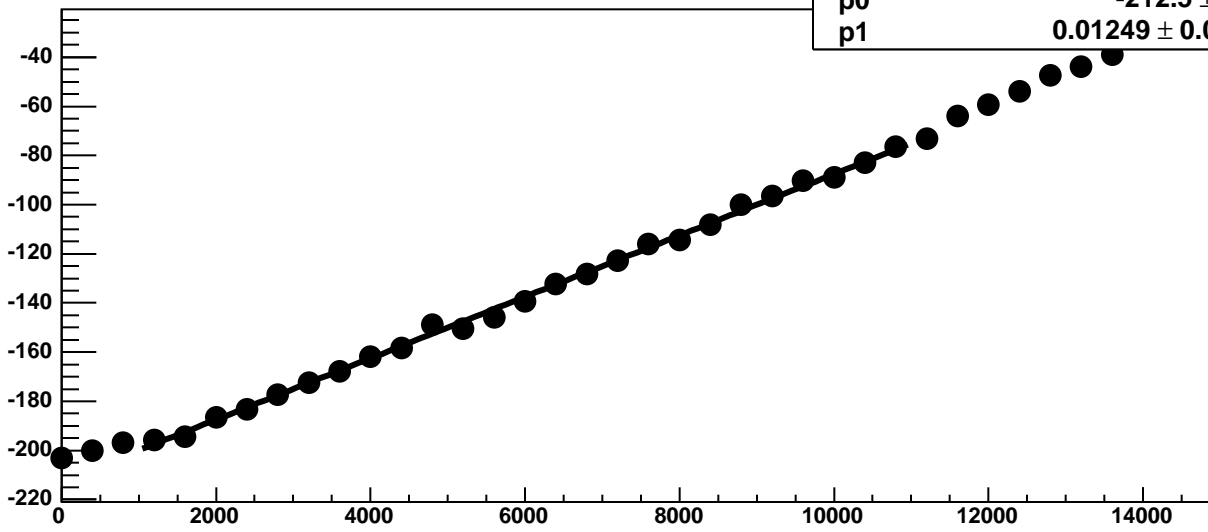


Chip 7, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



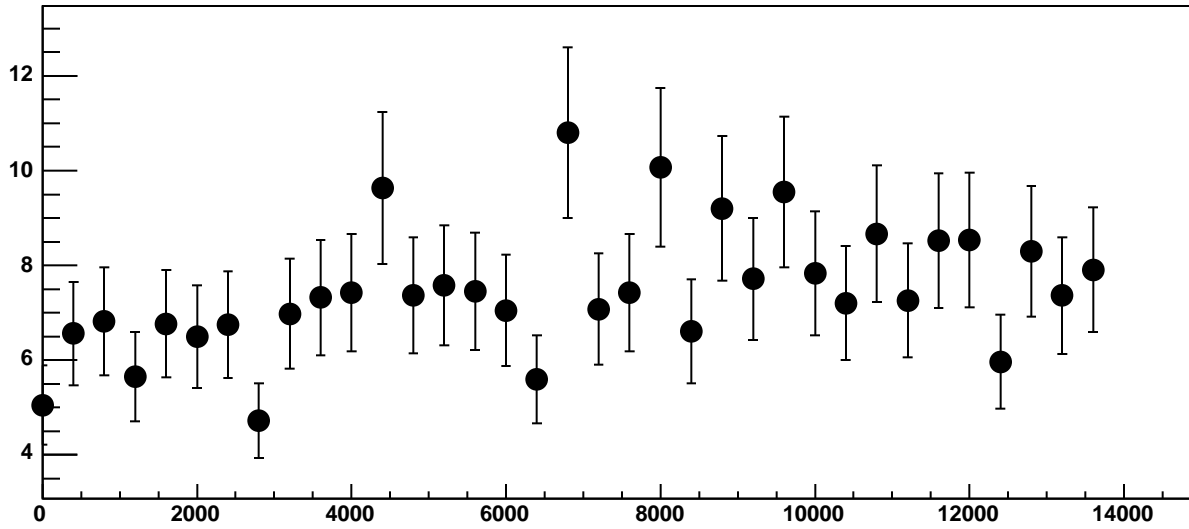


Chip 7, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

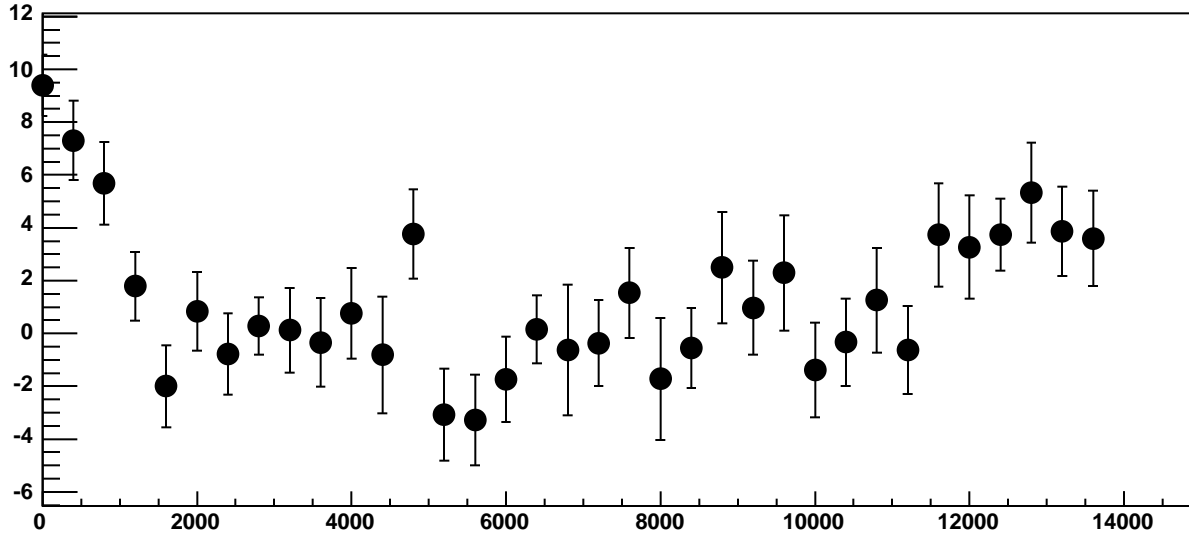


$\chi^2 / \text{ndf}$  22.96 / 23  
p0  $-212.5 \pm 0.6964$   
p1  $0.01249 \pm 0.0001129$

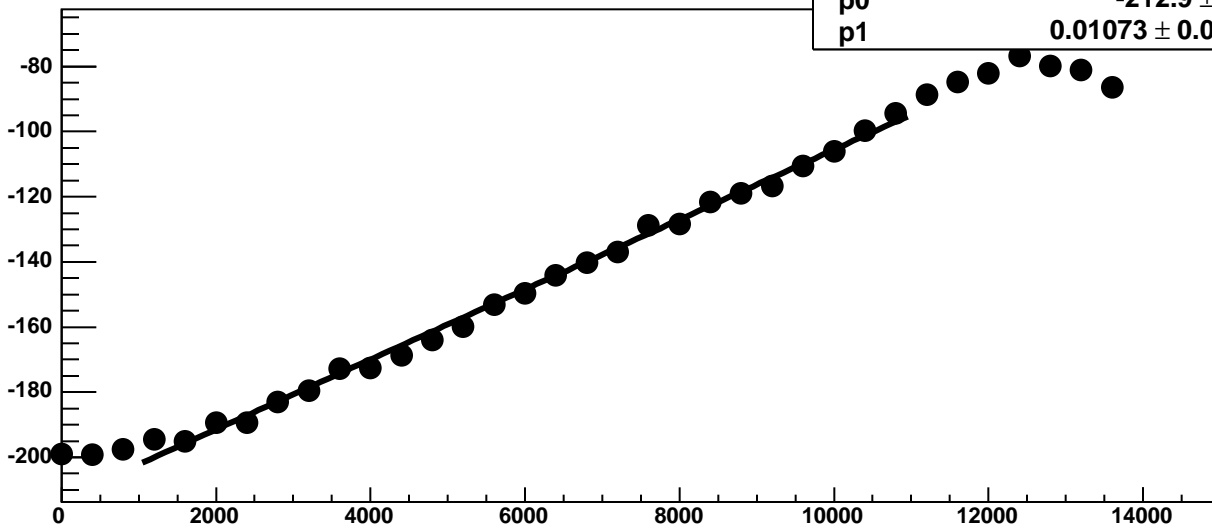
Chip 7, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



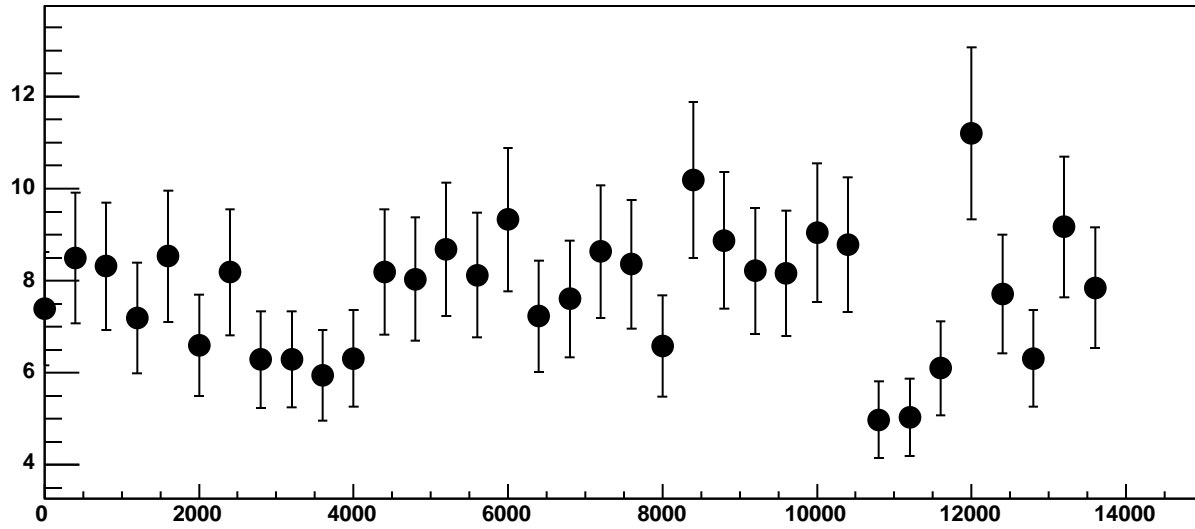
Chip 7, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



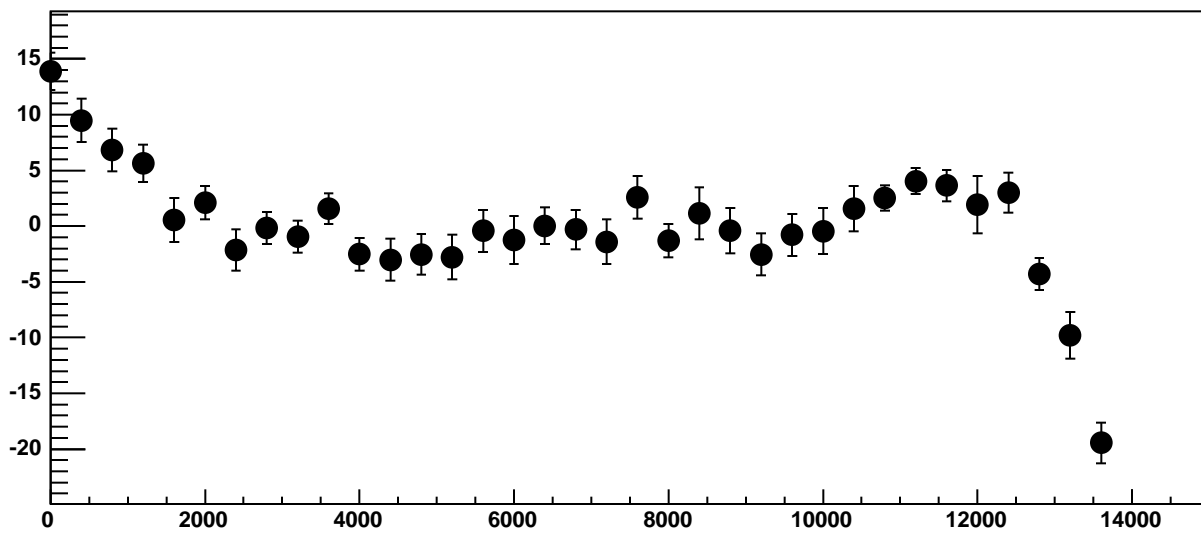
Chip 7, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



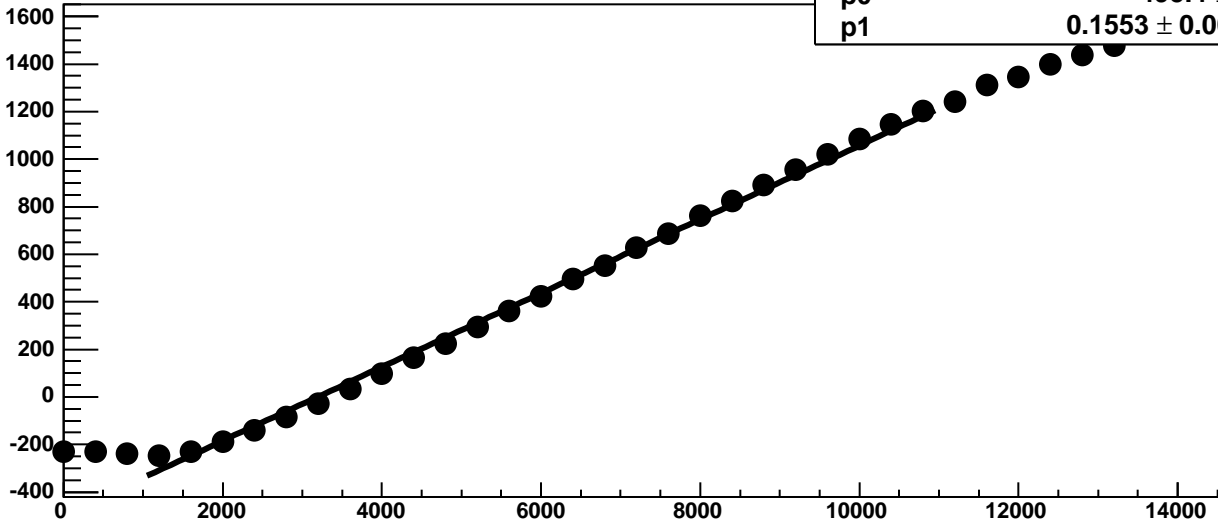
Chip 7, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



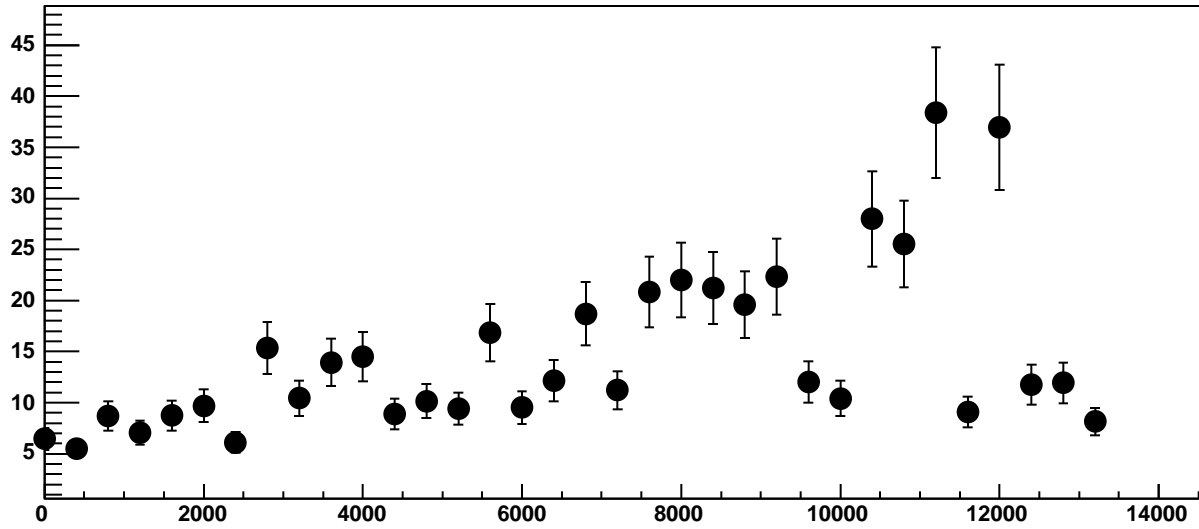
Chip 7, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



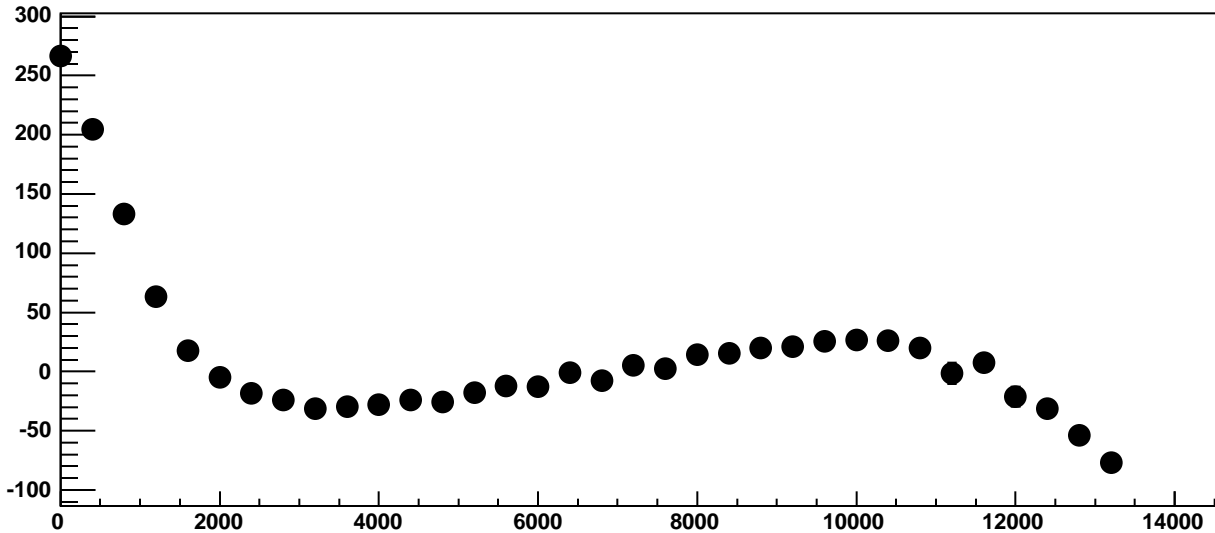
Chip 7, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



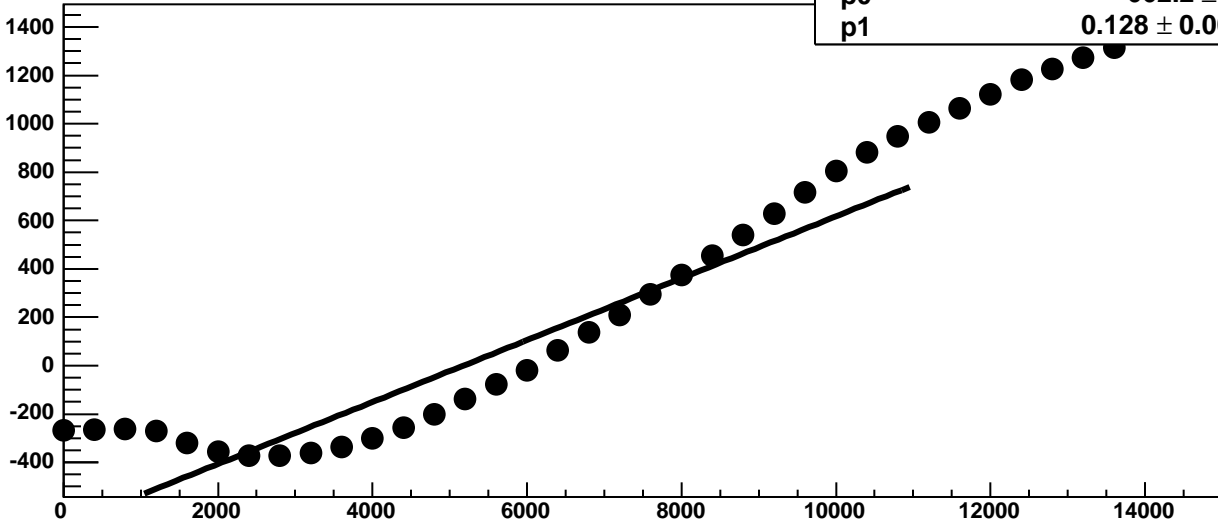
Chip 7, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 7, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

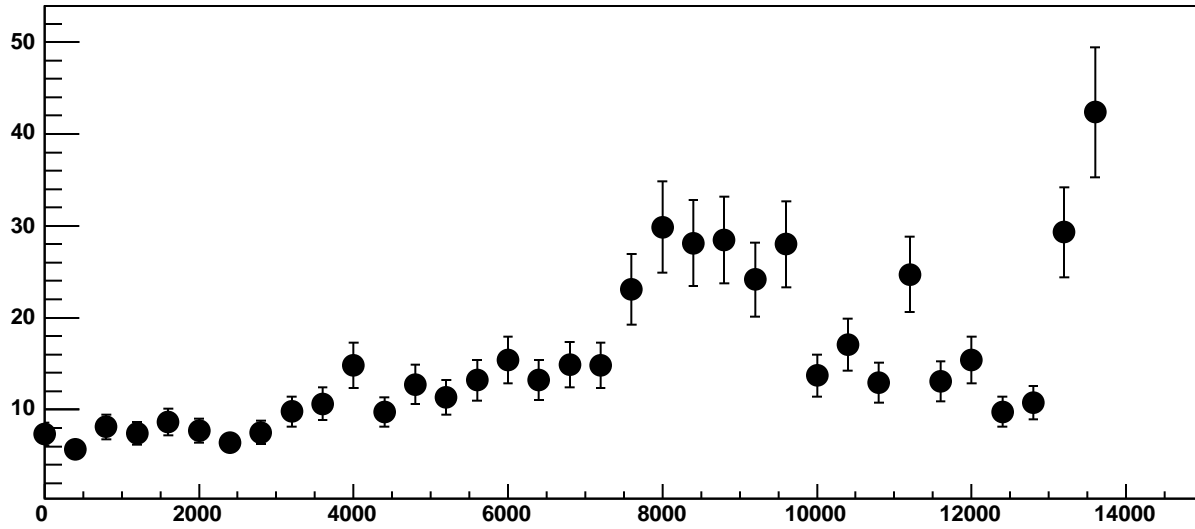


Chip 7, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

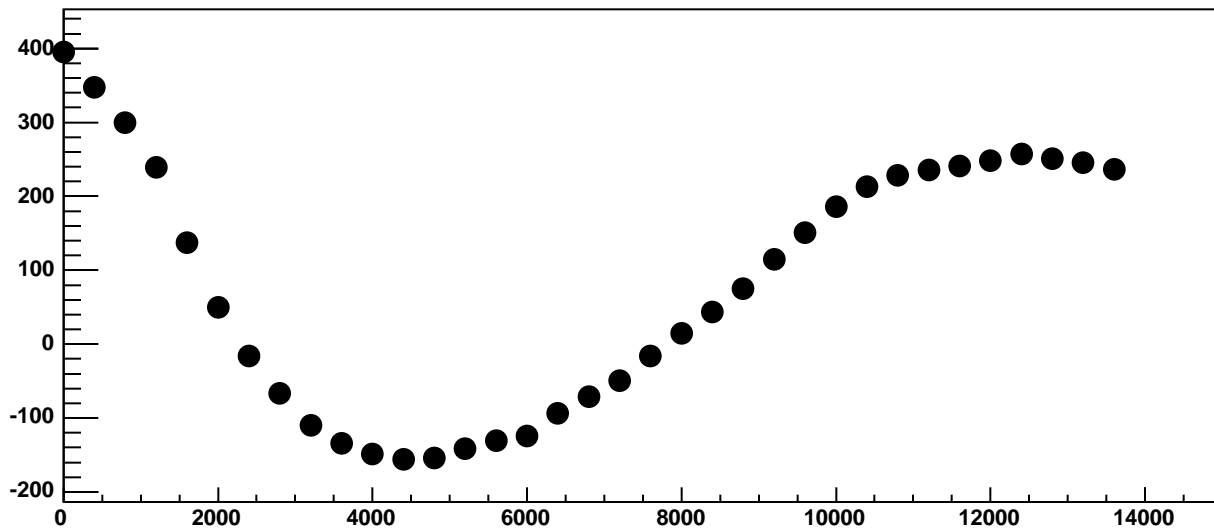


$\chi^2 / \text{ndf}$  6.306e+04 / 23  
p0 -662.2 ± 0.9745  
p1 0.128 ± 0.0001986

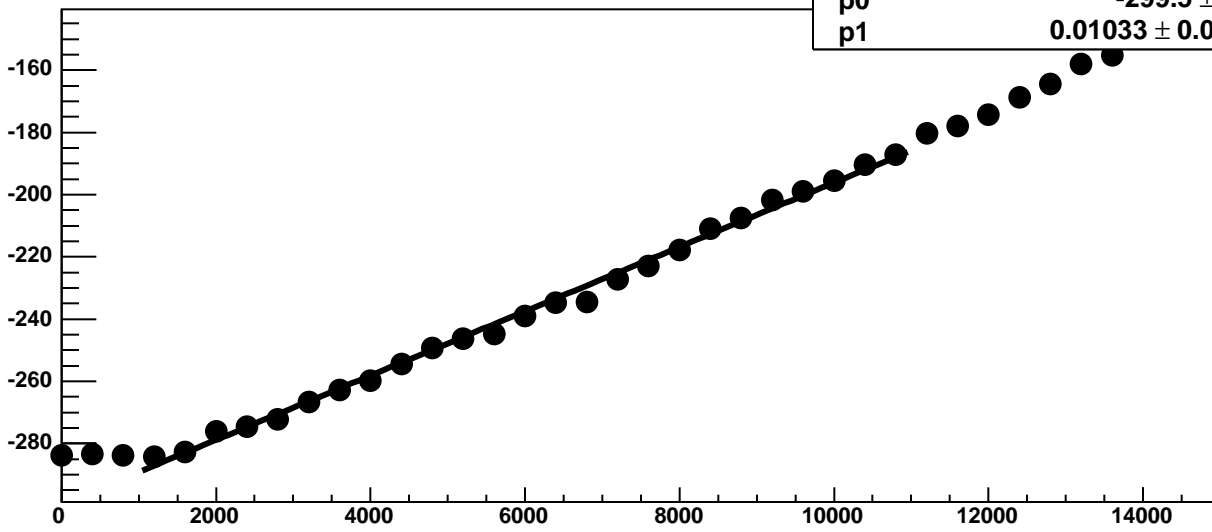
Chip 7, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



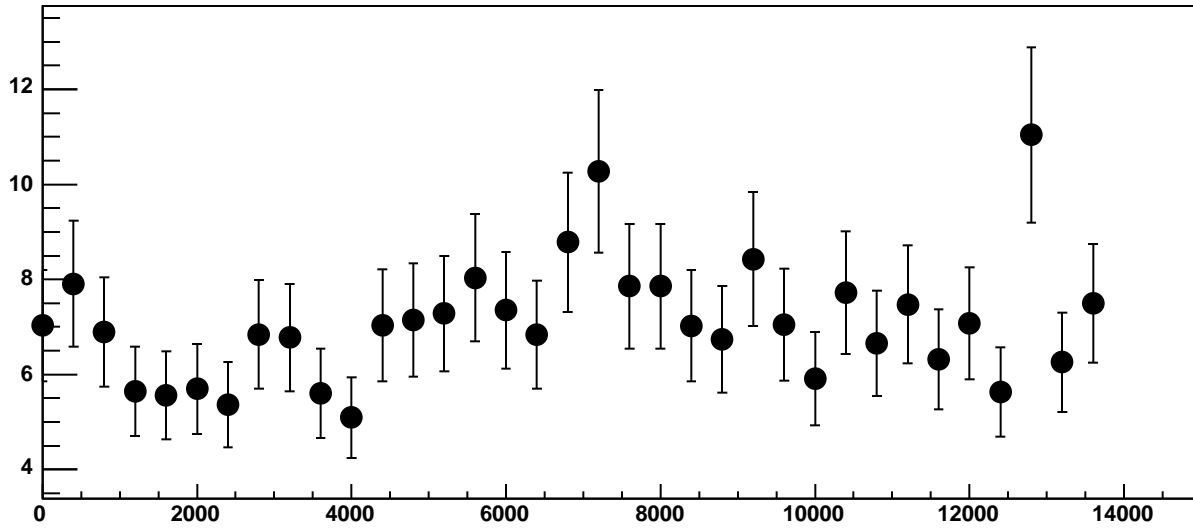
Chip 7, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



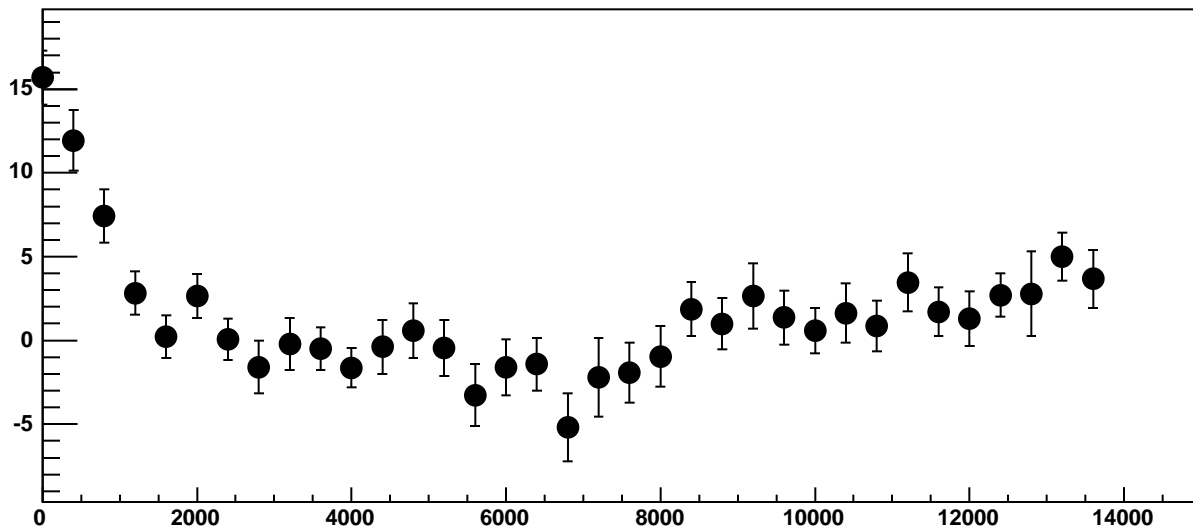
Chip 7, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC



Chip 7, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

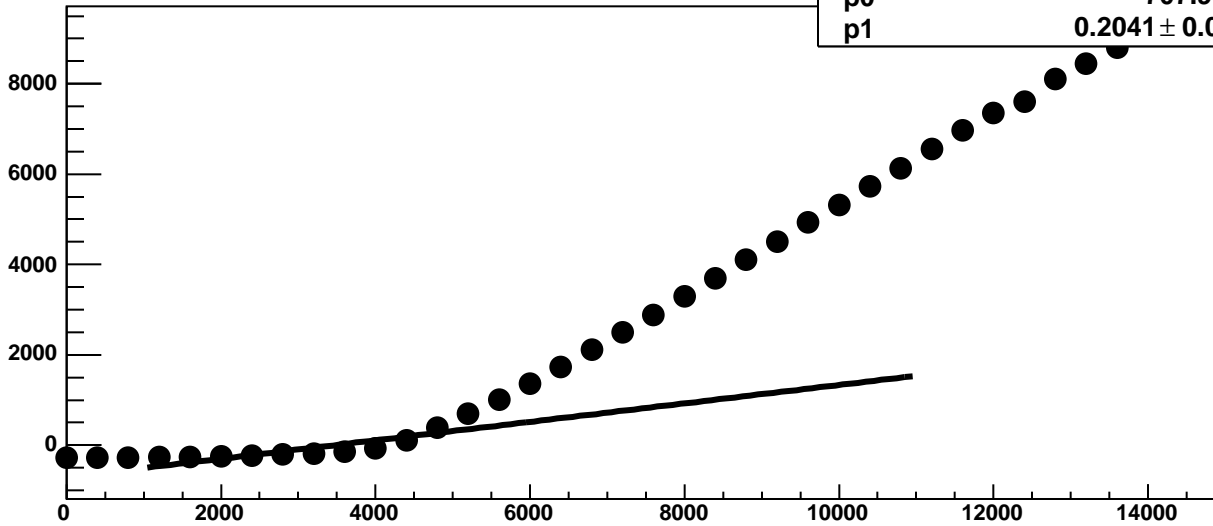


Chip 7, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

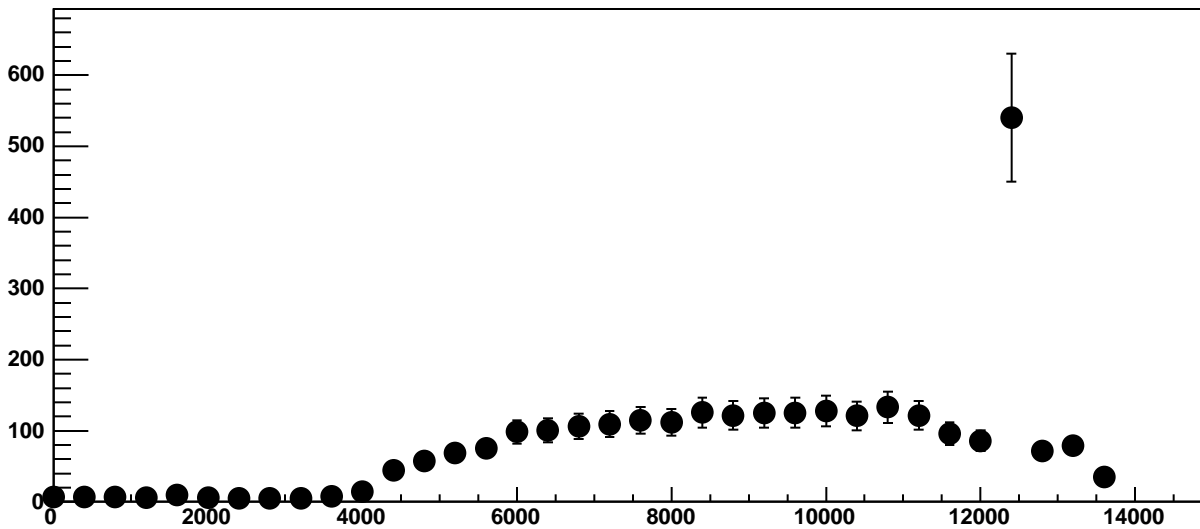


Chip 7, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

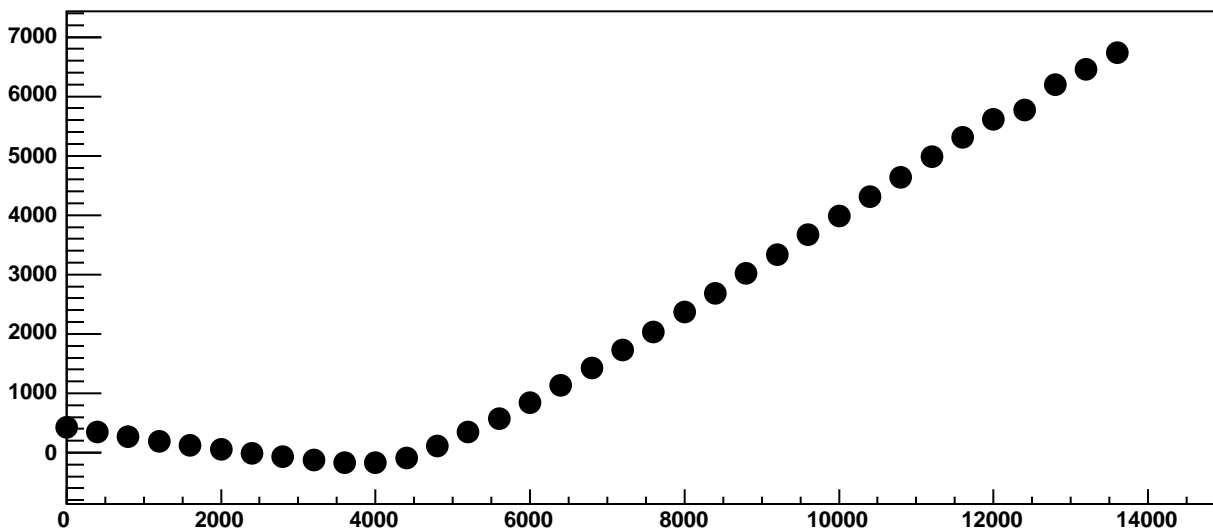
$\chi^2 / \text{ndf}$  1.913e+05 / 23  
p0 -707.9 ± 1.625  
p1 0.2041 ± 0.0006019



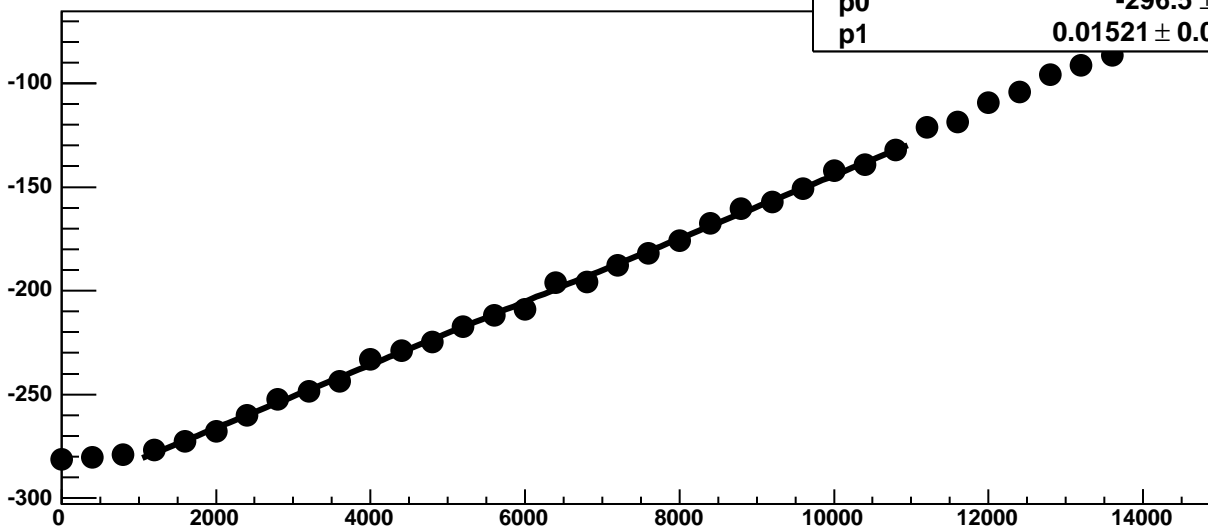
Chip 7, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



Chip 7, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC

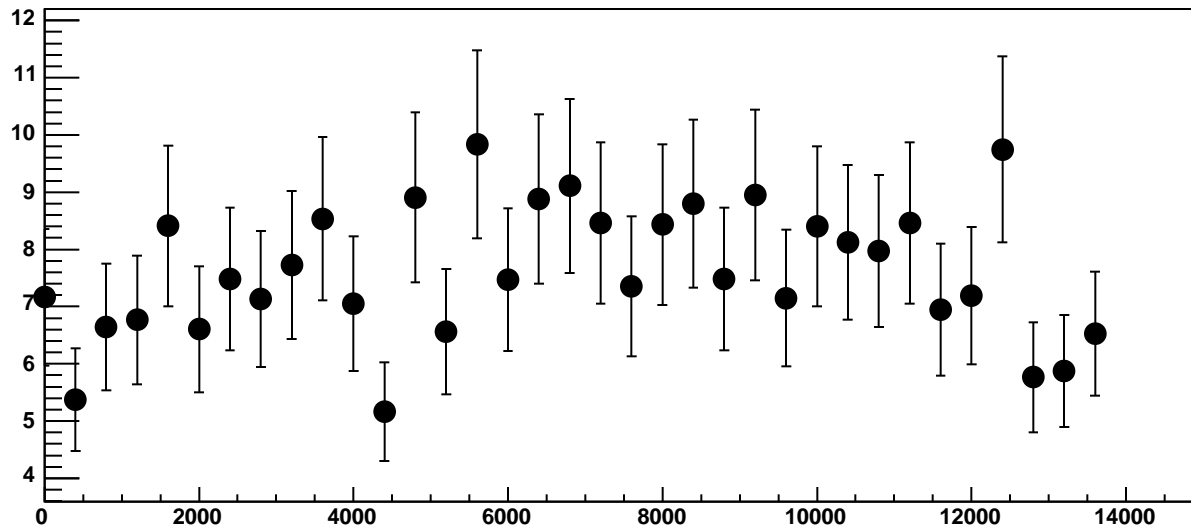


Chip 7, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

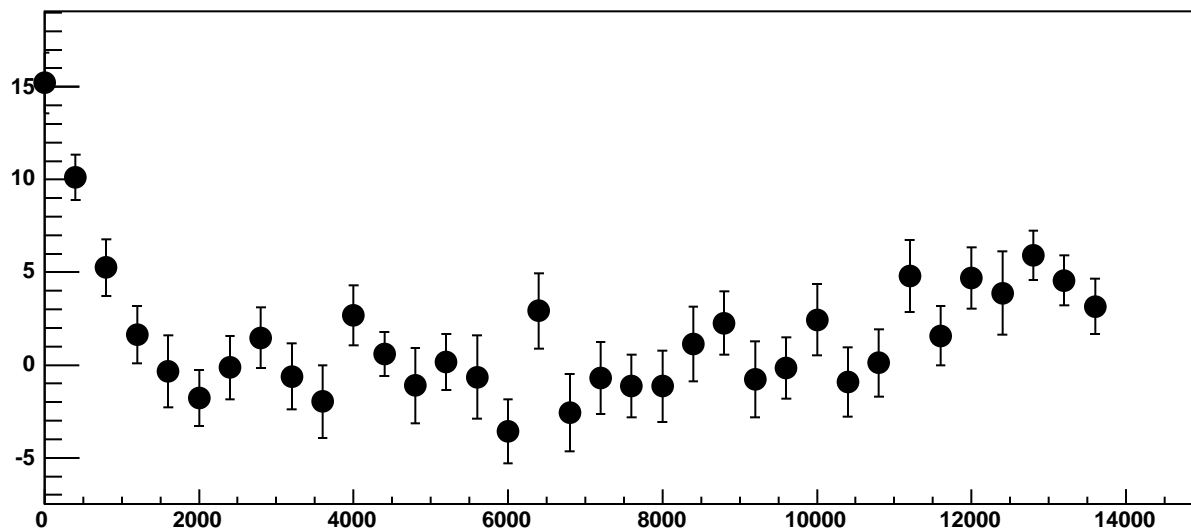


$\chi^2 / \text{ndf}$  20.79 / 23  
p0  $-296.5 \pm 0.7767$   
p1  $0.01521 \pm 0.0001214$

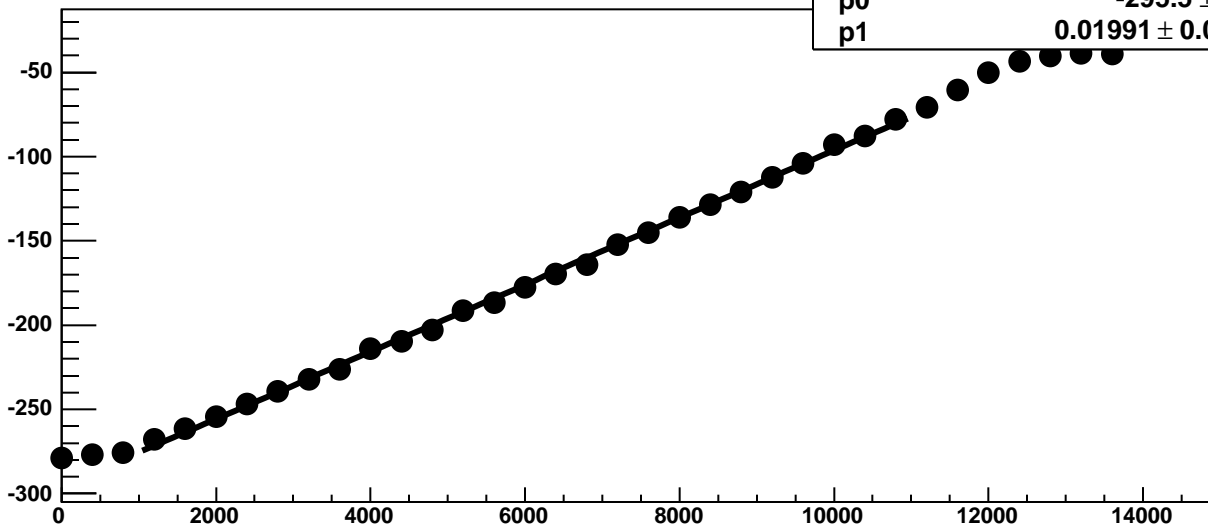
Chip 7, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 7, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

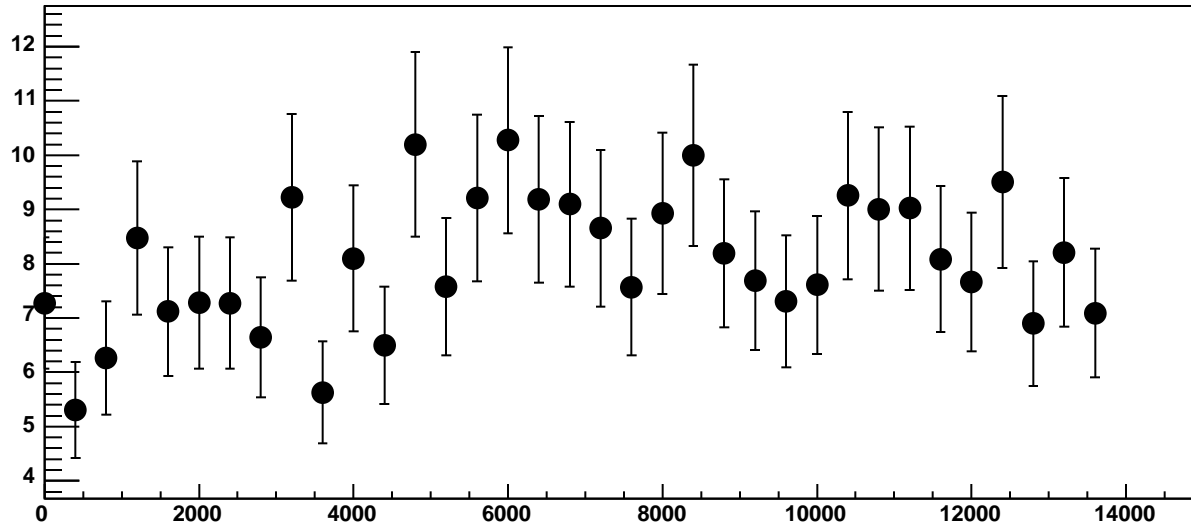


Chip 7, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

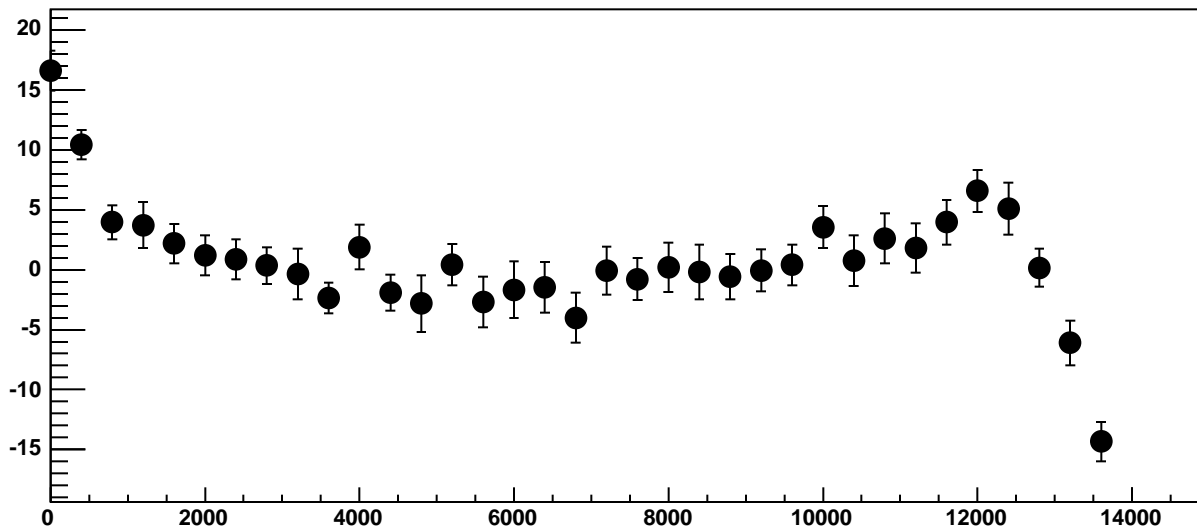


$\chi^2 / \text{ndf}$  26.46 / 23  
p0  $-295.5 \pm 0.7989$   
p1  $0.01991 \pm 0.0001251$

Chip 7, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

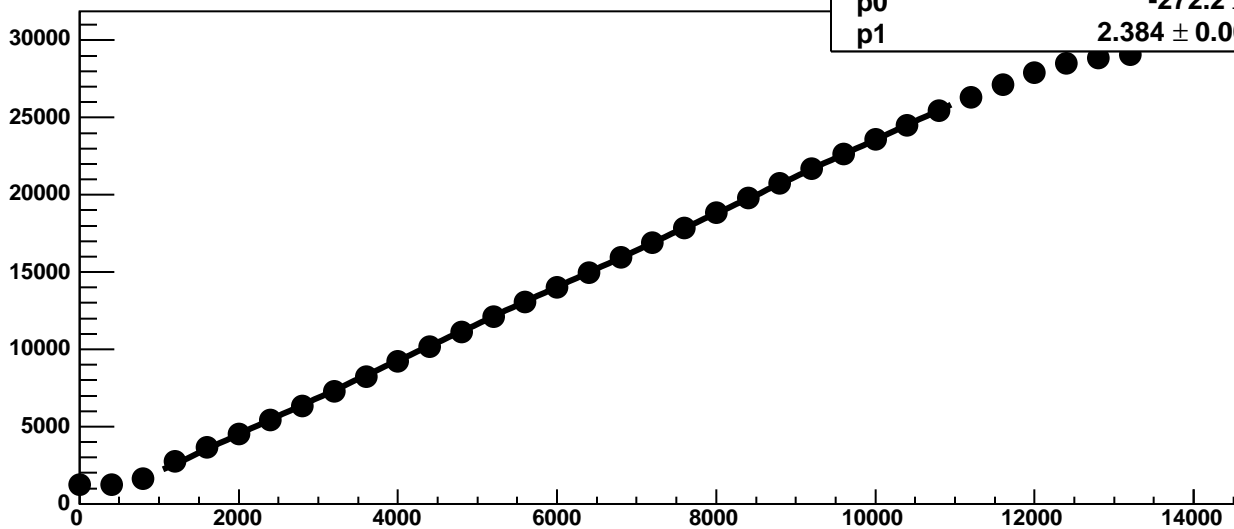


Chip 7, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



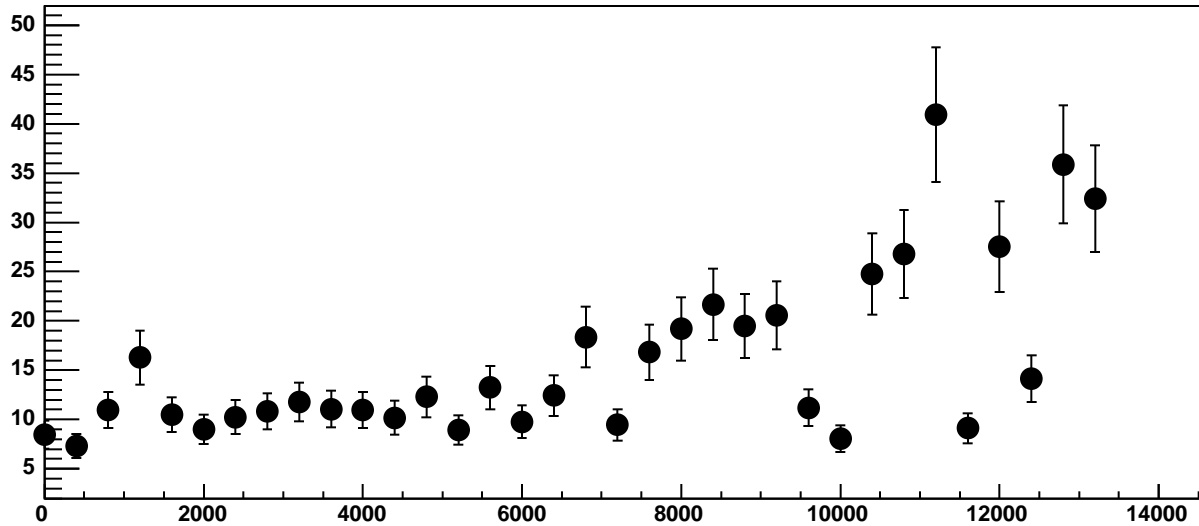


Chip 7, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC

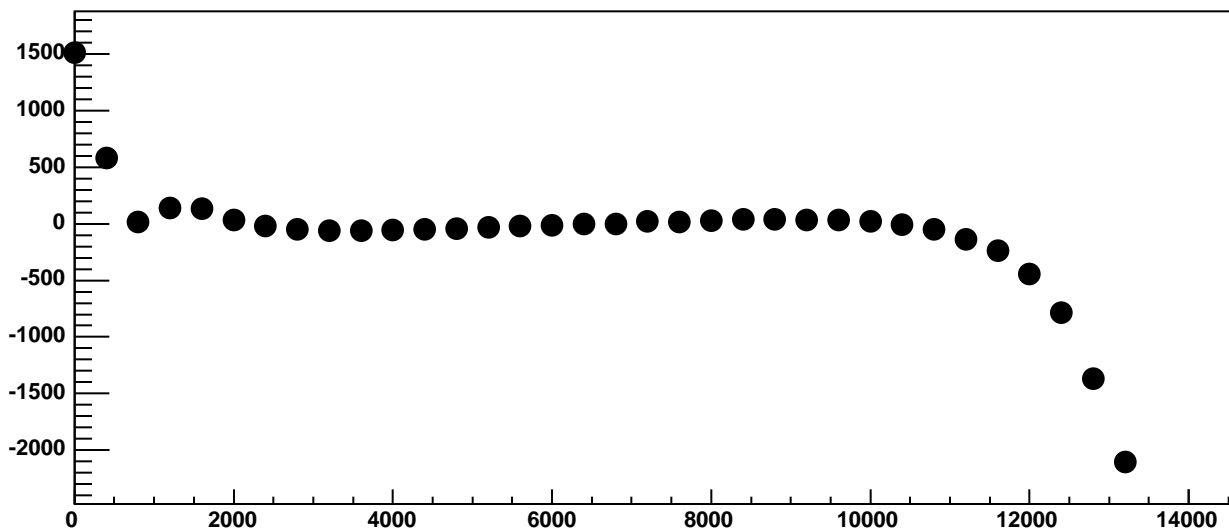


$\chi^2 / \text{ndf}$  8628 / 23  
p0  $-272.2 \pm 1.217$   
p1  $2.384 \pm 0.0002016$

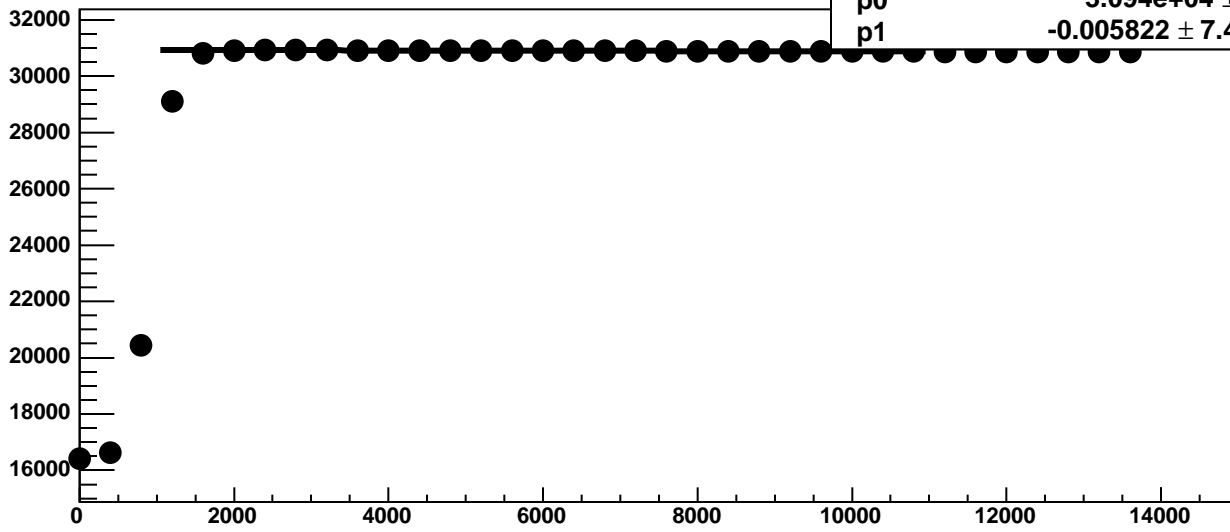
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



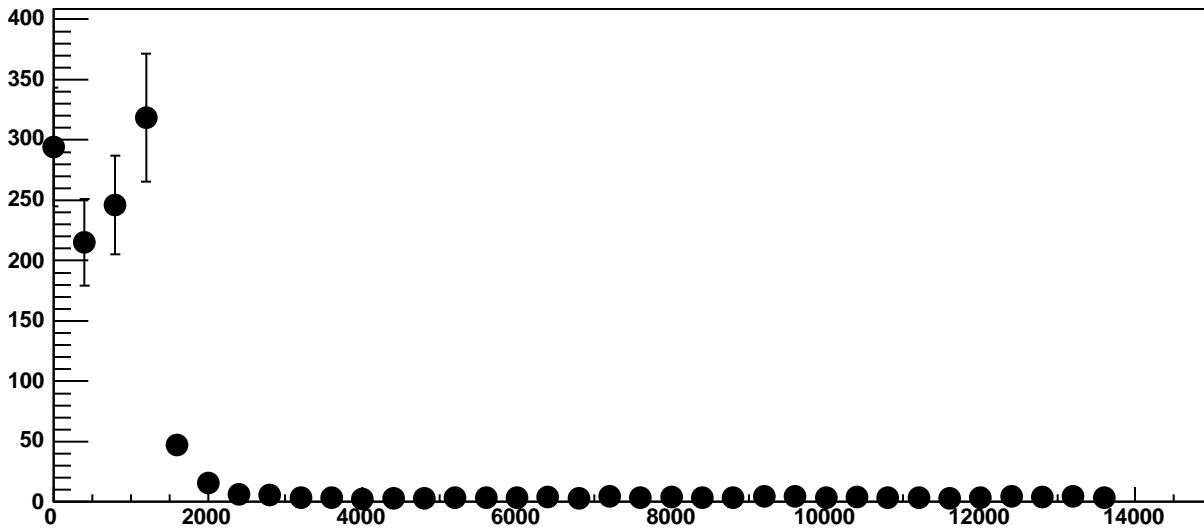
Chip 7, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



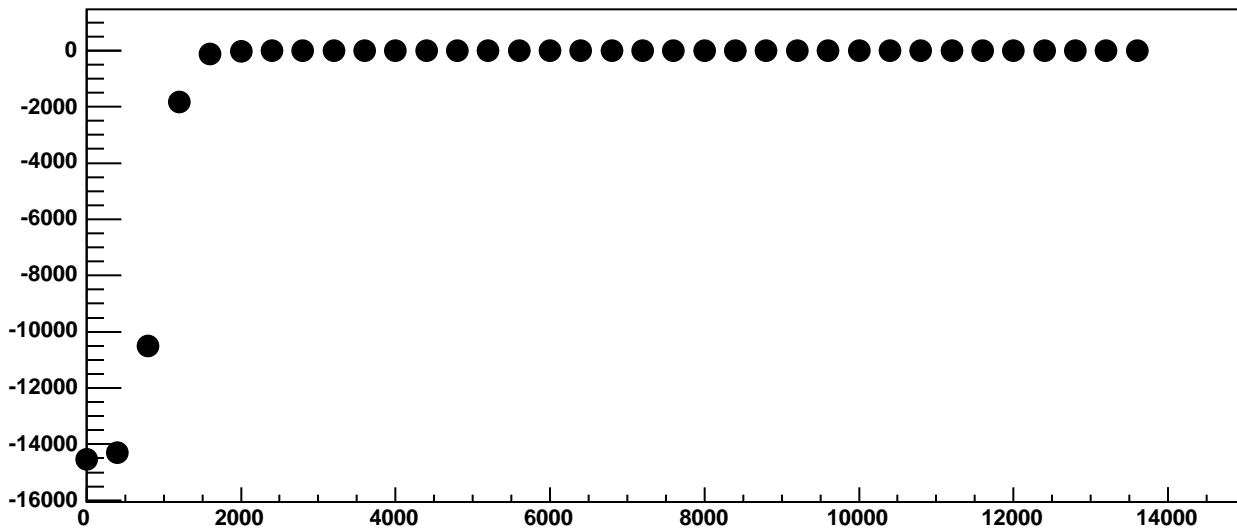
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



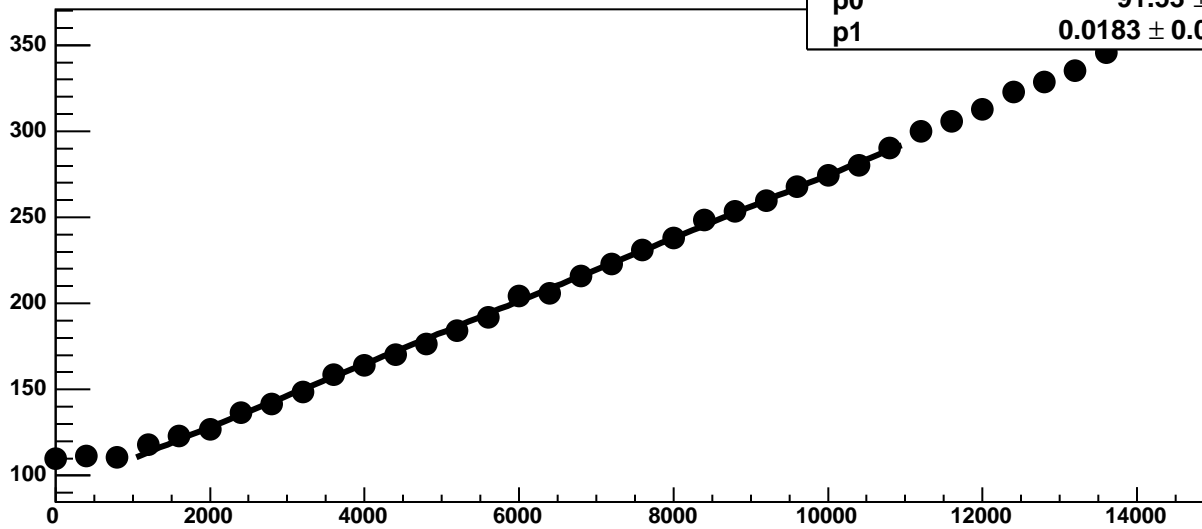
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC

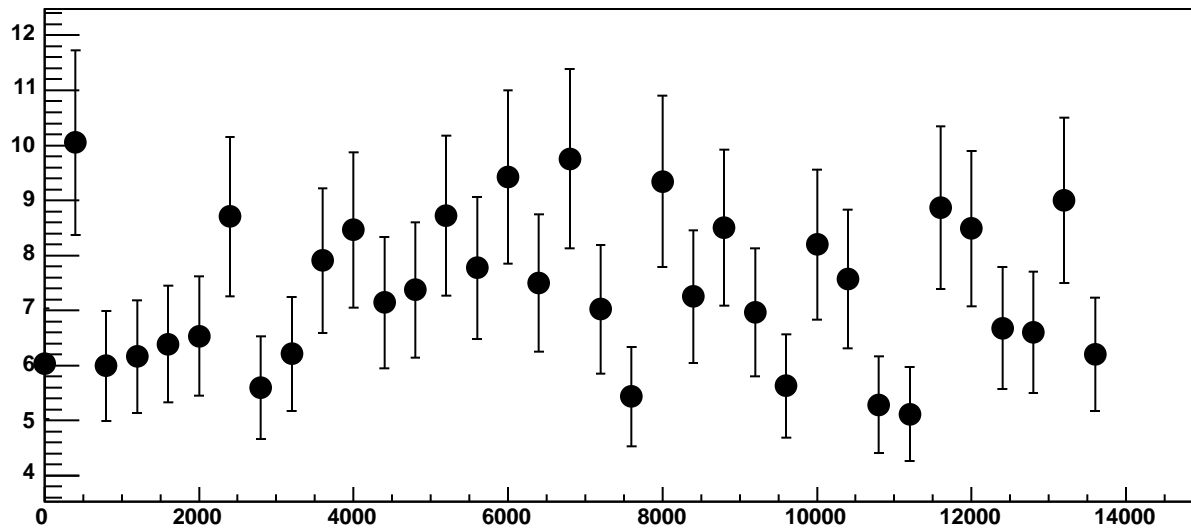


Chip 8, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

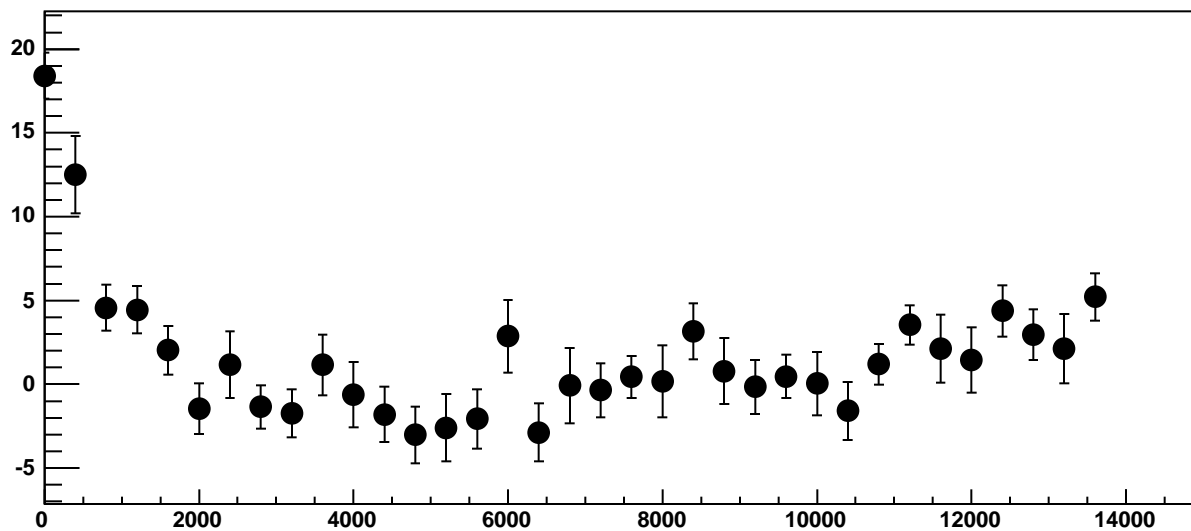


$\chi^2 / \text{ndf}$  34.06 / 23  
p0  $91.53 \pm 0.7069$   
p1  $0.0183 \pm 0.0001047$

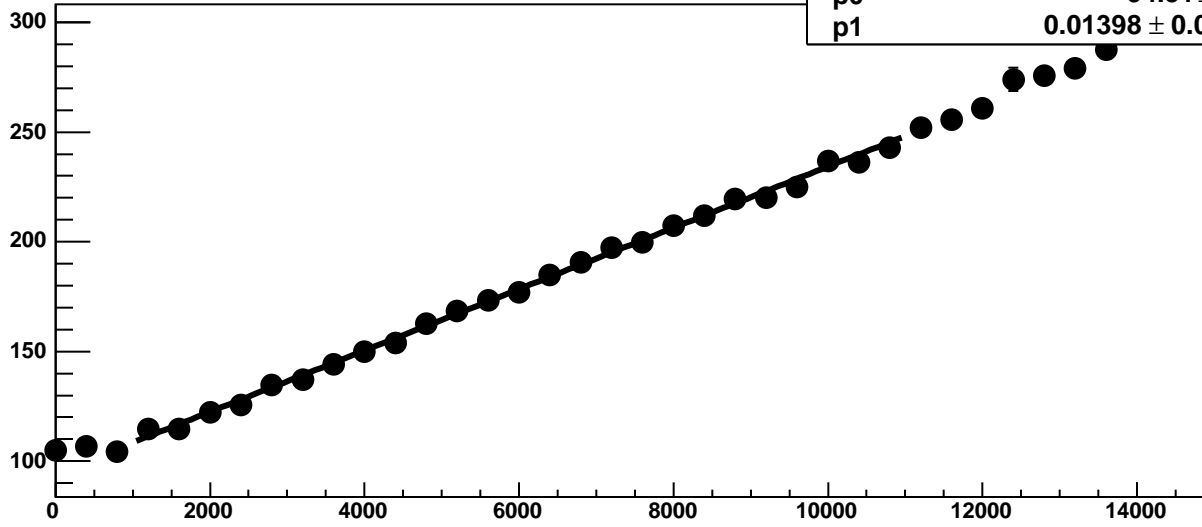
Chip 8, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

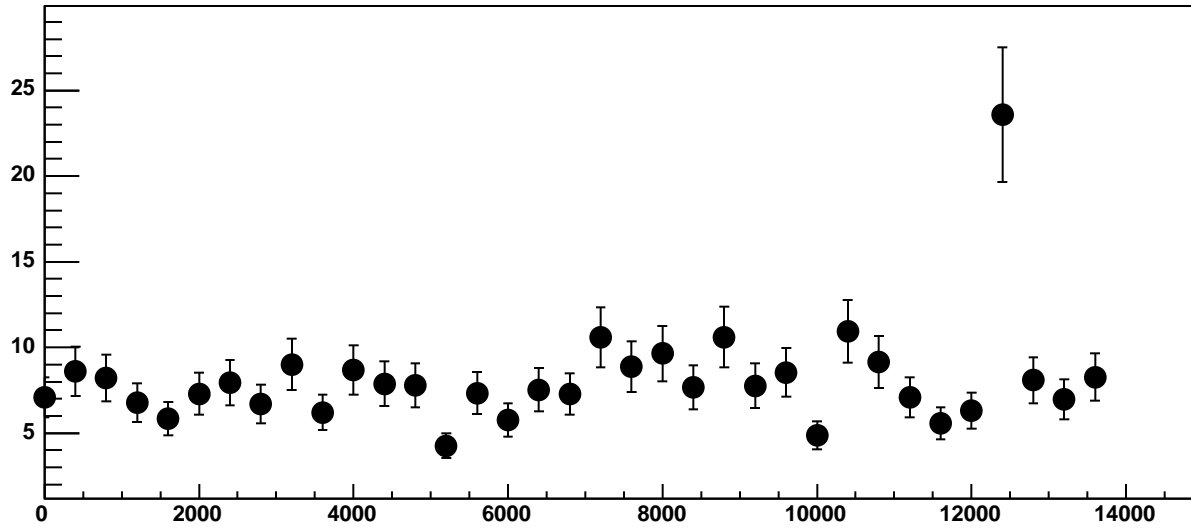


Chip 8, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

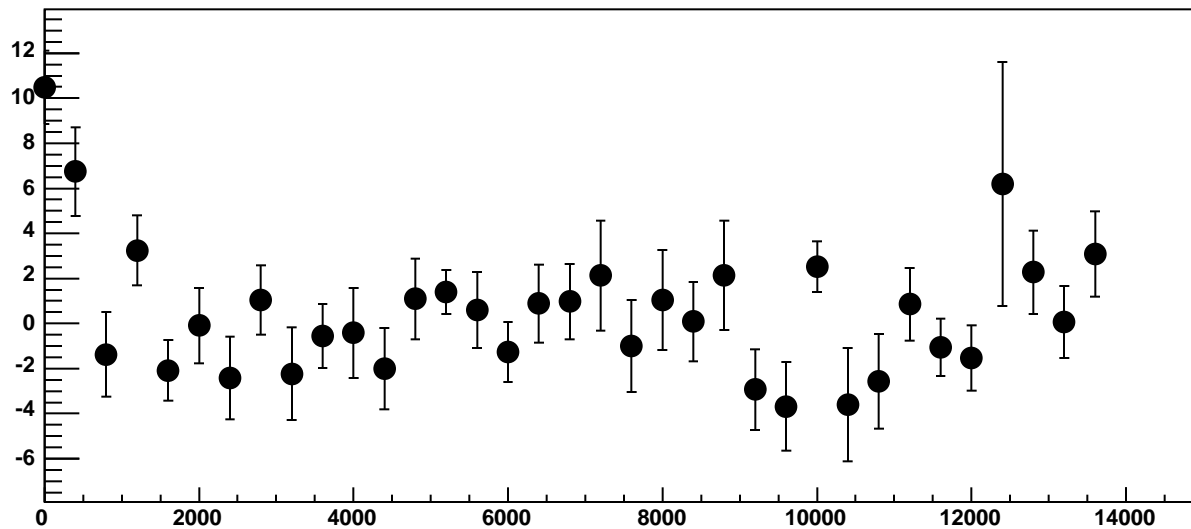


$\chi^2 / \text{ndf}$  32.52 / 23  
p0 94.51 ± 0.7391  
p1 0.01398 ± 0.0001163

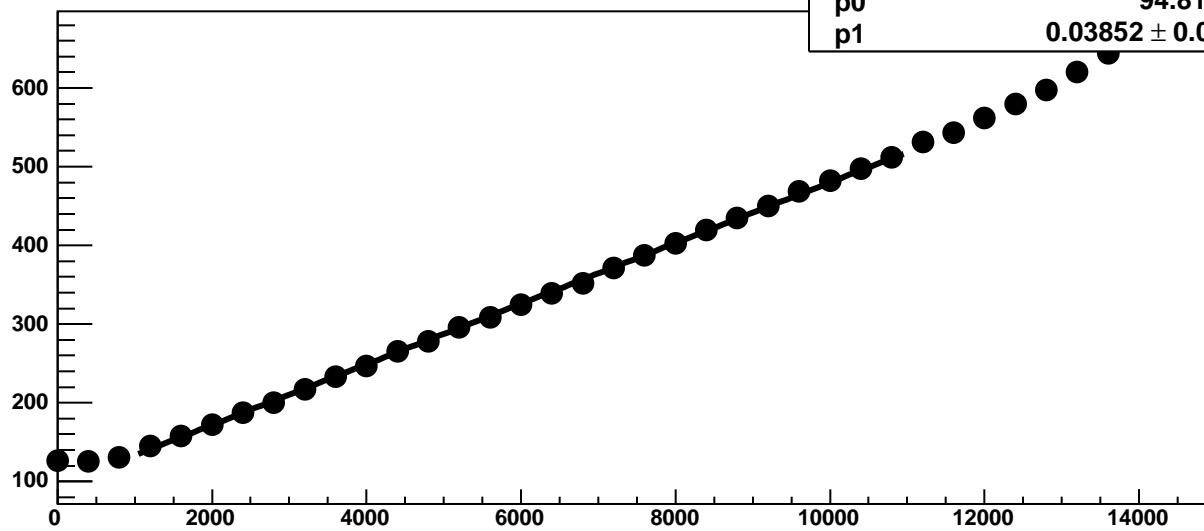
Chip 8, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

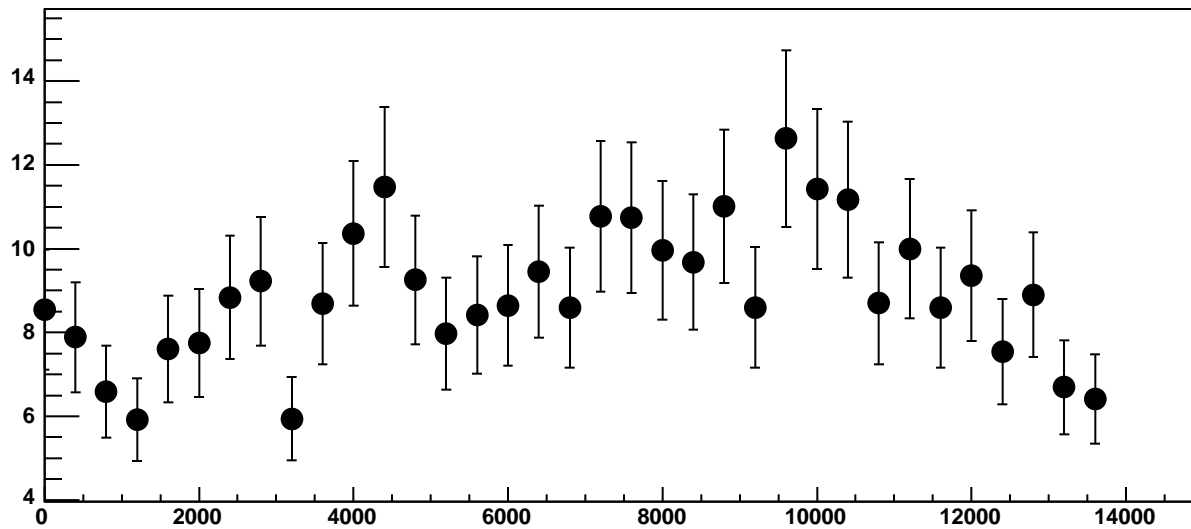


Chip 8, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

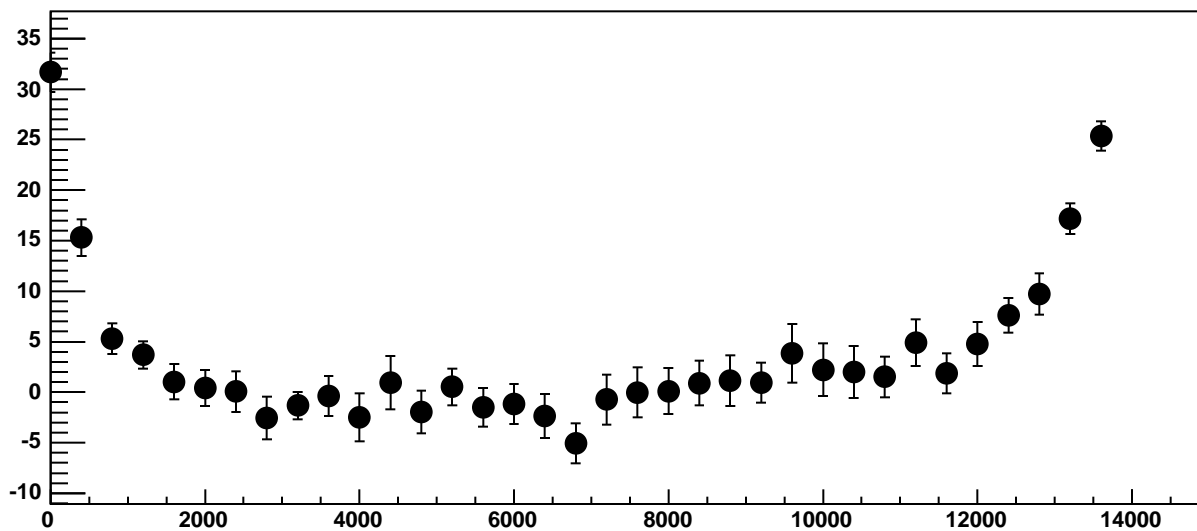


$\chi^2 / \text{ndf}$  25.5 / 23  
p0  $94.81 \pm 0.832$   
p1  $0.03852 \pm 0.0001378$

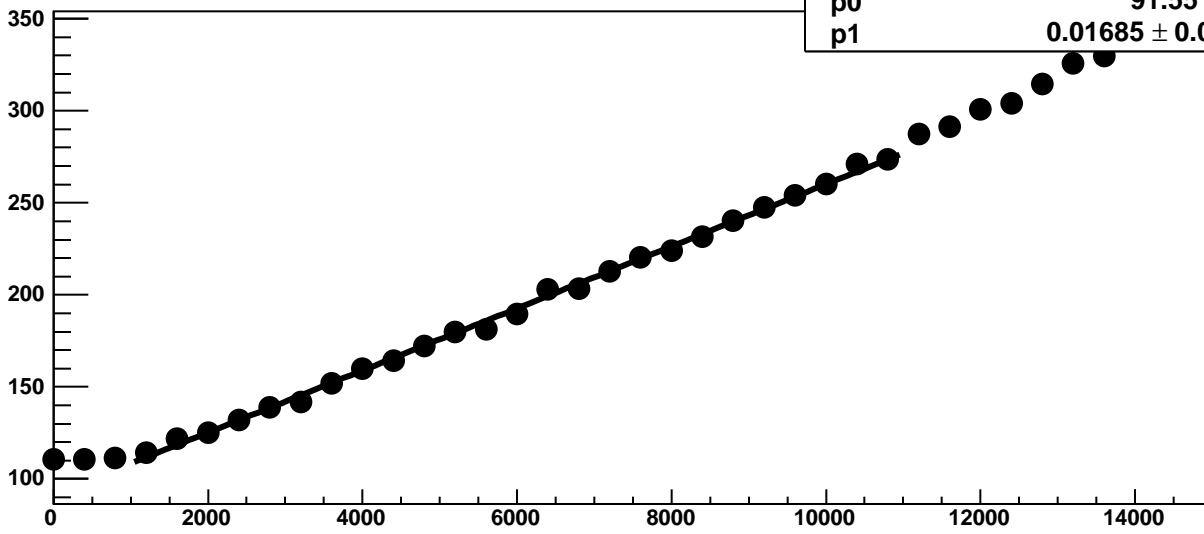
Chip 8, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

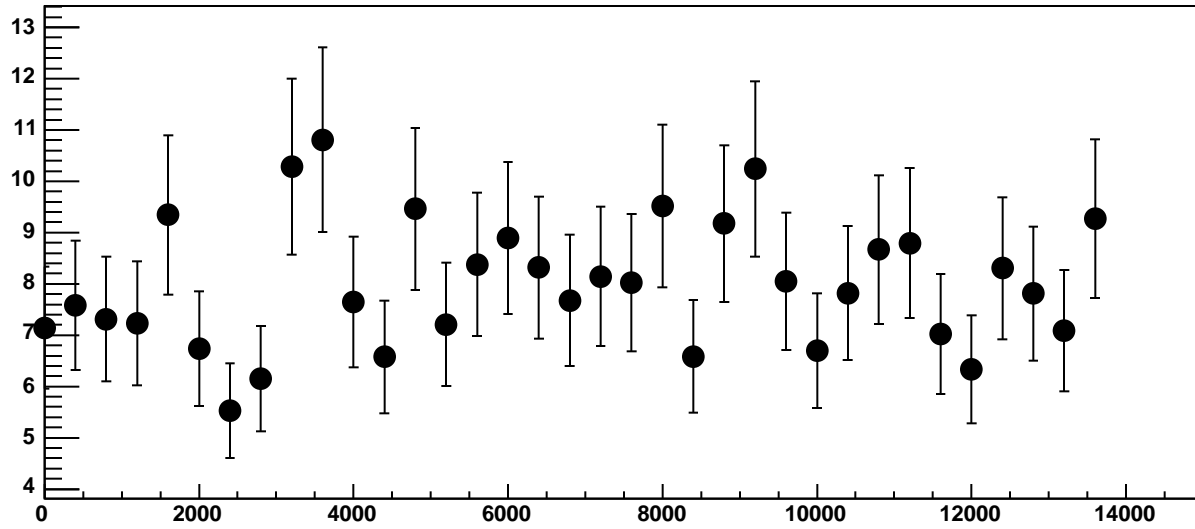


Chip 8, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

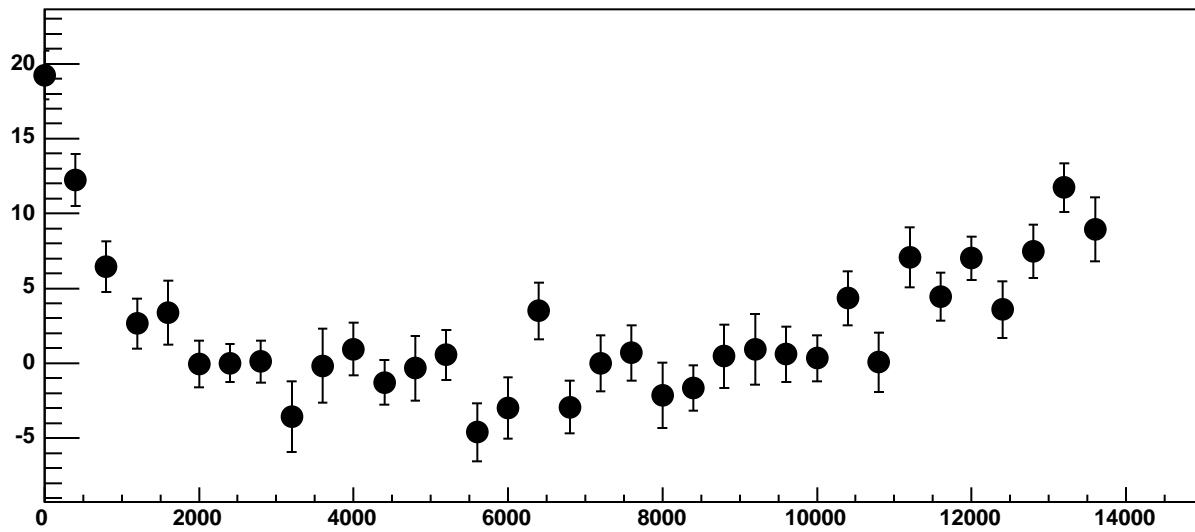


$\chi^2 / \text{ndf}$  31.01 / 23  
p0 91.55 ± 0.783  
p1 0.01685 ± 0.0001211

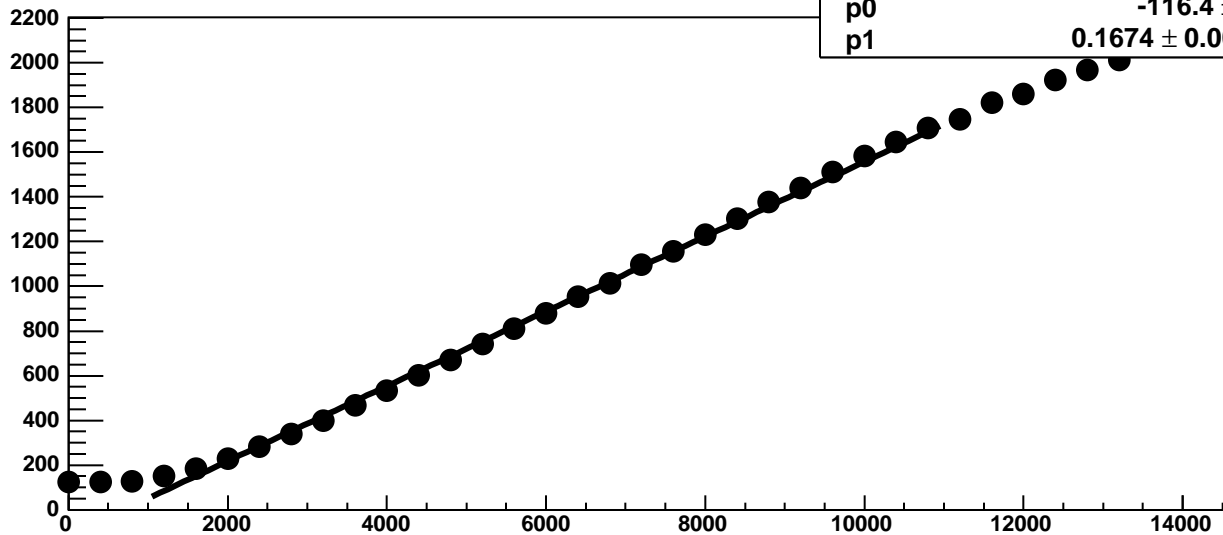
Chip 8, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



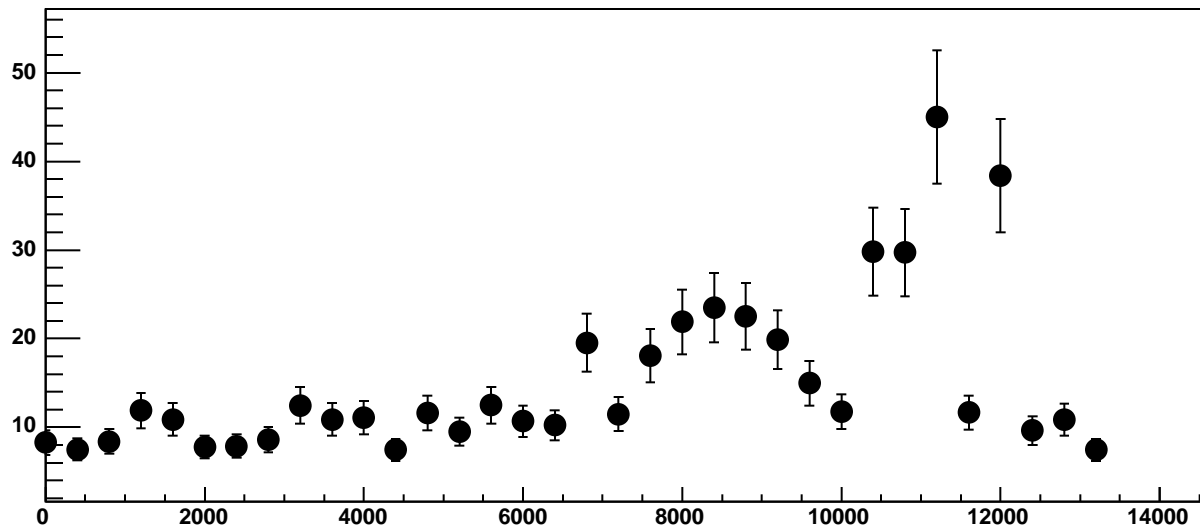
Chip 8, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



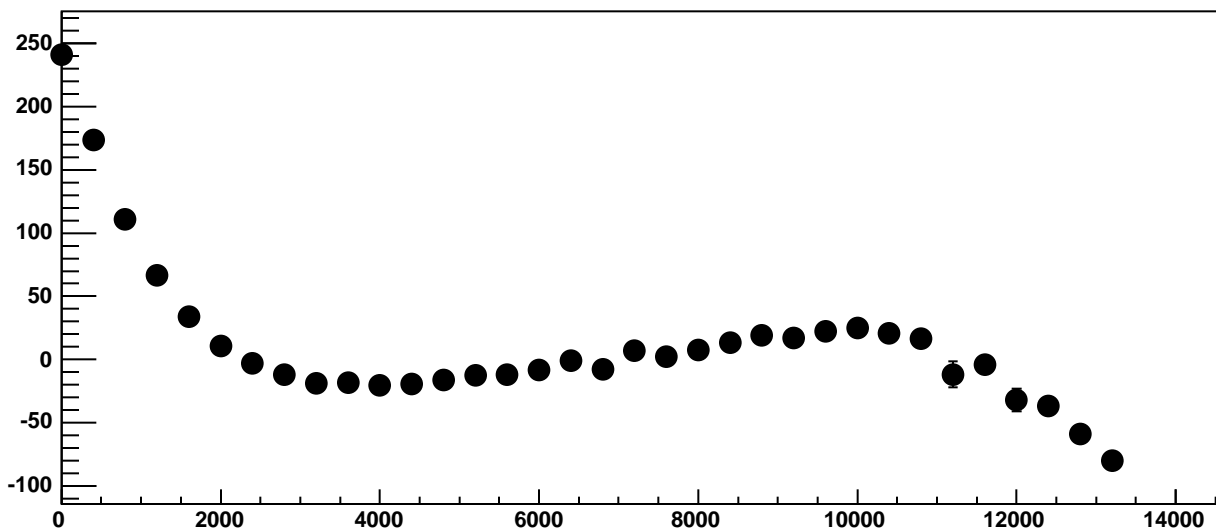
Chip 8, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



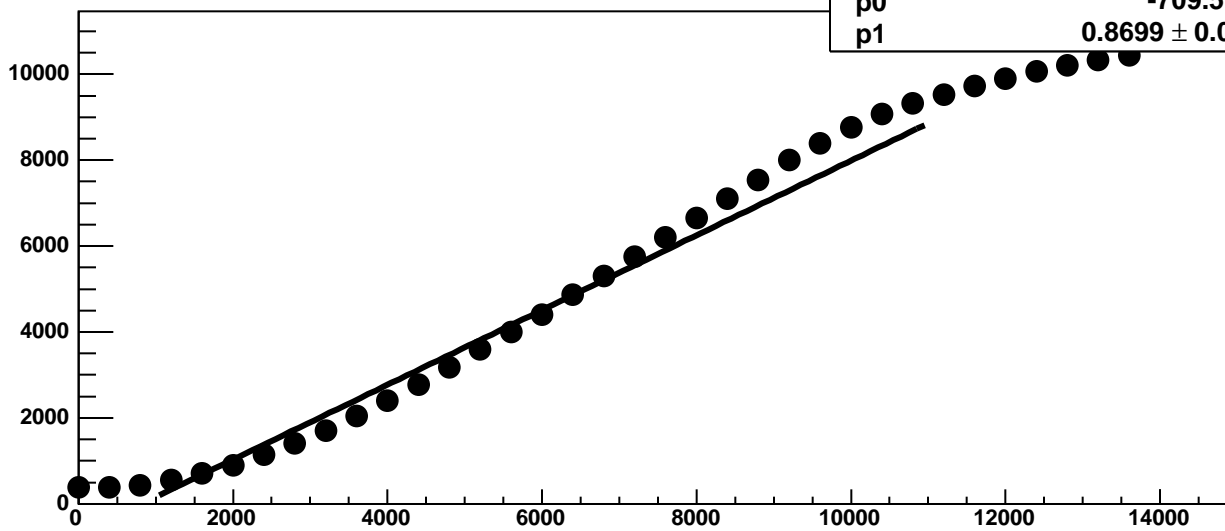
Chip 8, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



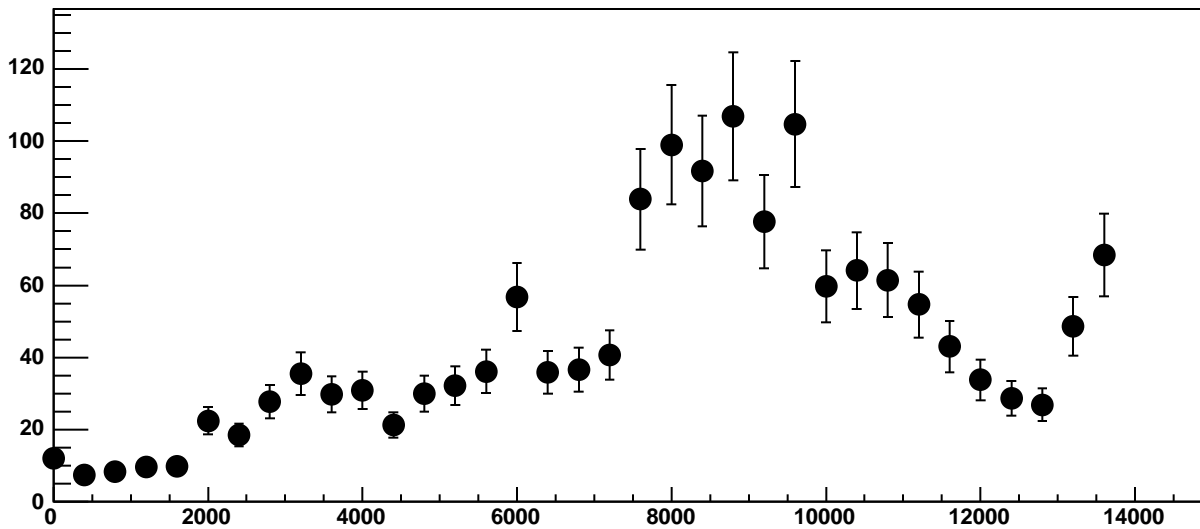
Chip 8, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



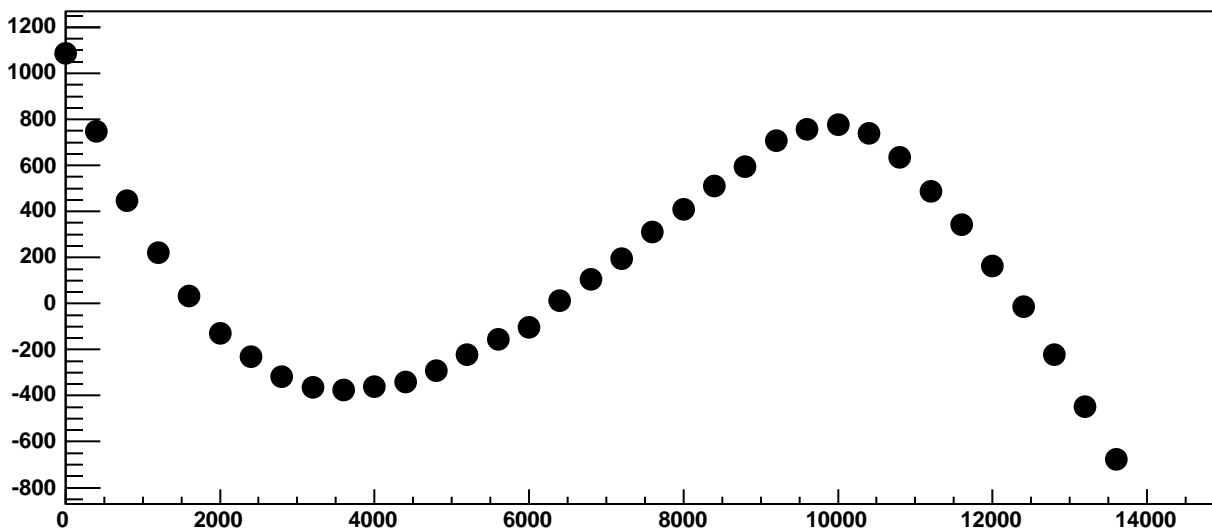
Chip 8, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC



Chip 8, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

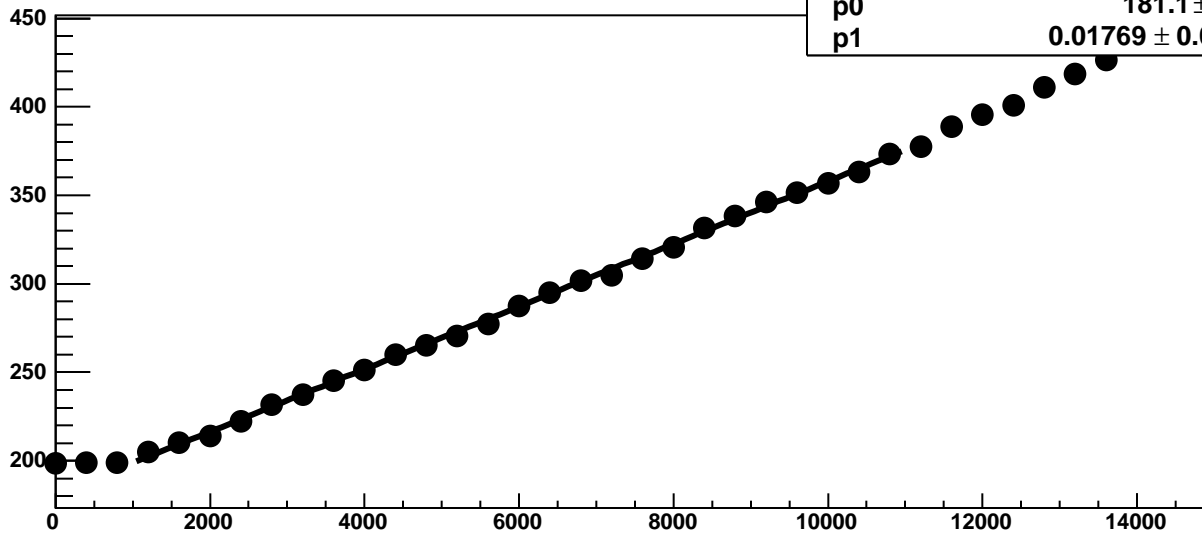


Chip 8, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC



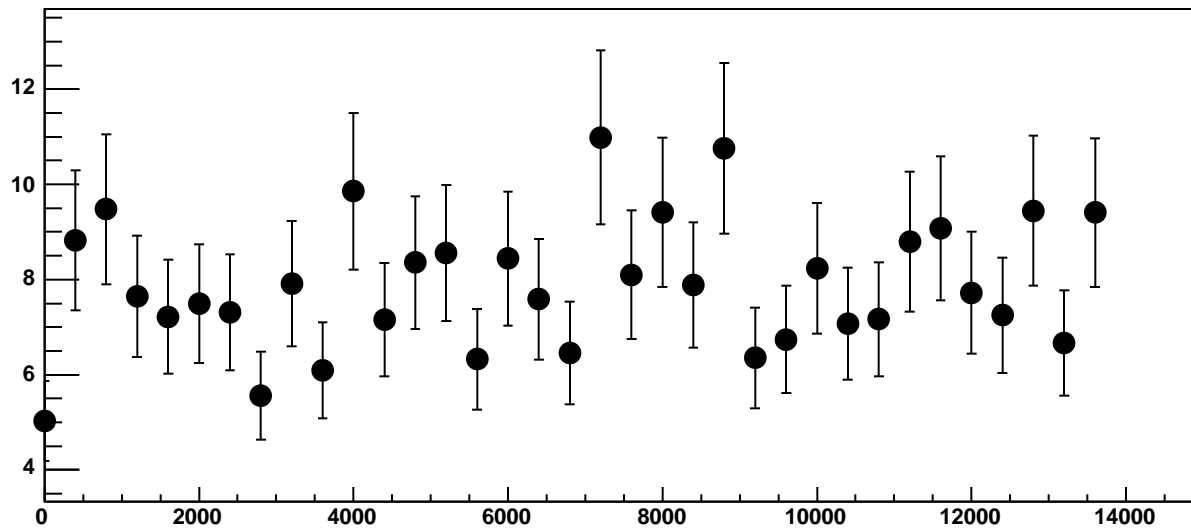


Chip 8, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

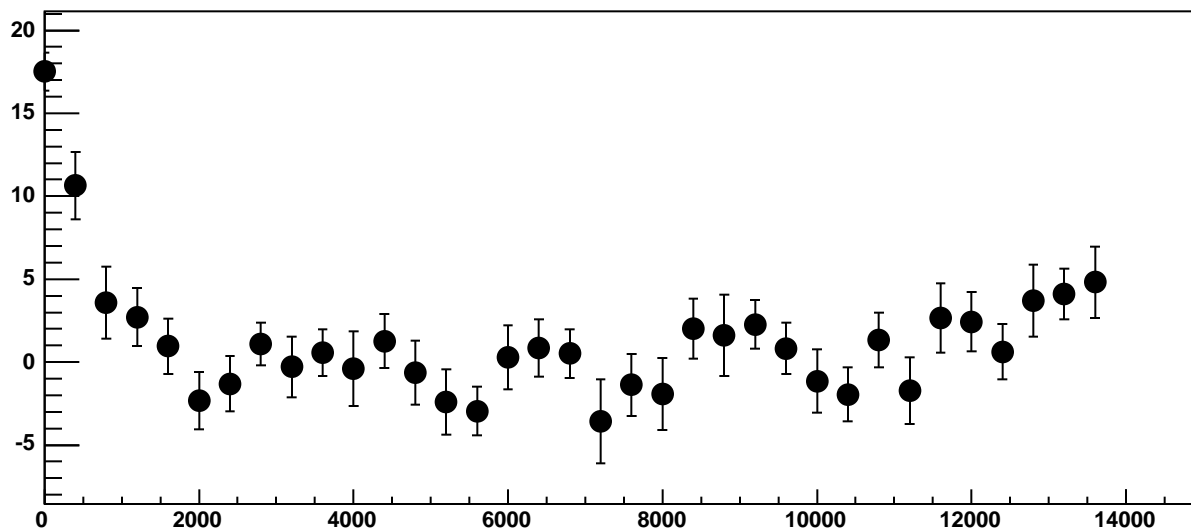


$\chi^2 / \text{ndf}$  23.04 / 23  
p0 181.1 ± 0.7618  
p1 0.01769 ± 0.0001161

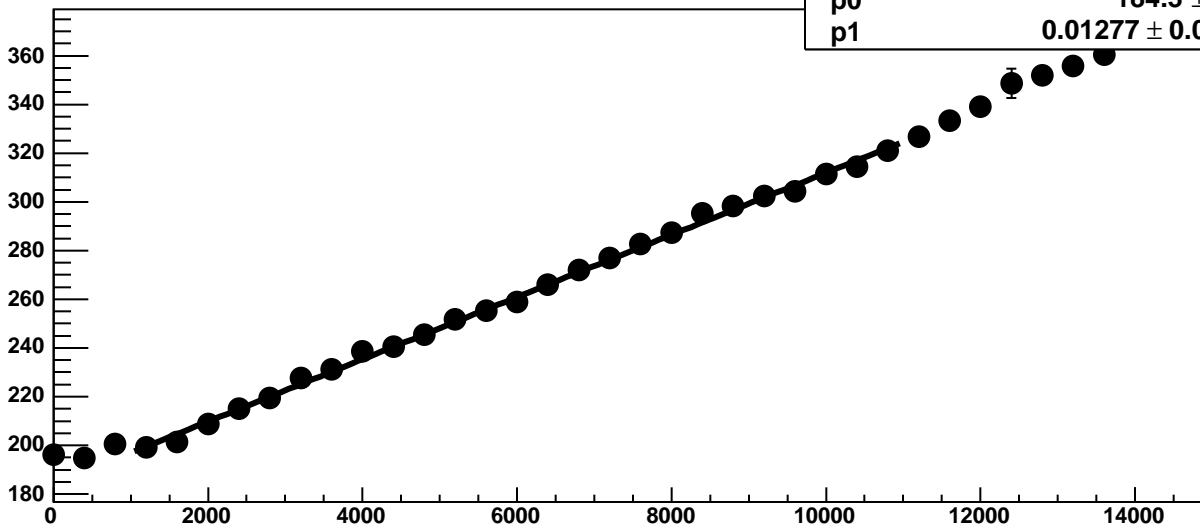
Chip 8, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

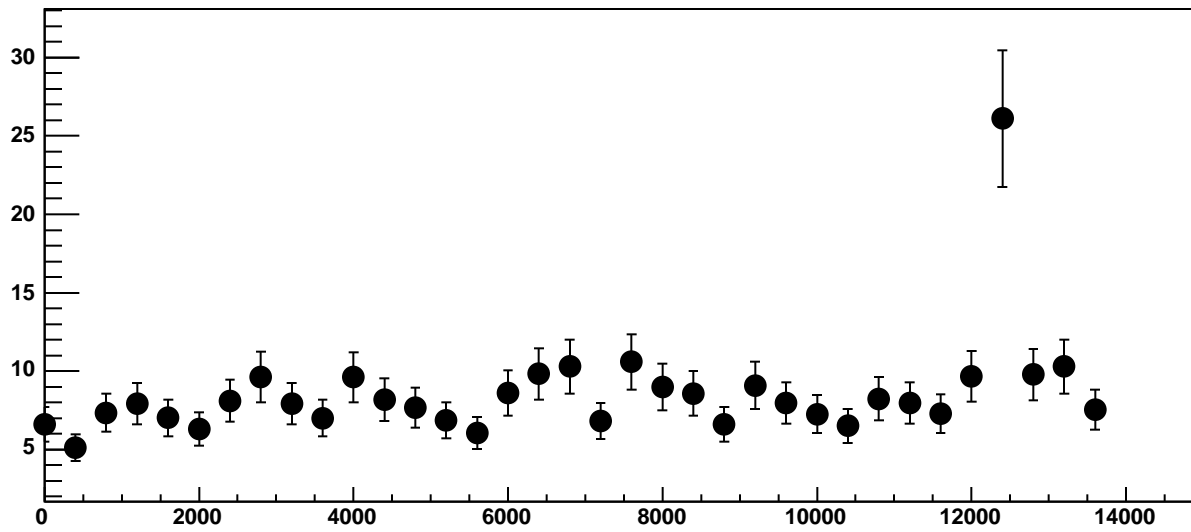


Chip 8, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

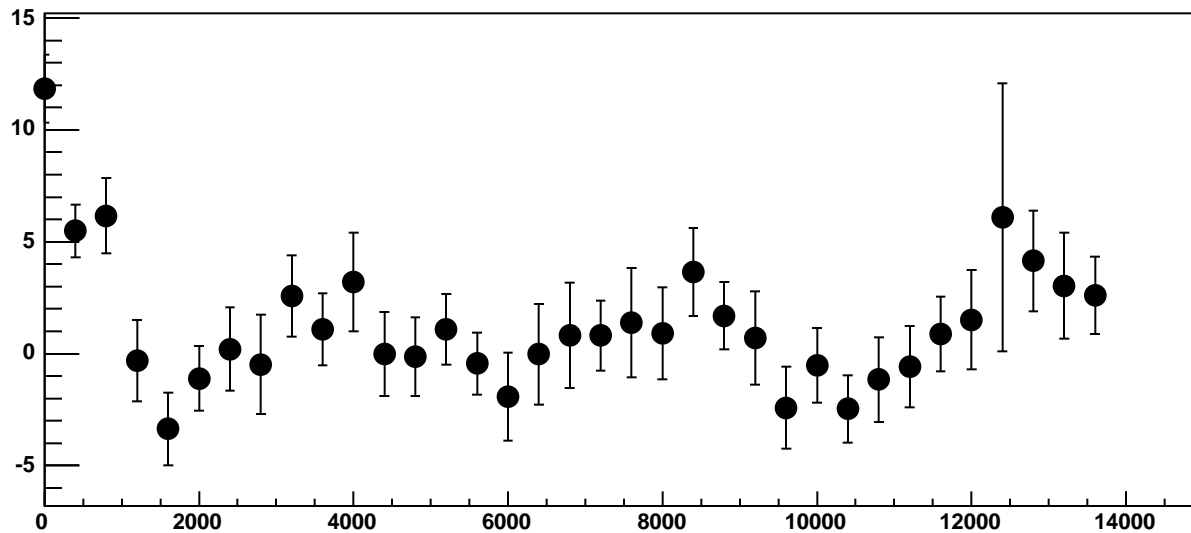


$\chi^2 / \text{ndf}$  21.83 / 23  
p0 184.3 ± 0.7994  
p1 0.01277 ± 0.0001203

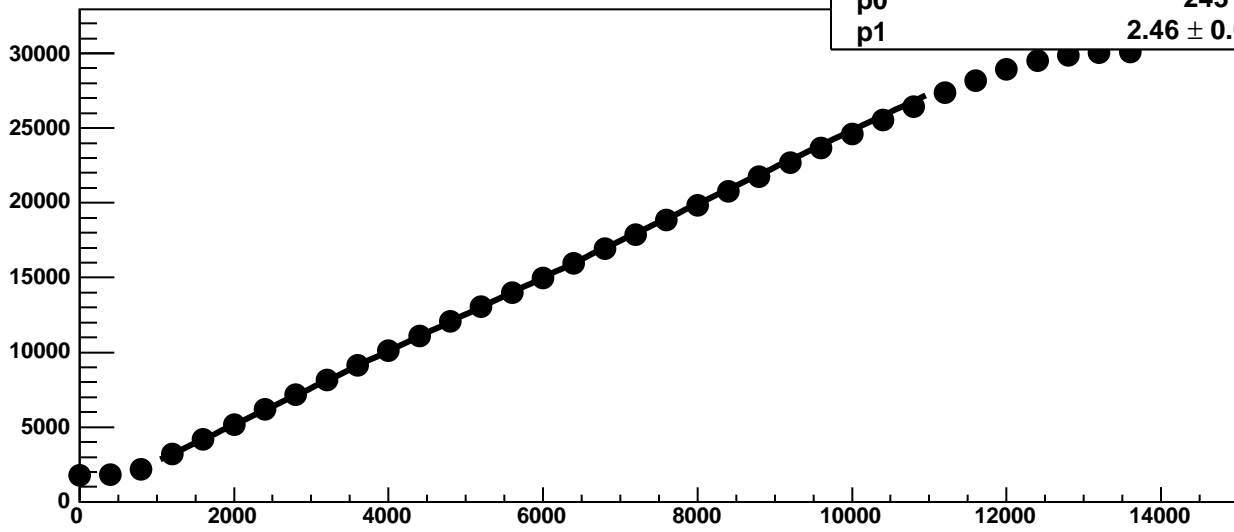
Chip 8, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



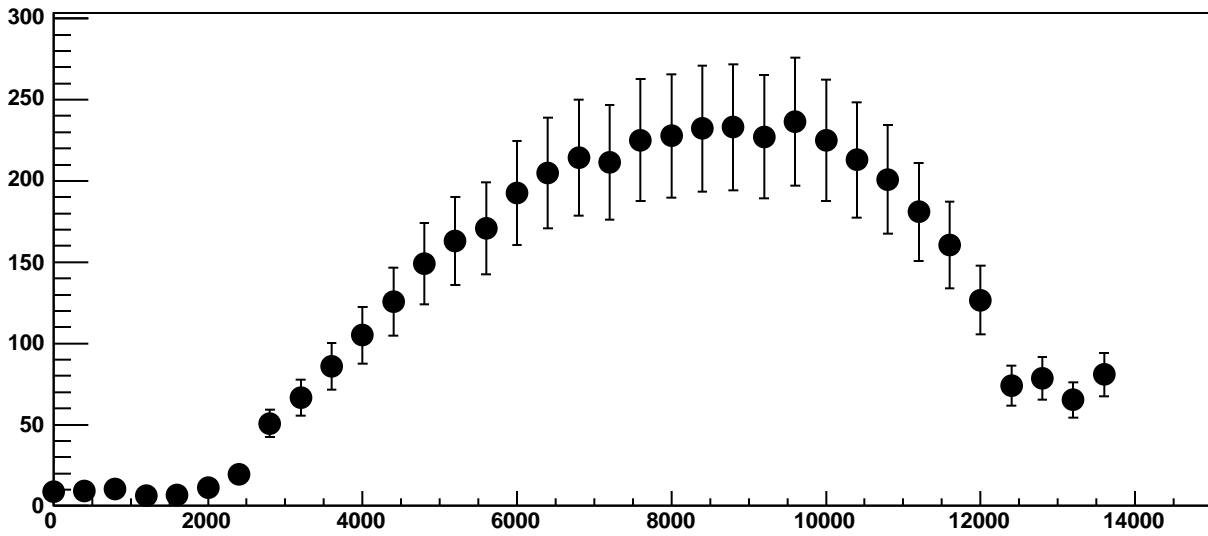
Chip 8, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



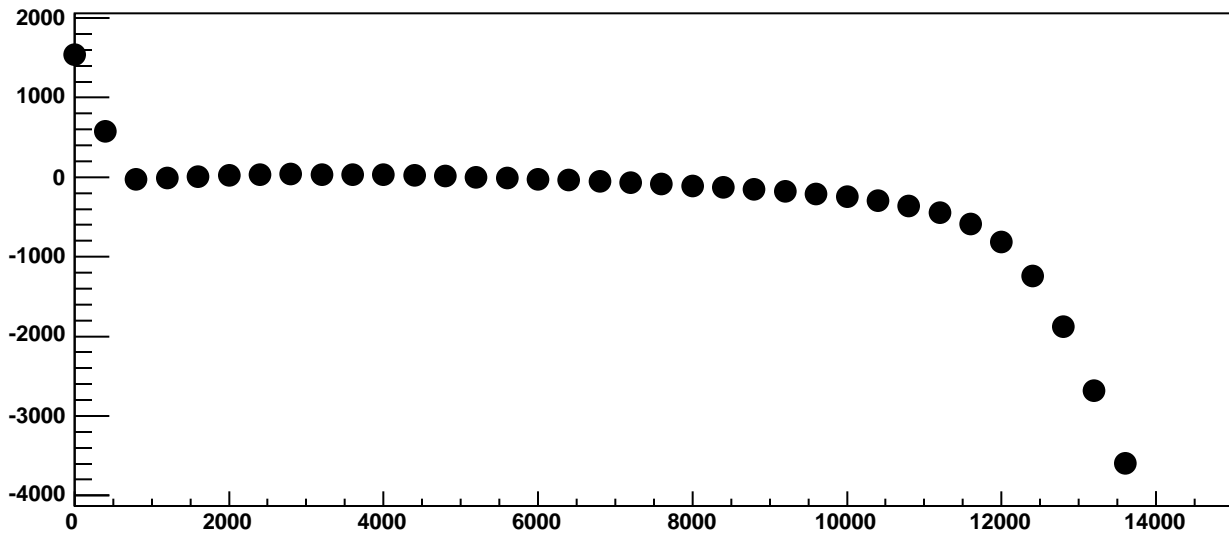
Chip 8, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC



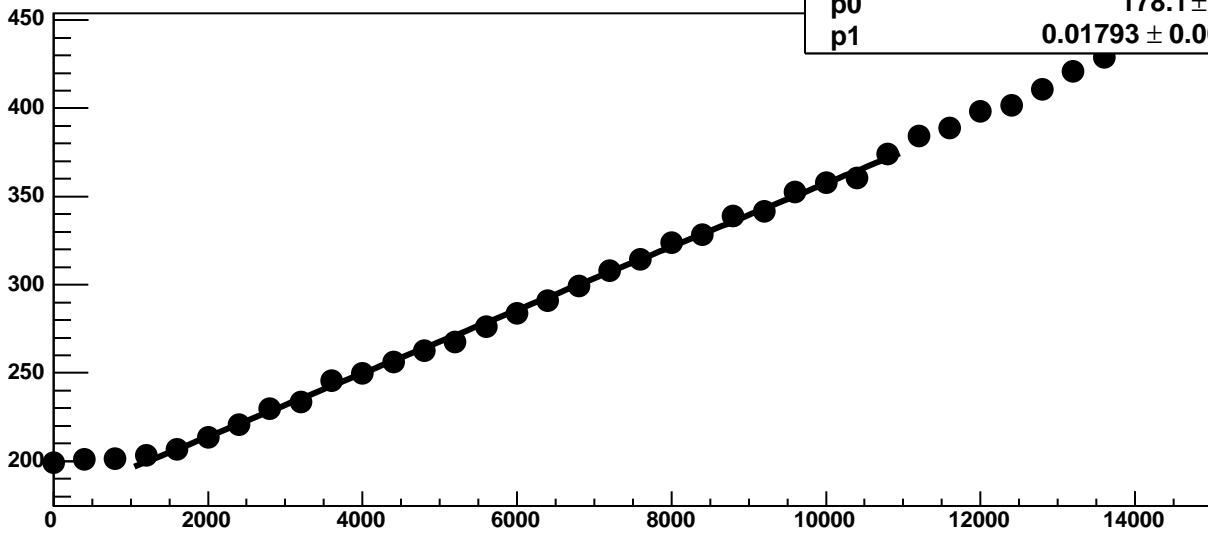
Chip 8, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

27.07 / 23

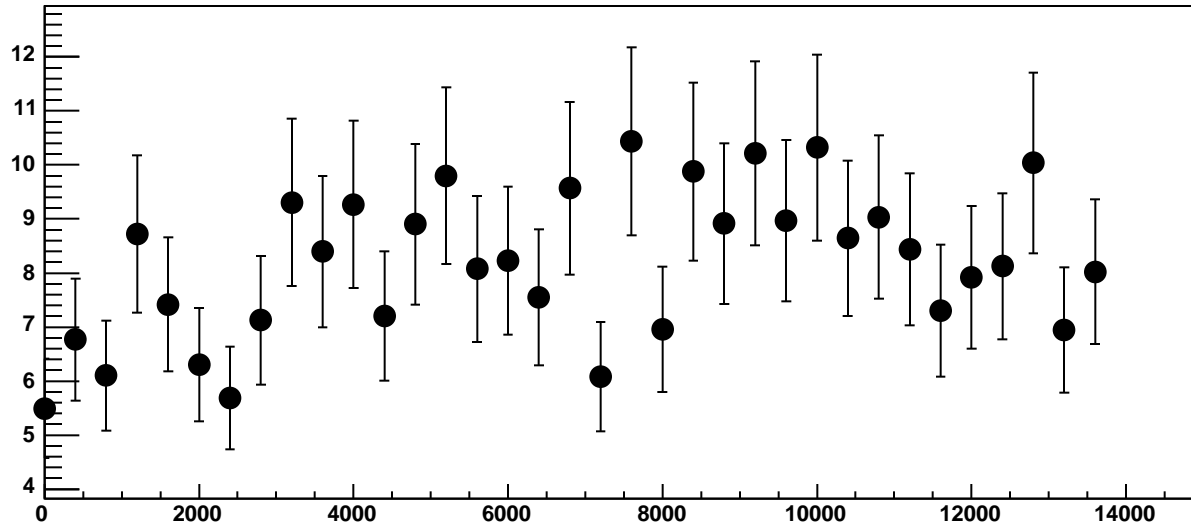
p0

$178.1 \pm 0.8083$

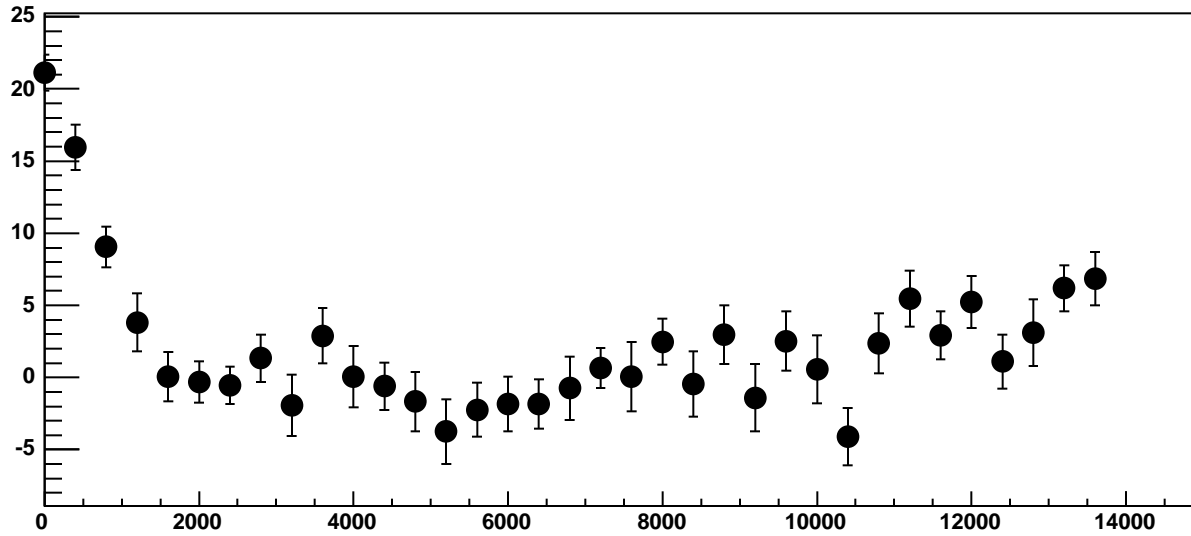
p1

$0.01793 \pm 0.0001295$

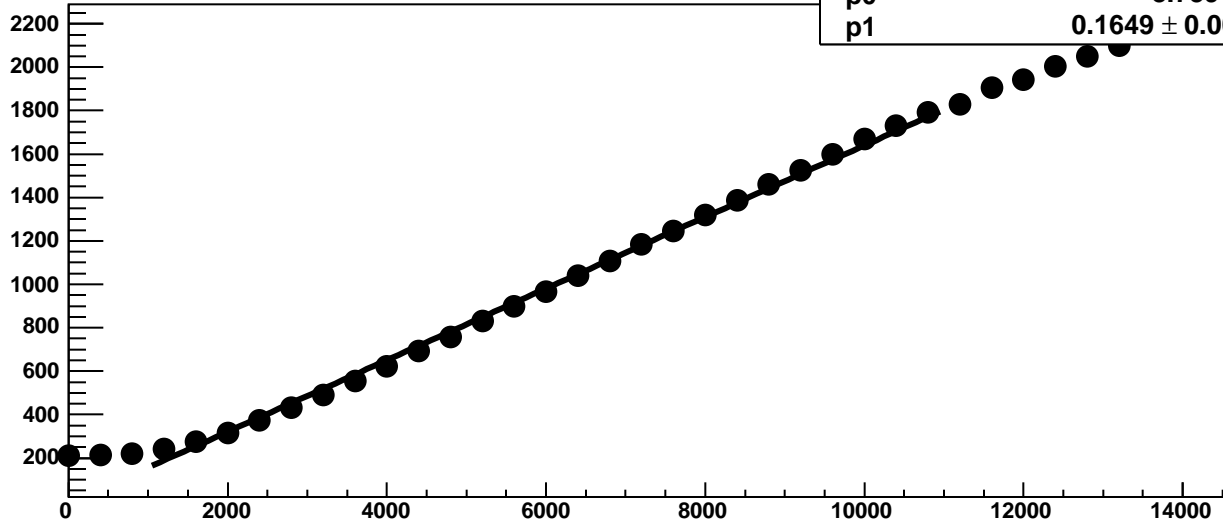
Chip 8, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



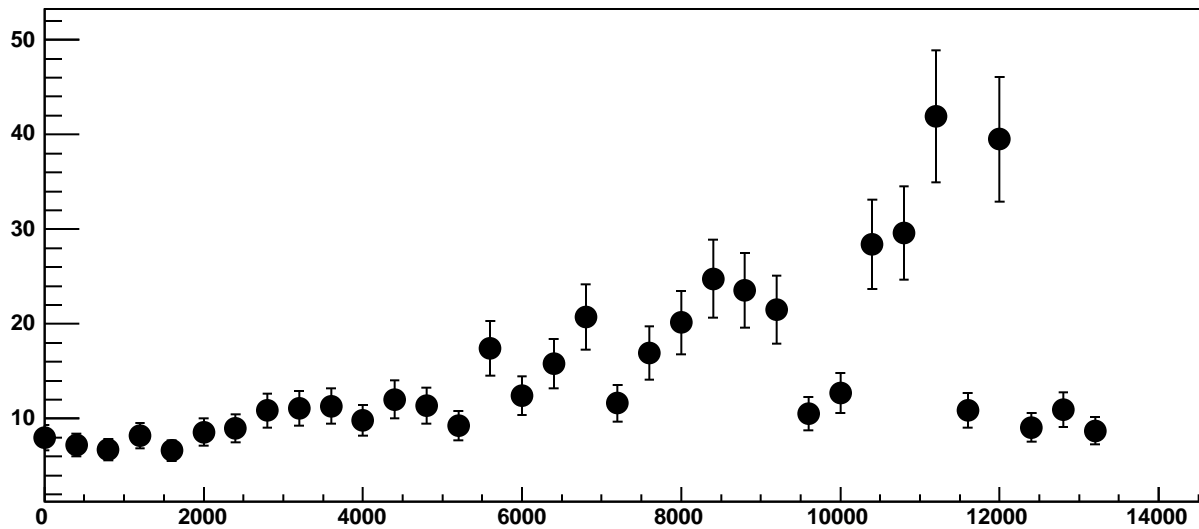
Chip 8, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



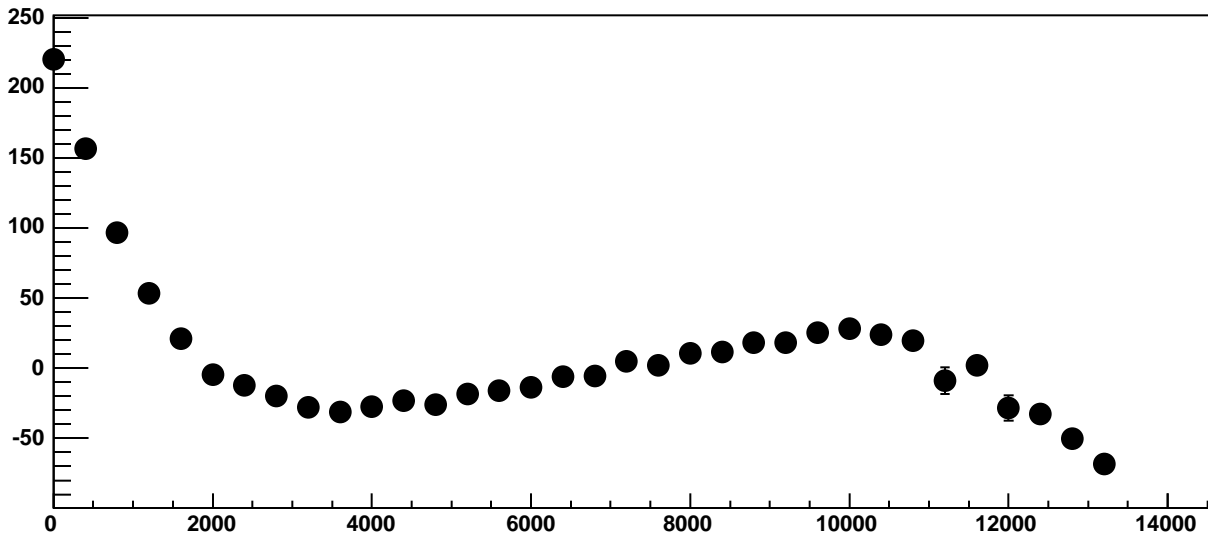
Chip 8, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



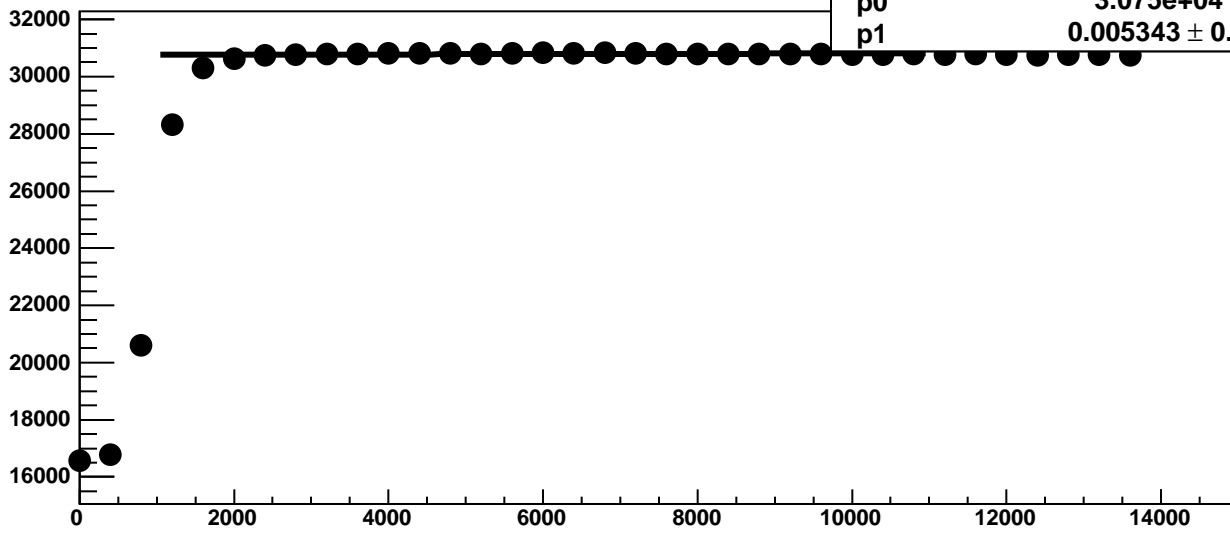
Chip 8, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



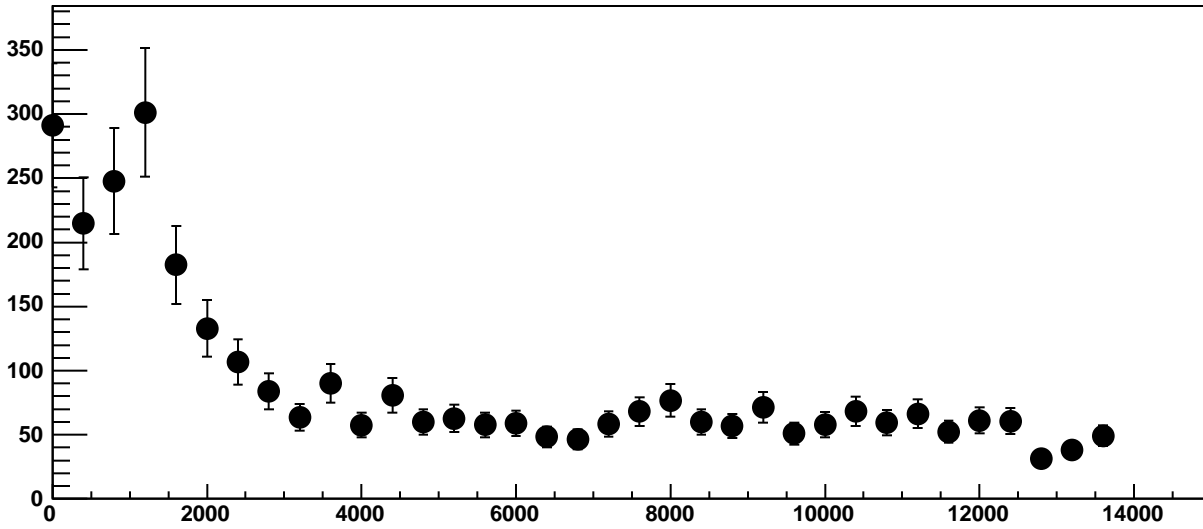
Chip 8, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



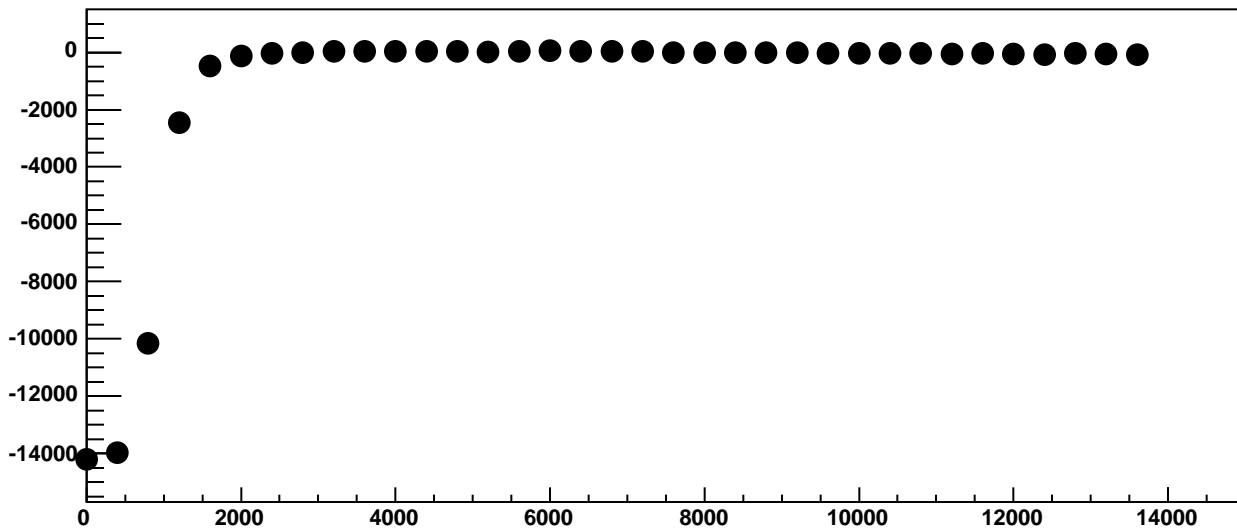
Chip 8, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



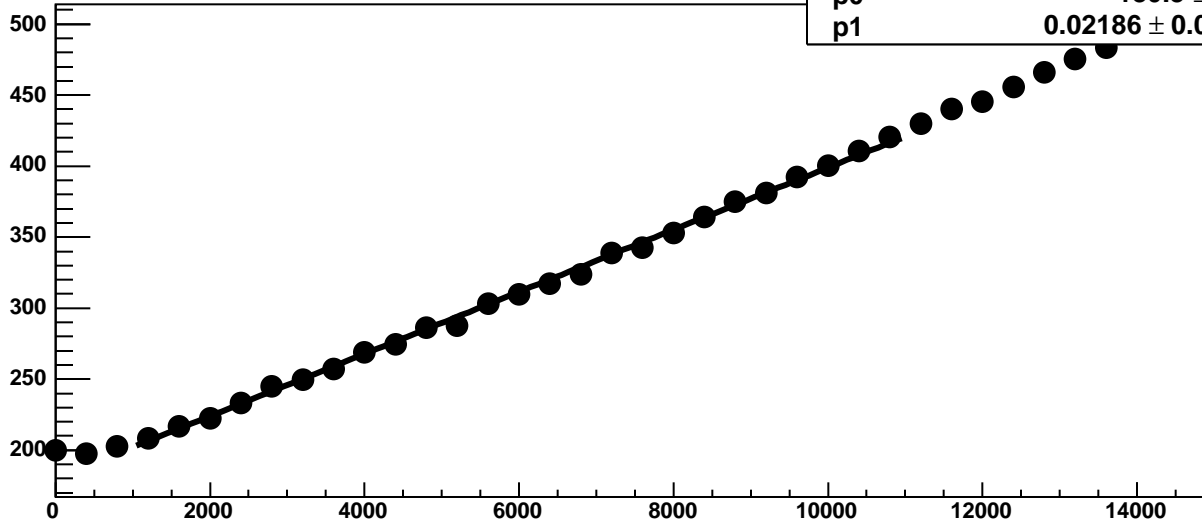
Chip 8, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

57.73 / 23

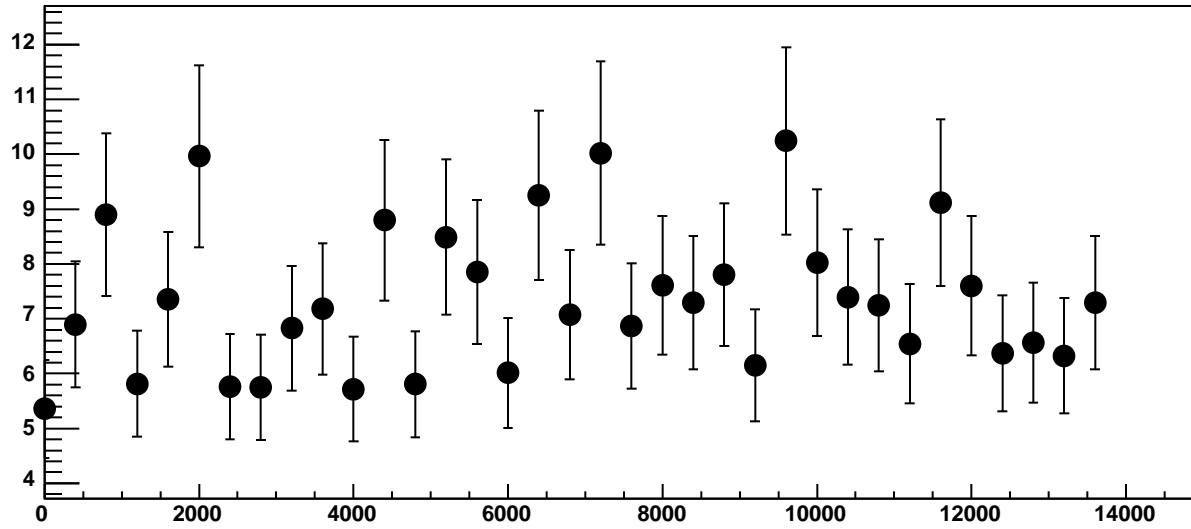
p0

$180.3 \pm 0.7159$

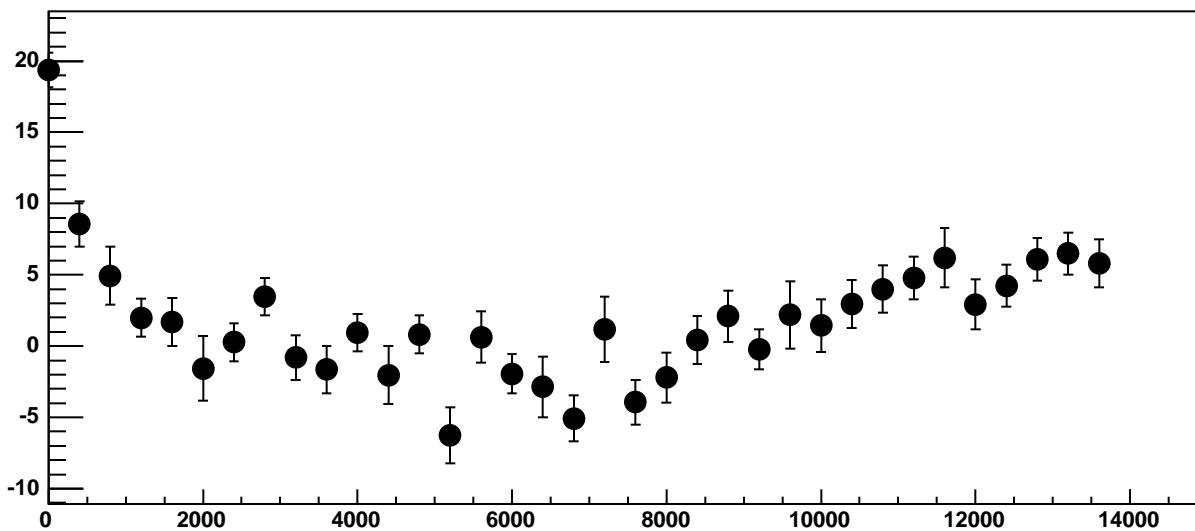
p1

$0.02186 \pm 0.0001125$

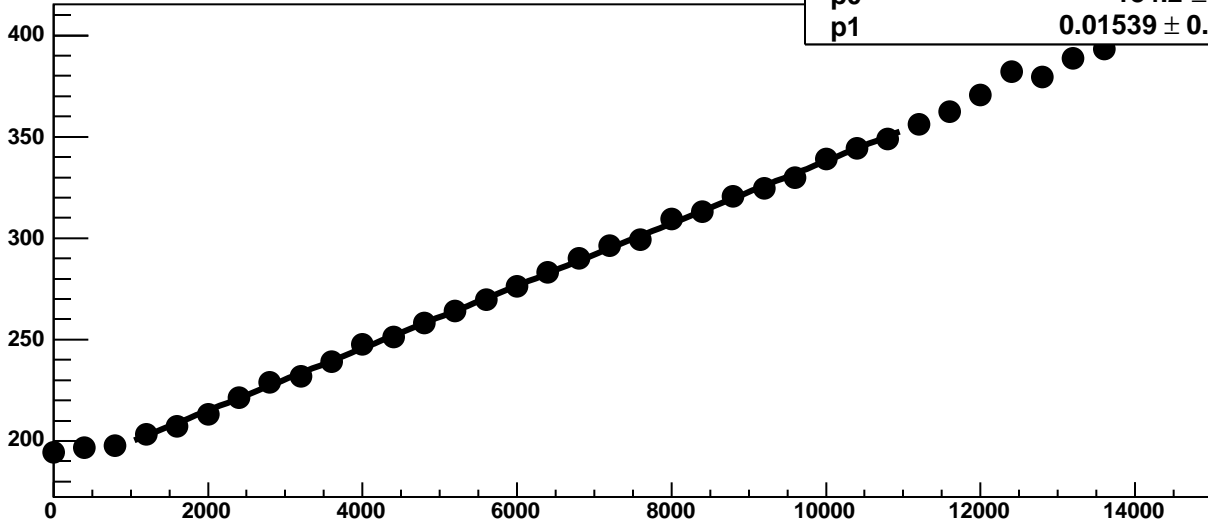
Chip 8, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

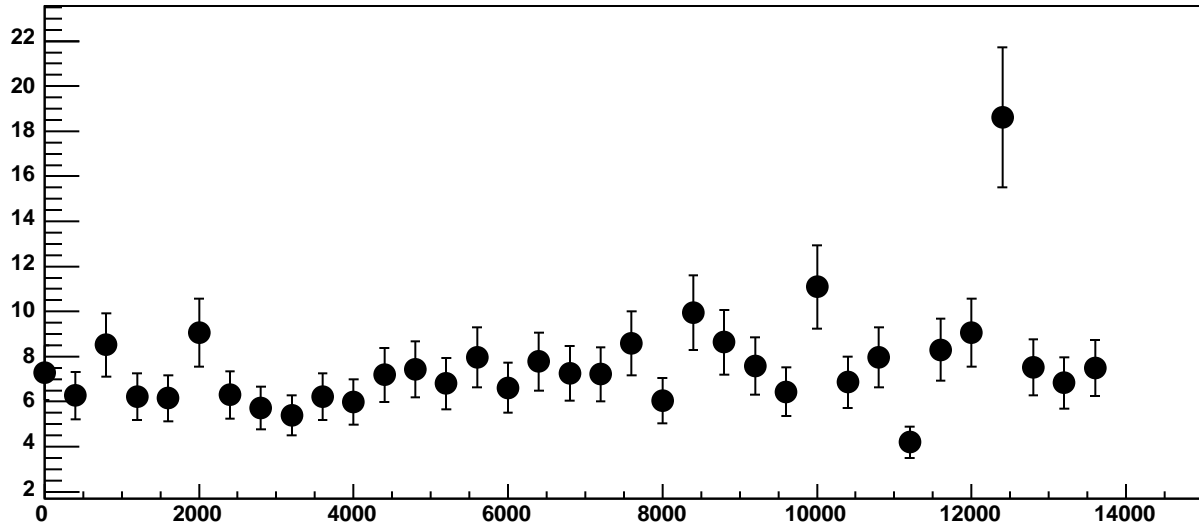


Chip 8, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

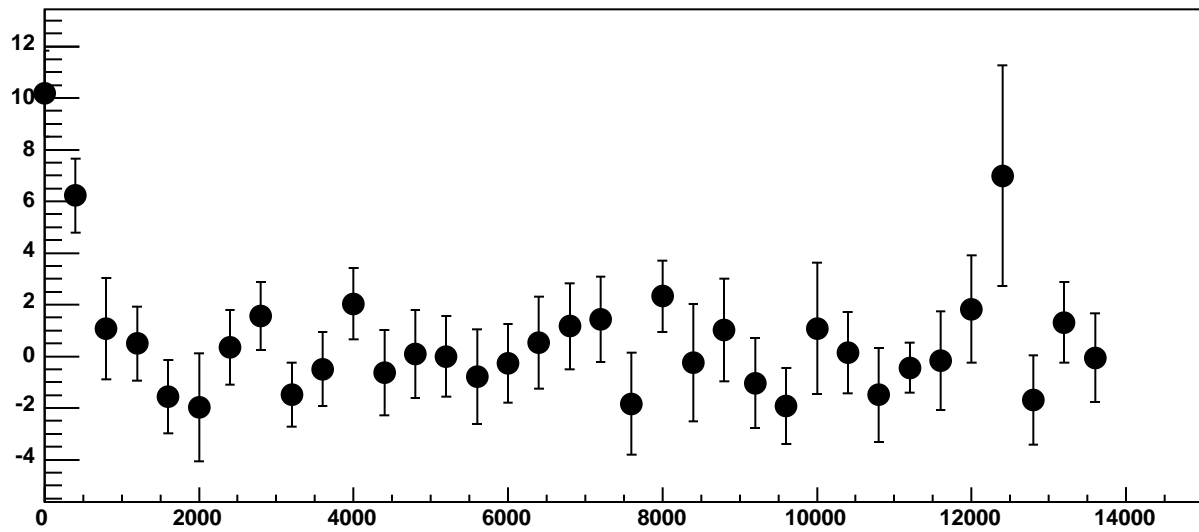


$\chi^2 / \text{ndf}$  16.05 / 23  
p0  $184.2 \pm 0.6974$   
p1  $0.01539 \pm 0.000112$

Chip 8, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

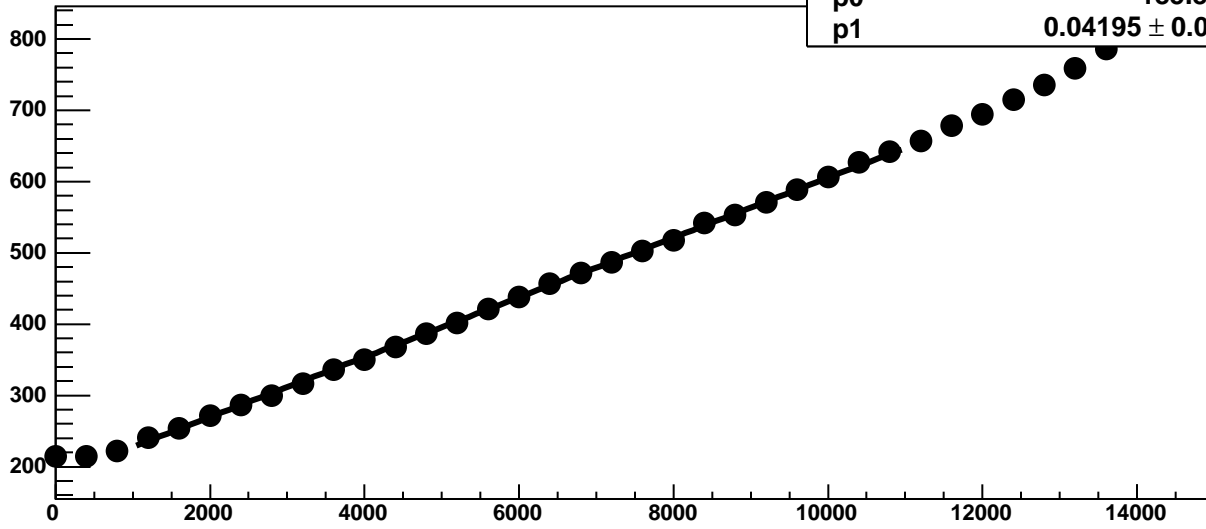


Chip 8, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



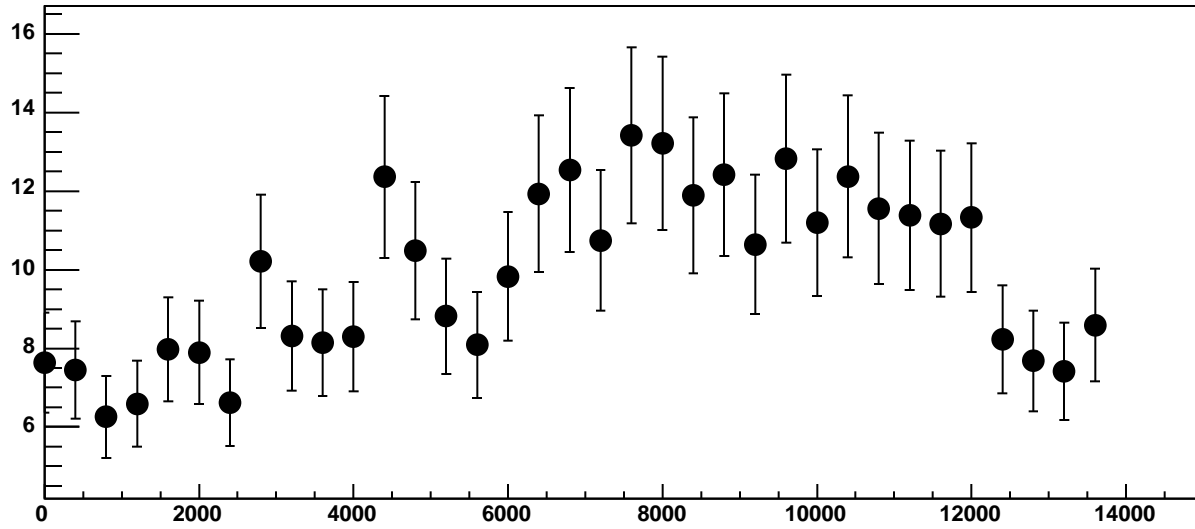


Chip 8, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

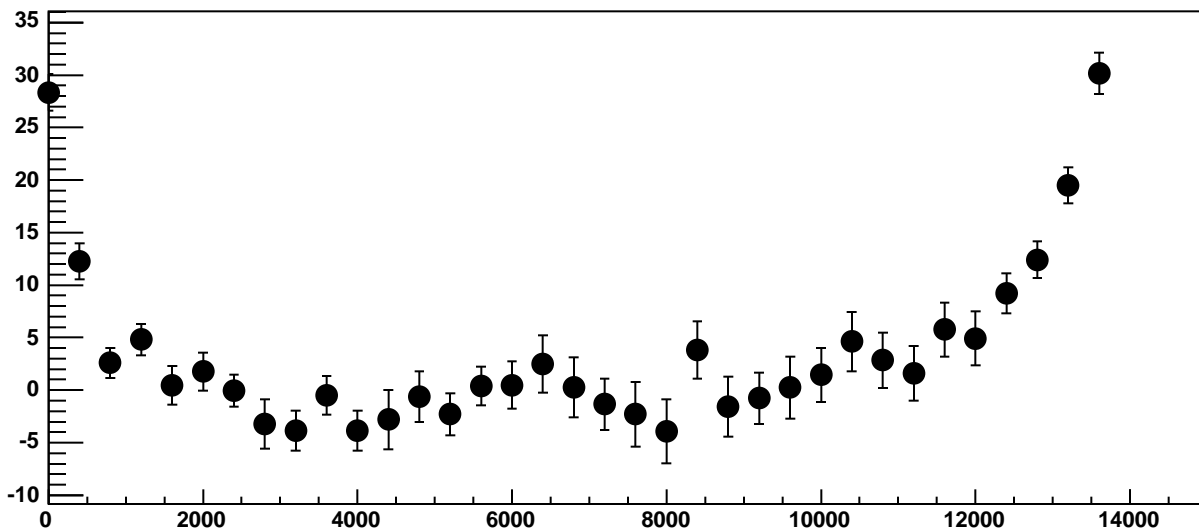


$\chi^2 / \text{ndf}$  33.58 / 23  
p0 185.8 ± 0.88  
p1 0.04195 ± 0.0001534

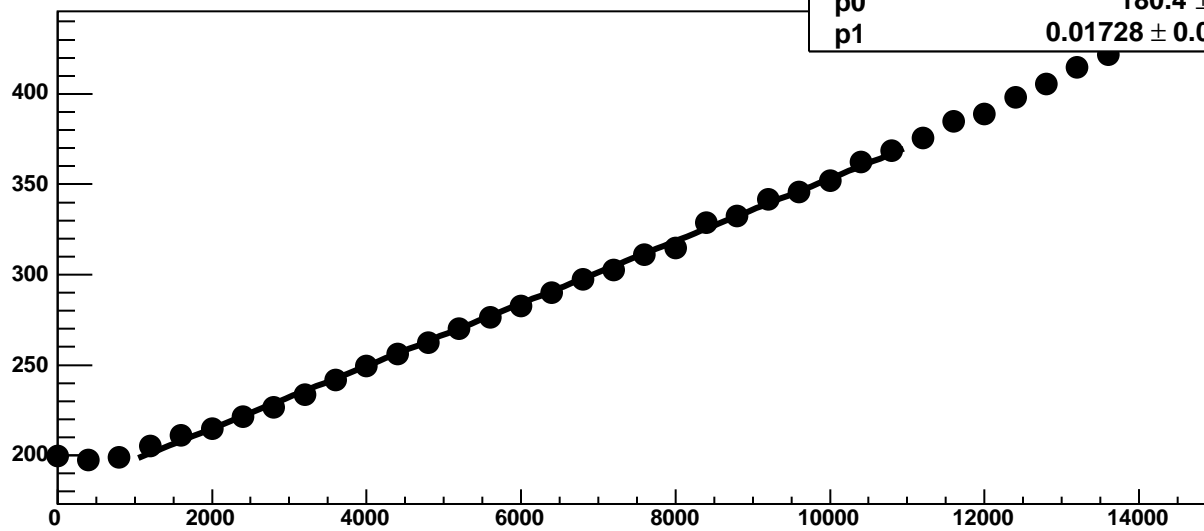
Chip 8, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

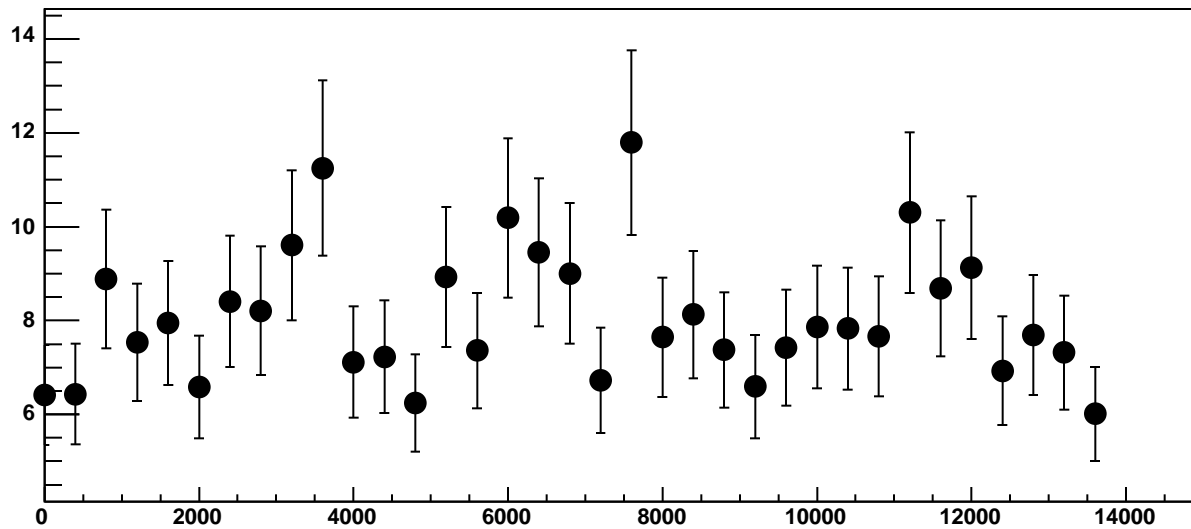


Chip 8, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

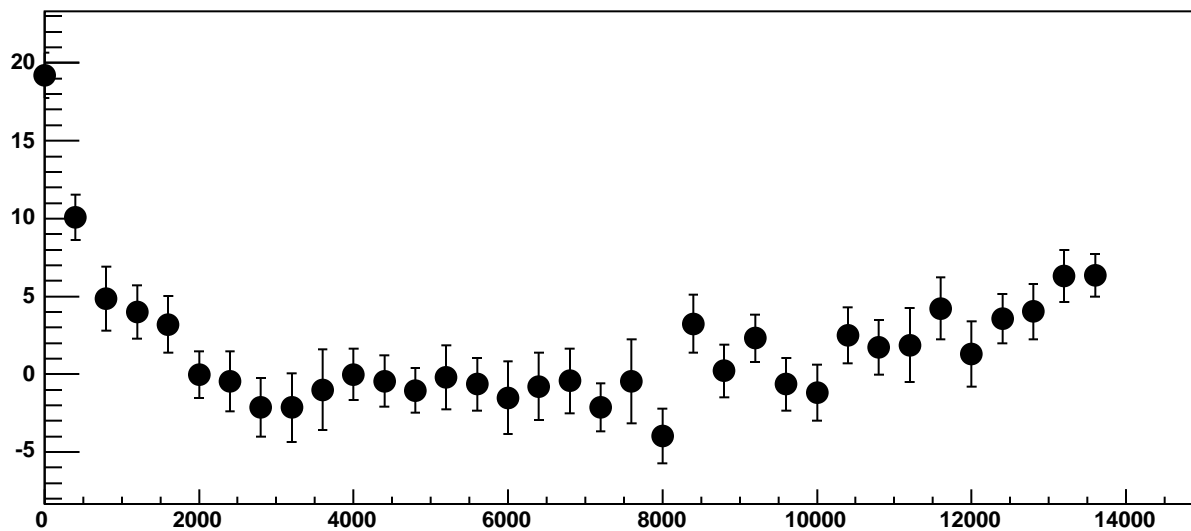


$\chi^2 / \text{ndf}$  28.02 / 23  
p0  $180.4 \pm 0.8229$   
p1  $0.01728 \pm 0.0001223$

Chip 8, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

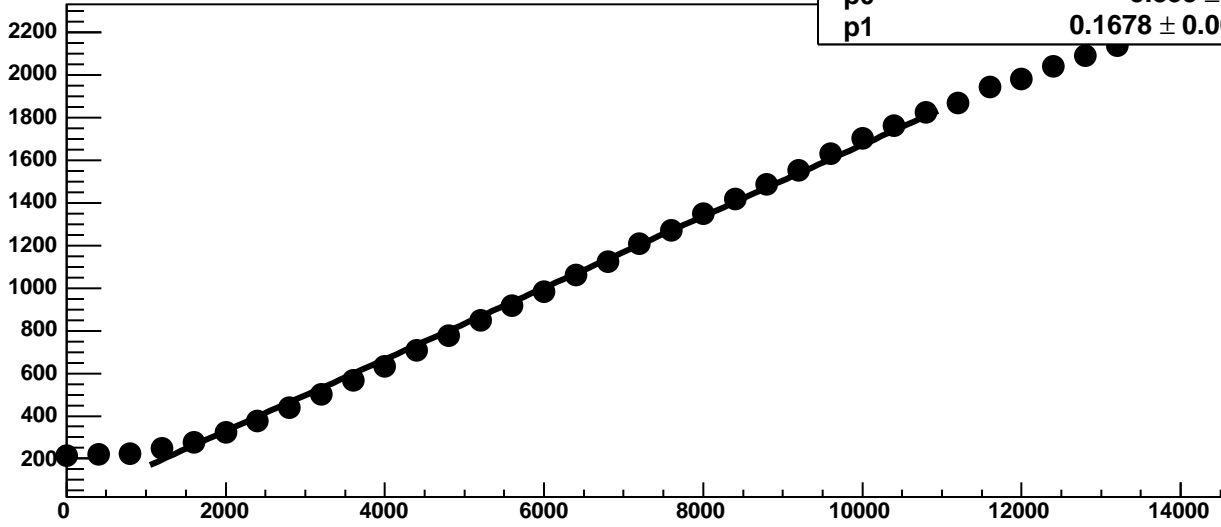


Chip 8, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

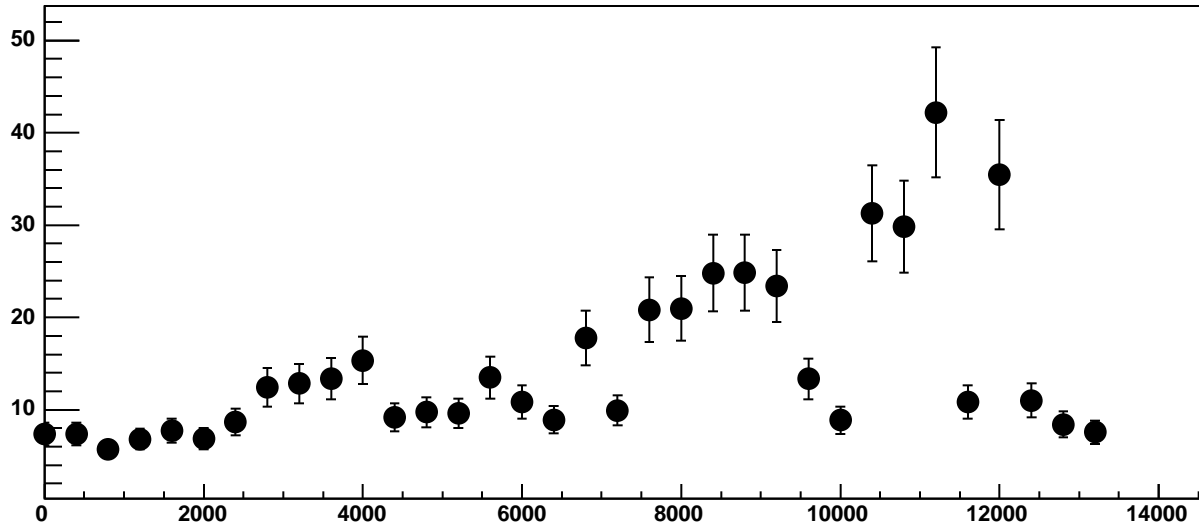


Chip 8, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

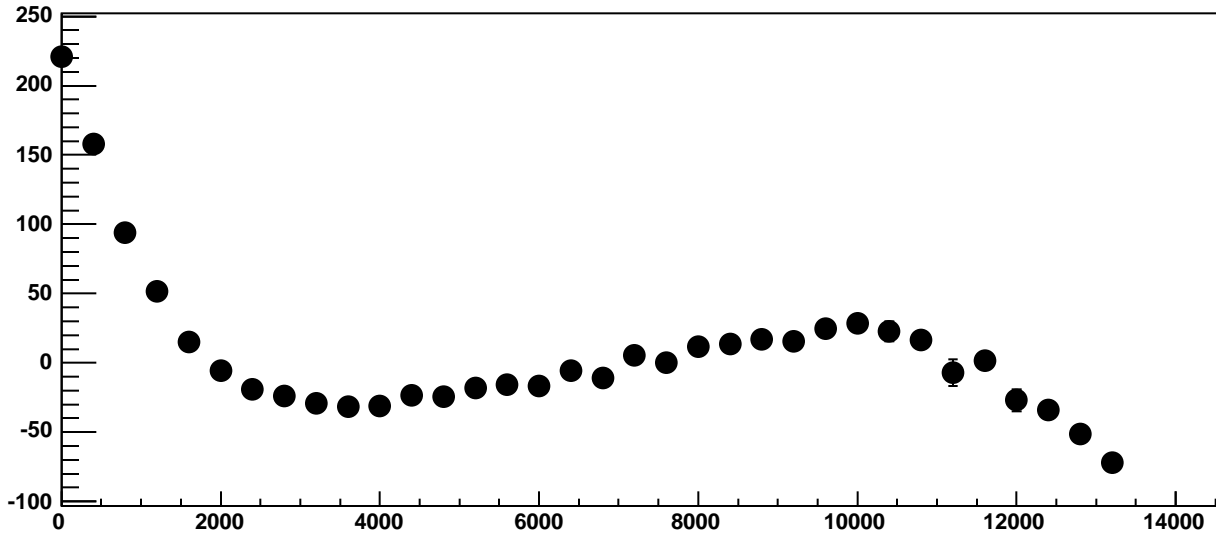
$\chi^2 / \text{ndf}$  2329 / 23  
p0  $-5.853 \pm 0.9894$   
p1  $0.1678 \pm 0.0001859$



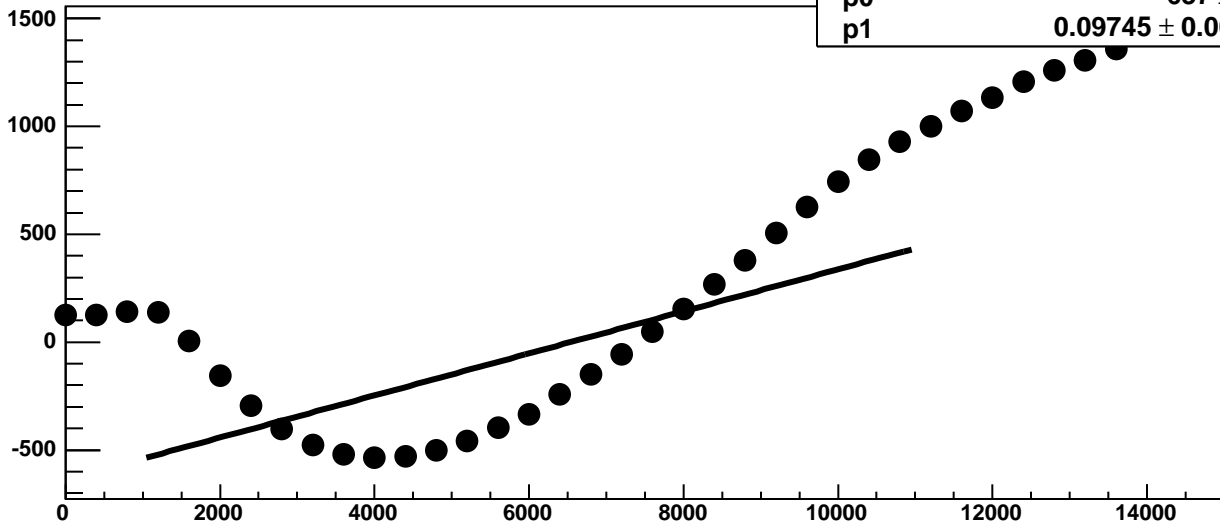
Chip 8, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

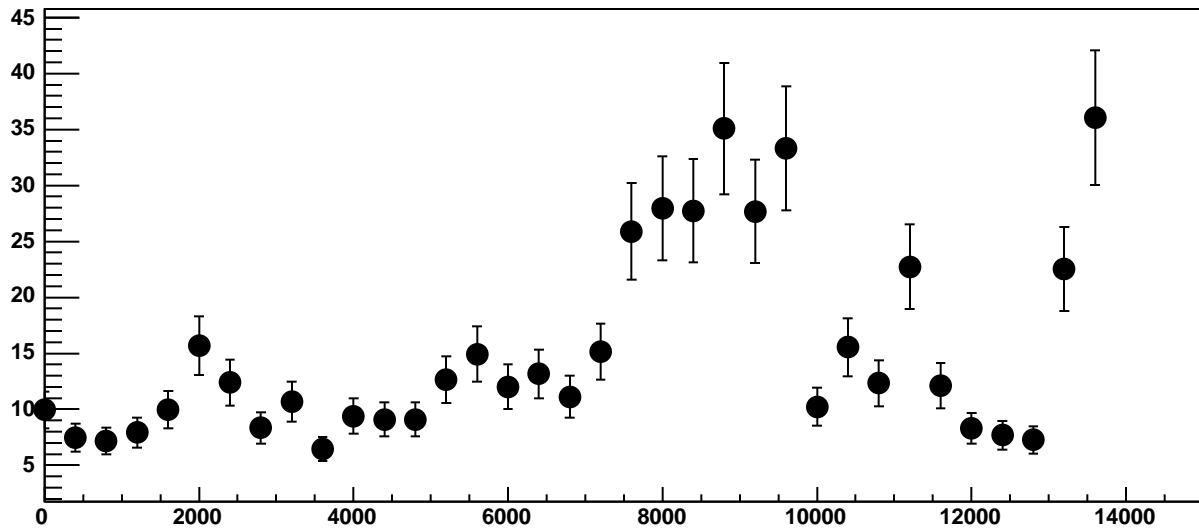


Chip 8, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

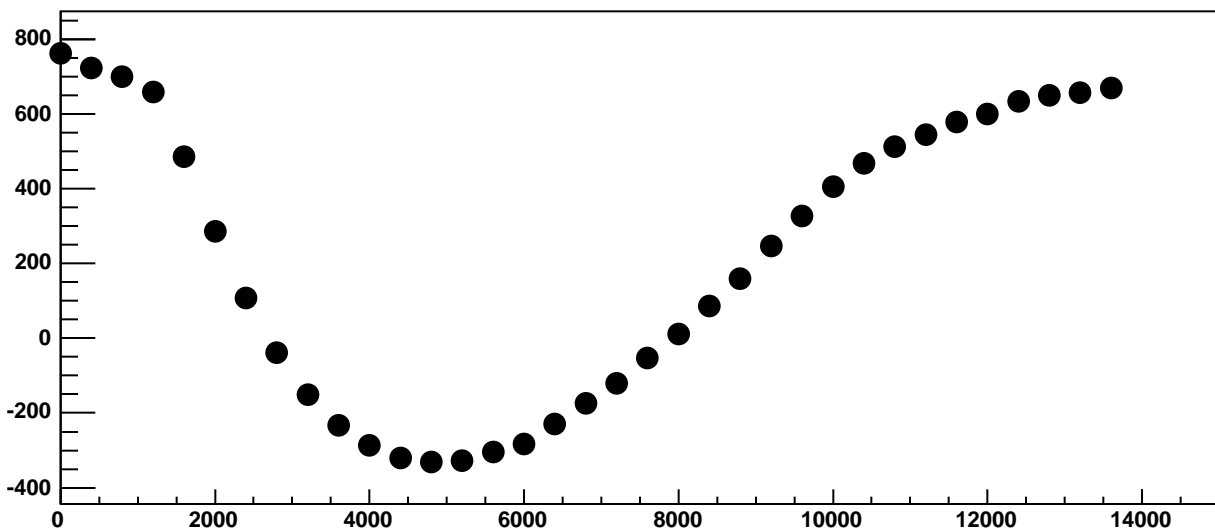


$\chi^2 / \text{ndf}$  4.061e+05 / 23  
p0 -637 ± 1.089  
p1 0.09745 ± 0.0002002

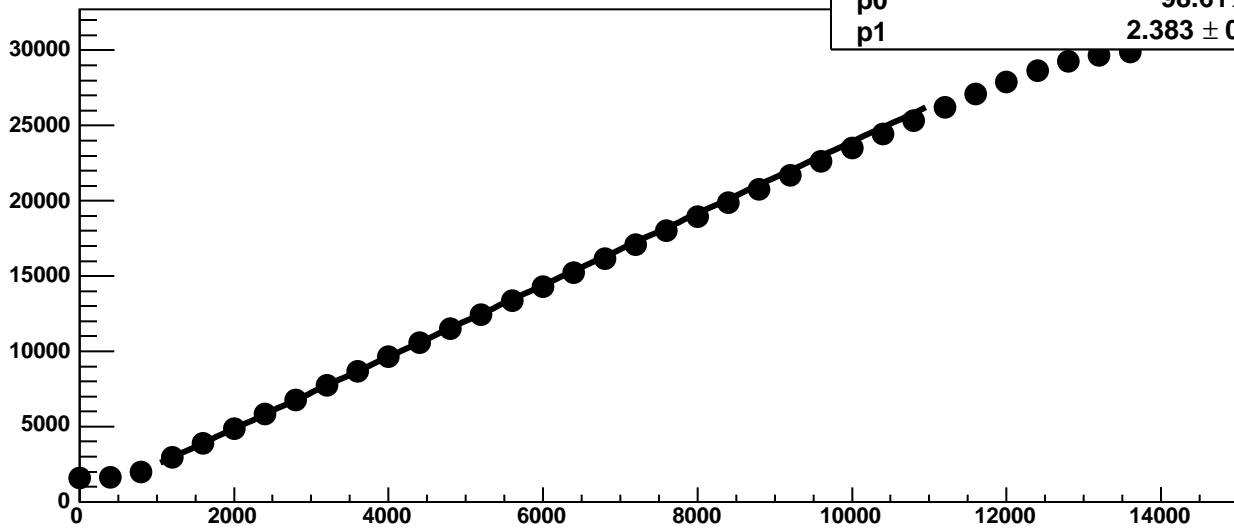
Chip 8, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



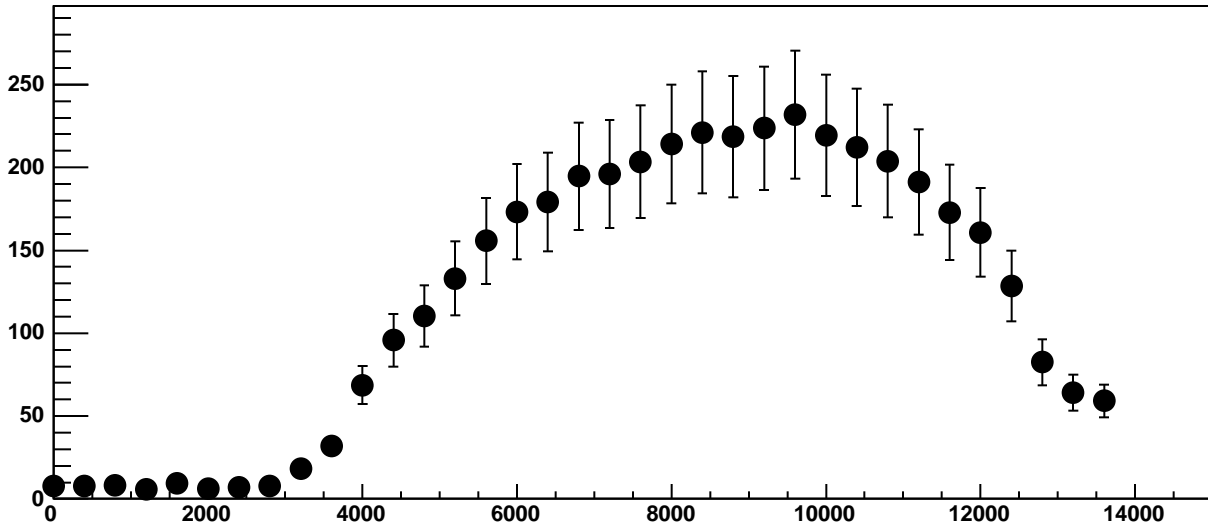
Chip 8, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



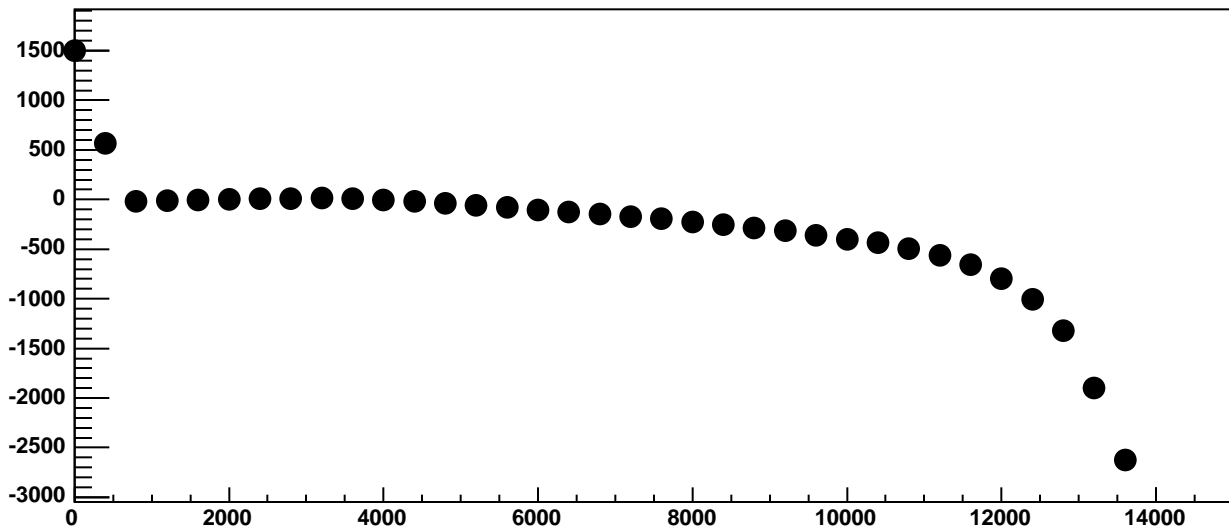
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC



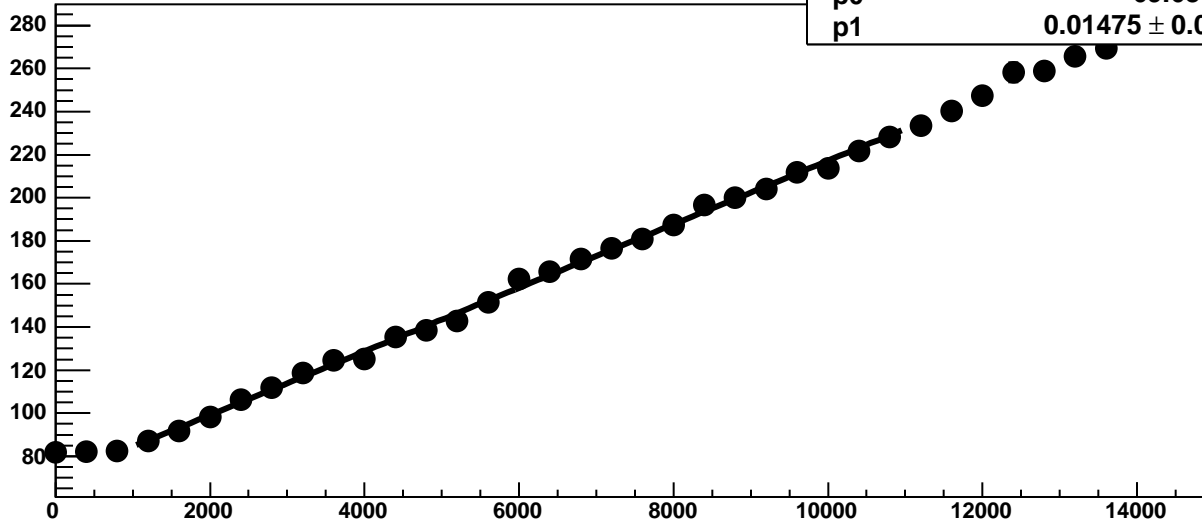
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



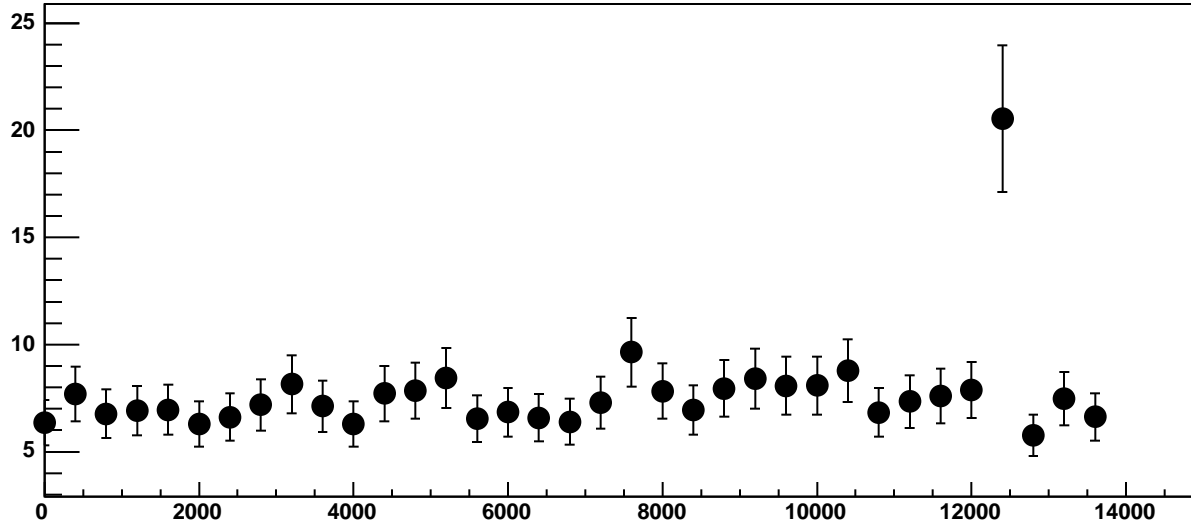
Chip 8, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC



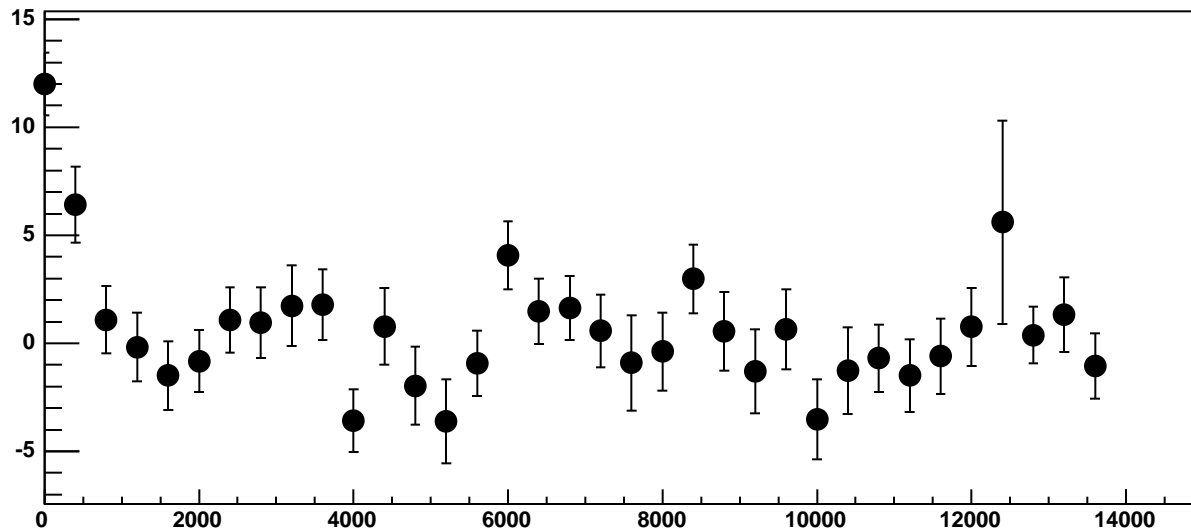
Chip 8, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



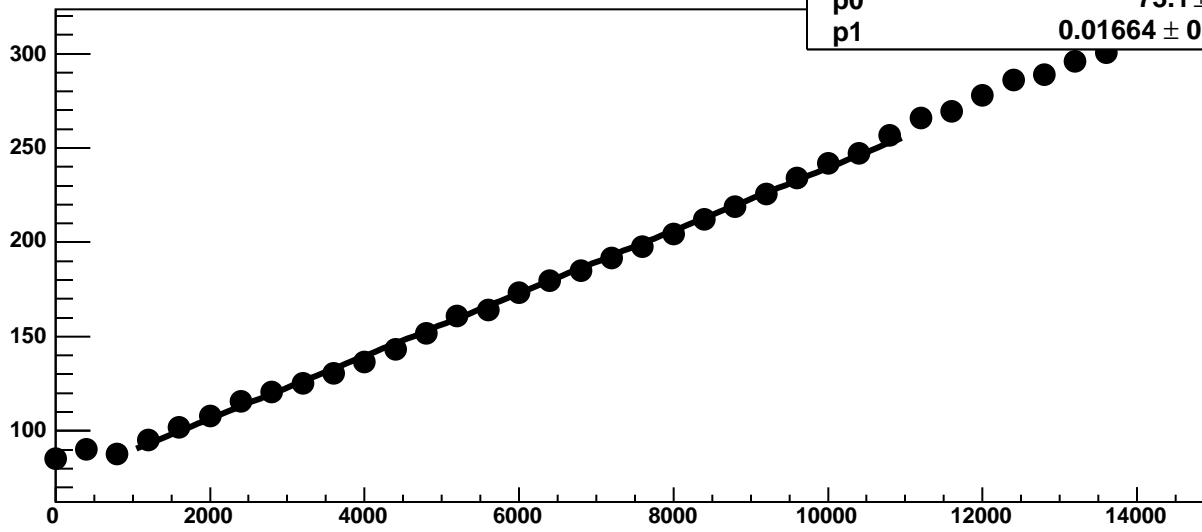
Chip 8, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

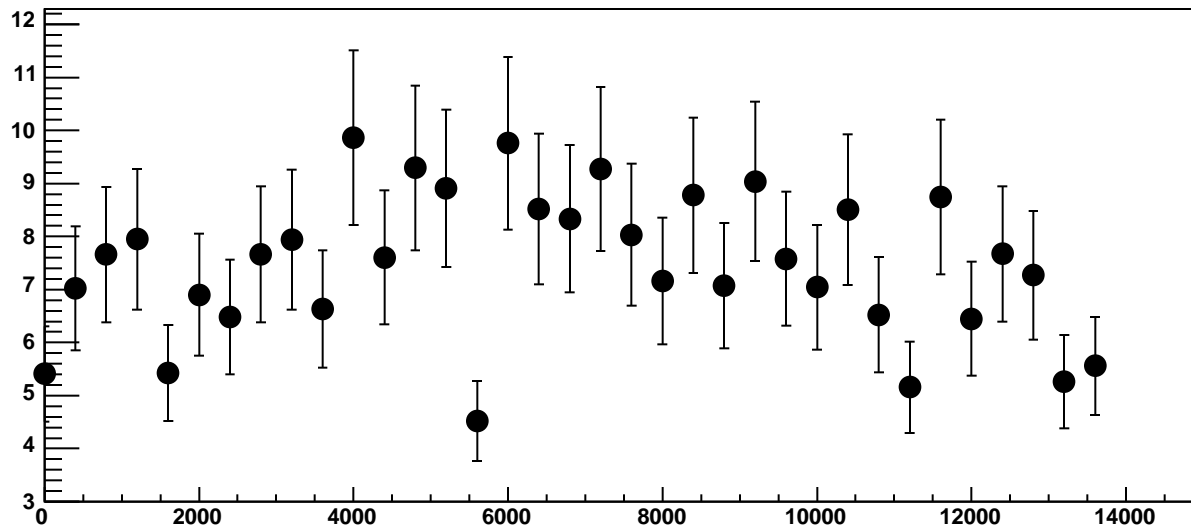


Chip 8, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

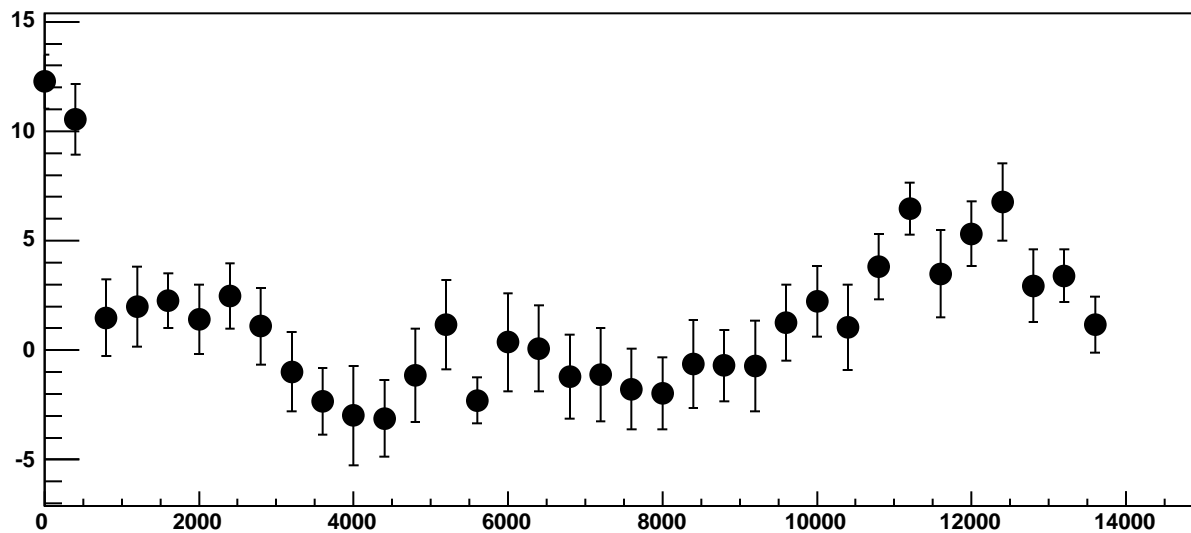


$\chi^2 / \text{ndf}$  34.15 / 23  
p0 73.1 ± 0.7408  
p1 0.01664 ± 0.000114

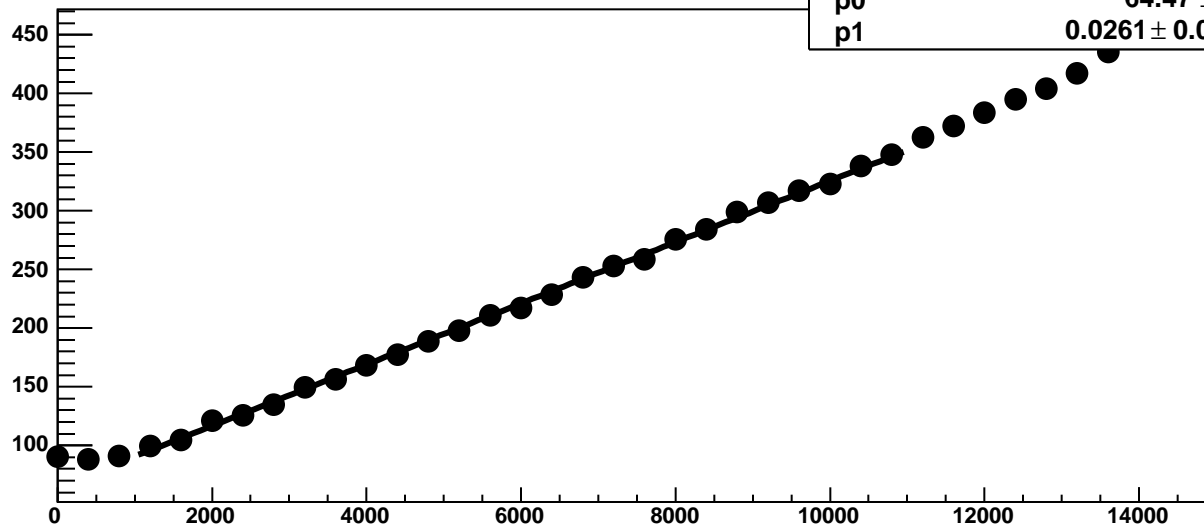
Chip 8, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

34.62 / 23

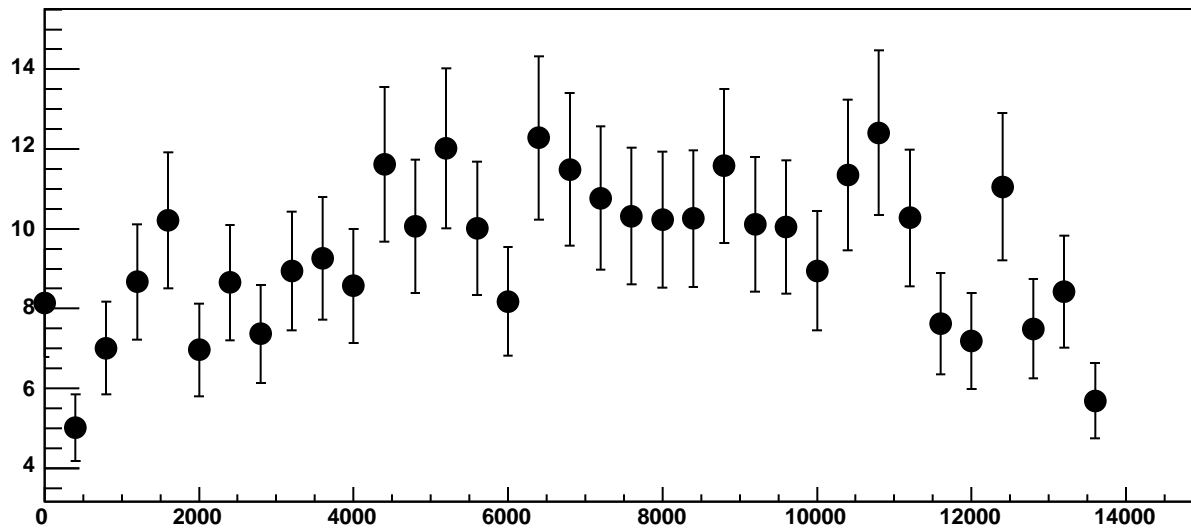
p0

$64.47 \pm 0.9411$

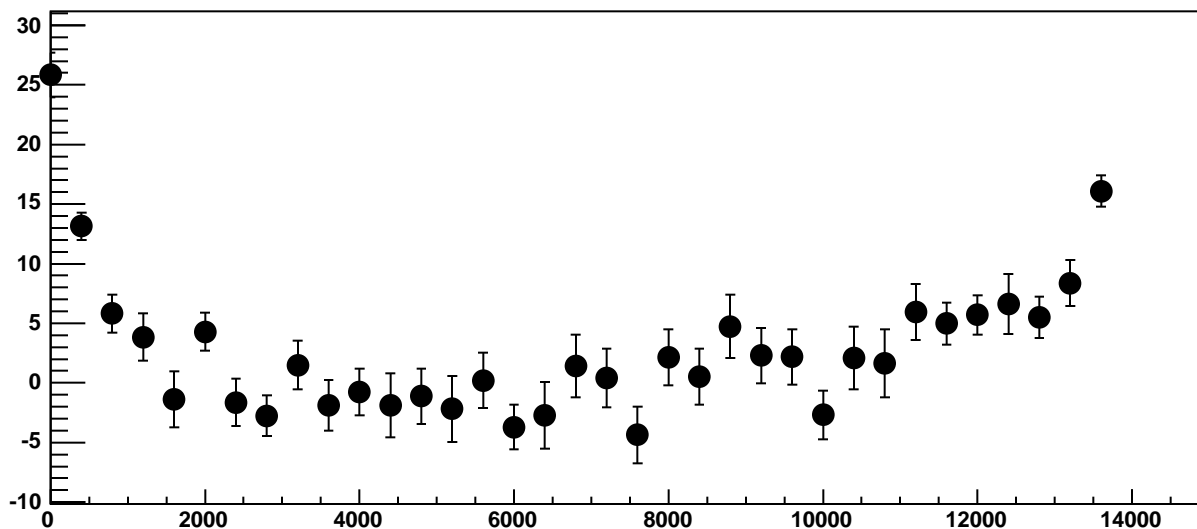
p1

$0.0261 \pm 0.0001515$

Chip 8, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

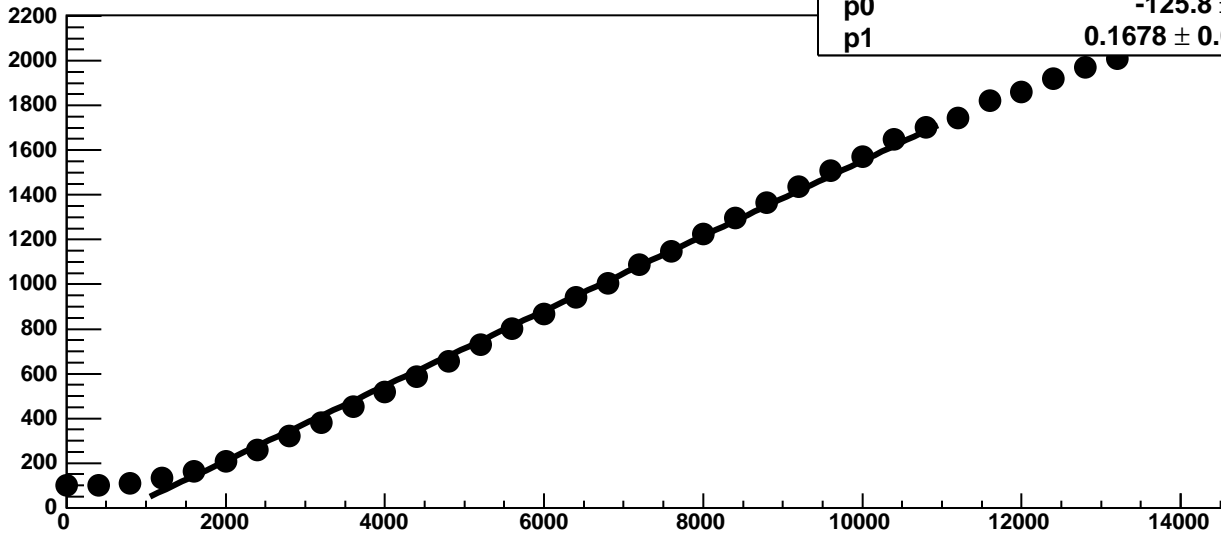


Chip 8, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

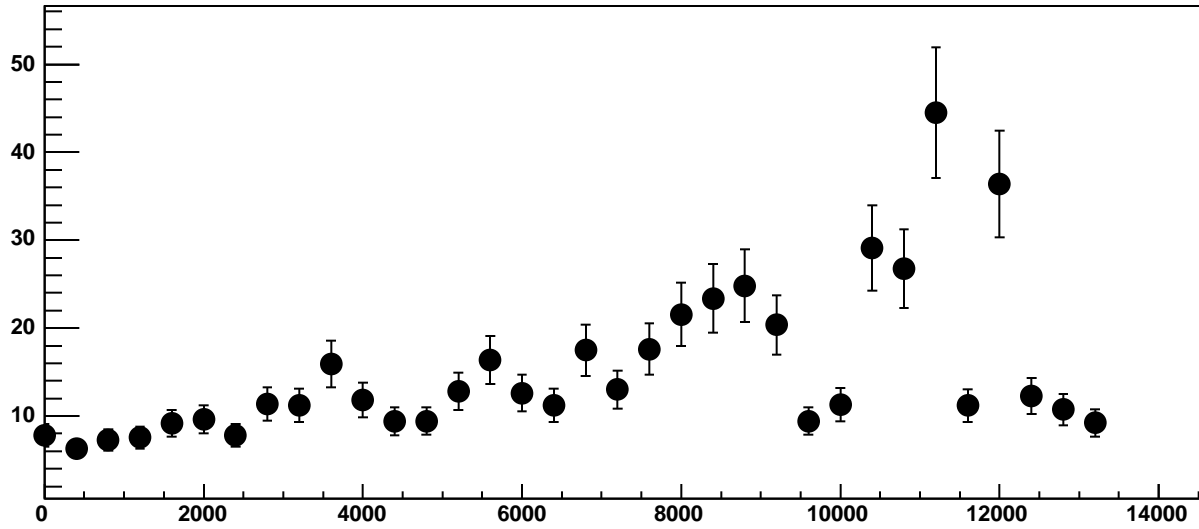




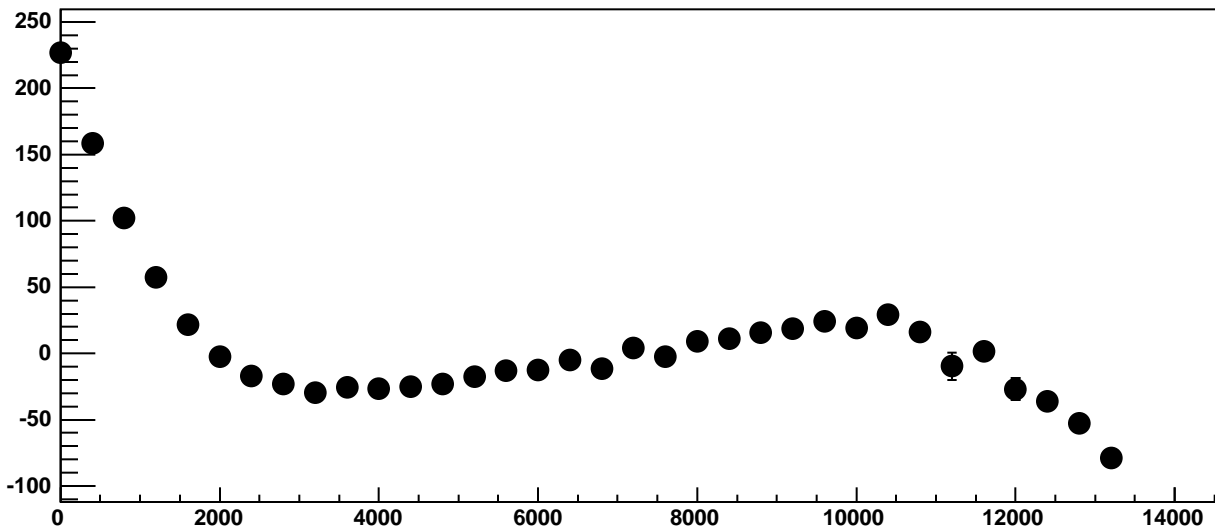
Chip 8, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



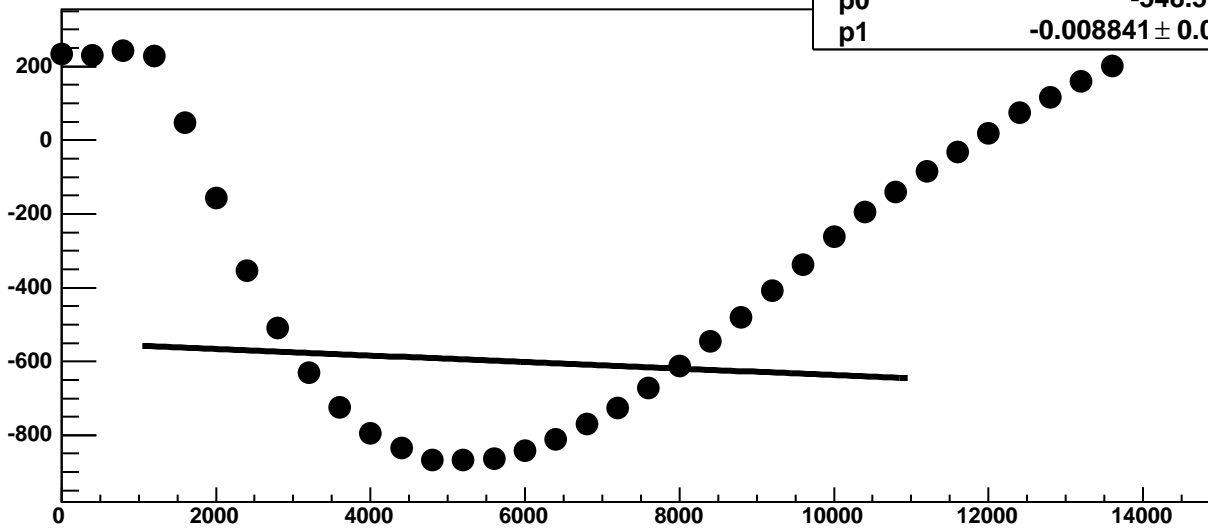
Chip 8, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

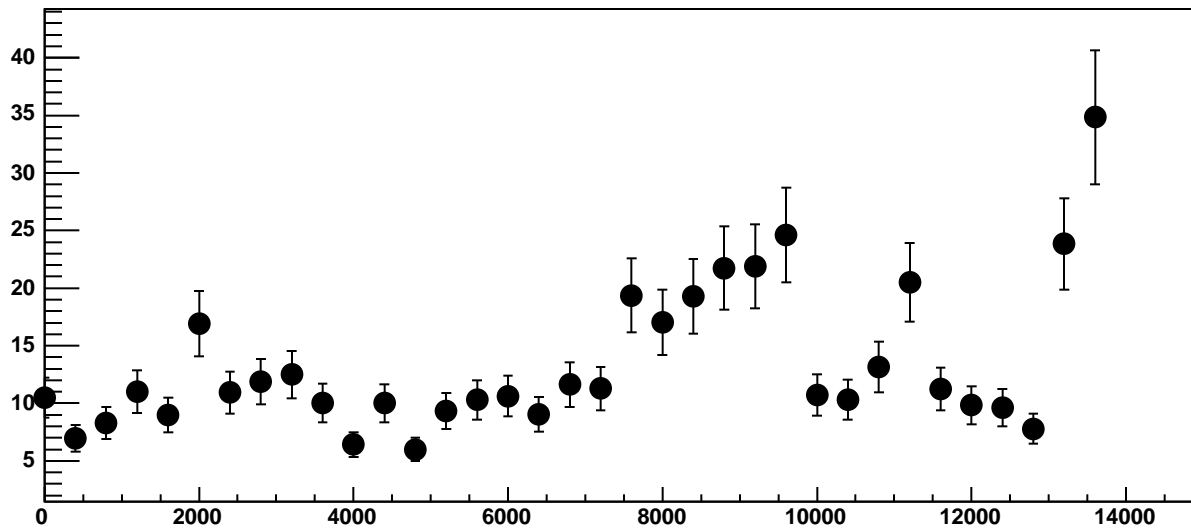


Chip 8, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

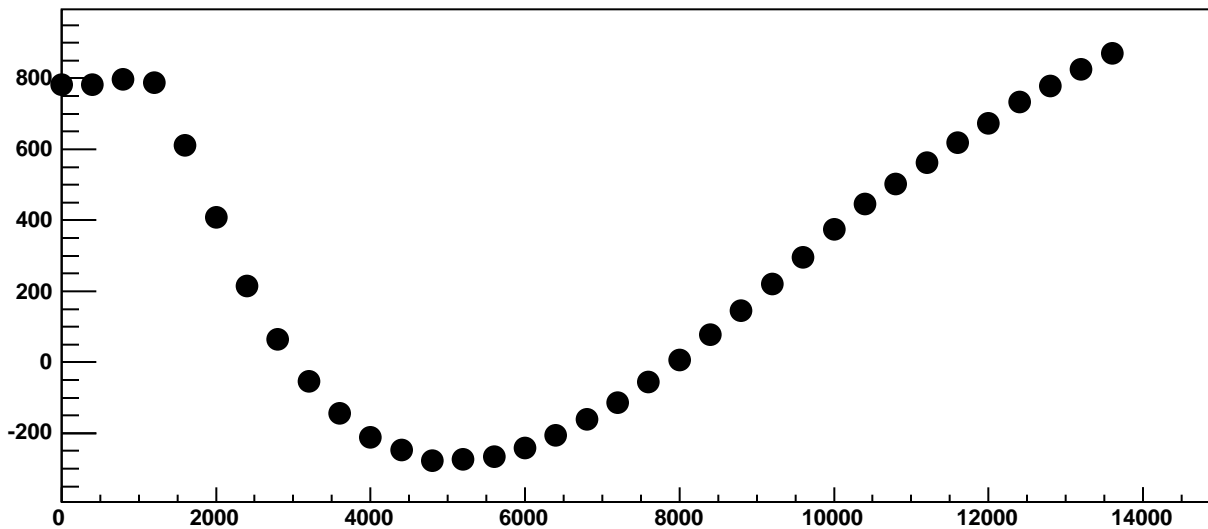


$\chi^2 / \text{ndf}$  4.275e+05 / 23  
p0 -548.5 ± 1.137  
p1 -0.008841 ± 0.0001937

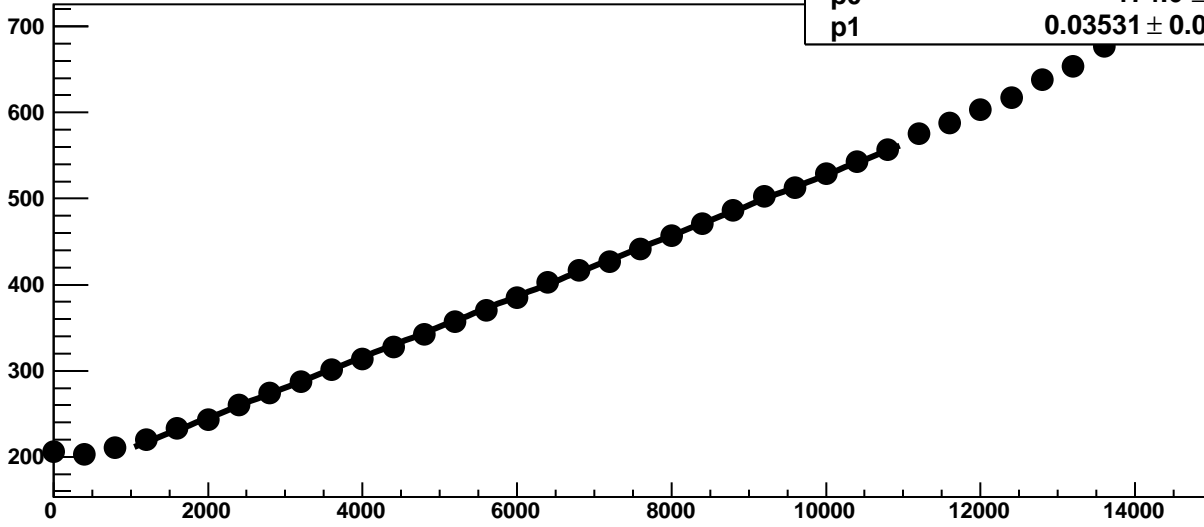
Chip 8, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

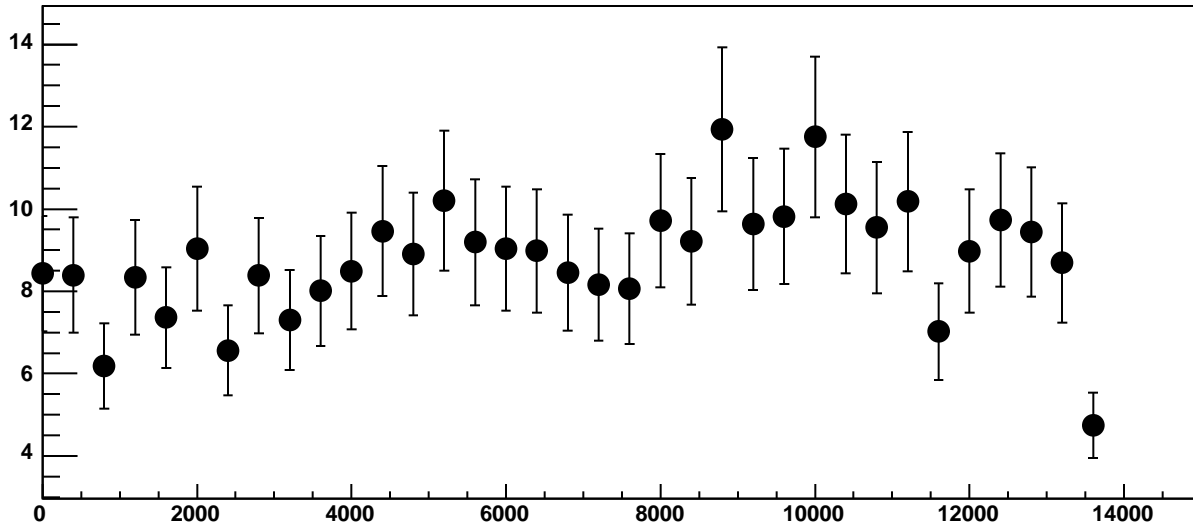


Chip 8, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

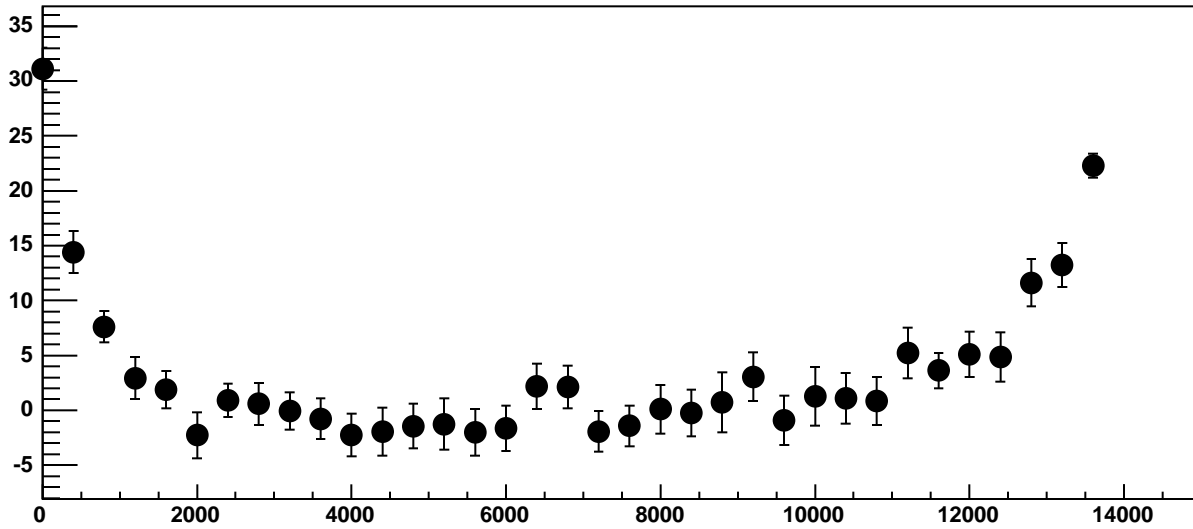


$\chi^2 / \text{ndf}$  16.64 / 23  
p0 174.6 ± 0.8744  
p1 0.03531 ± 0.0001415

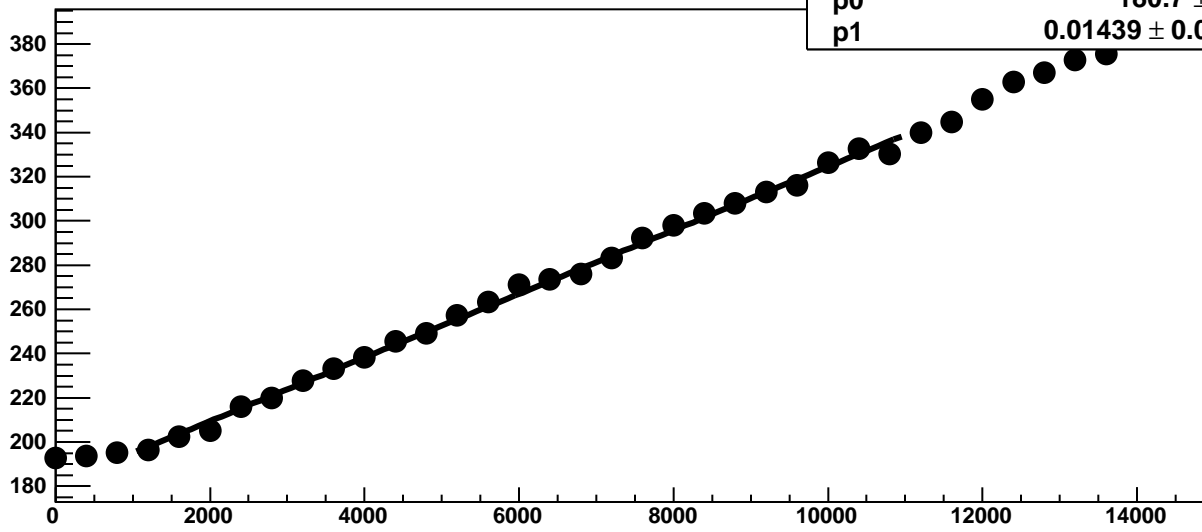
Chip 8, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



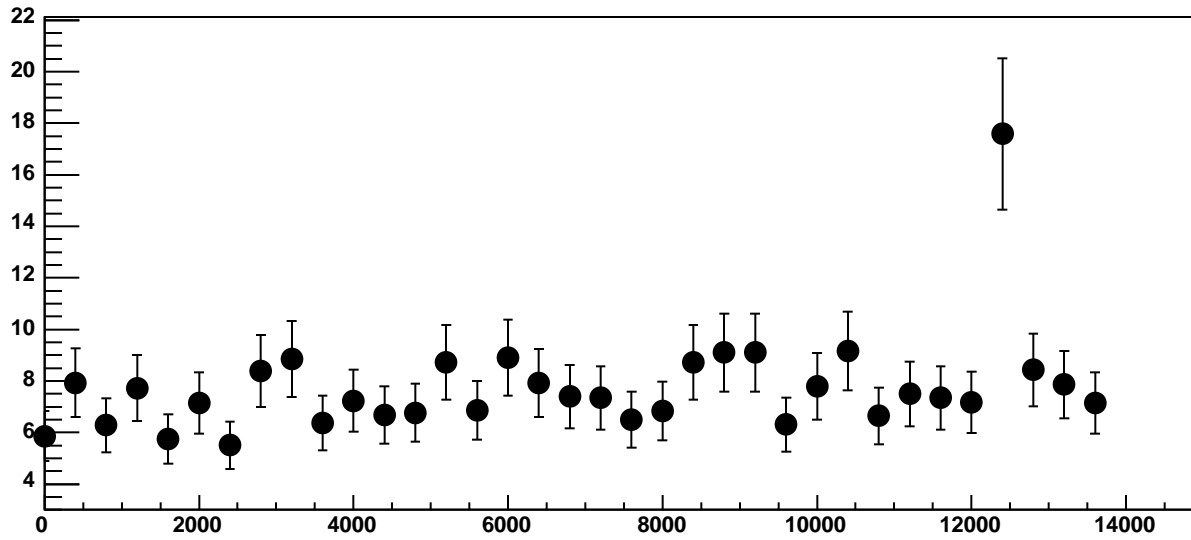
Chip 8, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



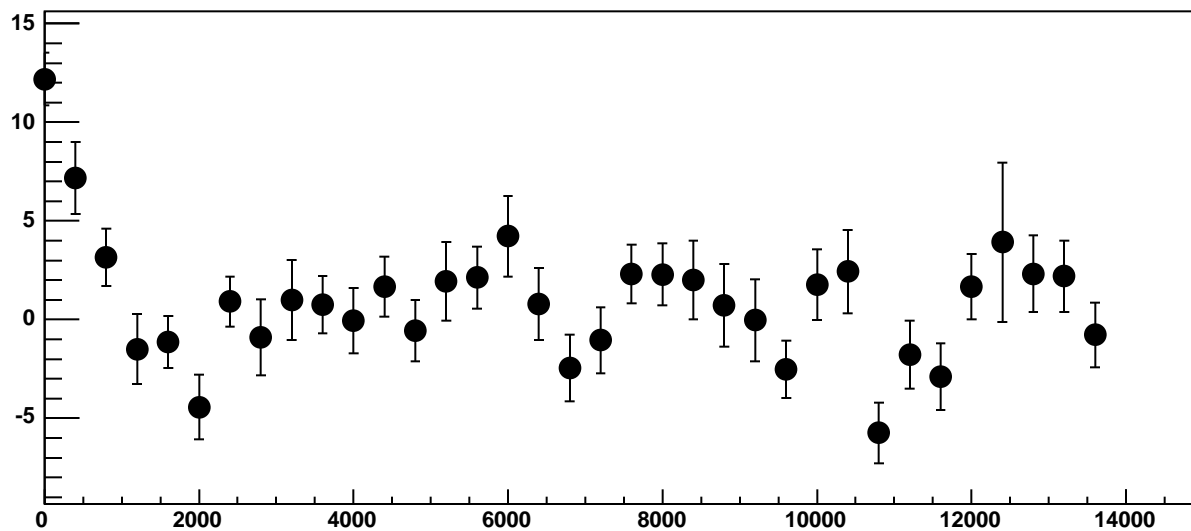
Chip 8, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



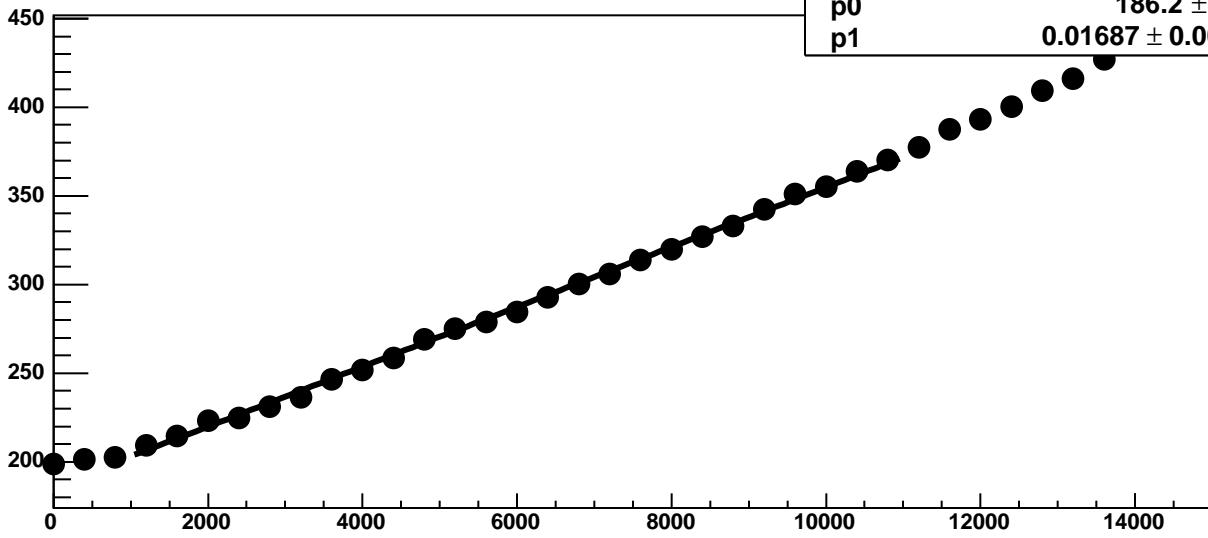
Chip 8, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

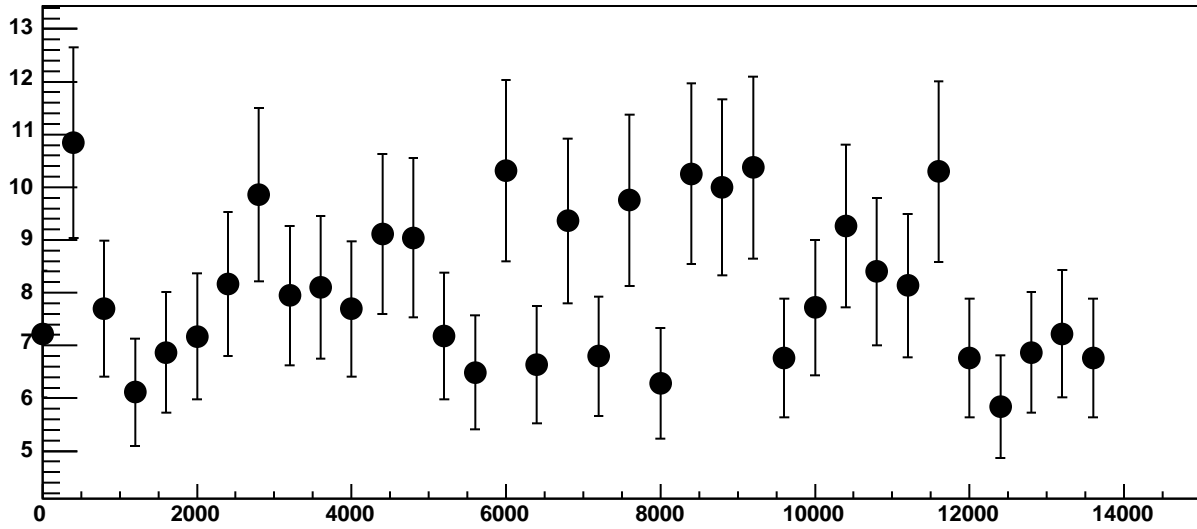


Chip 8, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

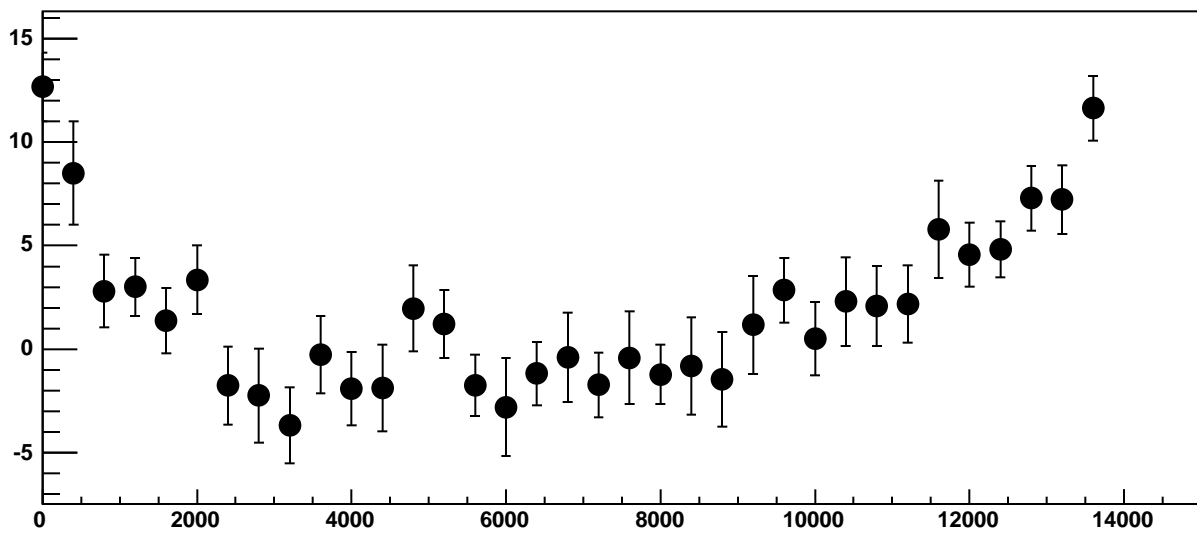


$\chi^2 / \text{ndf}$  30.78 / 23  
p0  $186.2 \pm 0.7956$   
p1  $0.01687 \pm 0.0001234$

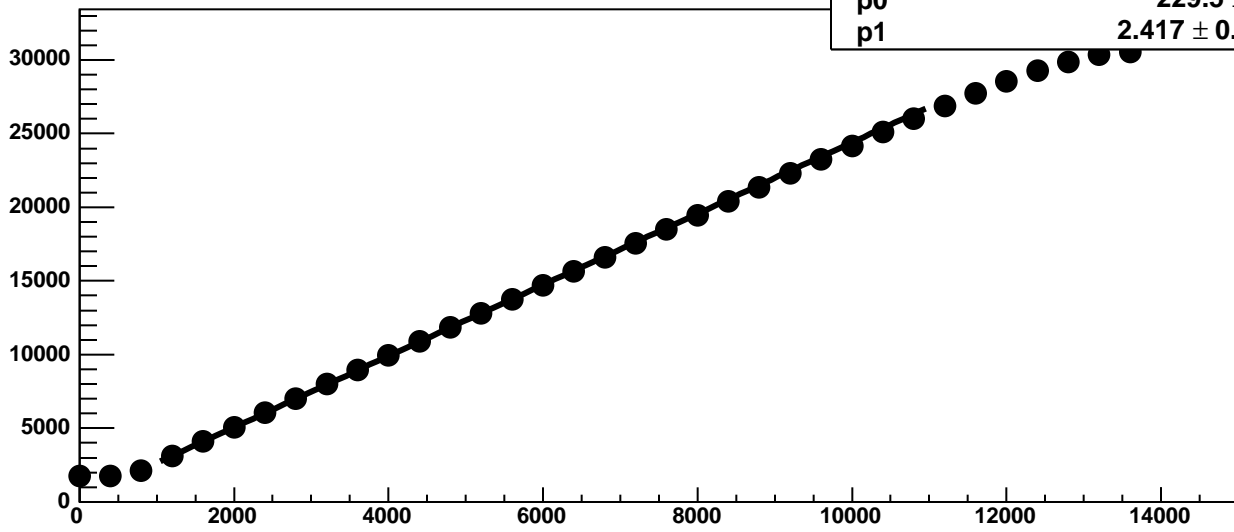
Chip 8, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

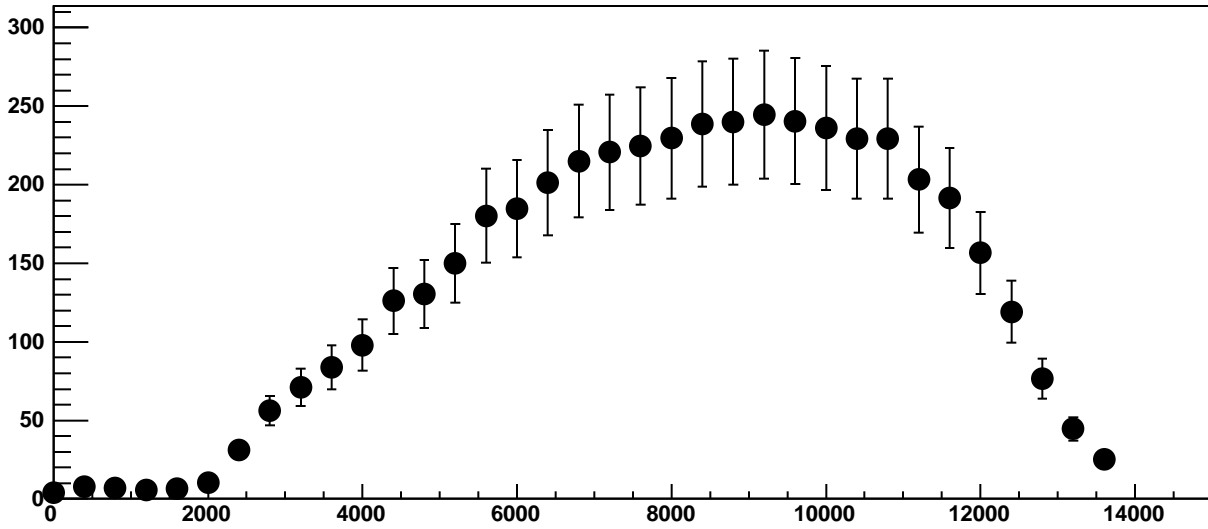


Chip 8, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

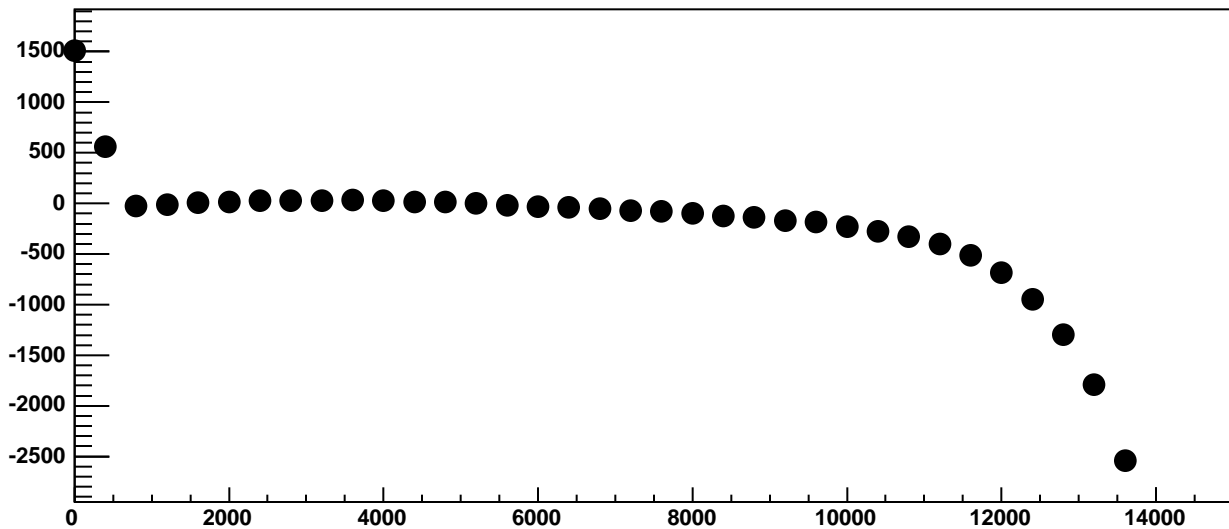


$\chi^2 / \text{ndf}$  295.1 / 23  
p0  $229.5 \pm 2.497$   
p1  $2.417 \pm 0.001511$

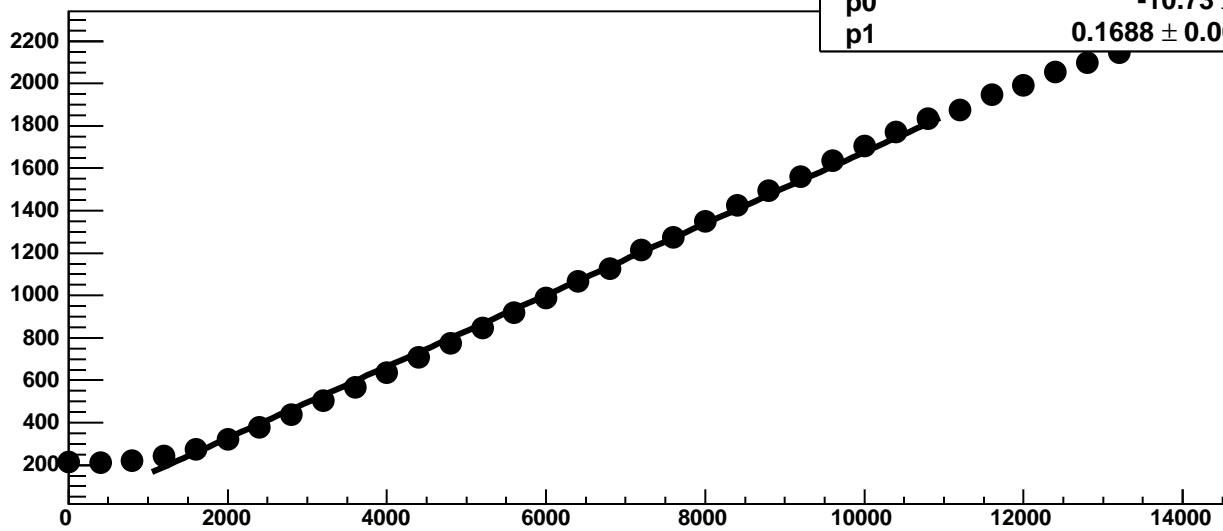
Chip 8, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



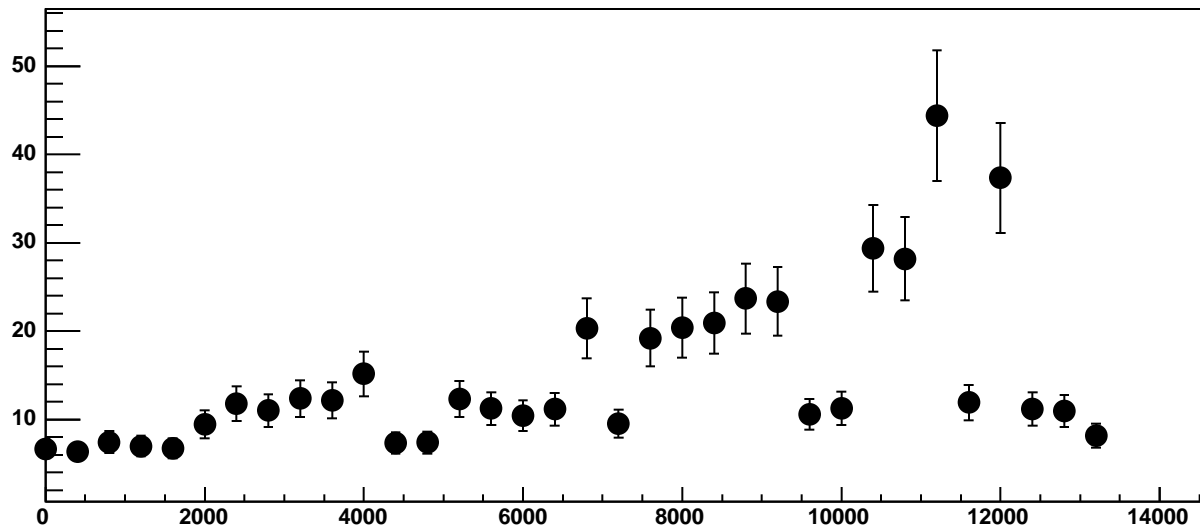
Chip 8, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



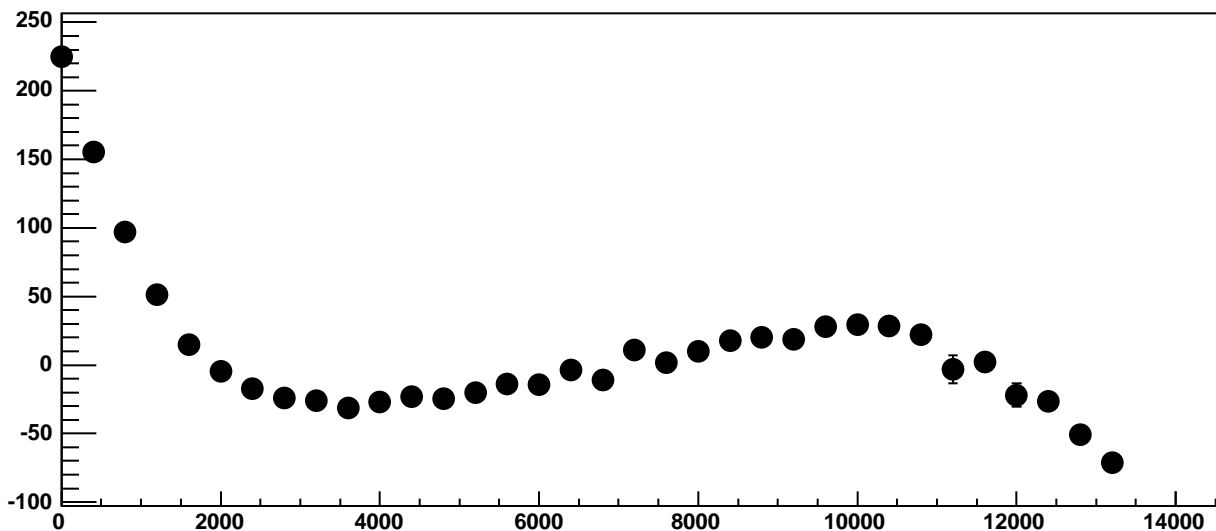
Chip 8, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



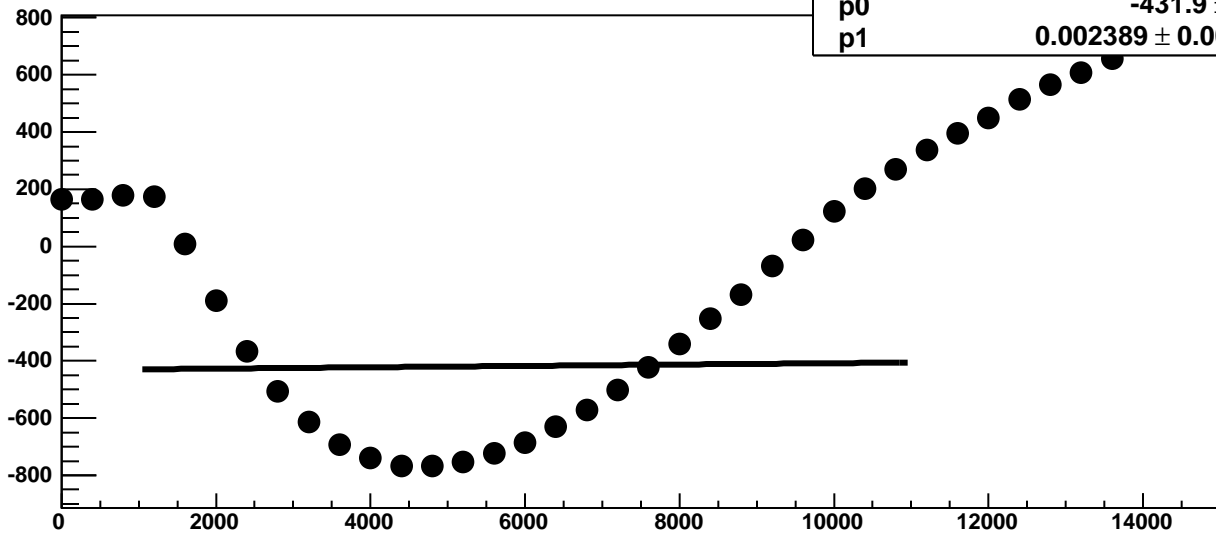
Chip 8, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



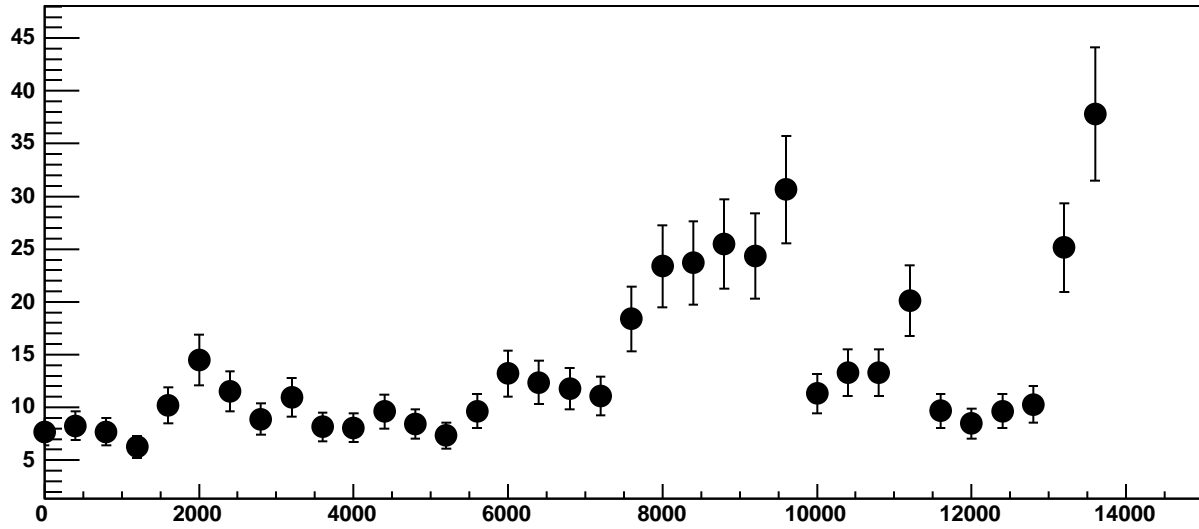
Chip 8, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



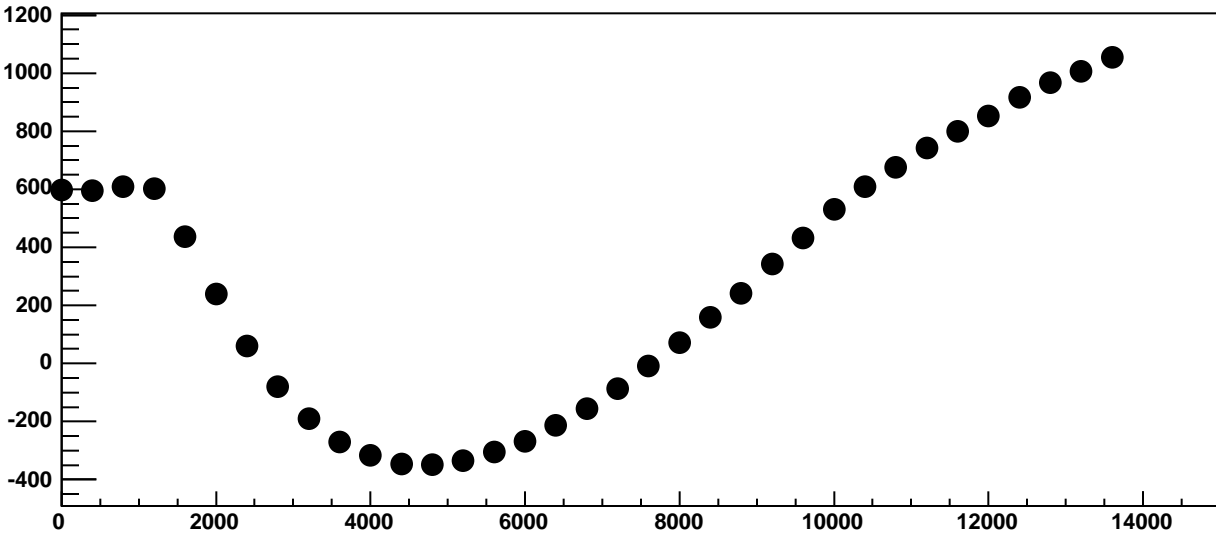
Chip 8, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



Chip 8, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



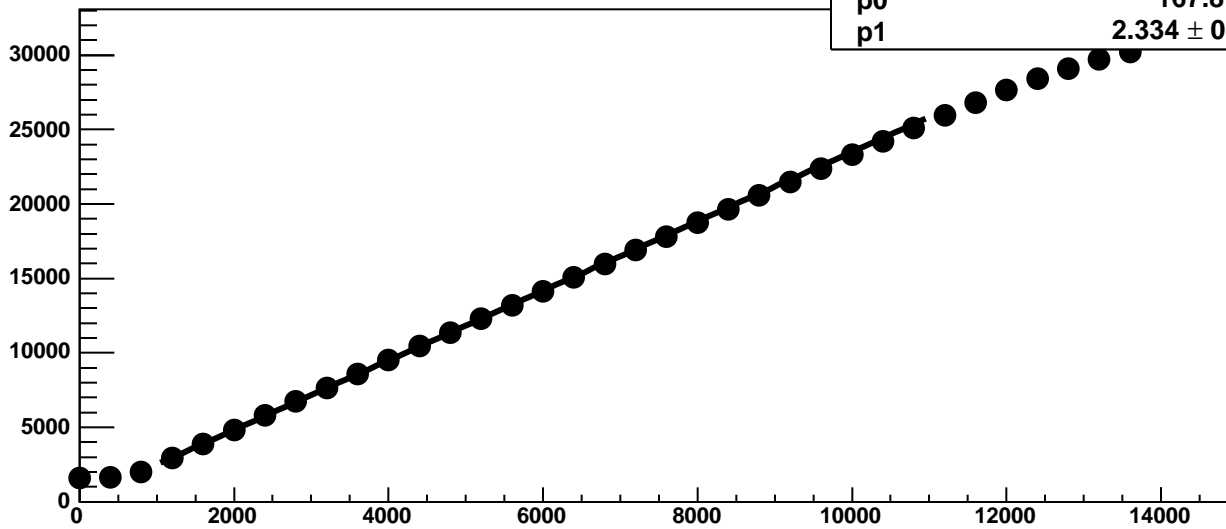
Chip 8, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



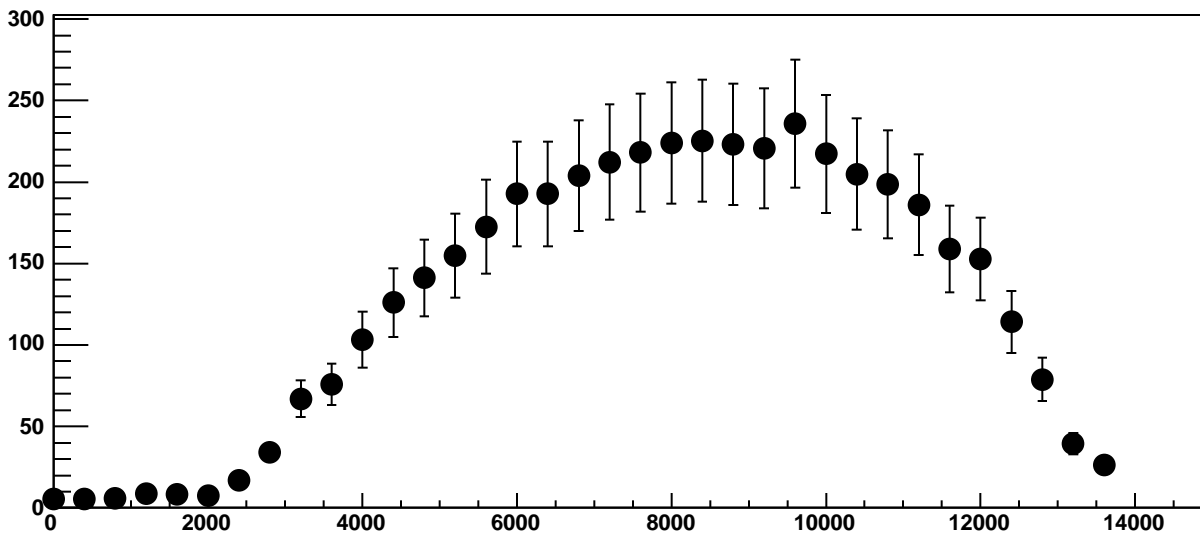


Chip 8, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC

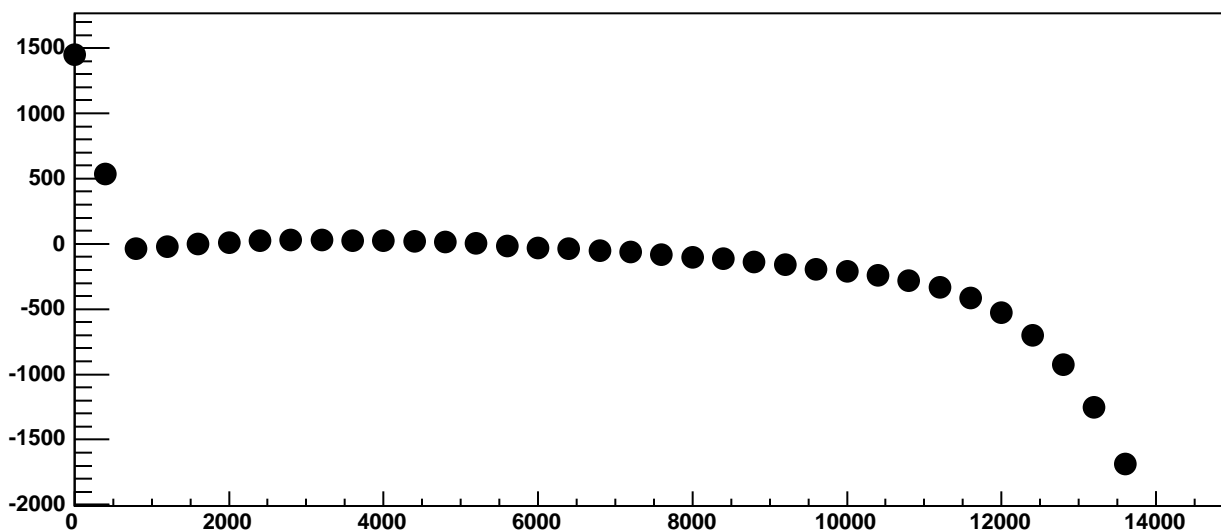
$\chi^2 / \text{ndf}$  307.7 / 23  
p0  $167.8 \pm 2.808$   
p1  $2.334 \pm 0.001468$



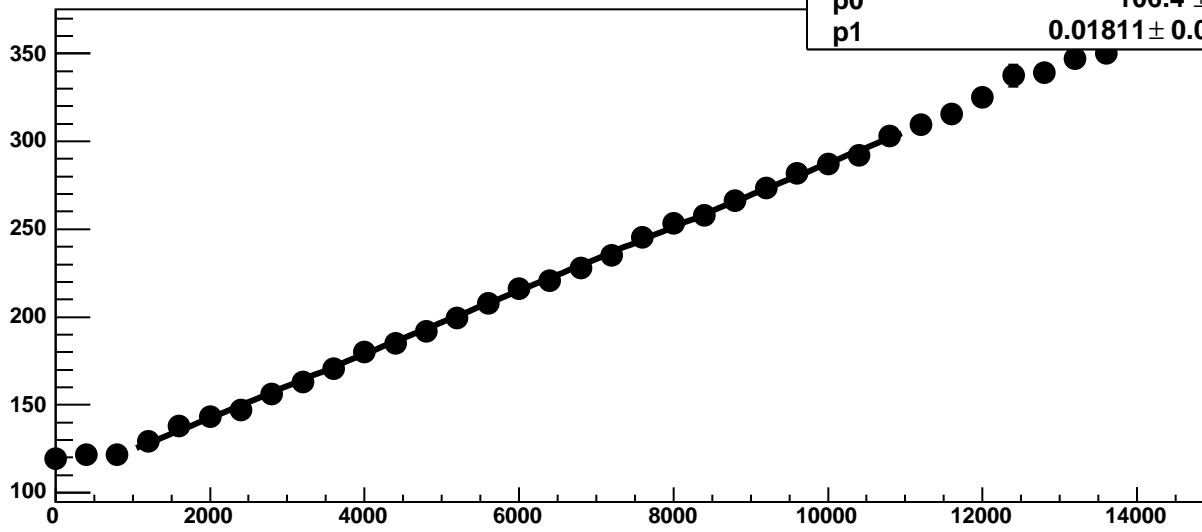
Chip 8, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

18.8 / 23

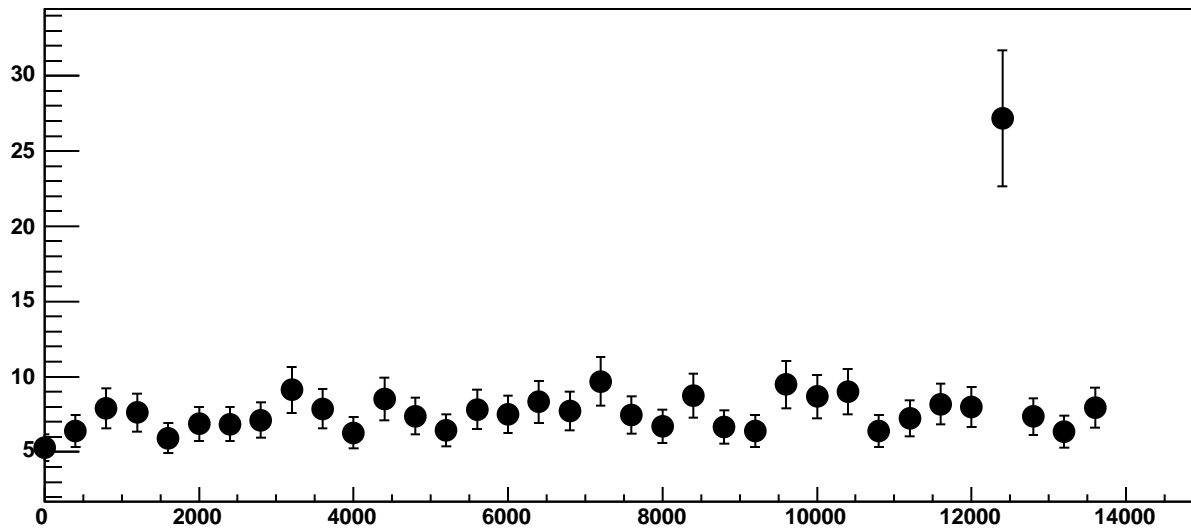
p0

$106.4 \pm 0.7529$

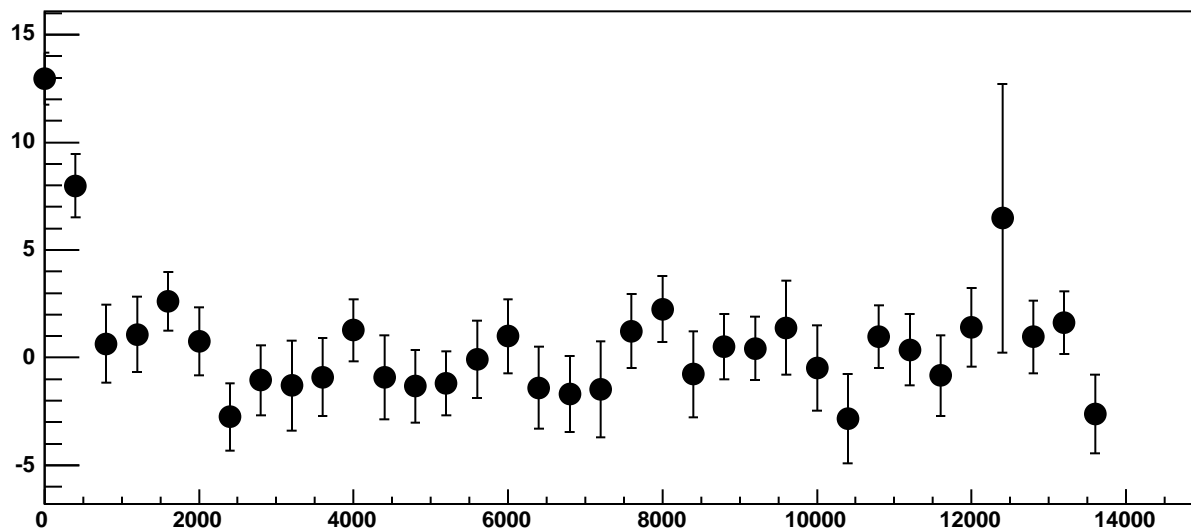
p1

$0.01811 \pm 0.0001155$

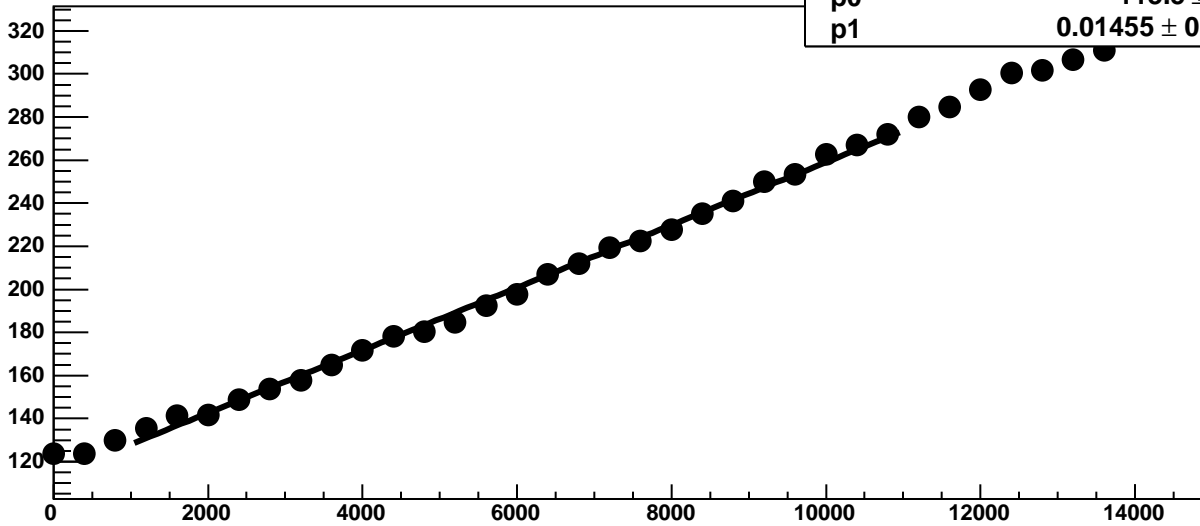
Chip 8, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

41.01 / 23

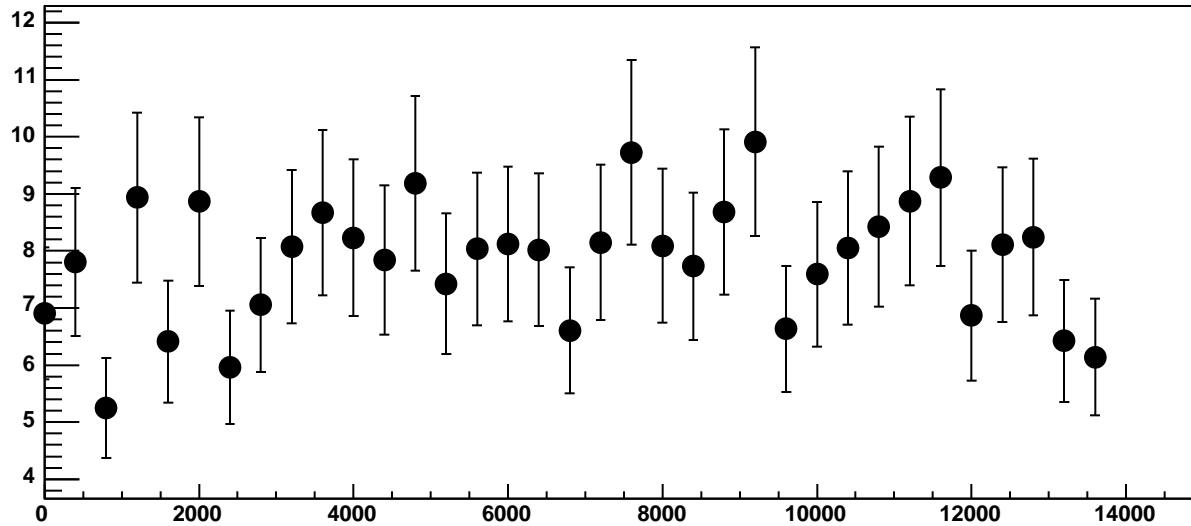
p0

$113.5 \pm 0.8039$

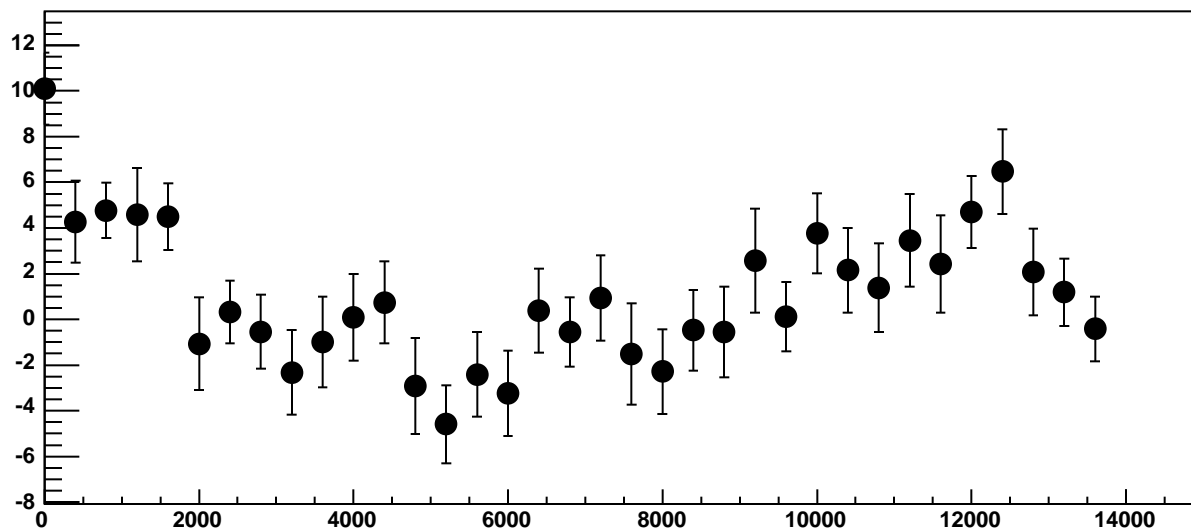
p1

$0.01455 \pm 0.000123$

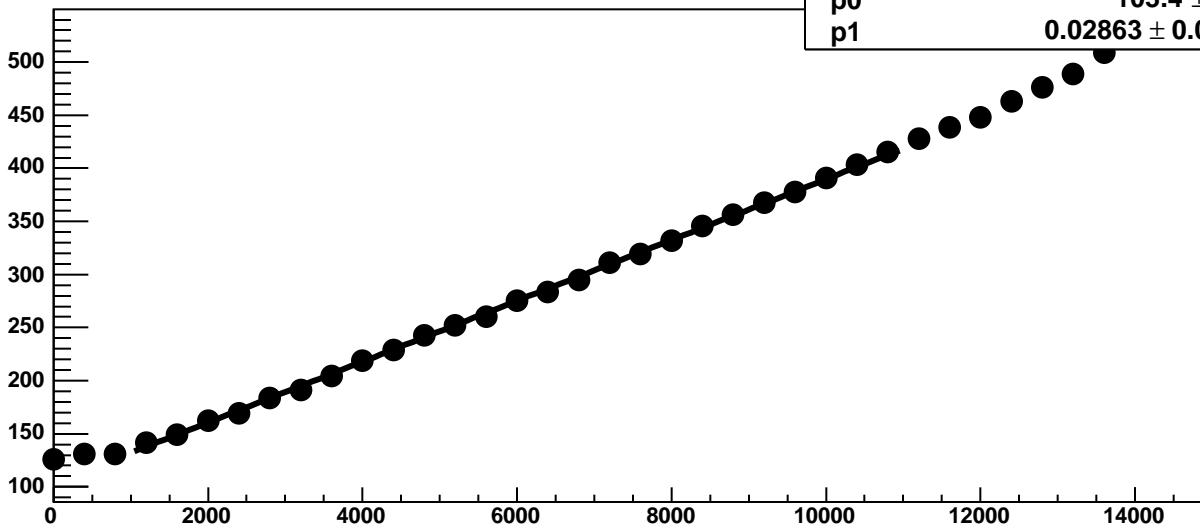
Chip 8, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

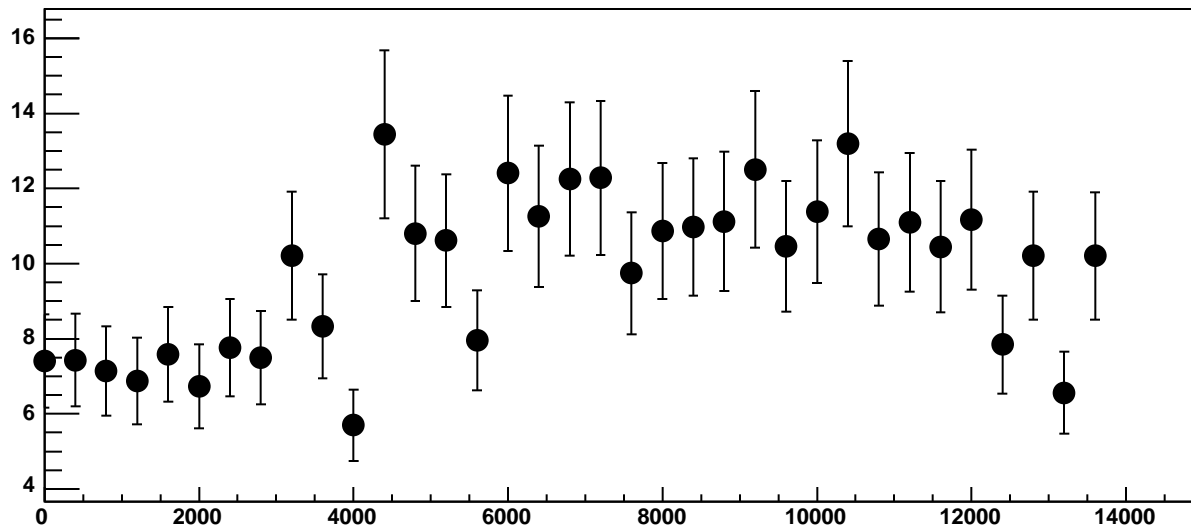


Chip 8, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

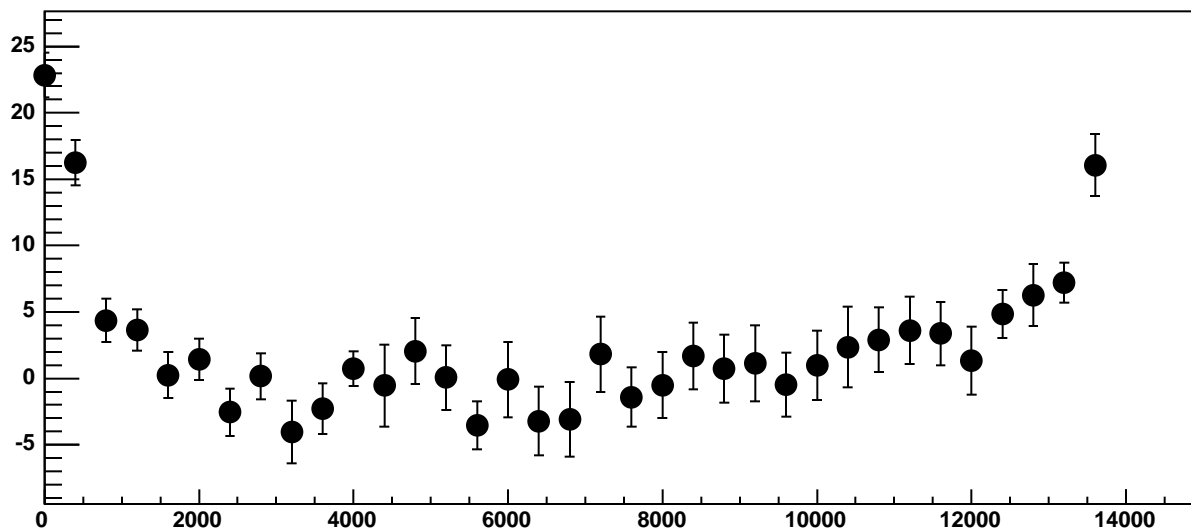


$\chi^2 / \text{ndf}$  23.98 / 23  
p0  $103.4 \pm 0.8525$   
p1  $0.02863 \pm 0.0001481$

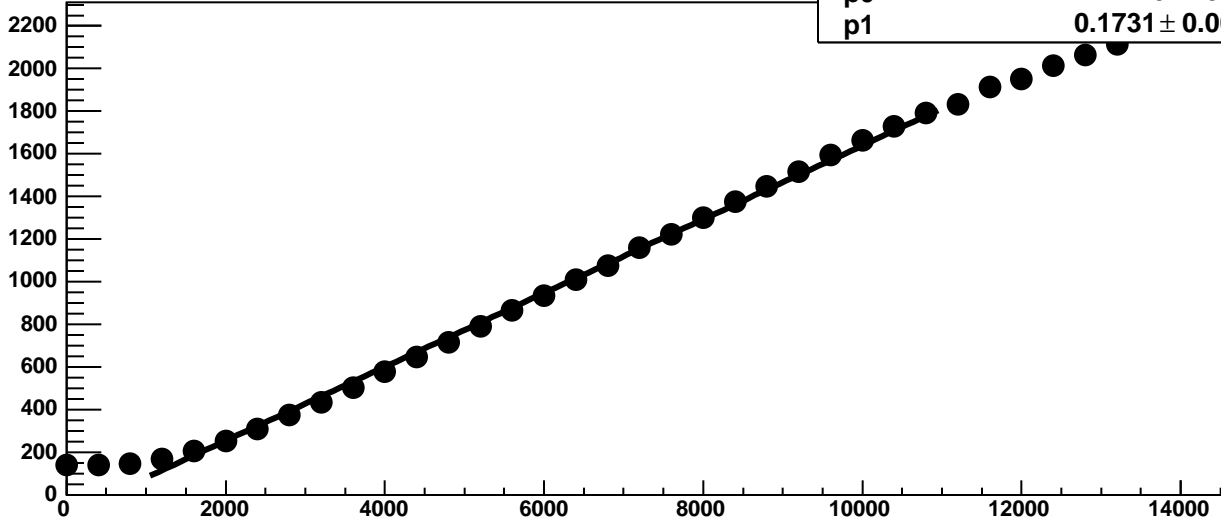
Chip 8, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1751 / 23

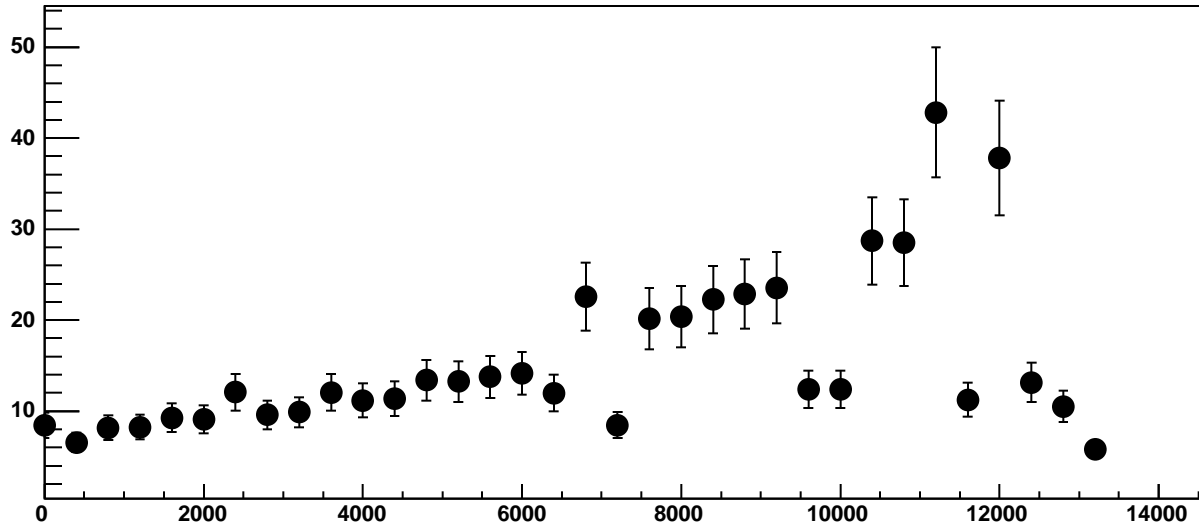
p0

$-92.15 \pm 1.14$

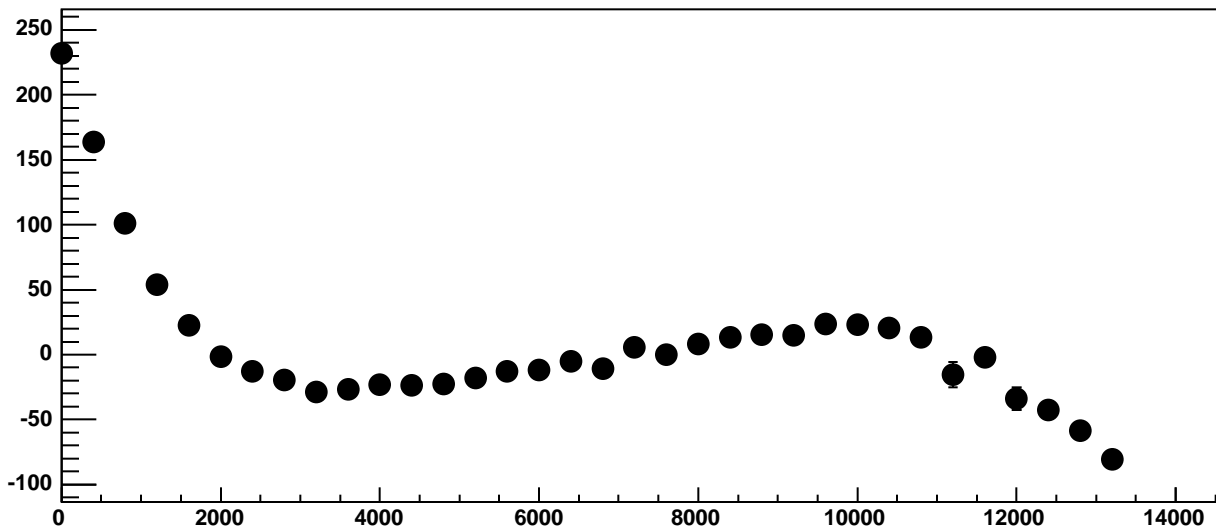
p1

$0.1731 \pm 0.0002103$

Chip 8, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

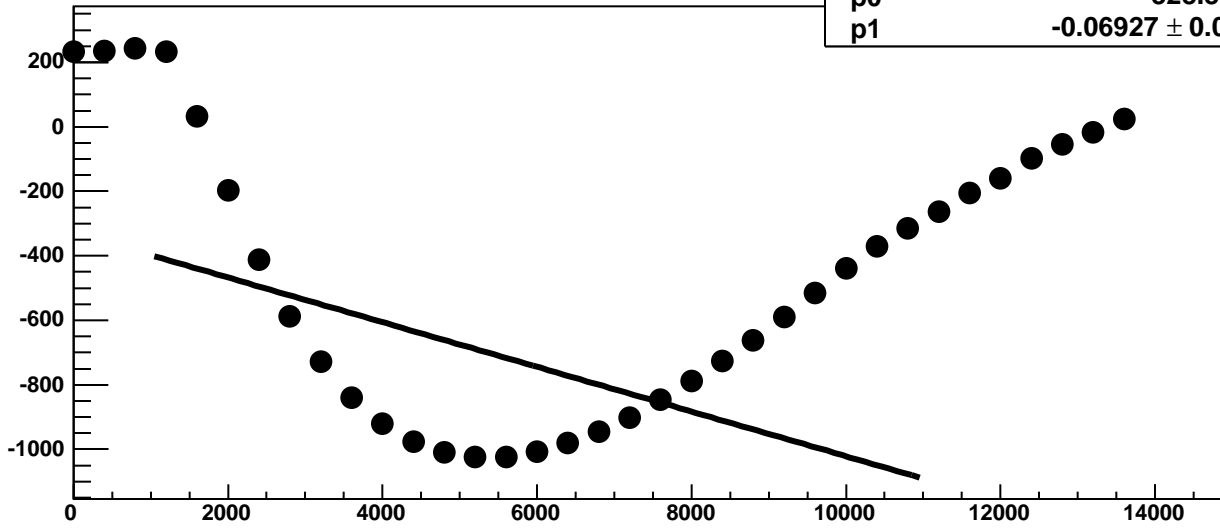
6.883e+05 / 23

p0

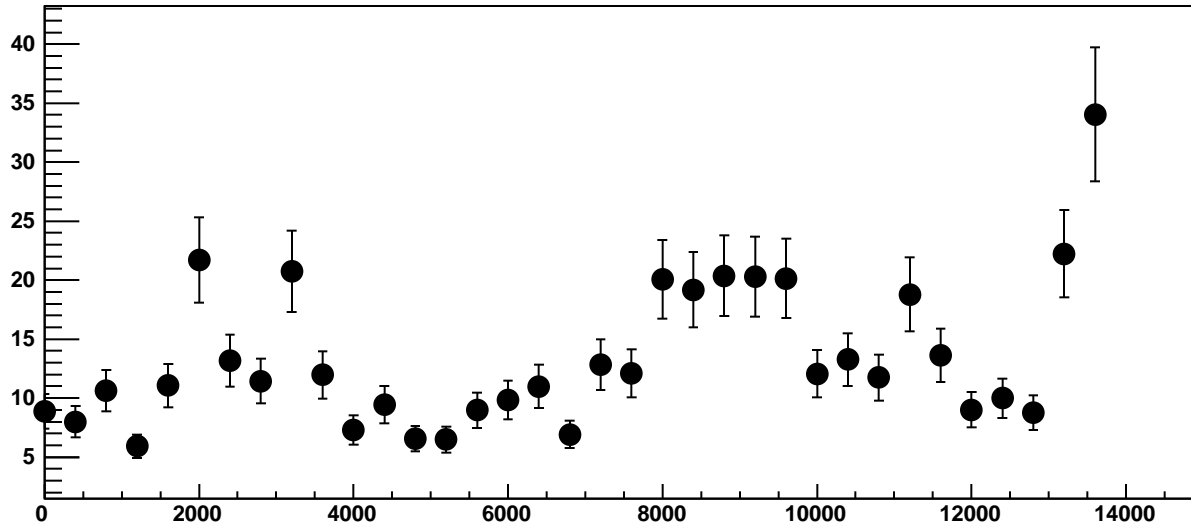
-328.8 ± 1.054

p1

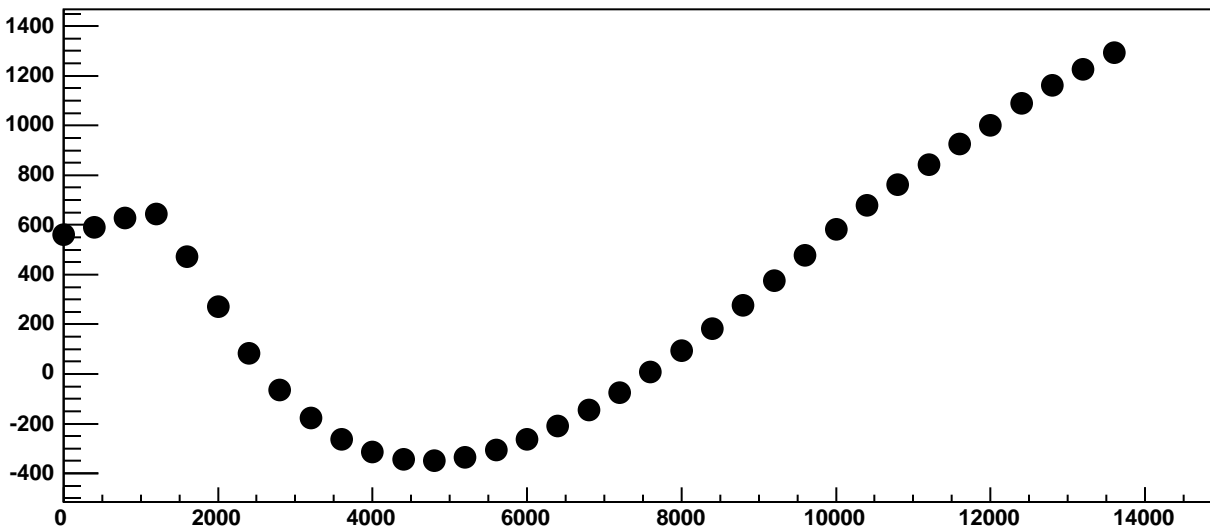
-0.06927 ± 0.0001825



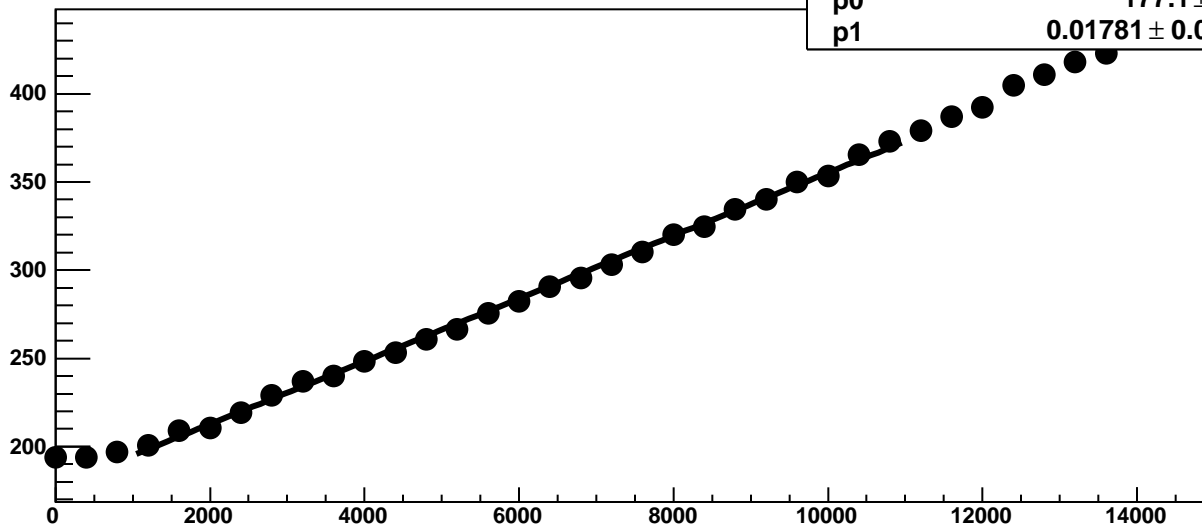
Chip 8, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

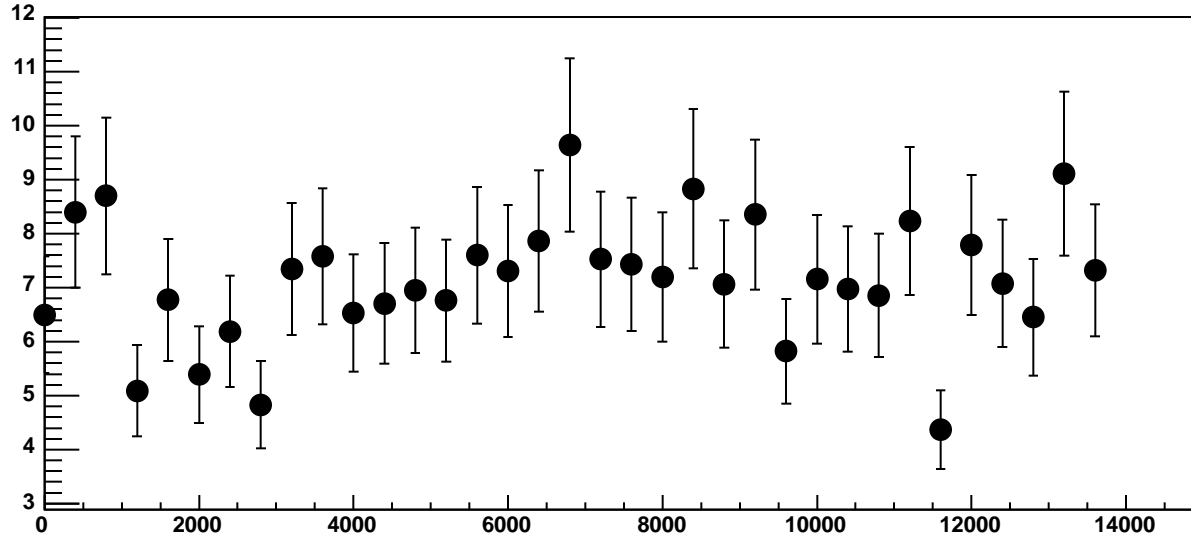


Chip 8, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

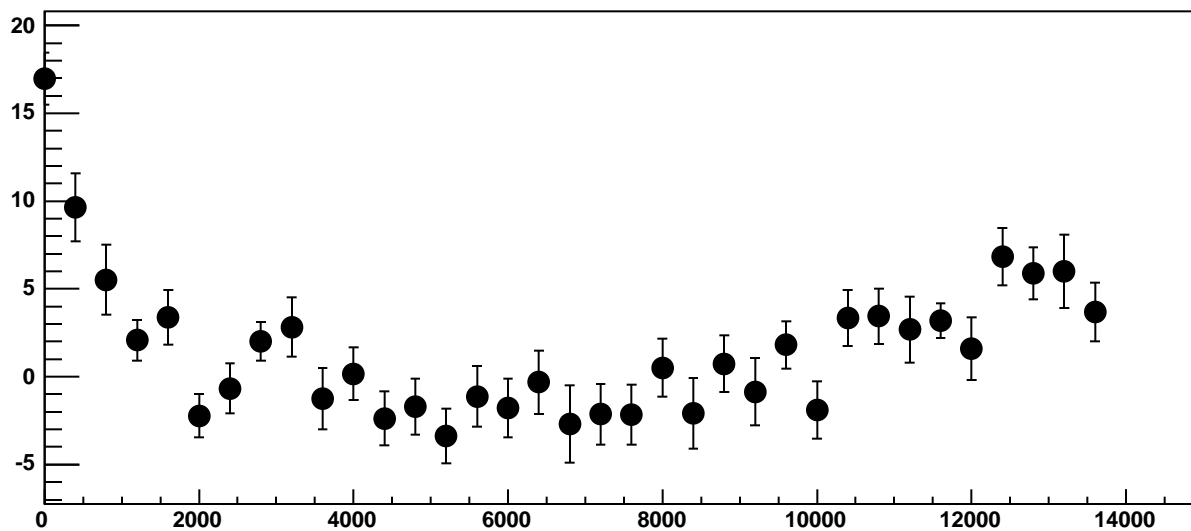


$\chi^2 / \text{ndf}$  46.27 / 23  
p0 177.1 ± 0.6423  
p1 0.01781 ± 0.0001018

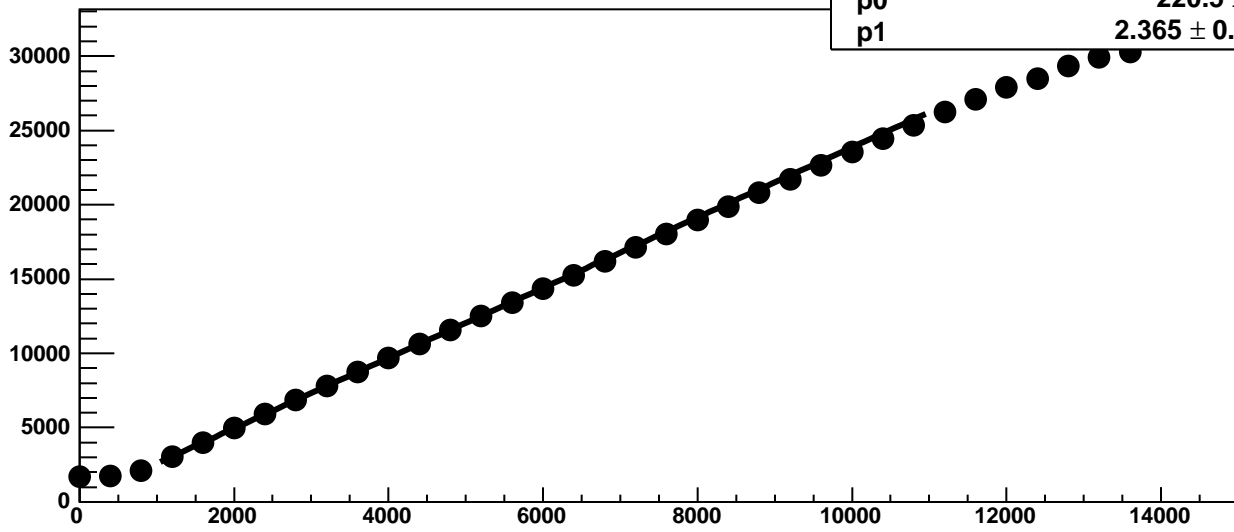
Chip 8, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



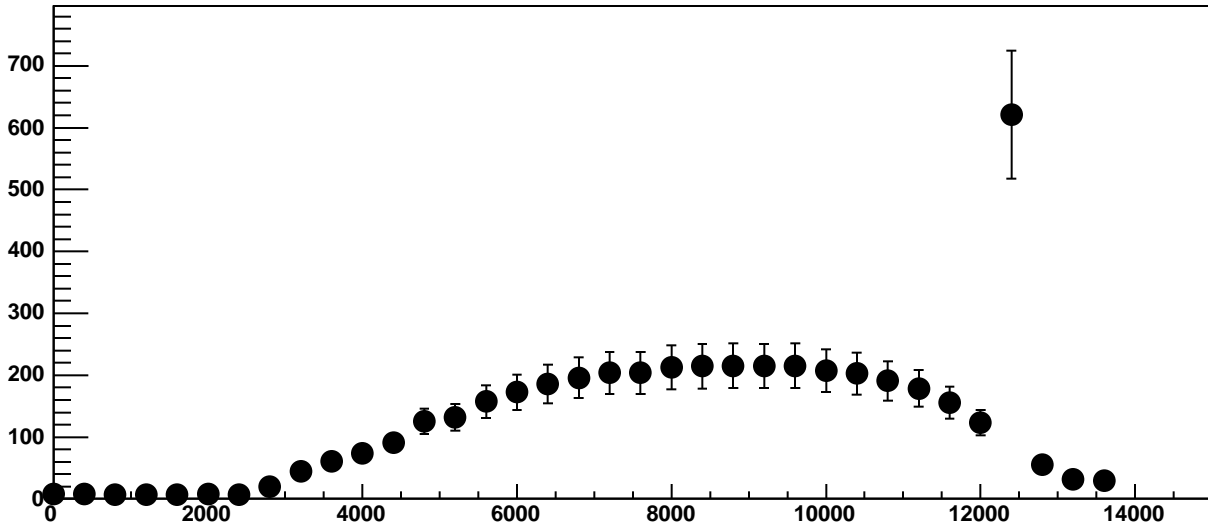
Chip 8, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



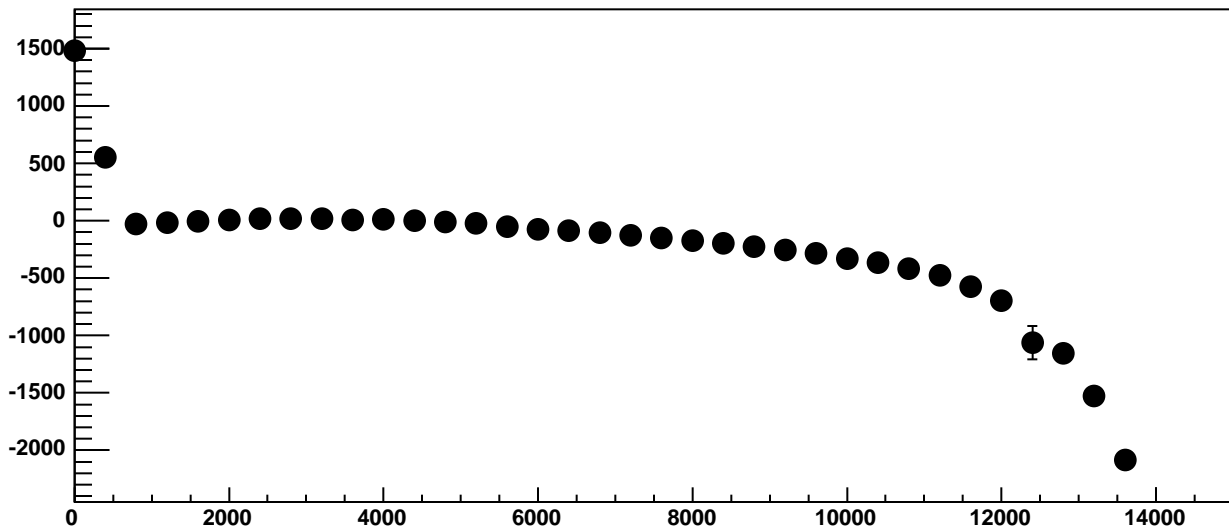
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



Chip 8, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

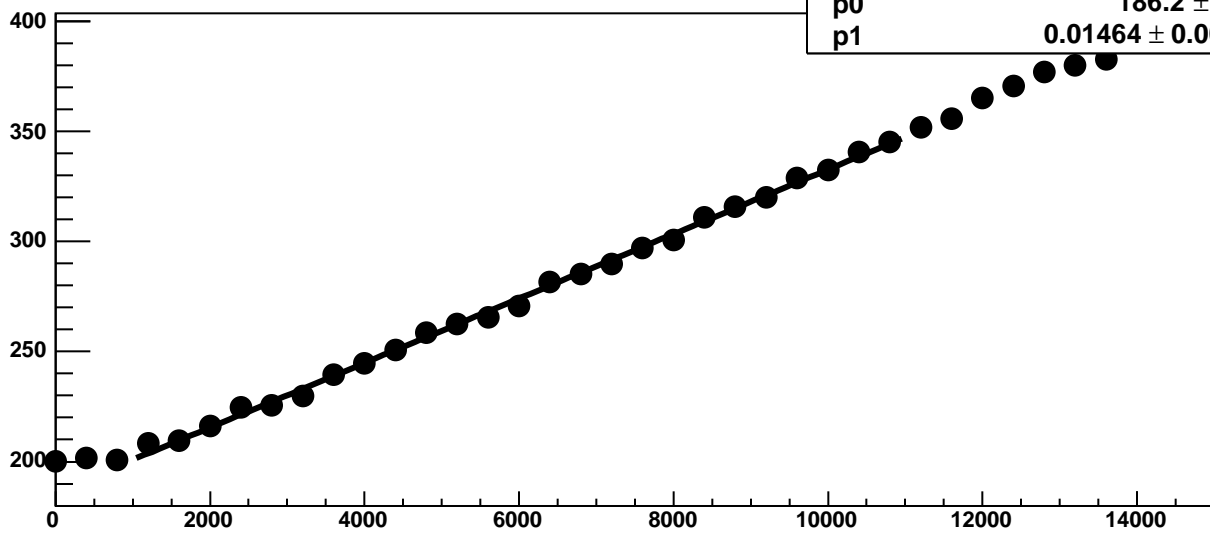


Chip 8, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



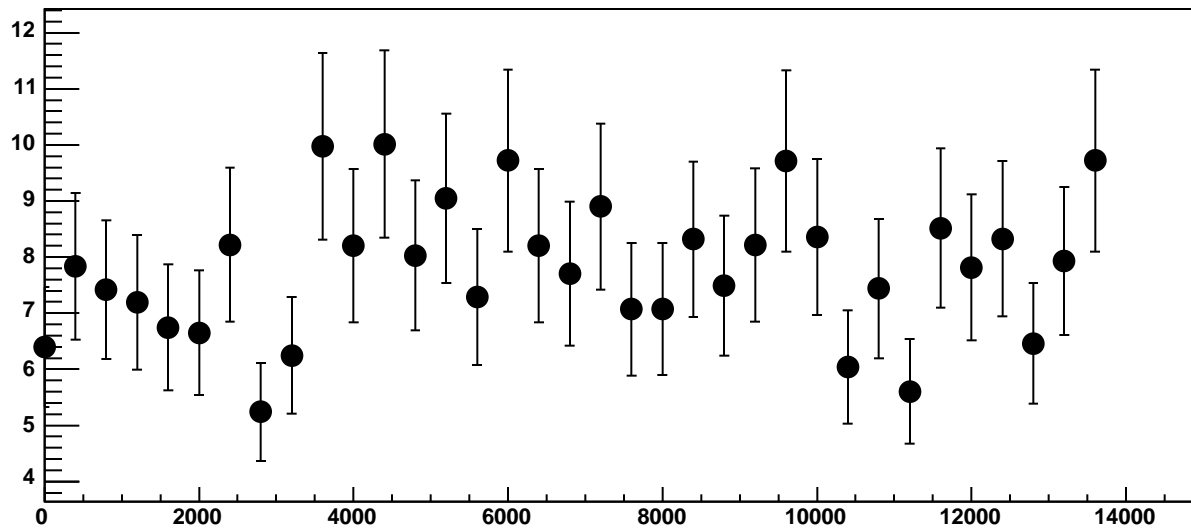


Chip 8, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

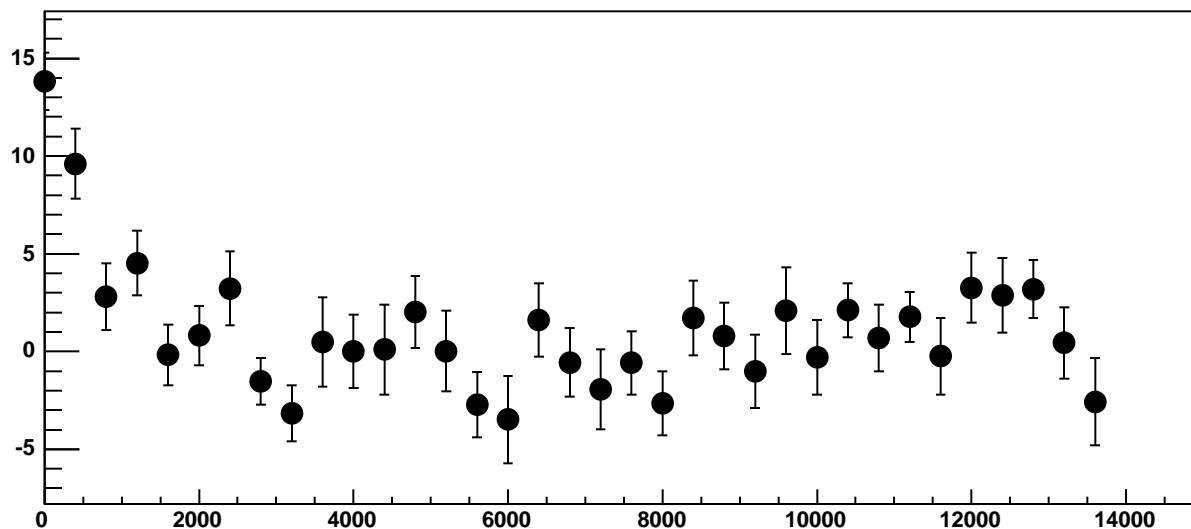


$\chi^2 / \text{ndf}$  32.83 / 23  
p0  $186.2 \pm 0.7505$   
p1  $0.01464 \pm 0.0001146$

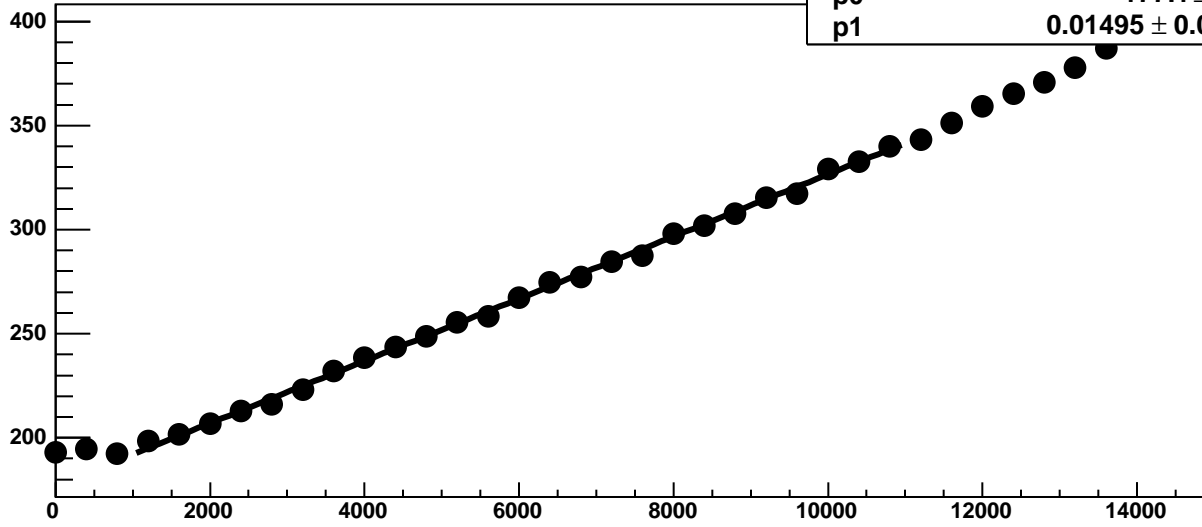
Chip 8, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

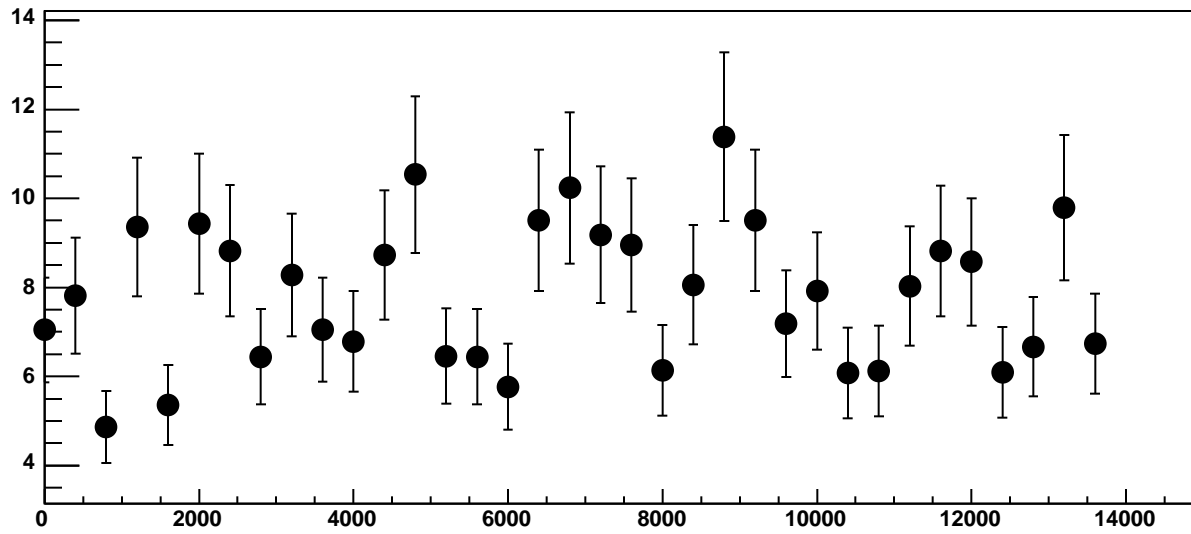


Chip 8, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

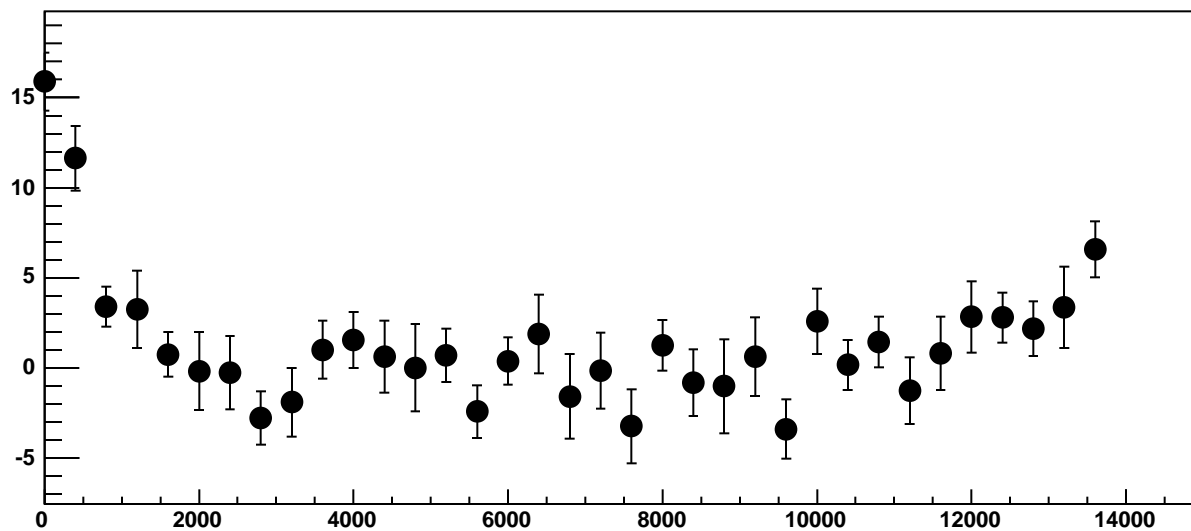


$\chi^2 / \text{ndf}$  23.95 / 23  
p0 177.1 ± 0.7716  
p1 0.01495 ± 0.0001149

Chip 8, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

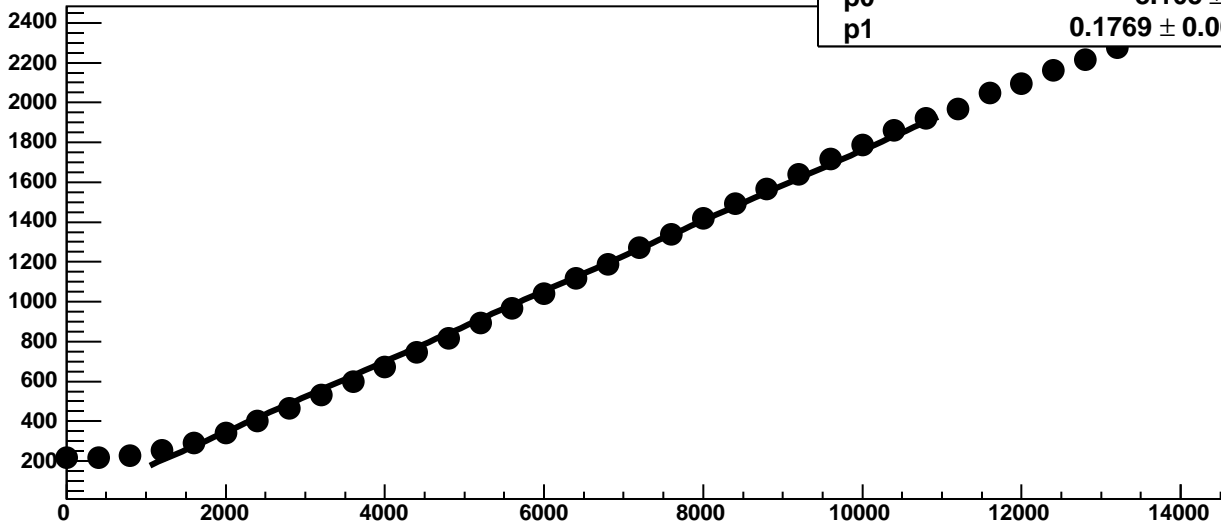


Chip 8, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

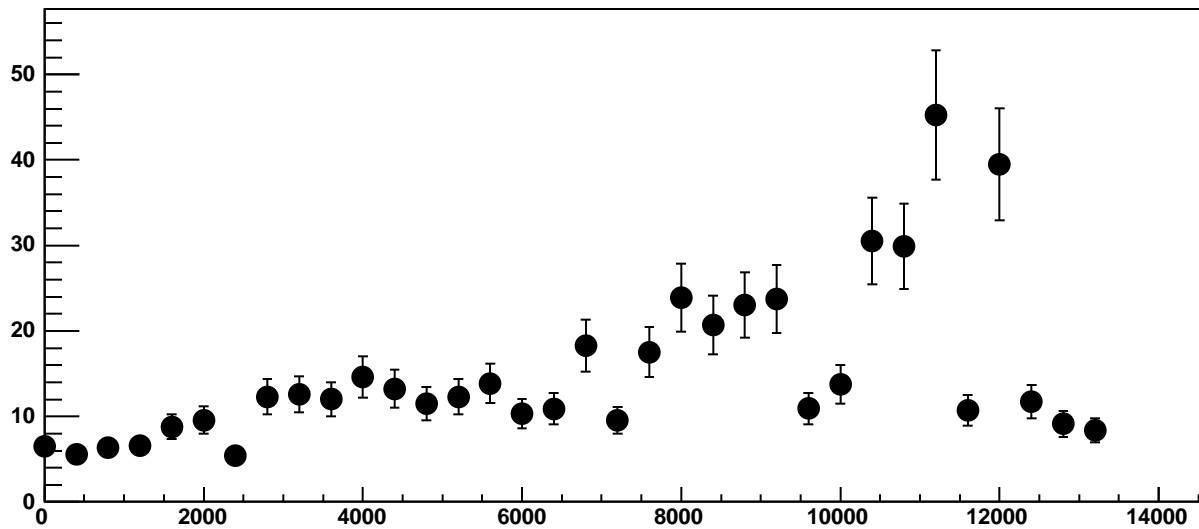


Chip 8, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

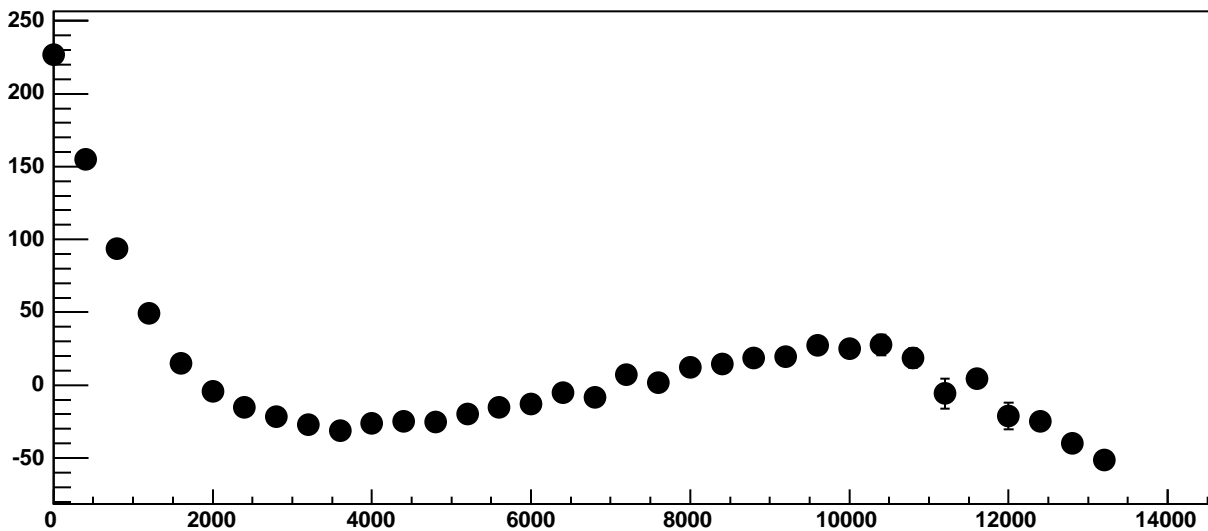
$\chi^2 / \text{ndf}$  2141 / 23  
p0  $-8.105 \pm 0.9911$   
p1  $0.1769 \pm 0.0001946$



Chip 8, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC

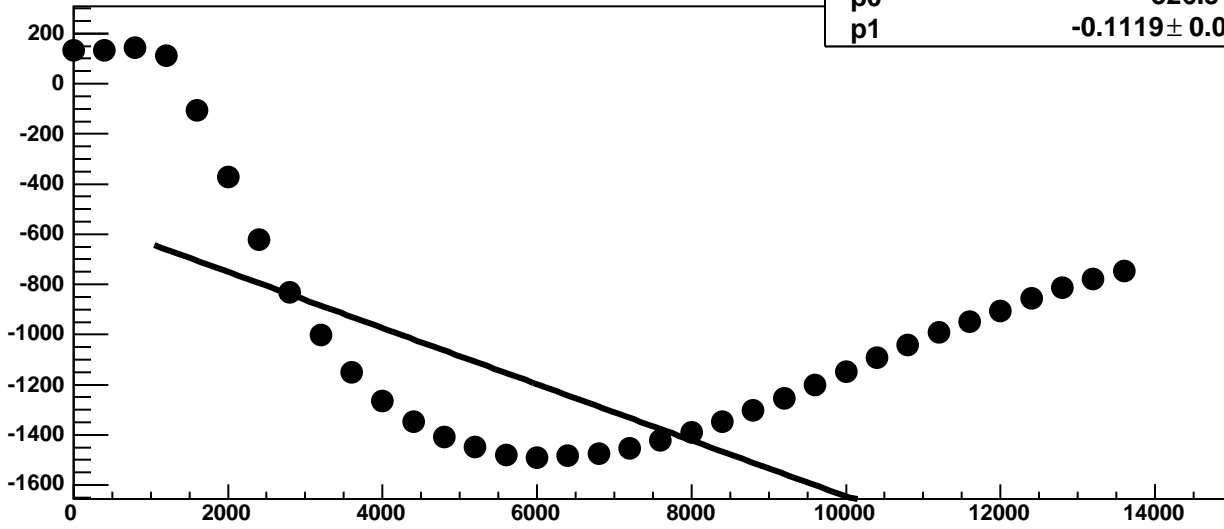


Chip 8, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

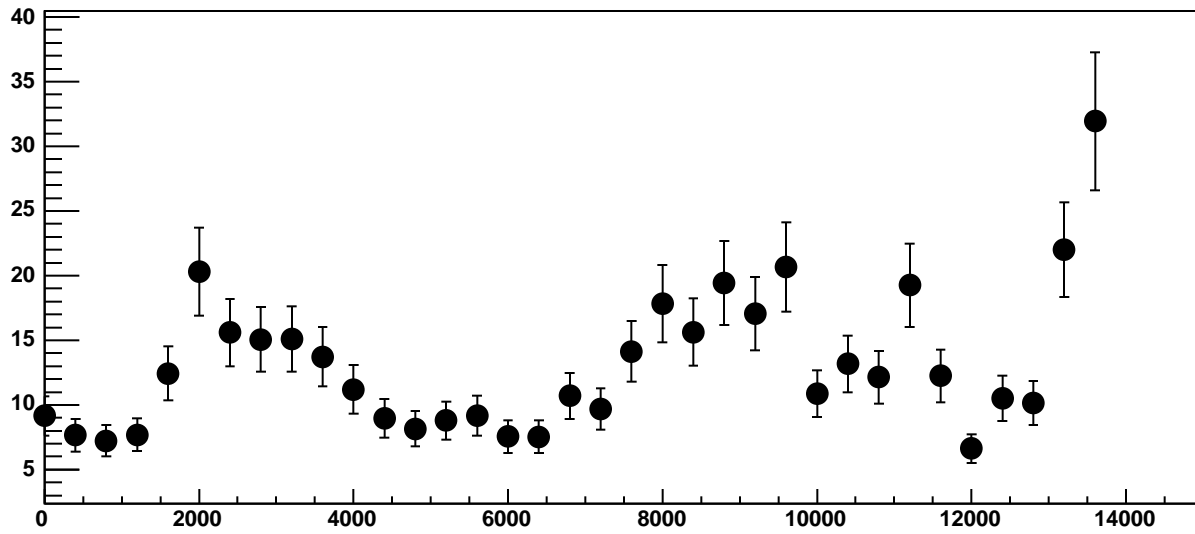


Chip 8, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

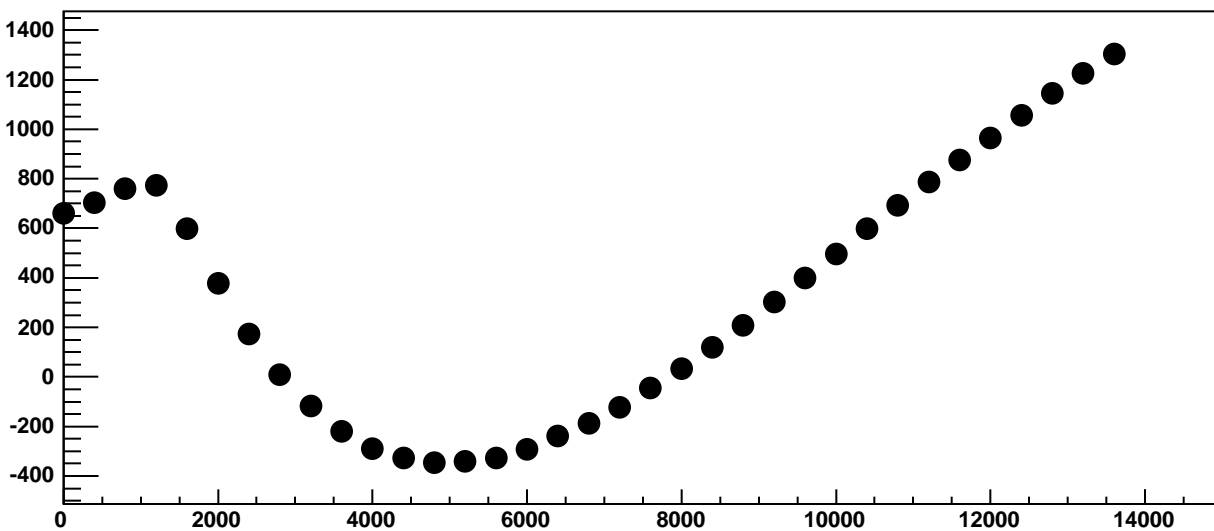
$\chi^2 / \text{ndf}$  5.891e+05 / 23  
p0 -526.3 ± 1.226  
p1 -0.1119 ± 0.0001976



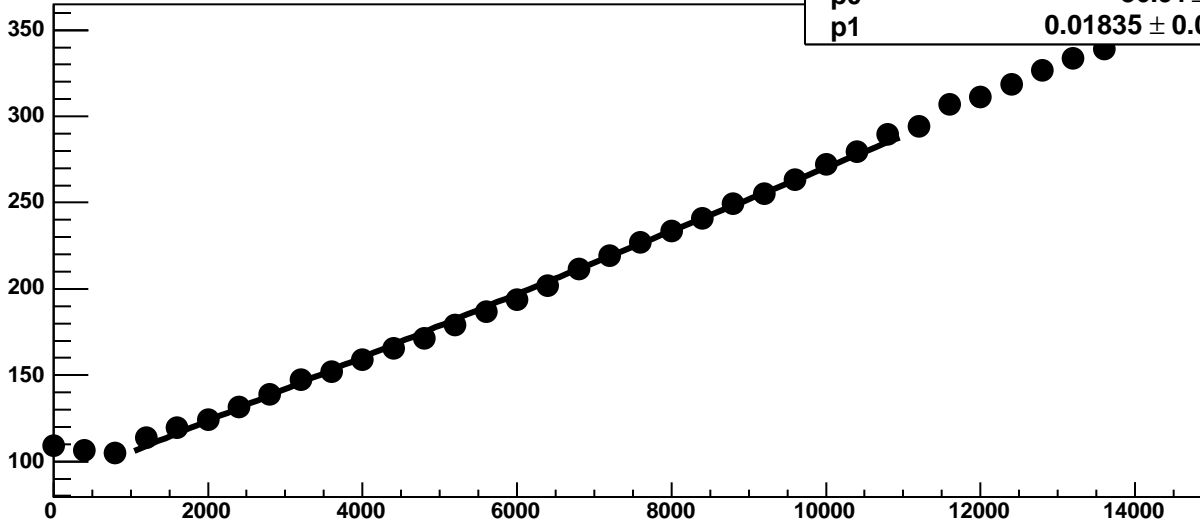
Chip 8, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

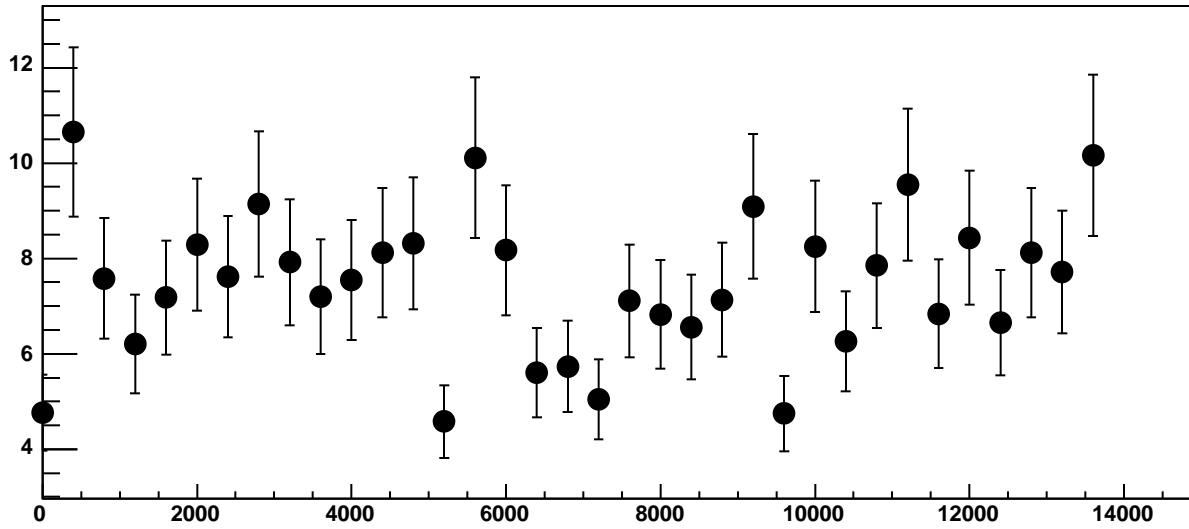


Chip 8, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

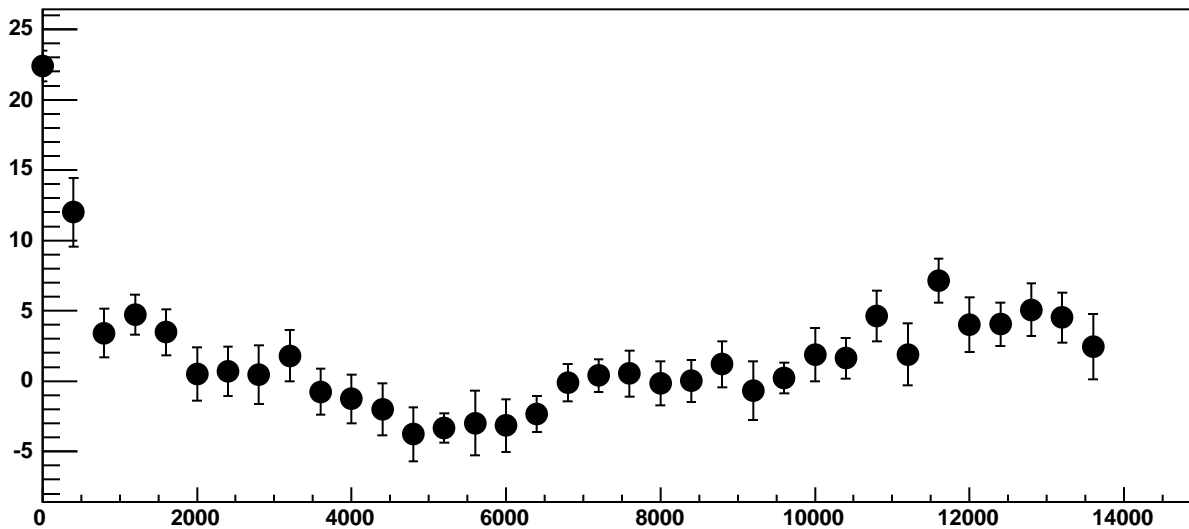


$\chi^2 / \text{ndf}$  49.93 / 23  
p0  $86.91 \pm 0.7647$   
p1  $0.01835 \pm 0.0001117$

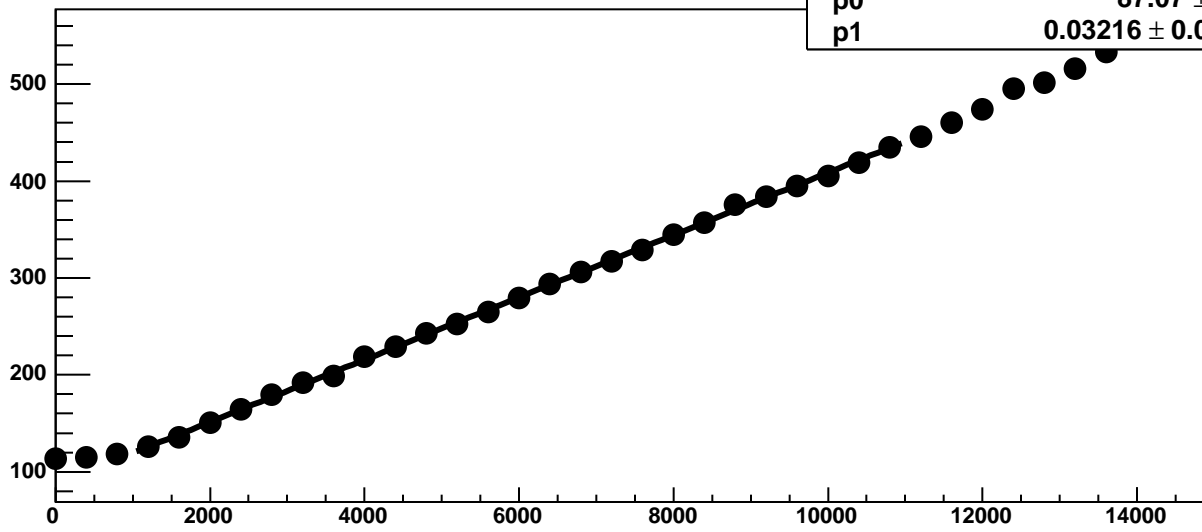
Chip 8, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

26.84 / 23

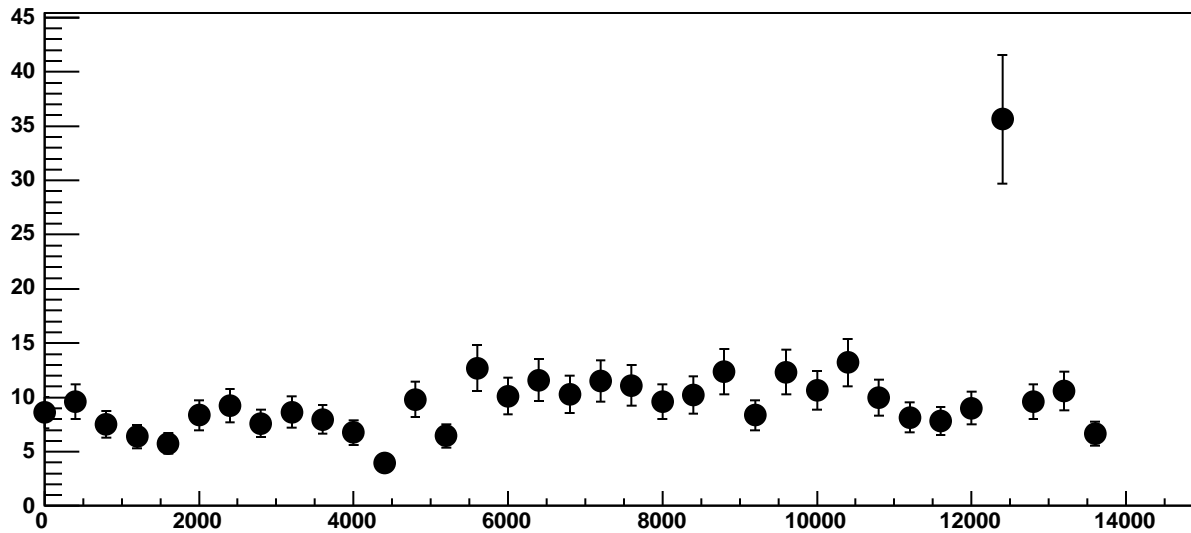
p0

$87.07 \pm 0.7907$

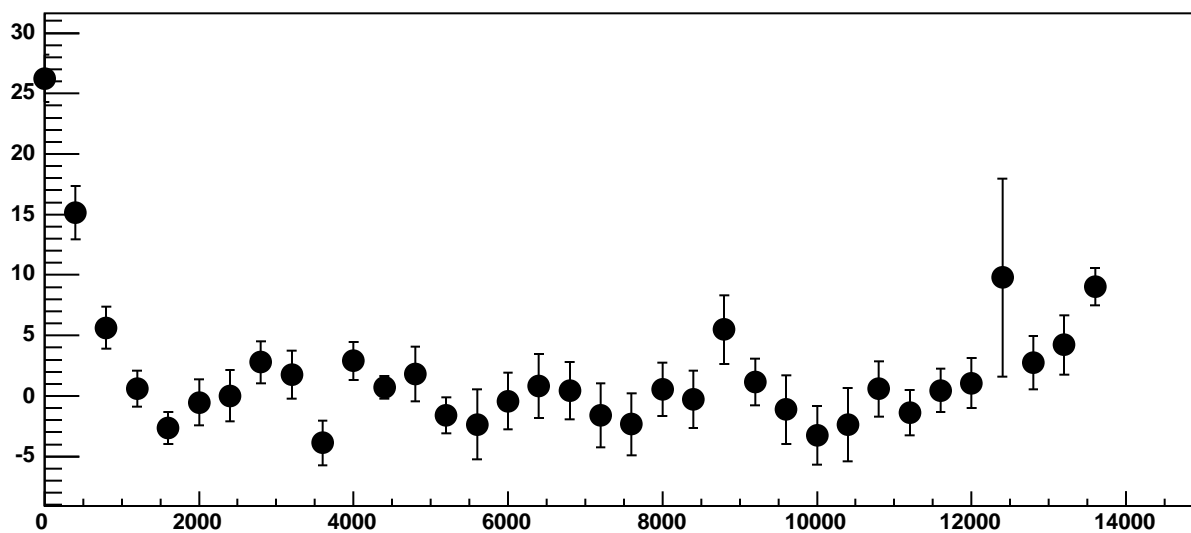
p1

$0.03216 \pm 0.0001402$

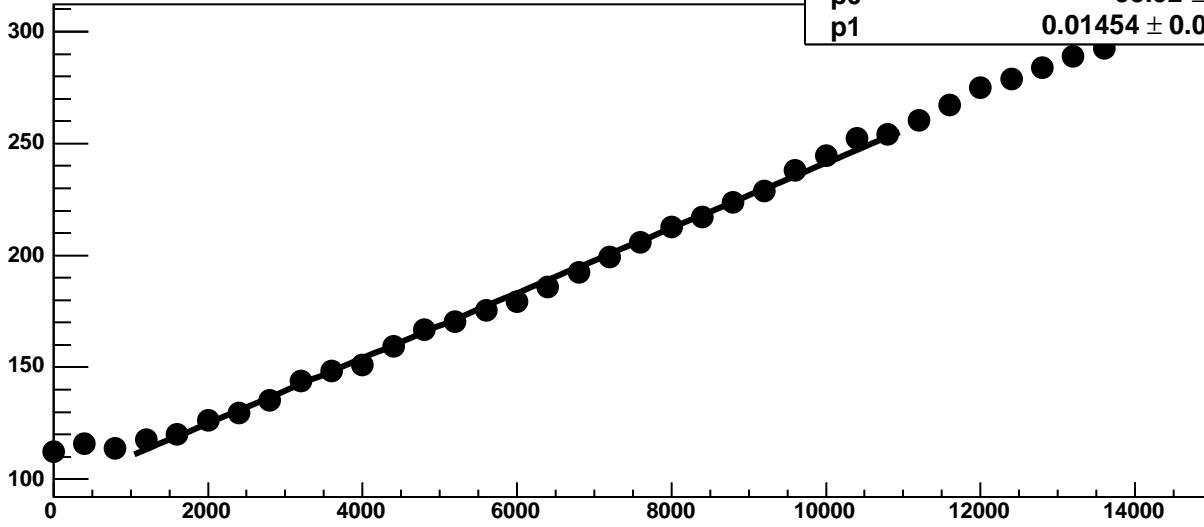
Chip 8, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

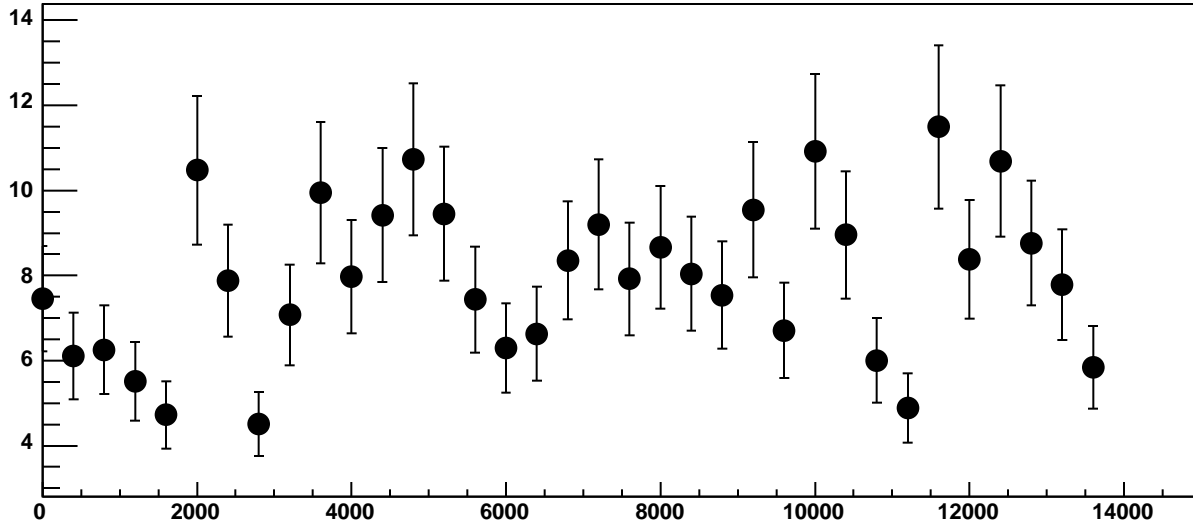


Chip 8, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

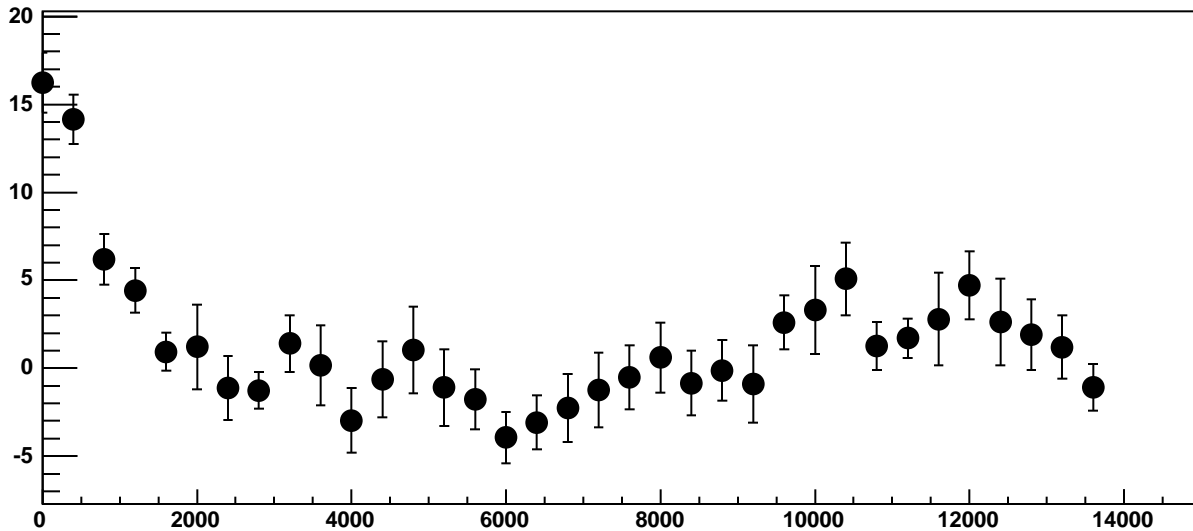


$\chi^2 / \text{ndf}$  45.46 / 23  
p0  $95.92 \pm 0.6827$   
p1  $0.01454 \pm 0.0001089$

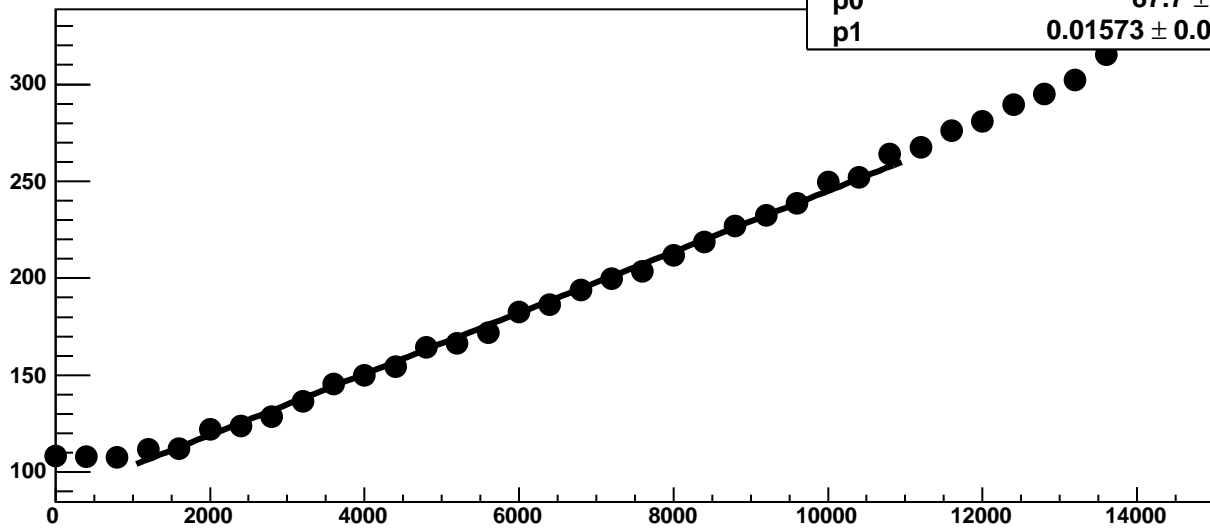
Chip 8, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

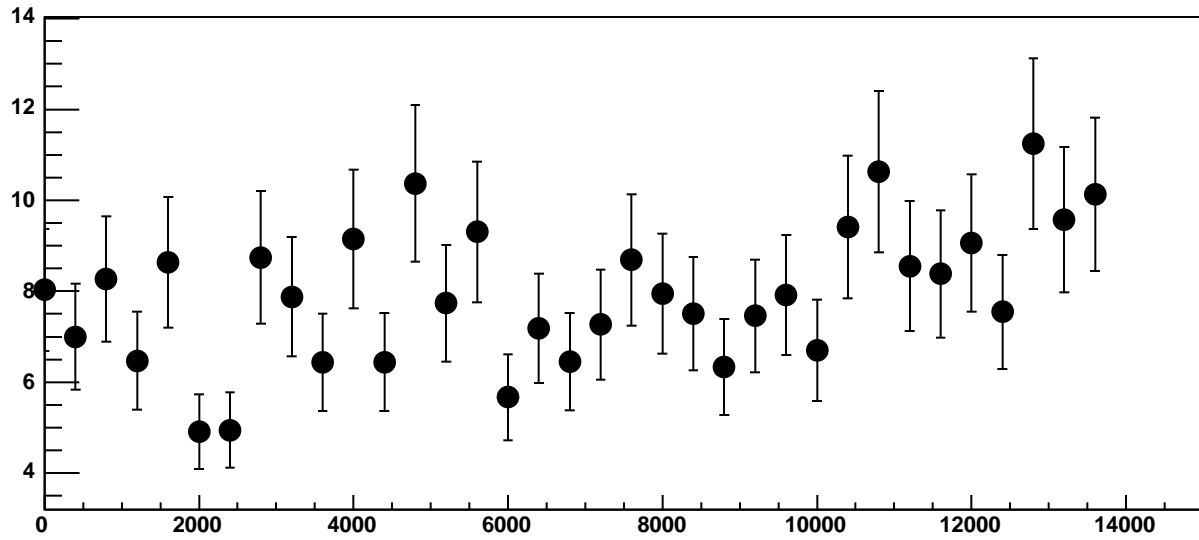


Chip 8, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

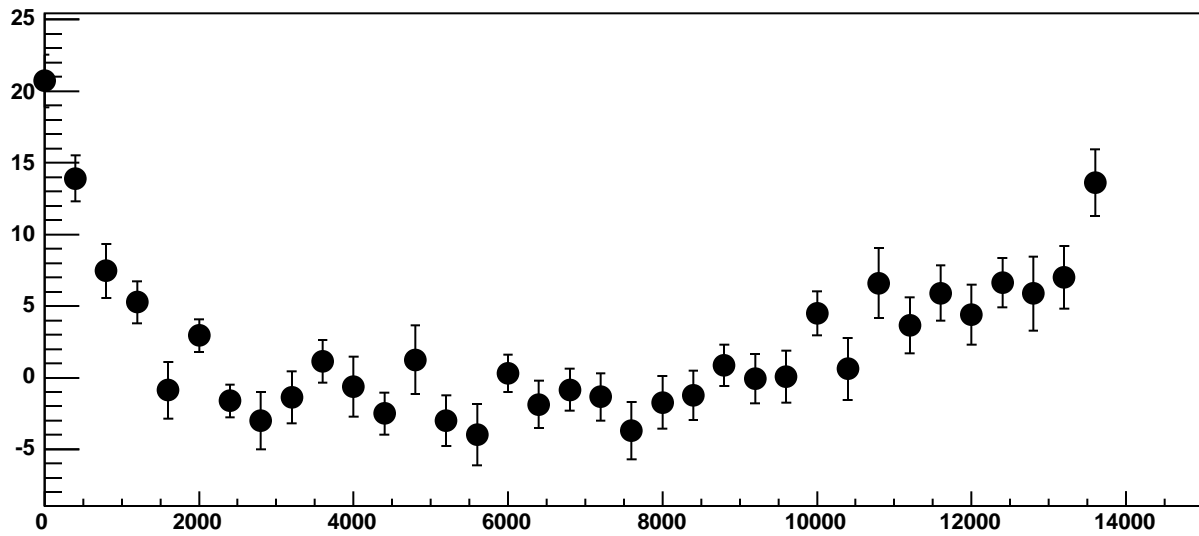


$\chi^2 / \text{ndf}$  58 / 23  
p0  $87.7 \pm 0.7138$   
p1  $0.01573 \pm 0.0001139$

Chip 8, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

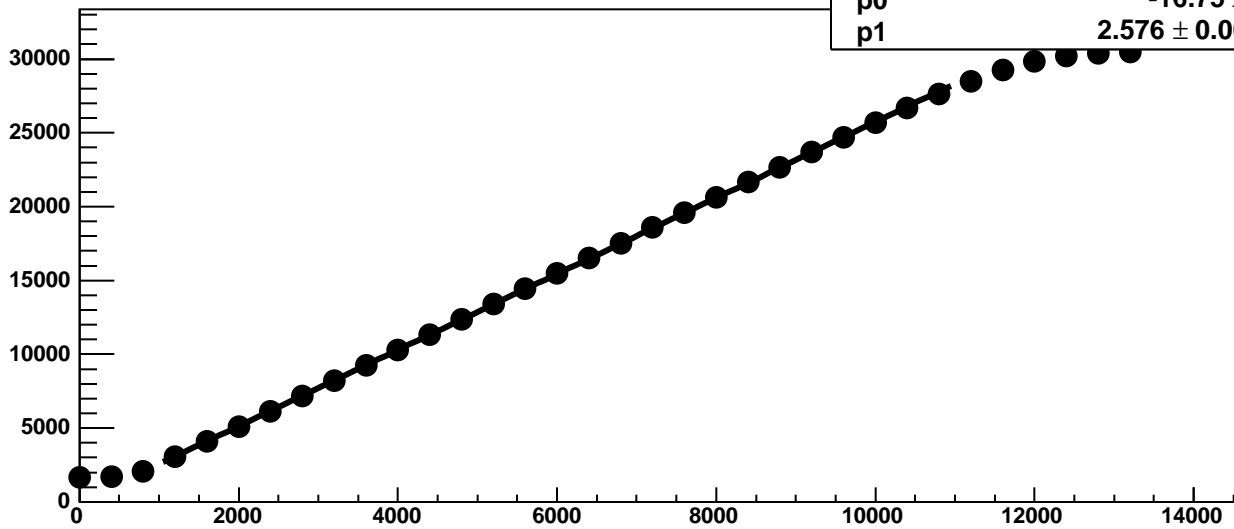


Chip 8, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

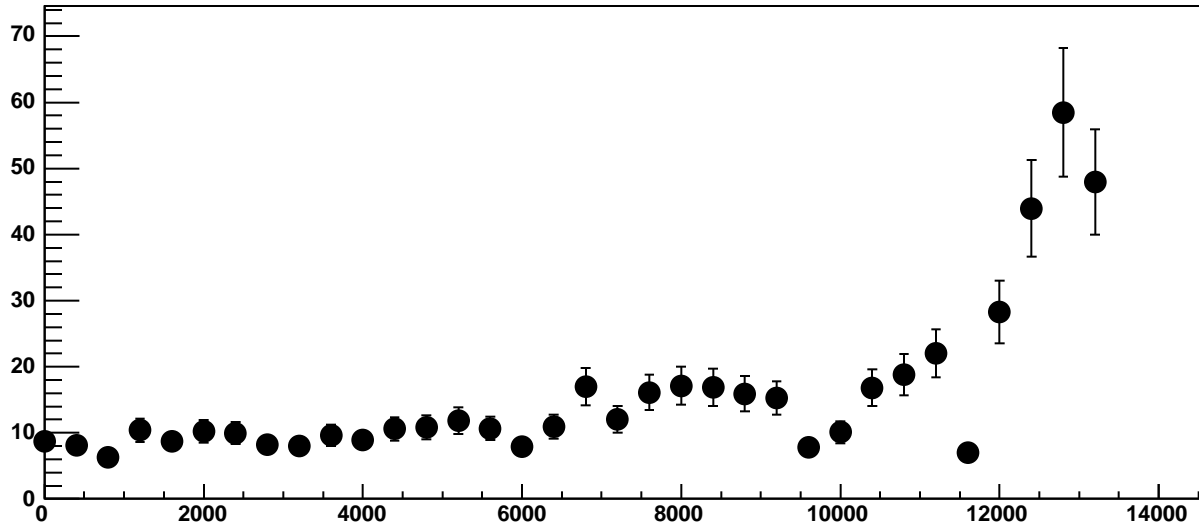




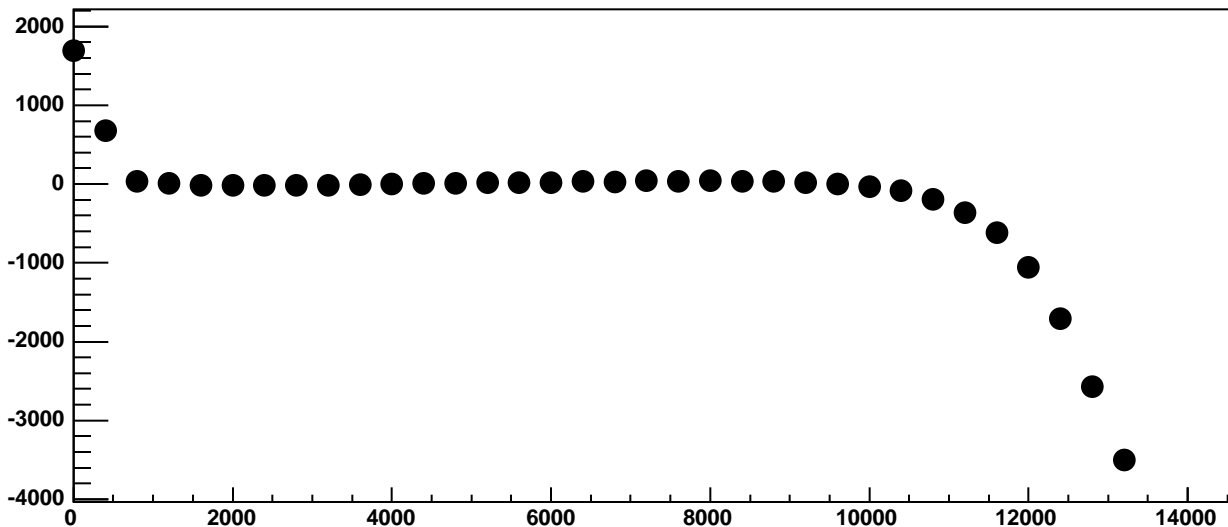
Chip 8, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



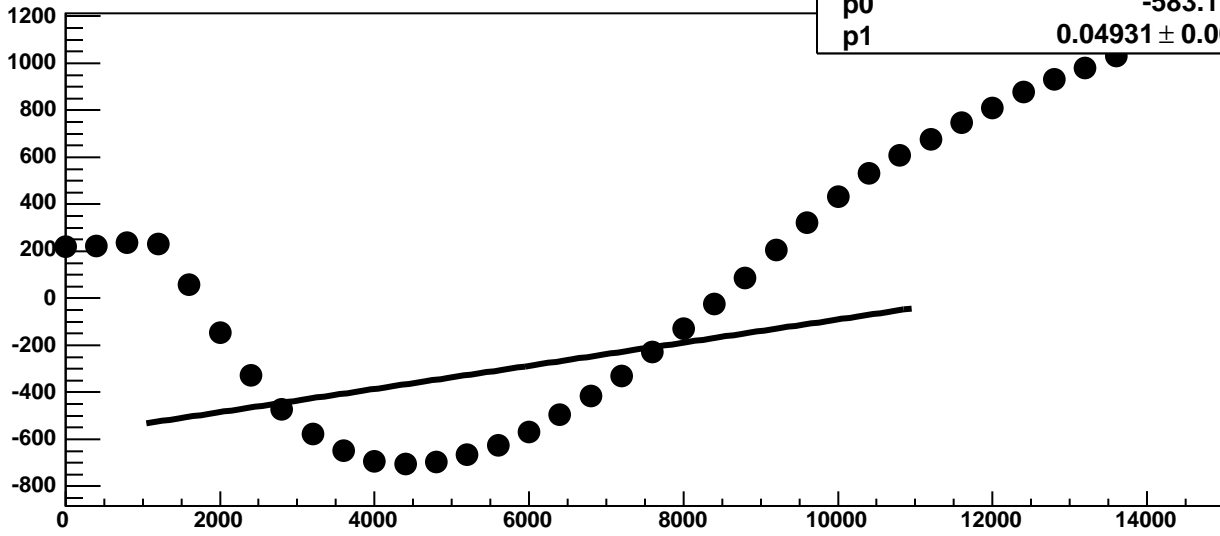
Chip 8, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

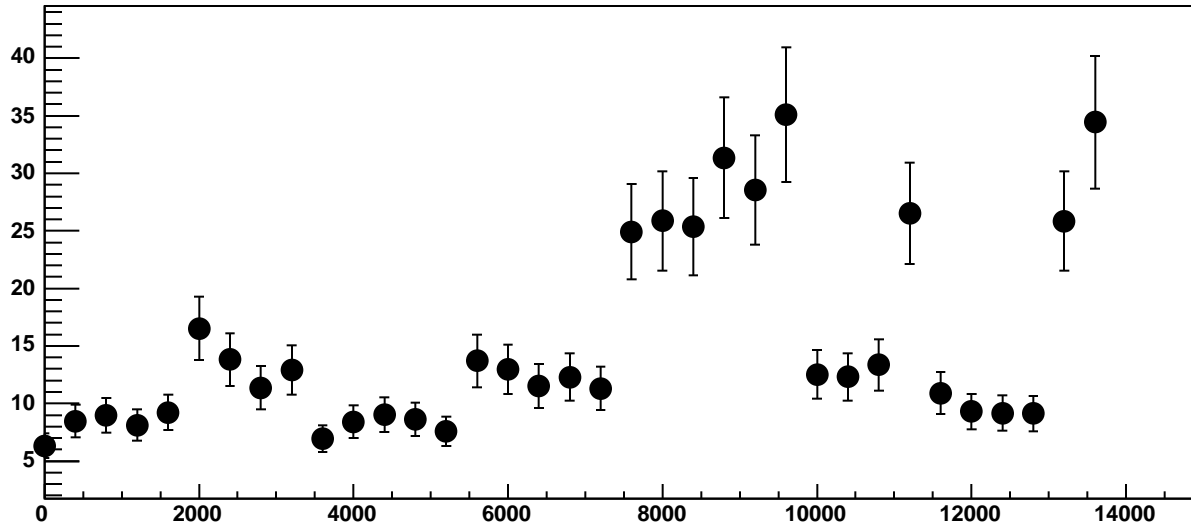


Chip 8, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

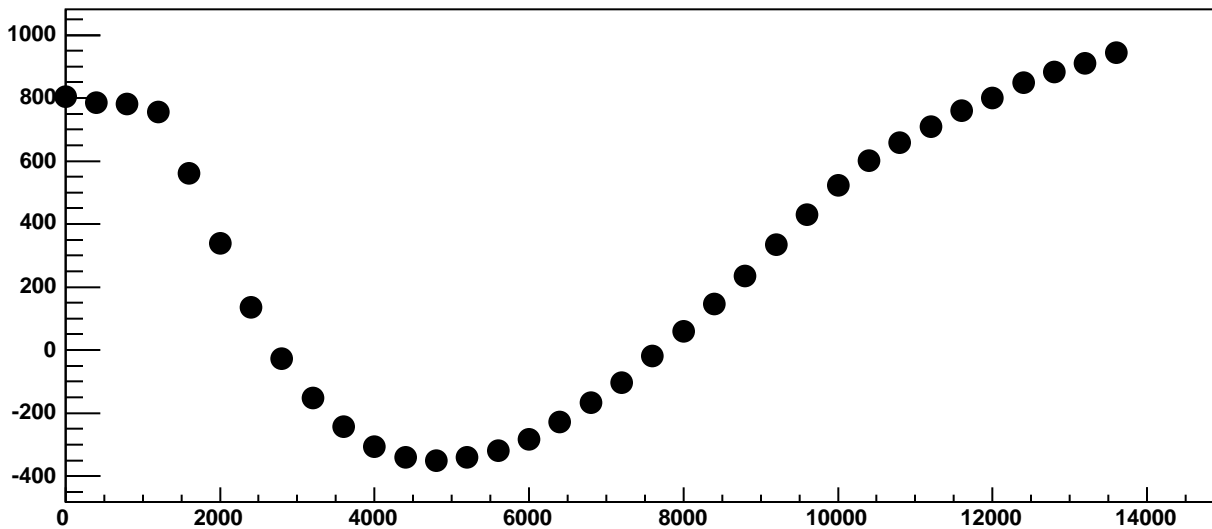


$\chi^2 / \text{ndf}$  5.559e+05 / 23  
p0 -583.1 ± 1.131  
p1 0.04931 ± 0.0002049

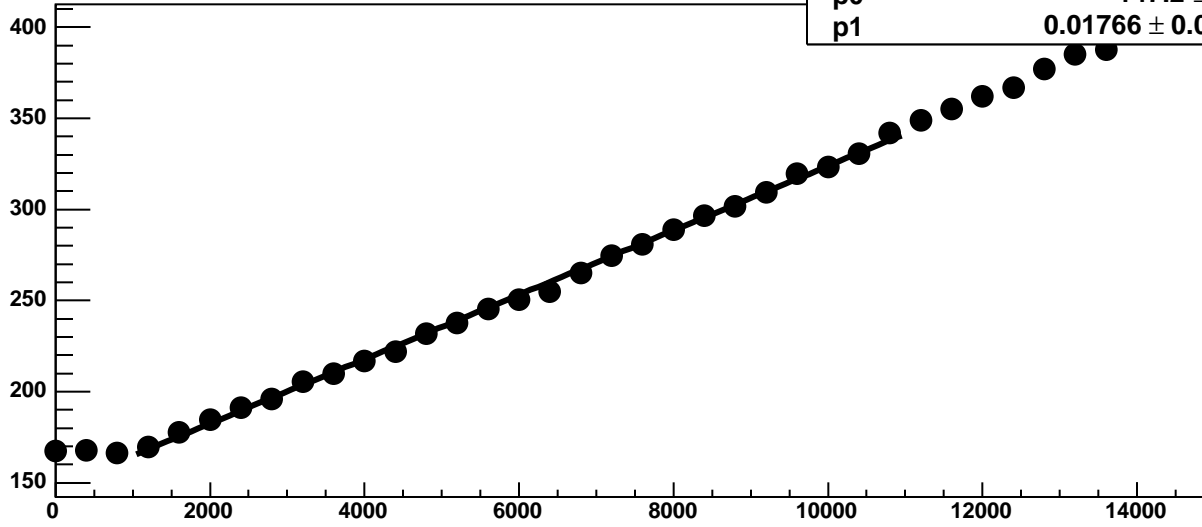
Chip 8, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

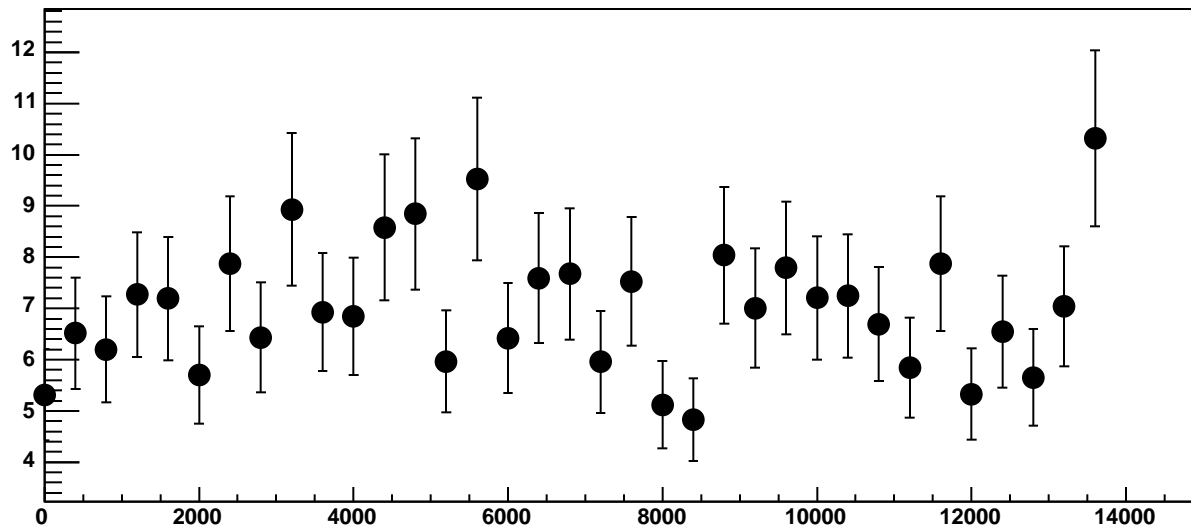


Chip 8, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

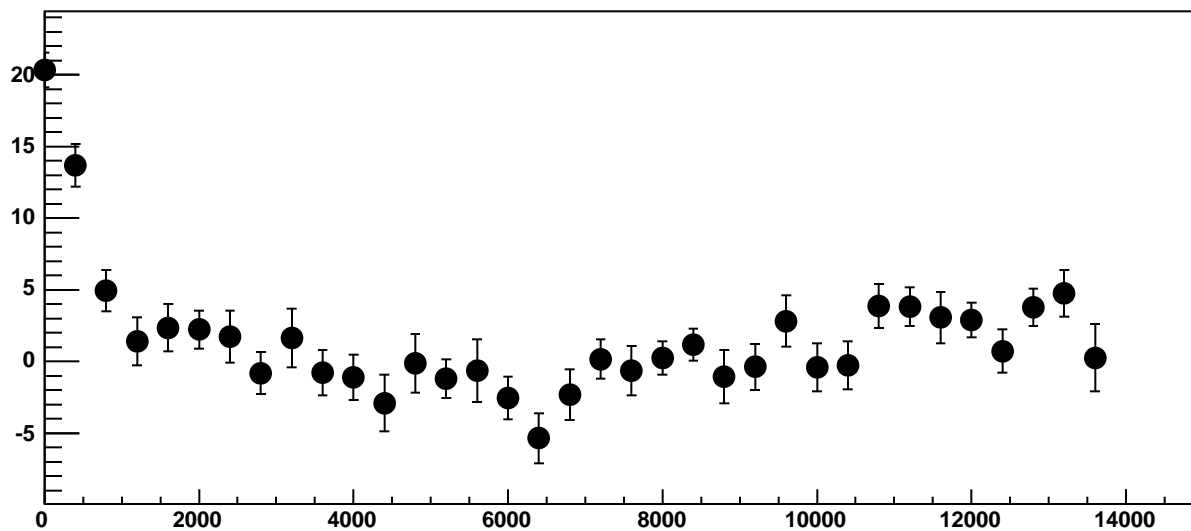


$\chi^2 / \text{ndf}$  35.99 / 23  
p0 147.2 ± 0.7445  
p1 0.01766 ± 0.0001099

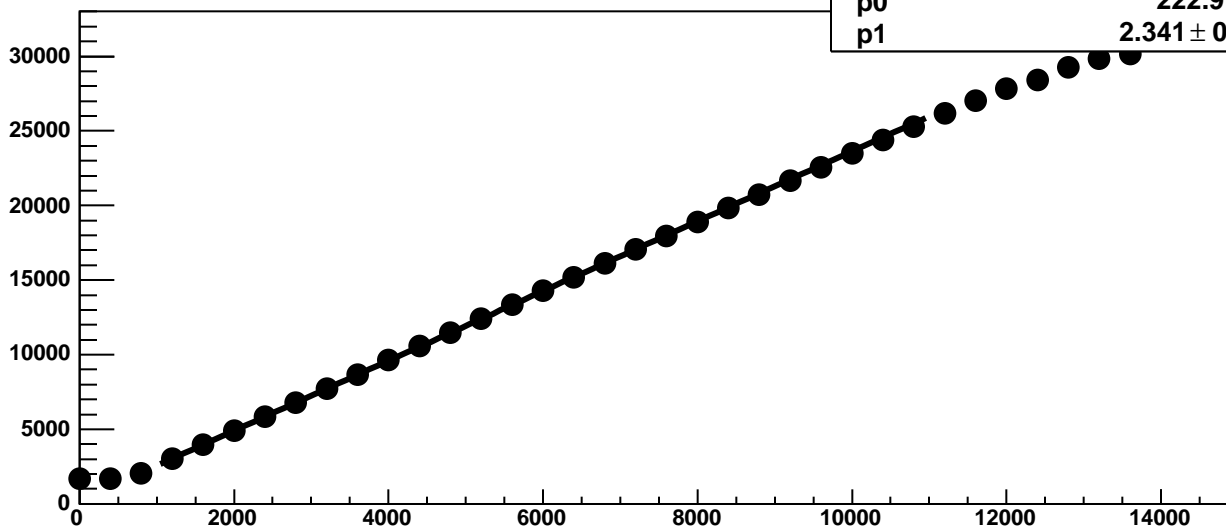
Chip 8, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

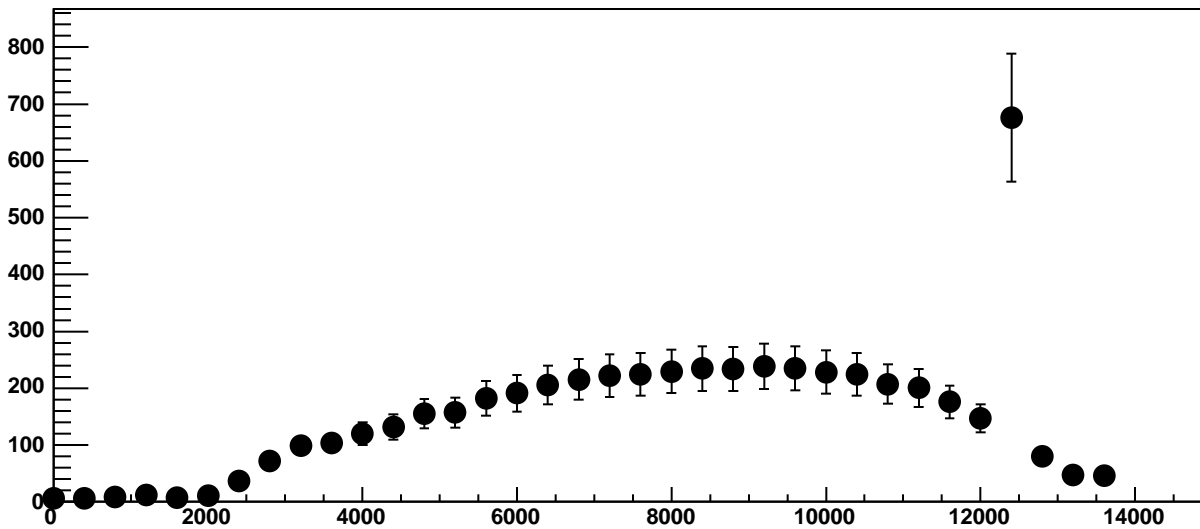


Chip 8, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

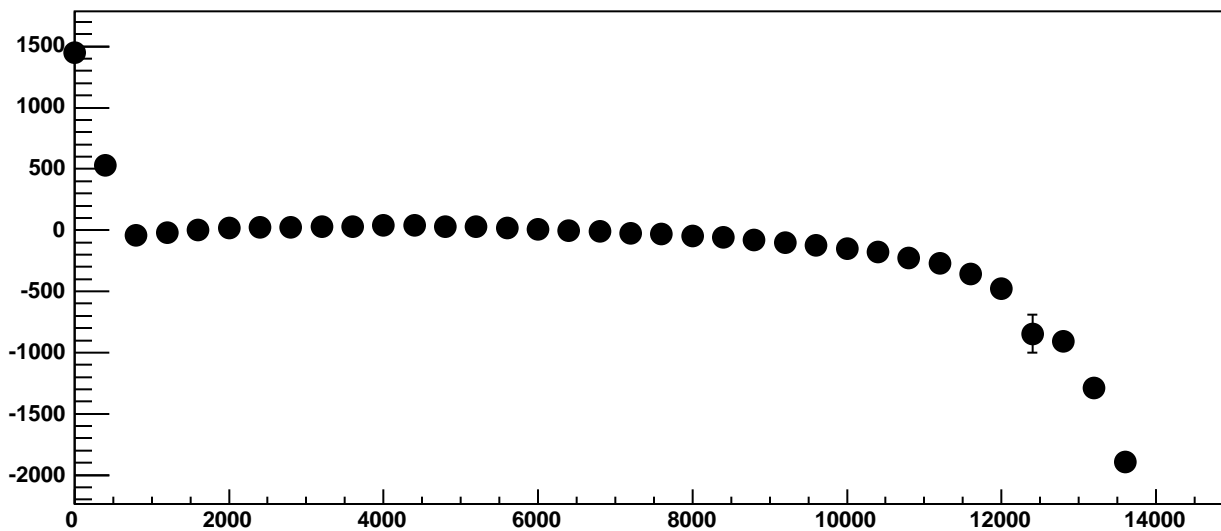


$\chi^2 / \text{ndf}$  181.6 / 23  
p0  $222.9 \pm 3.188$   
p1  $2.341 \pm 0.001701$

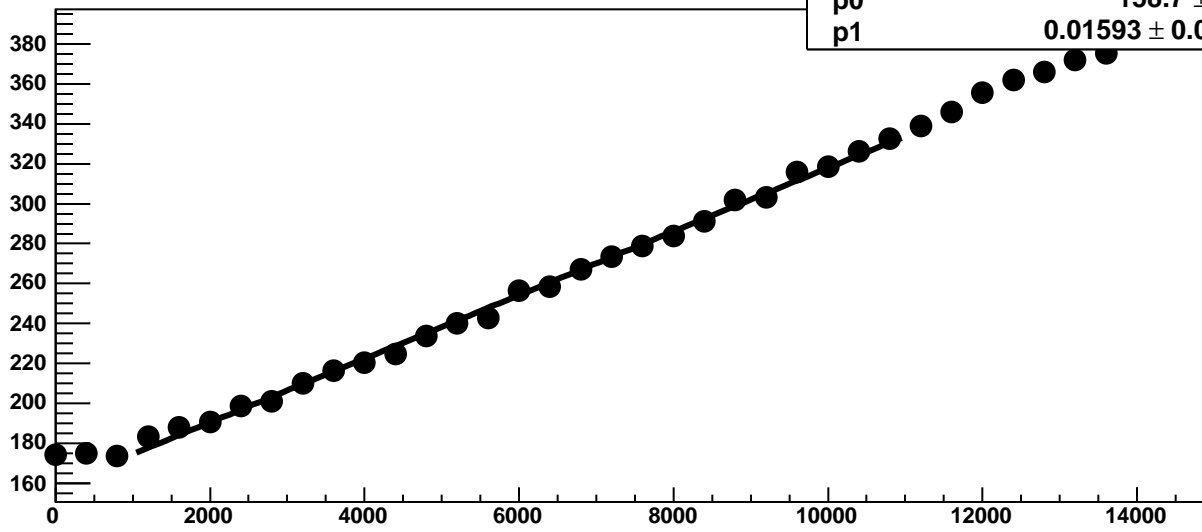
Chip 8, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

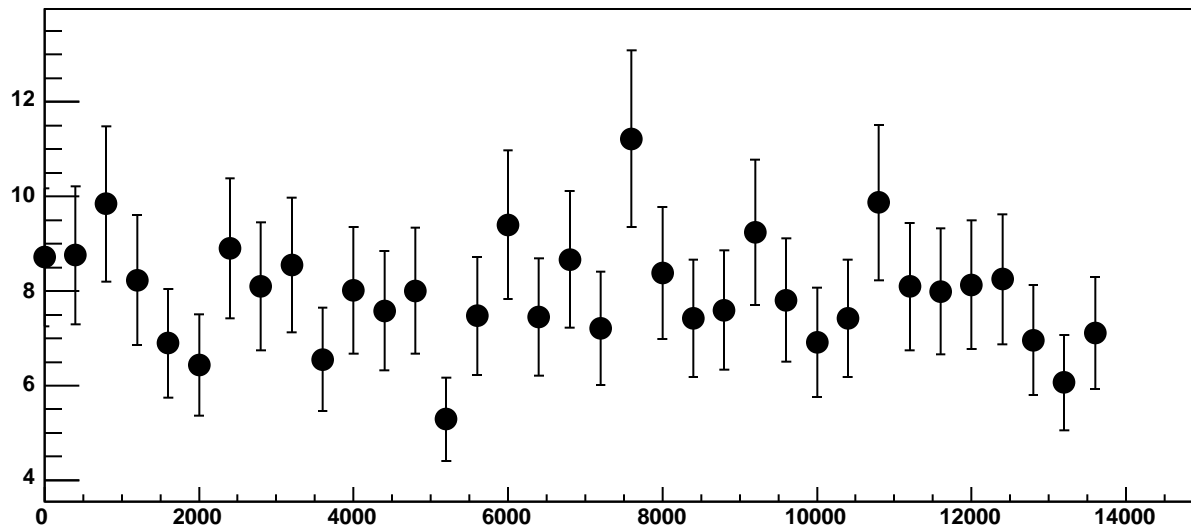


Chip 8, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

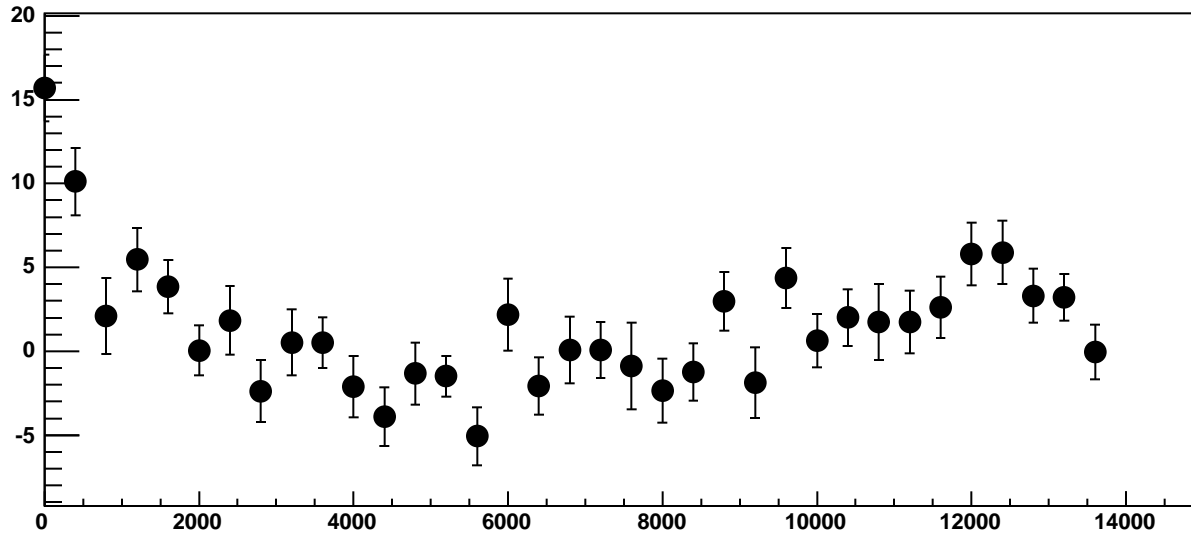


$\chi^2 / \text{ndf}$  50.56 / 23  
p0 158.7 ± 0.8005  
p1 0.01593 ± 0.0001236

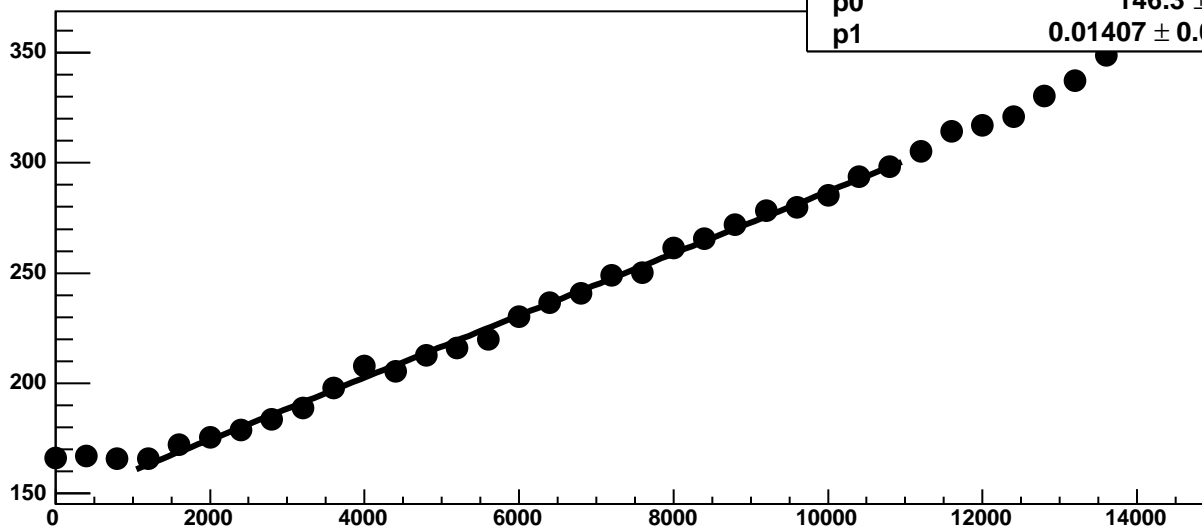
Chip 8, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

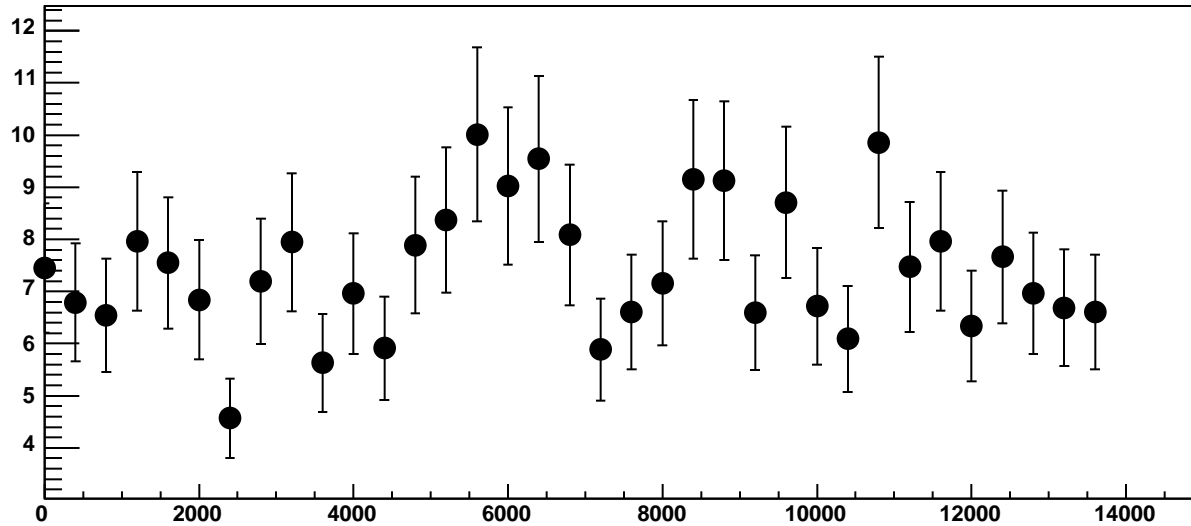


Chip 8, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

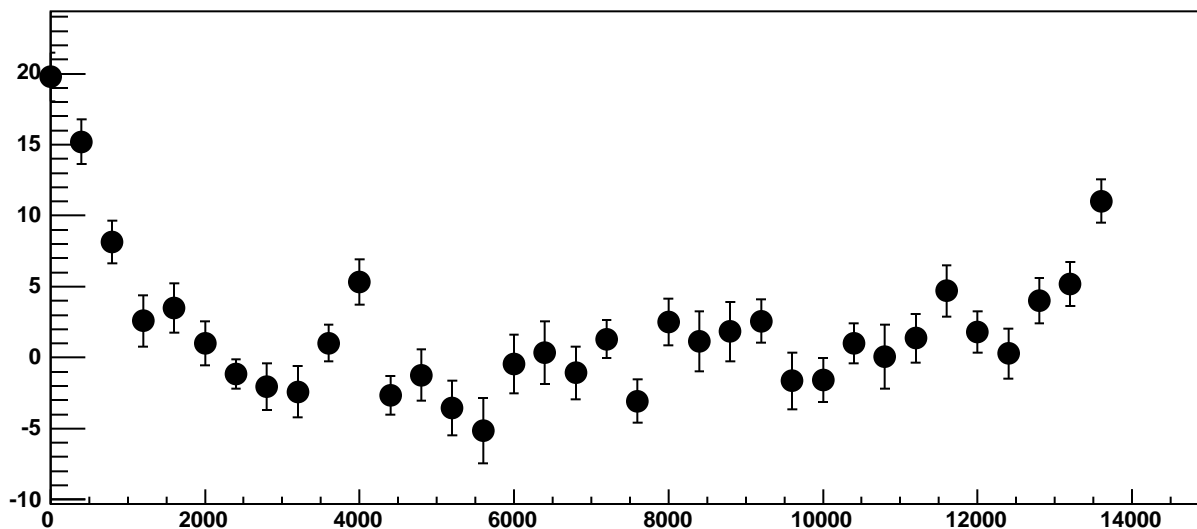


$\chi^2 / \text{ndf}$  49.4 / 23  
p0 146.3 ± 0.7183  
p1 0.01407 ± 0.0001121

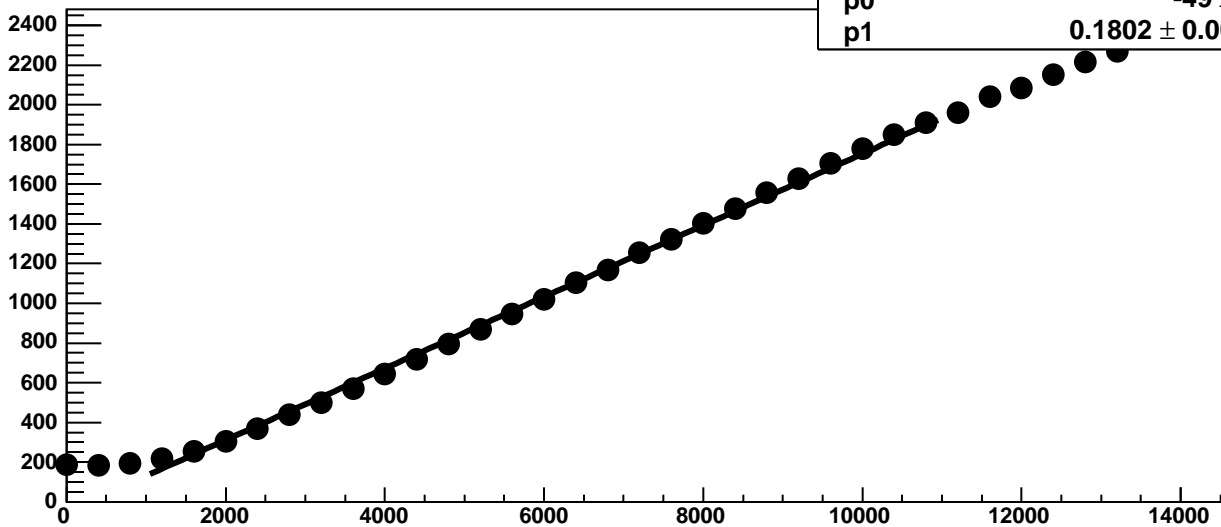
Chip 8, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



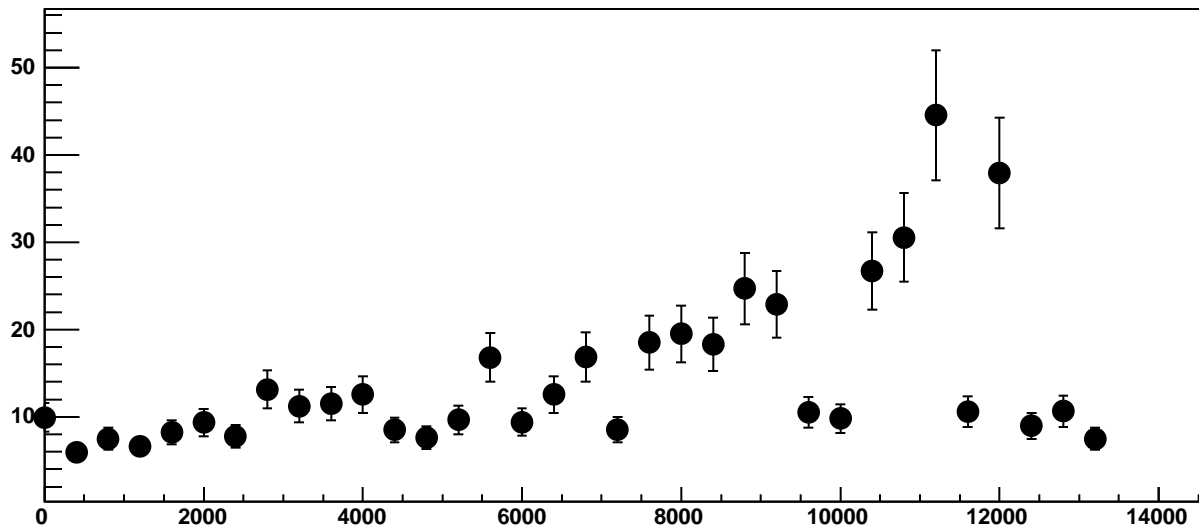
Chip 8, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



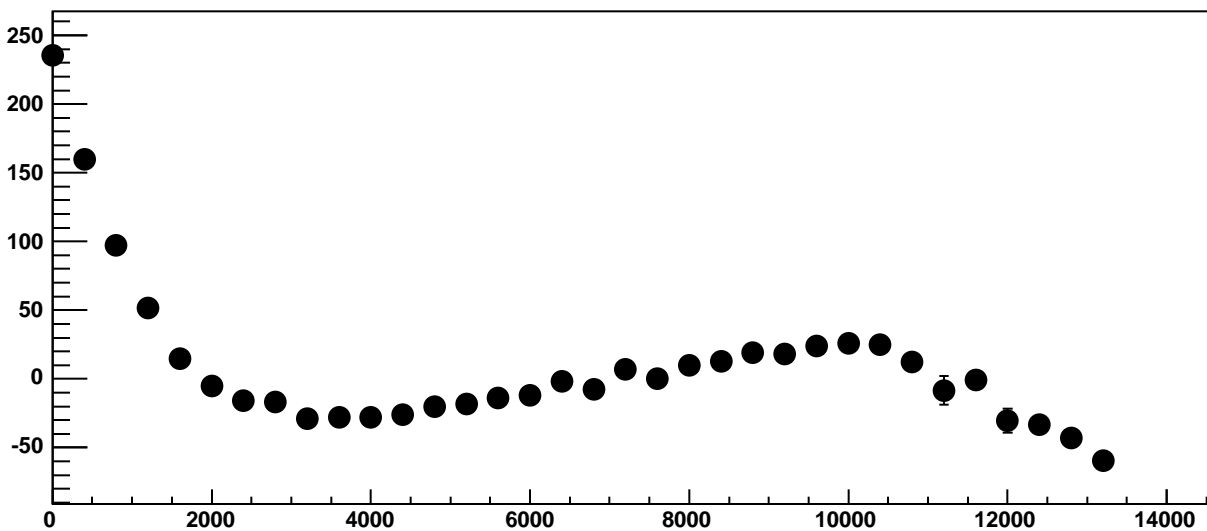
Chip 8, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC



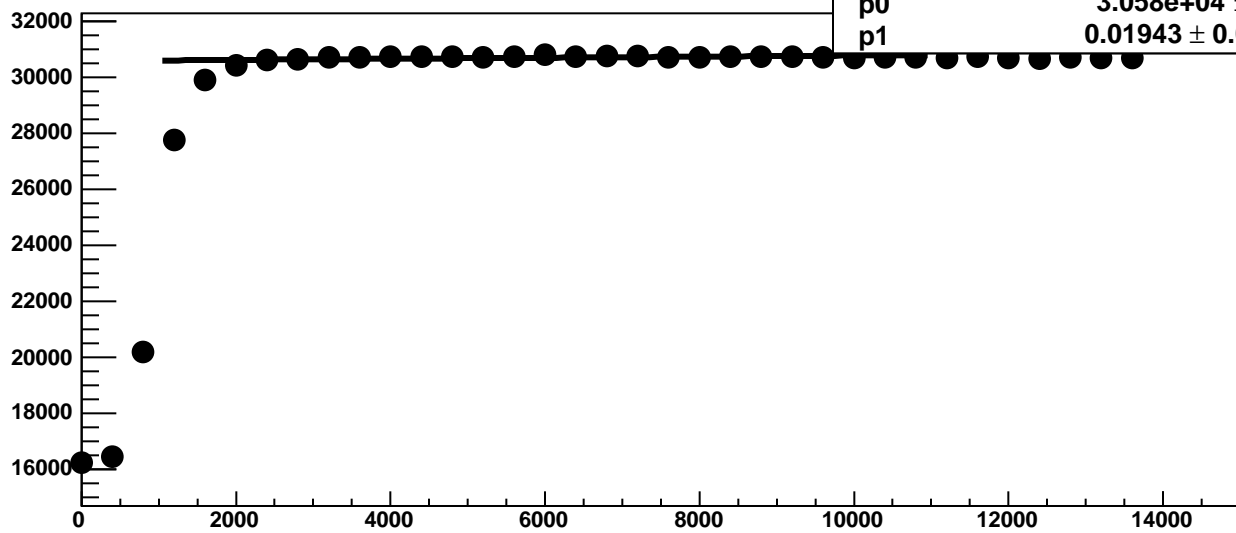
Chip 8, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



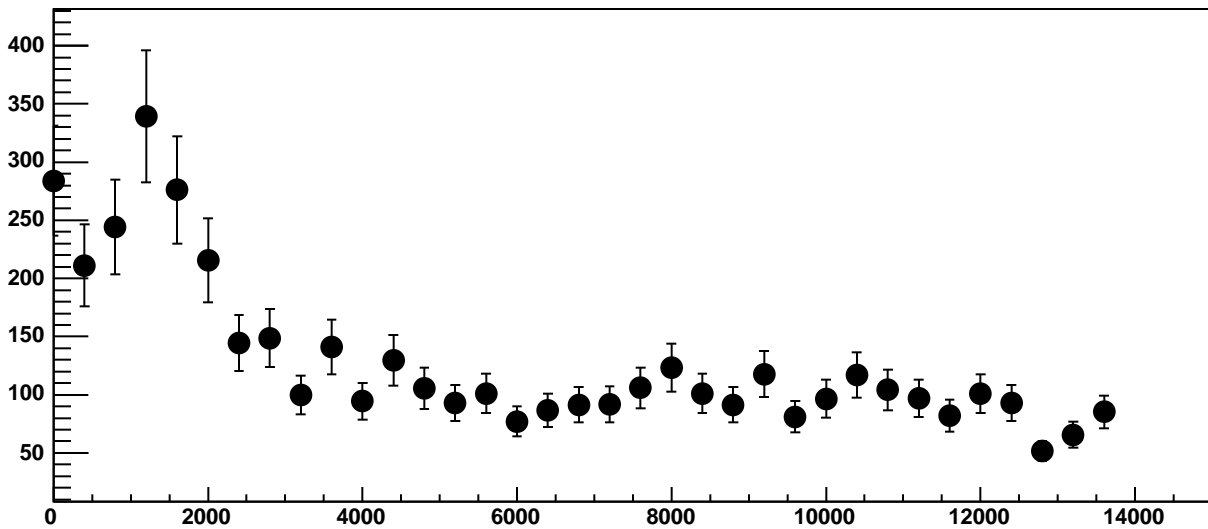
Chip 8, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



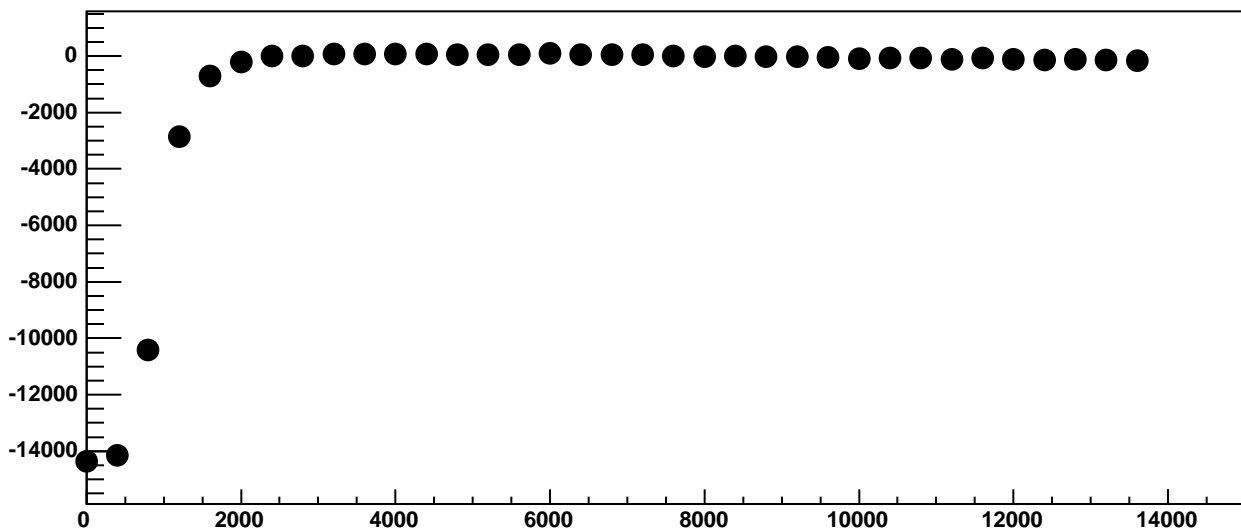
Chip 8, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



Chip 8, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

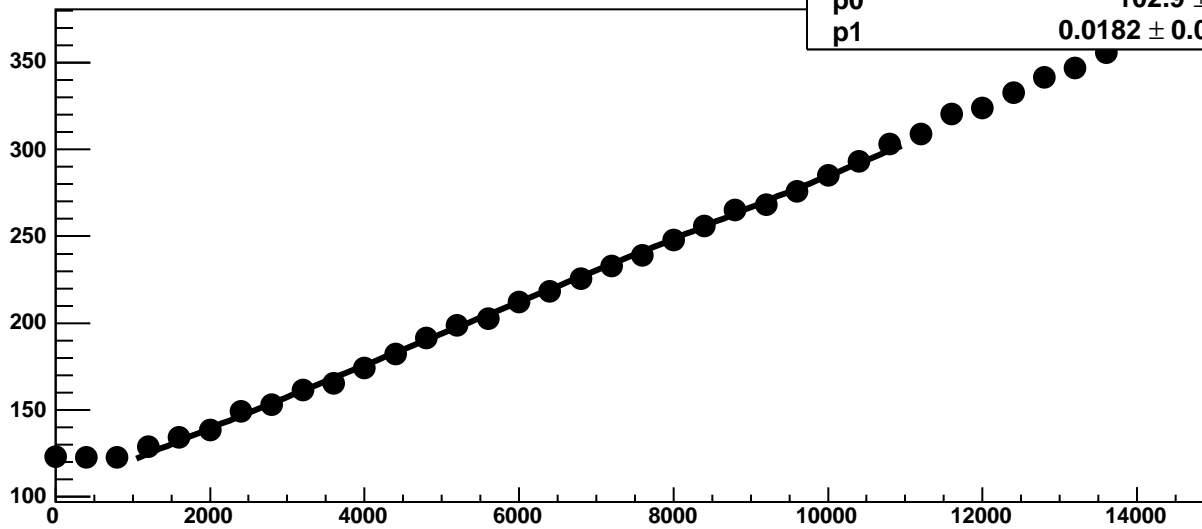


Chip 8, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



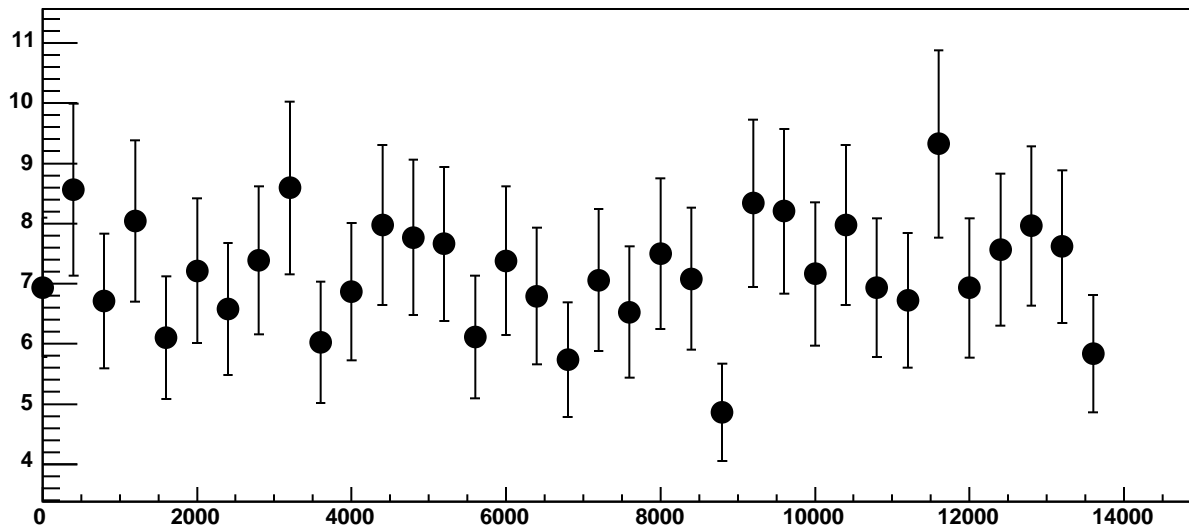


Chip 8, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

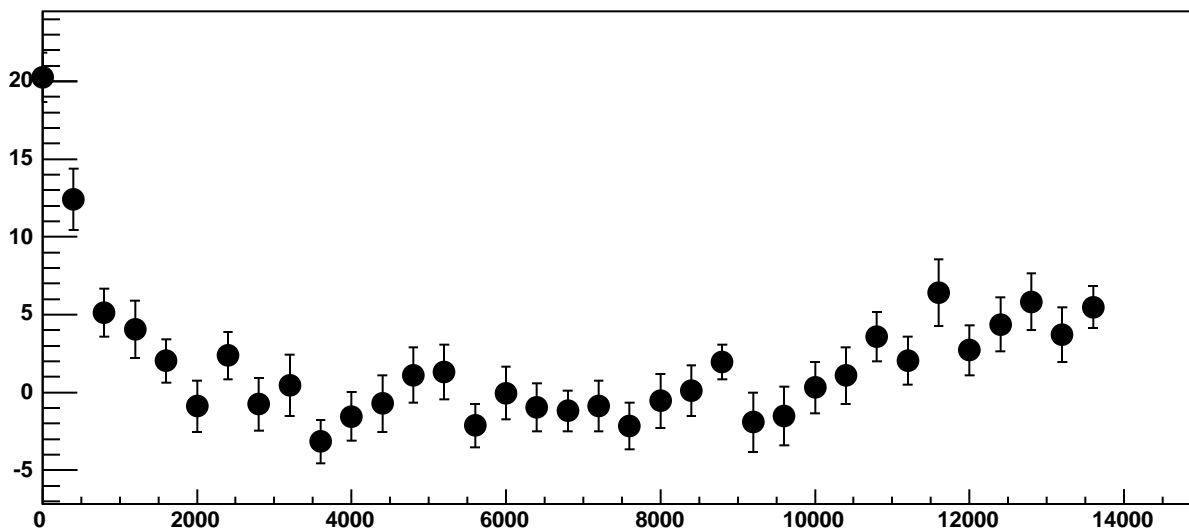


$\chi^2 / \text{ndf}$  33.36 / 23  
p0  $102.9 \pm 0.7489$   
p1  $0.0182 \pm 0.0001124$

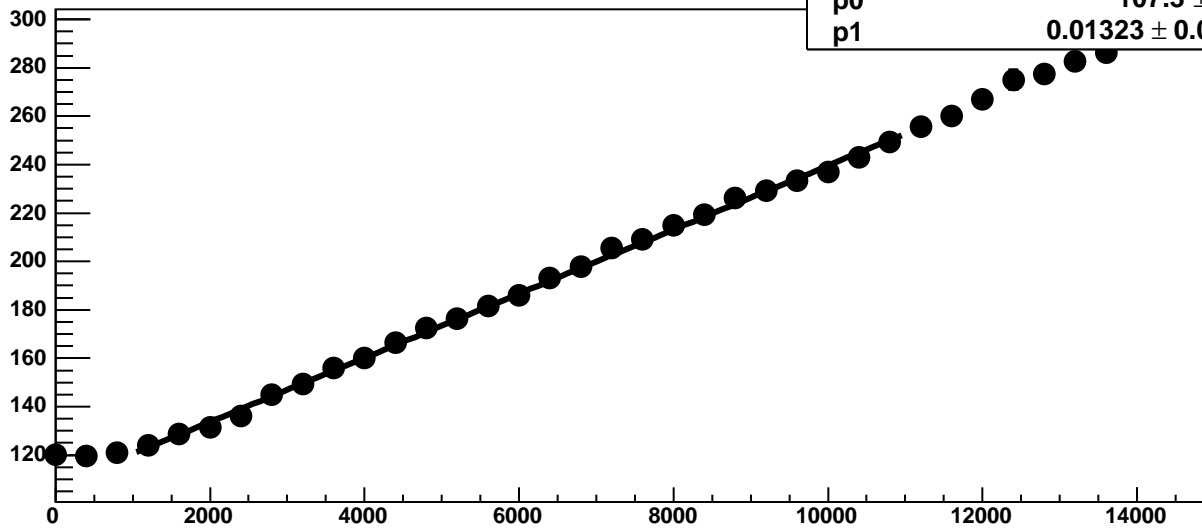
Chip 8, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

23.43 / 23

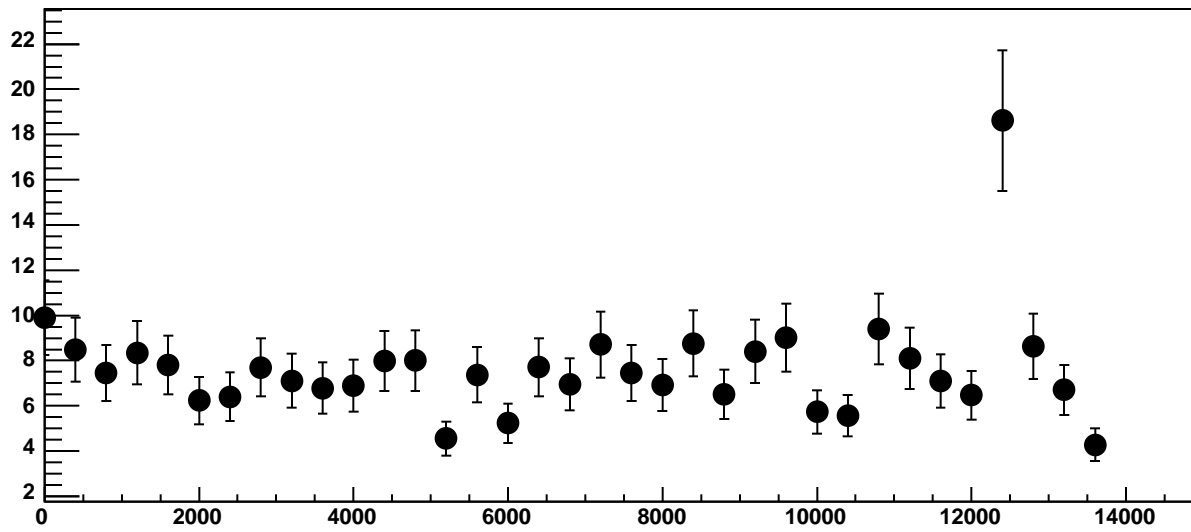
p0

$107.3 \pm 0.7478$

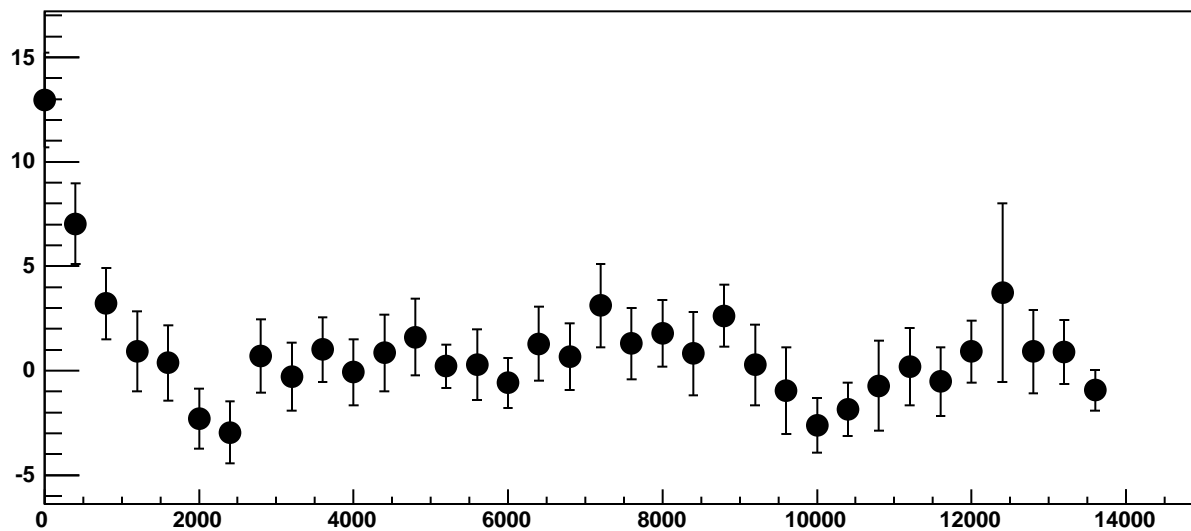
p1

$0.01323 \pm 0.0001132$

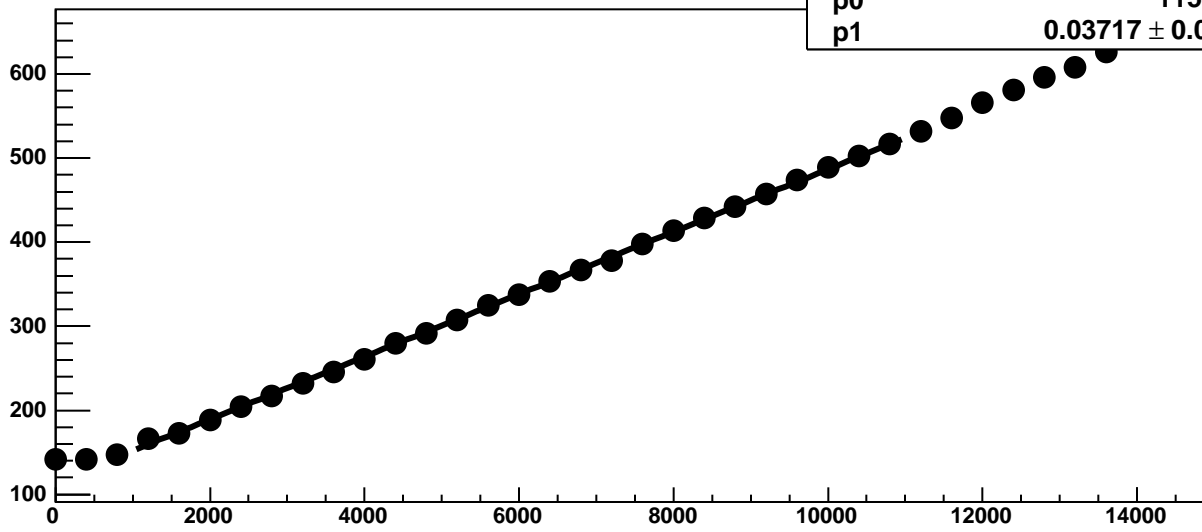
Chip 8, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

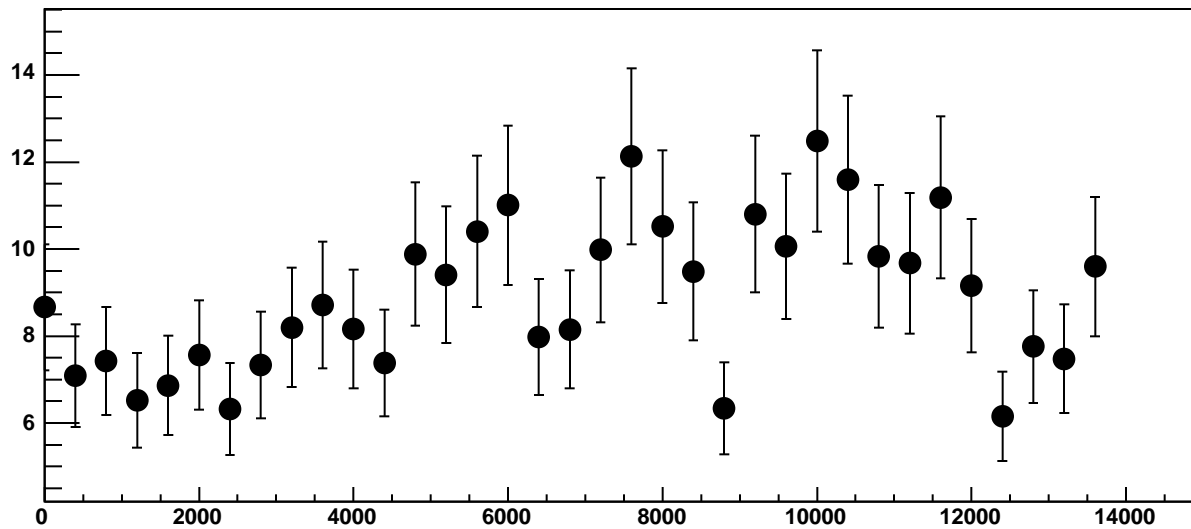


Chip 8, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

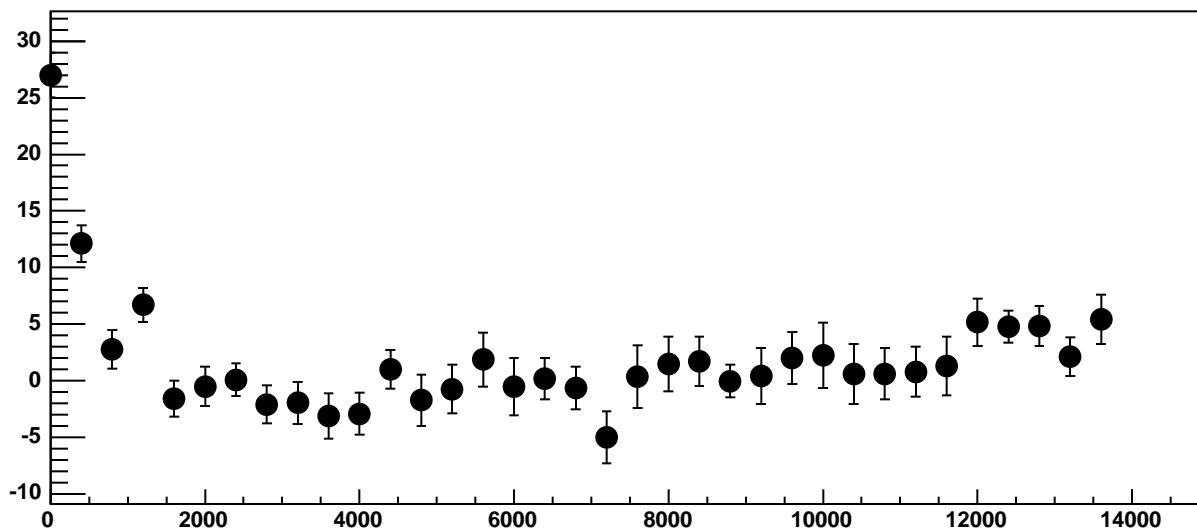


$\chi^2 / \text{ndf}$  37.72 / 23  
p0 115 ± 0.807  
p1 0.03717 ± 0.0001343

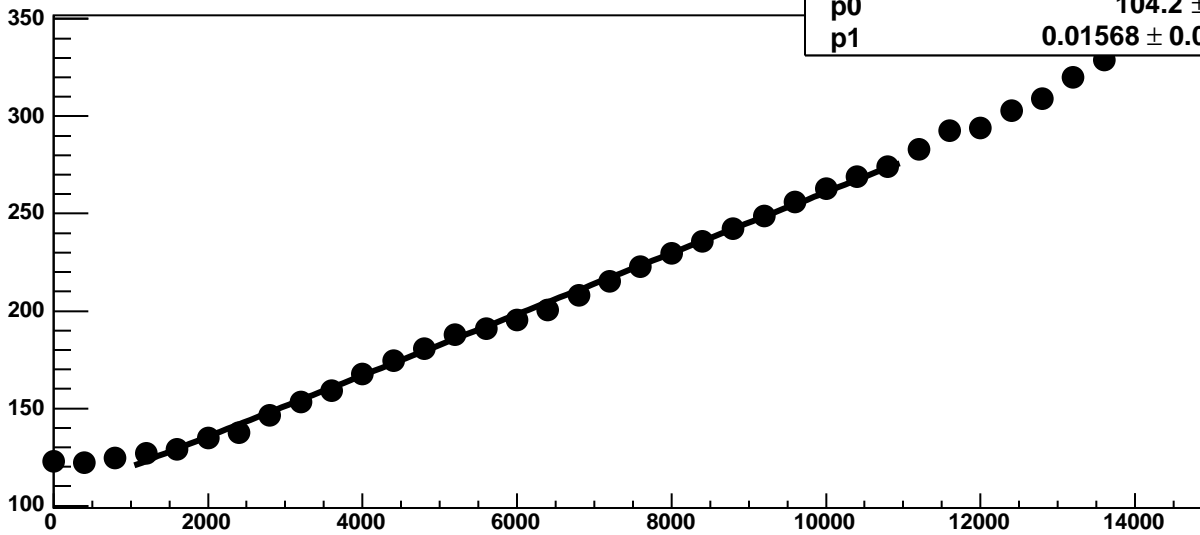
Chip 8, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

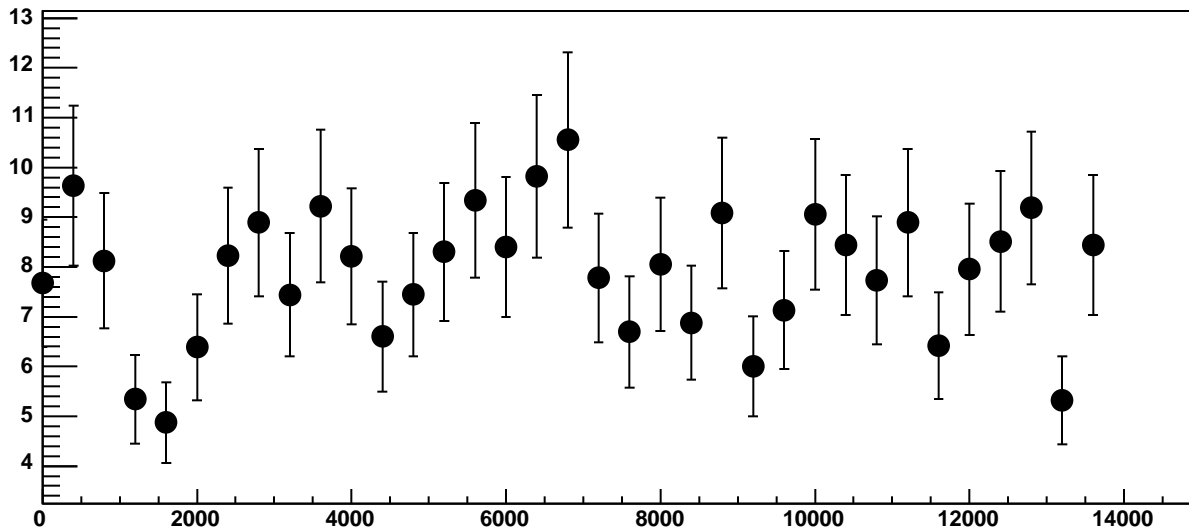


Chip 8, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

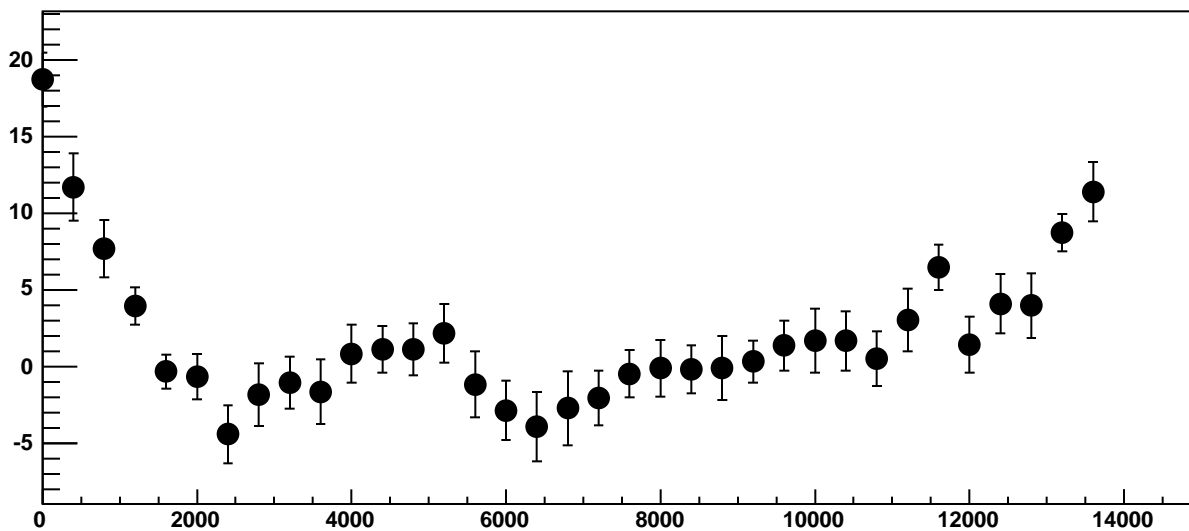


$\chi^2 / \text{ndf}$  30.75 / 23  
p0  $104.2 \pm 0.6998$   
p1  $0.01568 \pm 0.0001097$

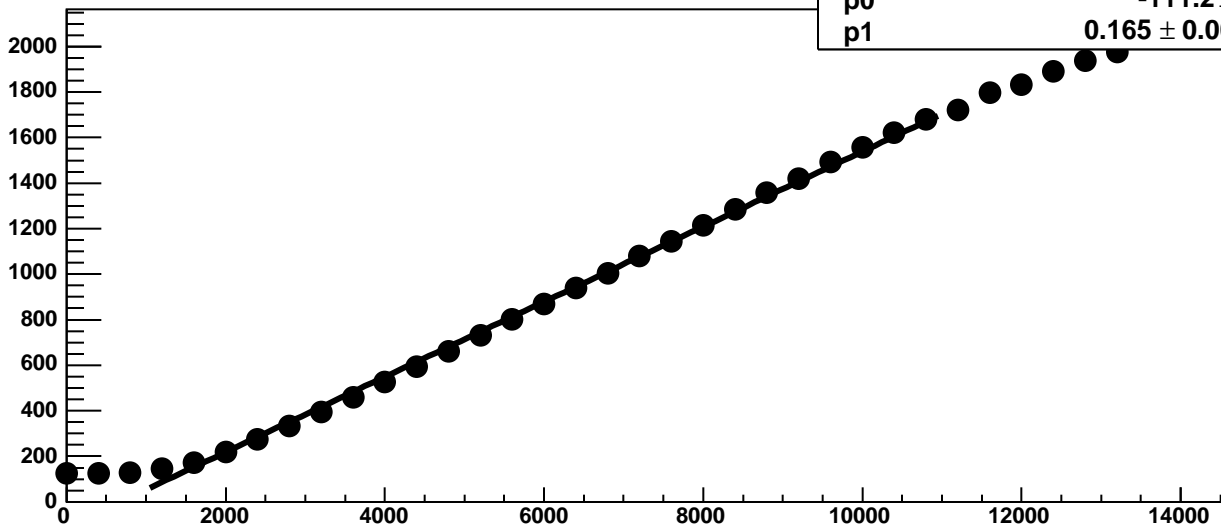
Chip 8, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

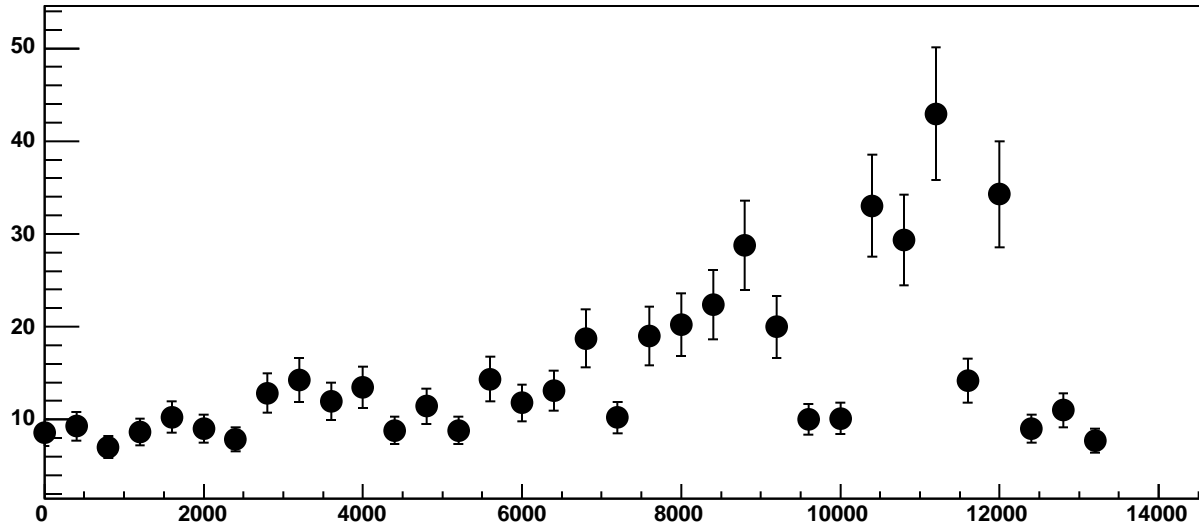


Chip 8, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

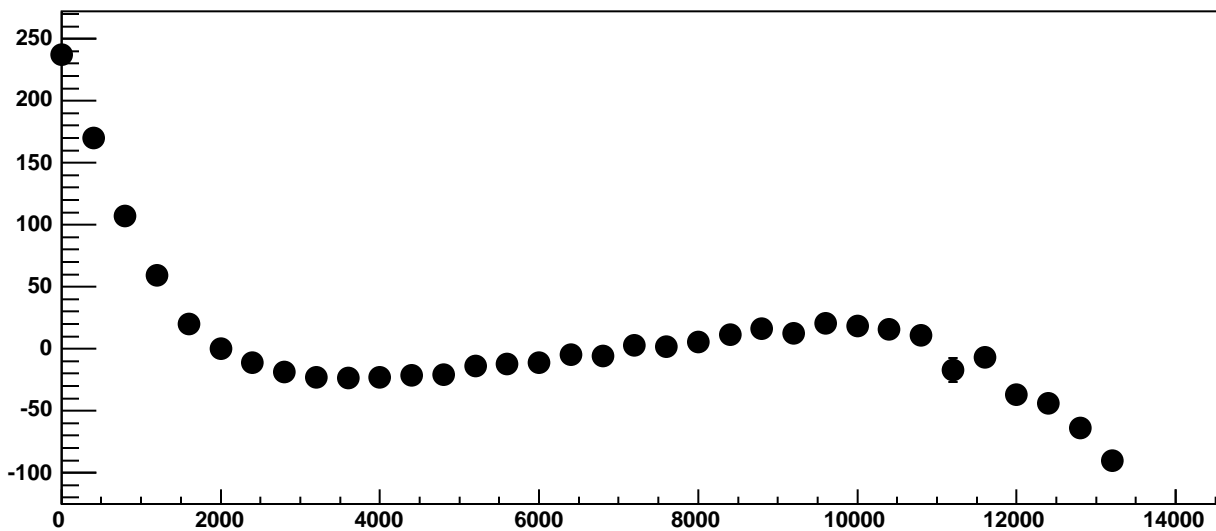


$\chi^2 / \text{ndf}$  1653 / 23  
p0  $-111.2 \pm 1.126$   
p1  $0.165 \pm 0.0002003$

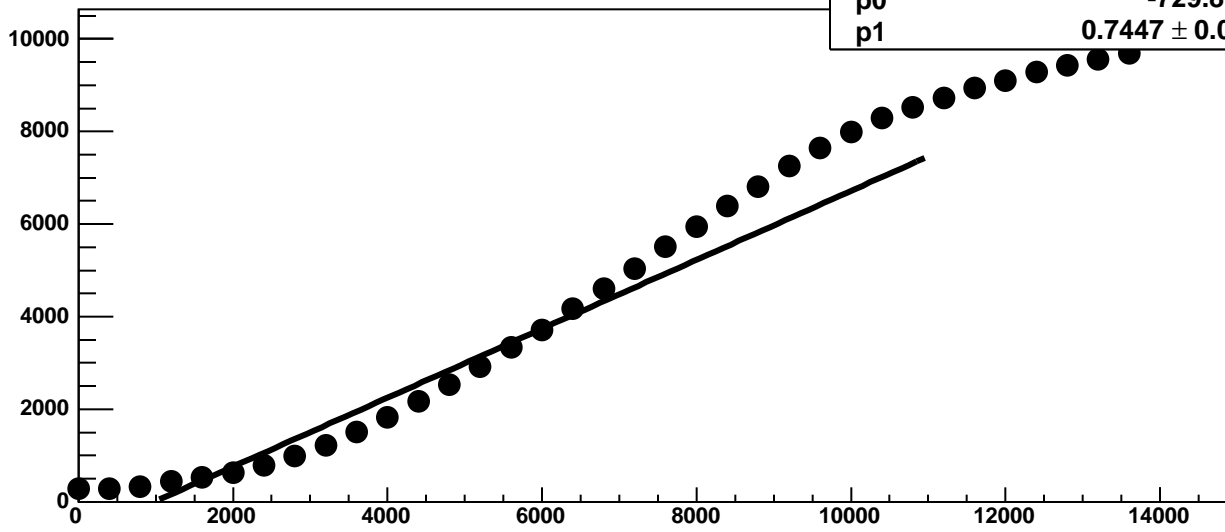
Chip 8, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

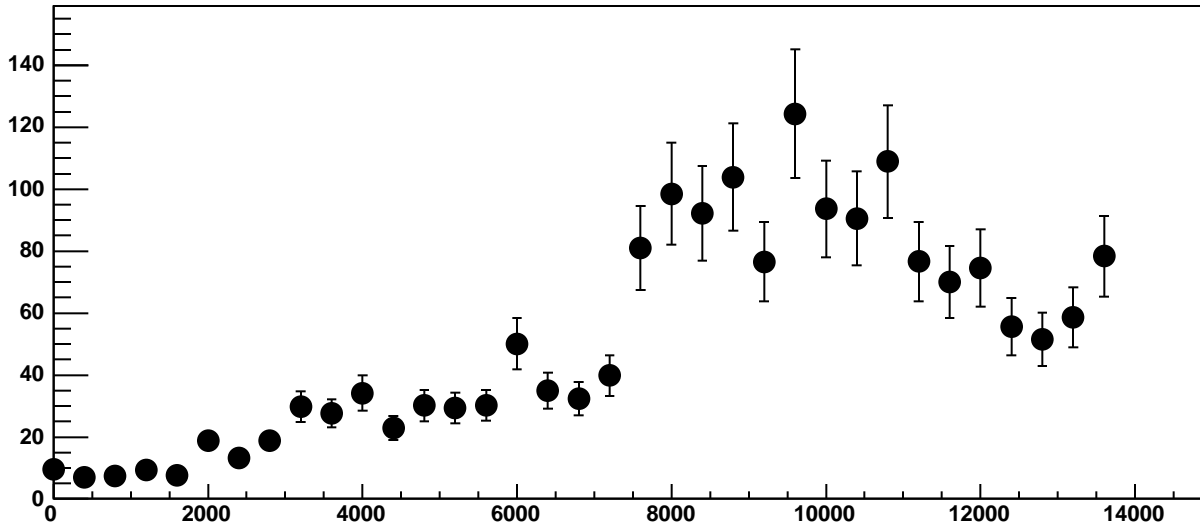


Chip 8, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

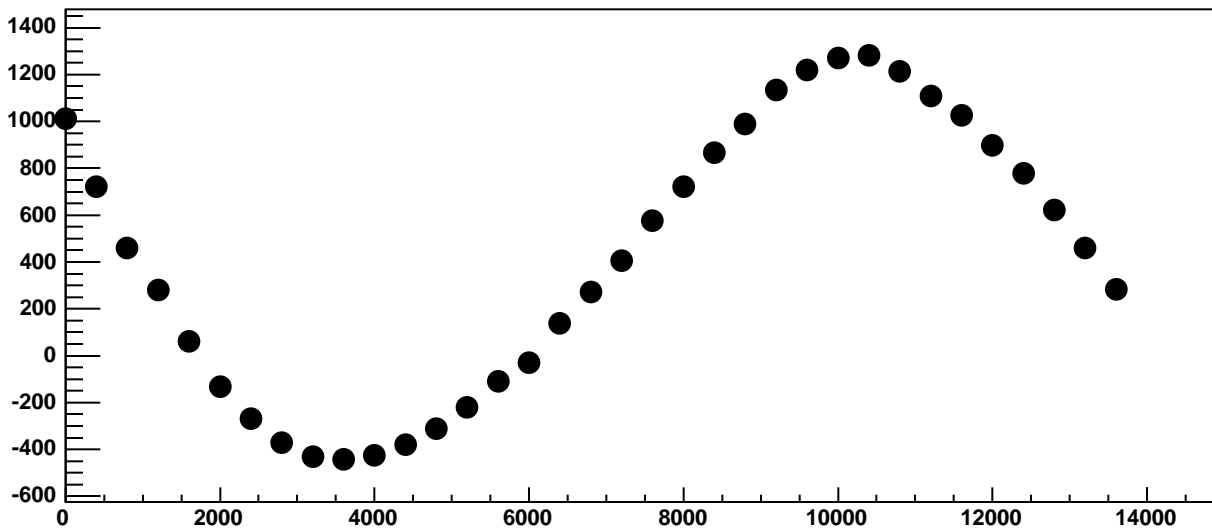


$\chi^2 / \text{ndf}$  7.943e+04 / 23  
p0 -729.8 ± 1.767  
p1 0.7447 ± 0.0005667

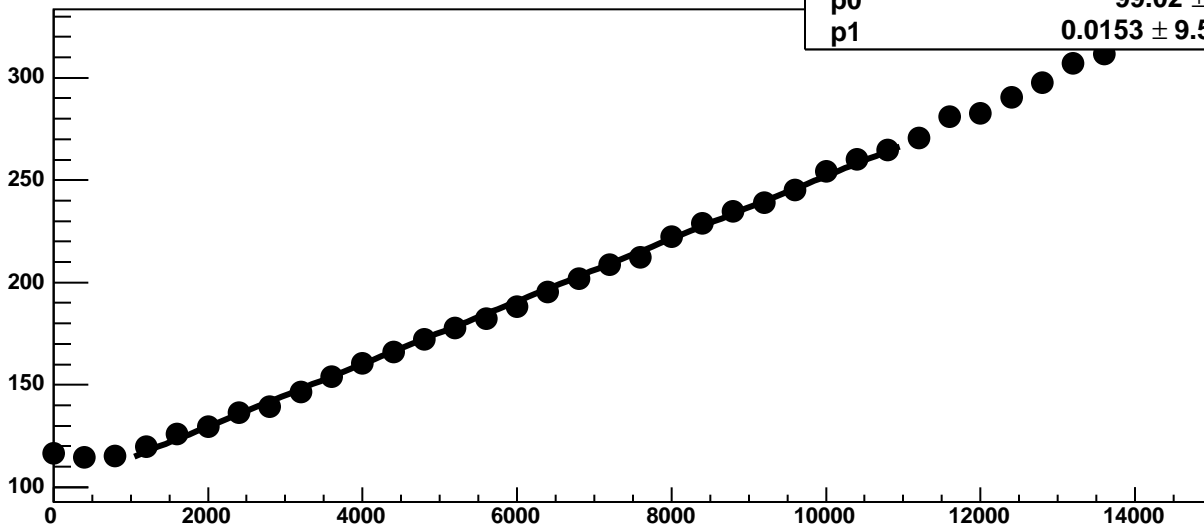
Chip 8, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC

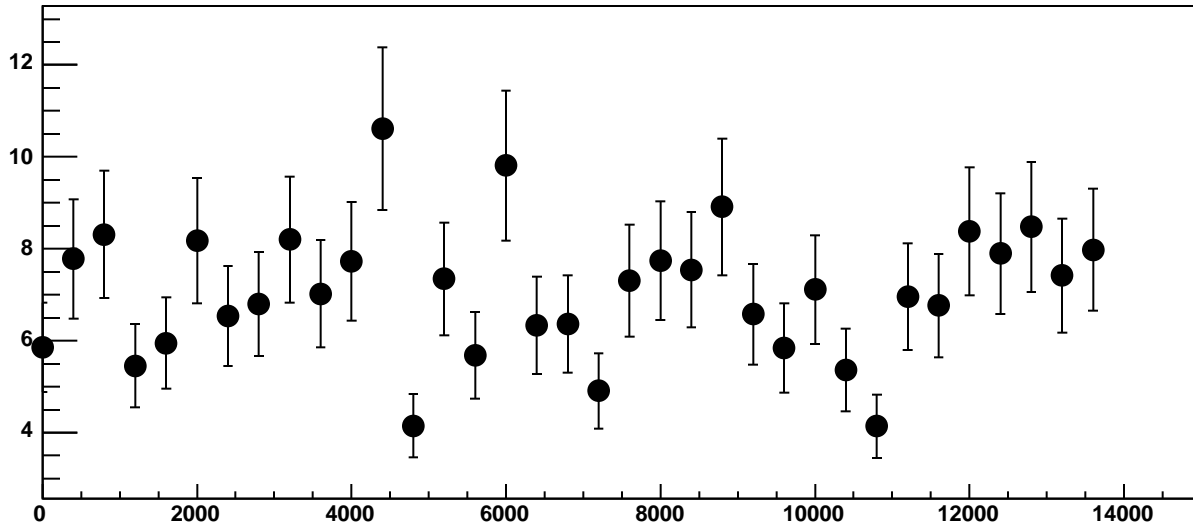


Chip 8, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

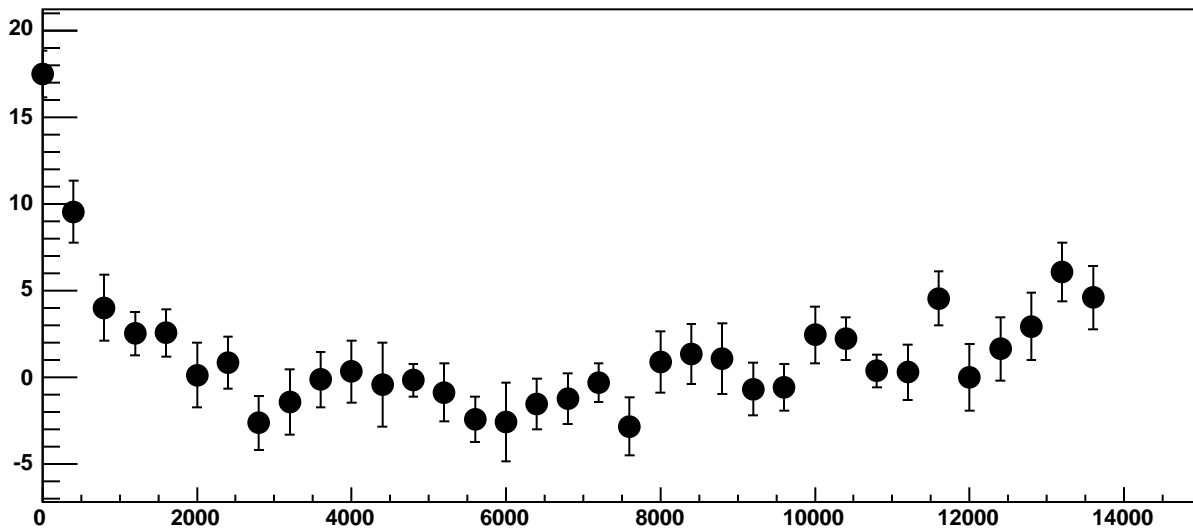


$\chi^2 / \text{ndf}$  28.42 / 23  
p0 99.02 ± 0.6656  
p1 0.0153 ± 9.525e-05

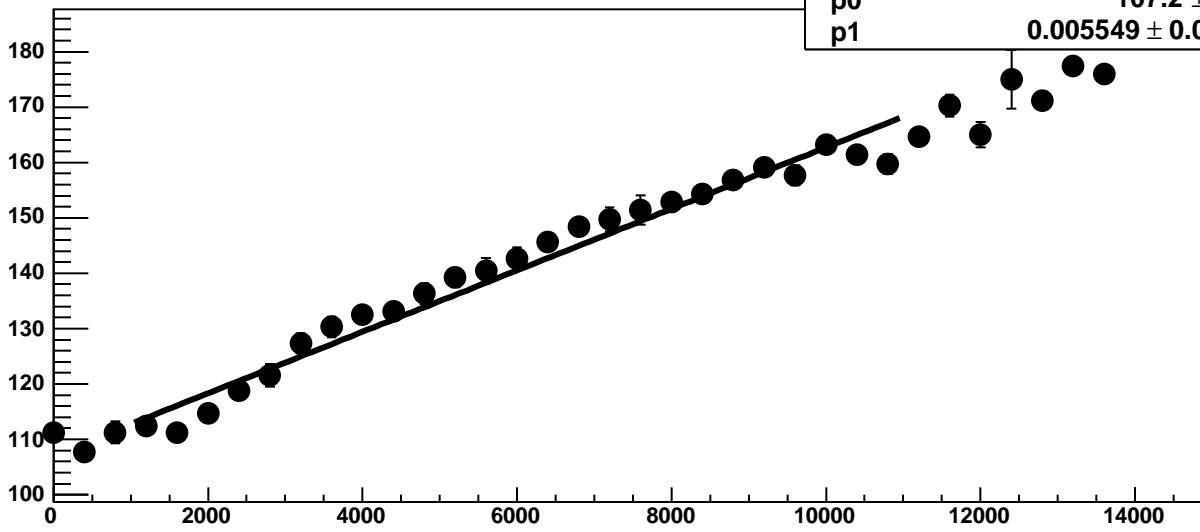
Chip 8, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



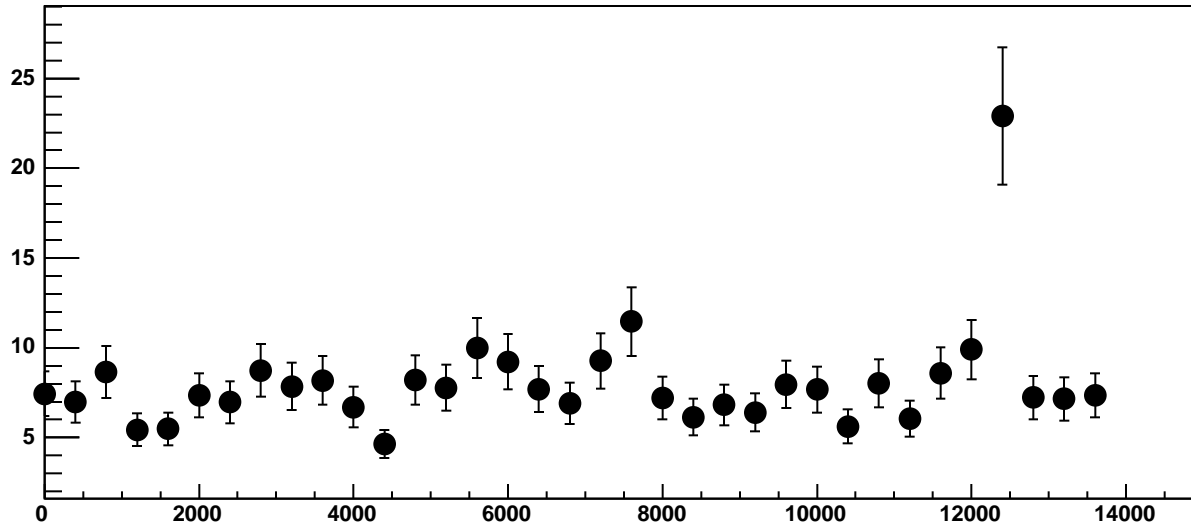
Chip 8, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



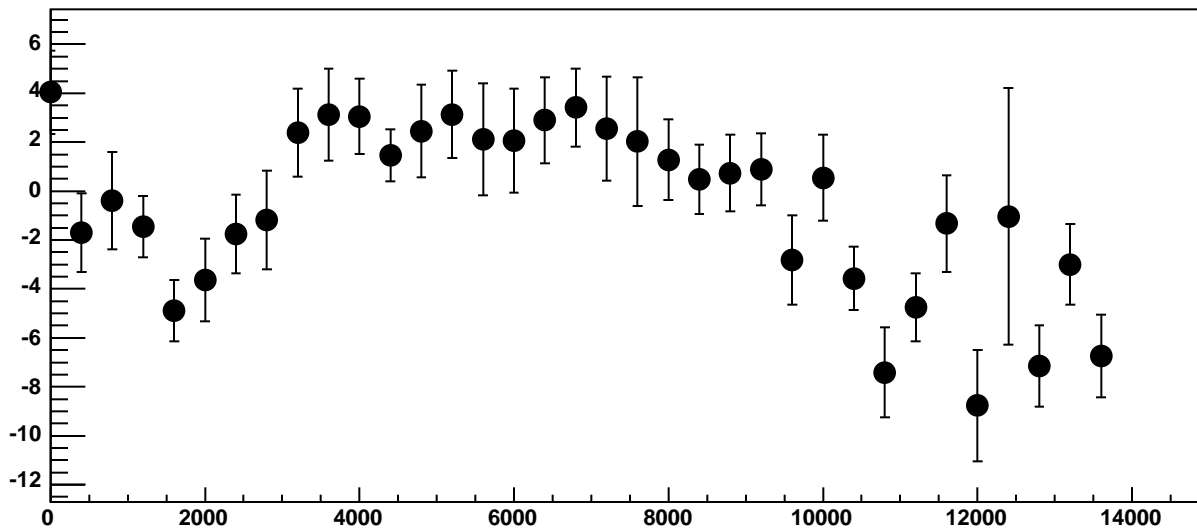
Chip 8, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 8, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



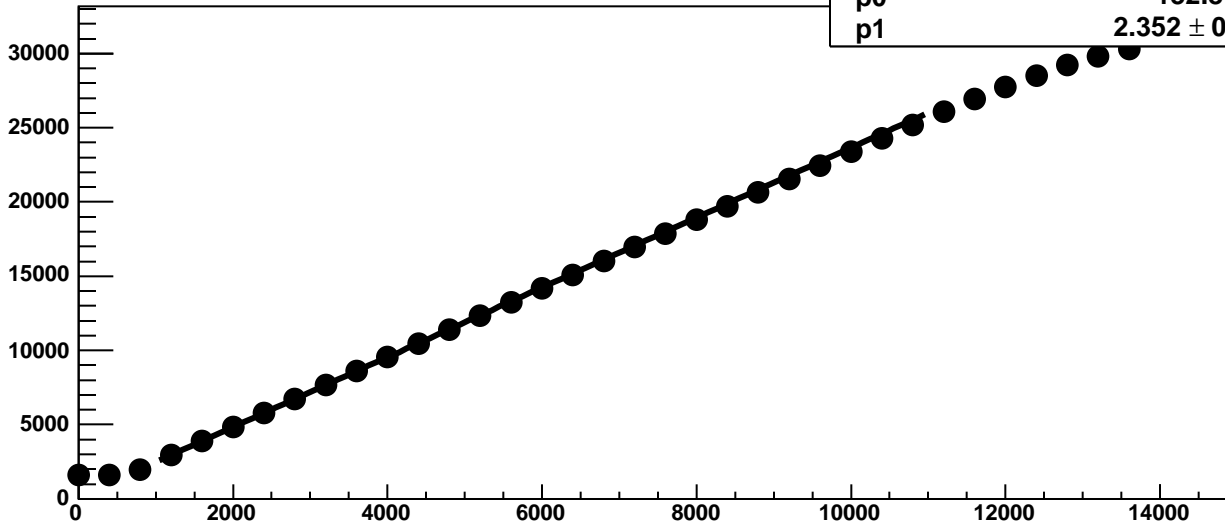
Chip 8, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



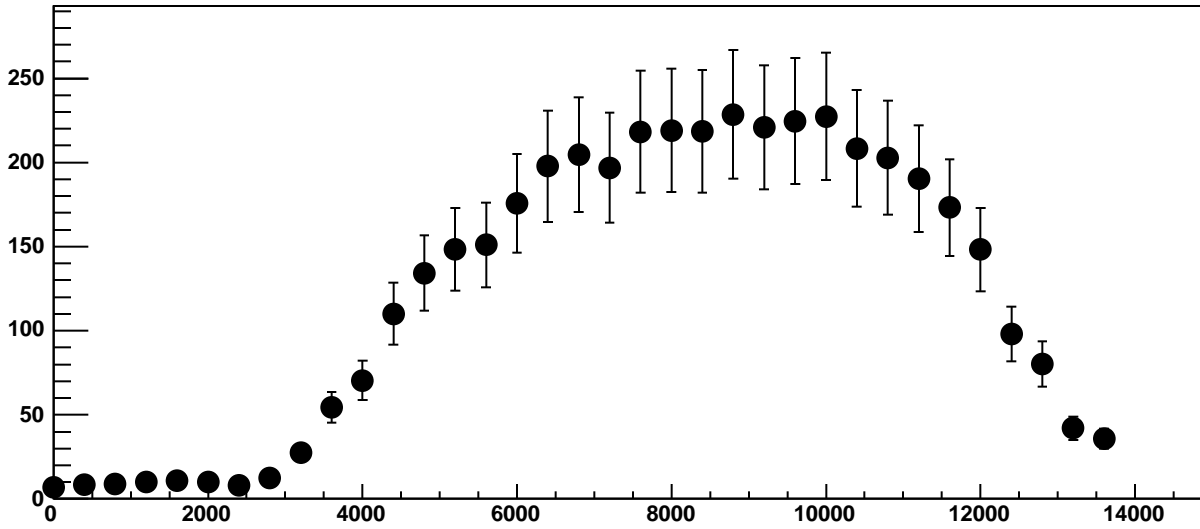


Chip 8, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC

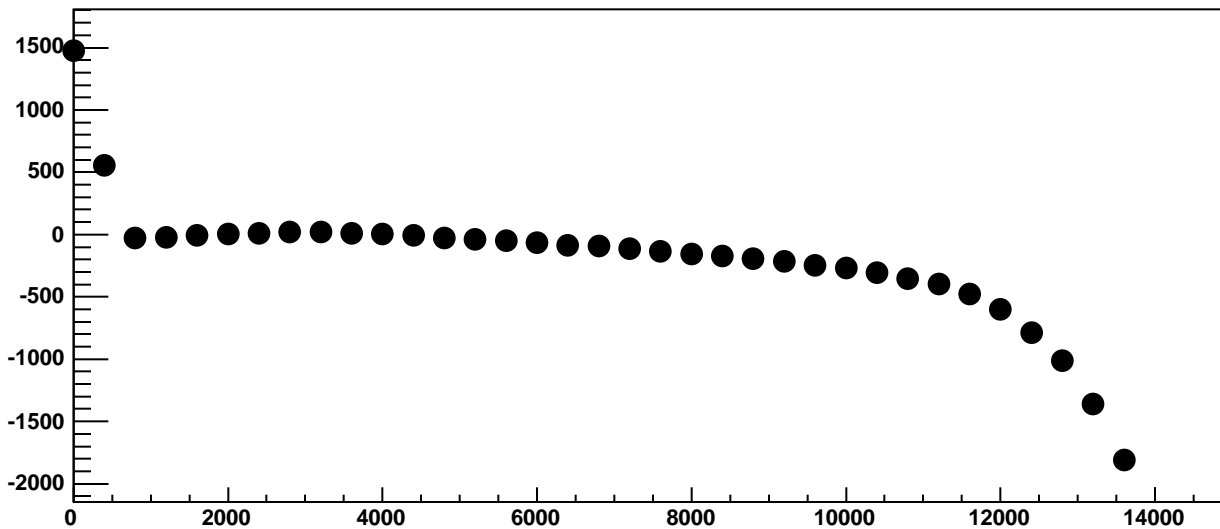
$\chi^2 / \text{ndf}$  410.7 / 23  
p0  $132.8 \pm 2.877$   
p1  $2.352 \pm 0.001291$



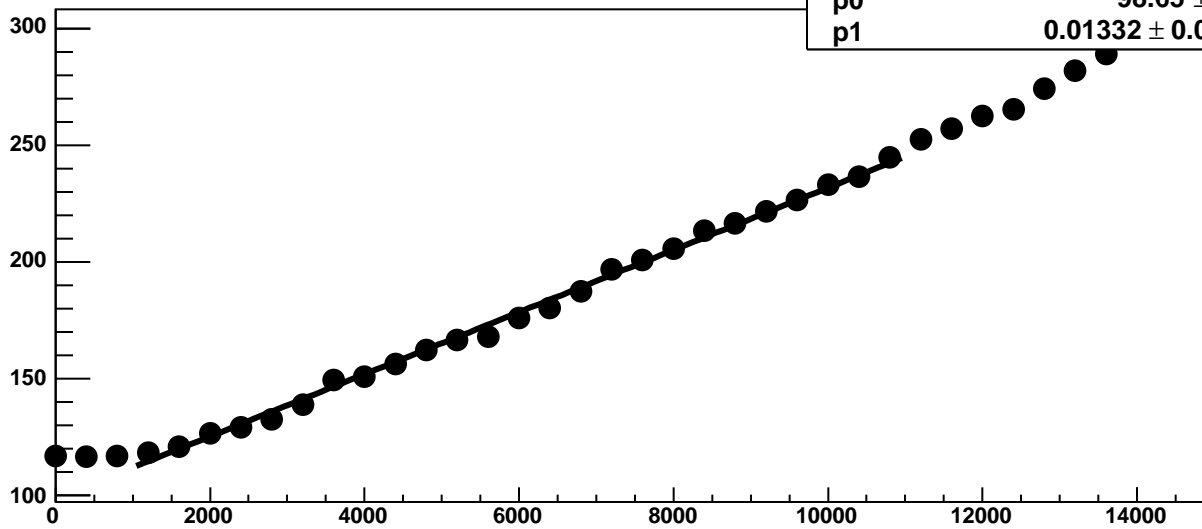
Chip 8, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

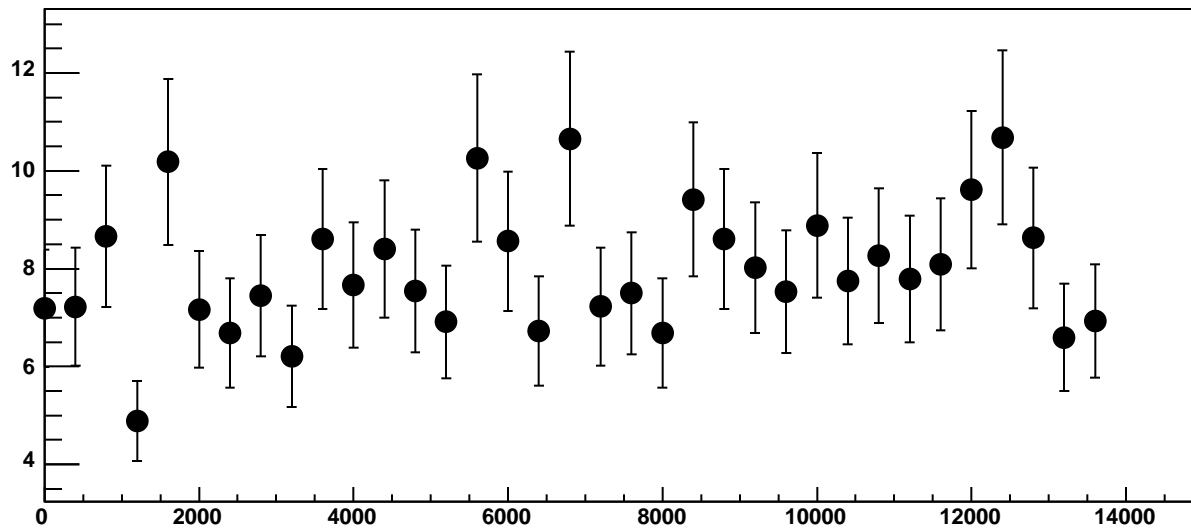


Chip 8, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

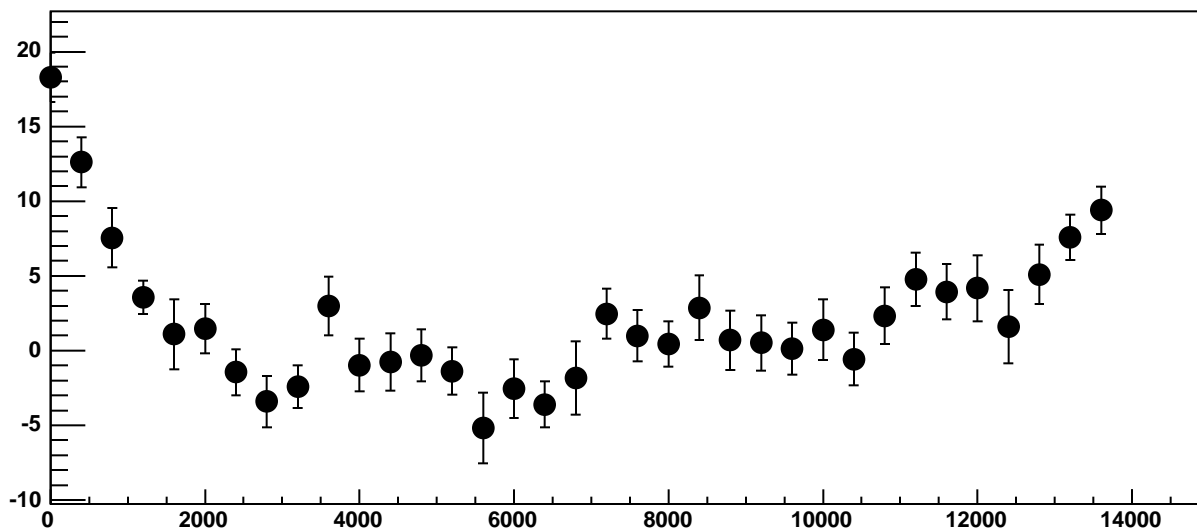


$\chi^2 / \text{ndf}$  41.74 / 23  
p0 98.65 ± 0.7377  
p1 0.01332 ± 0.0001164

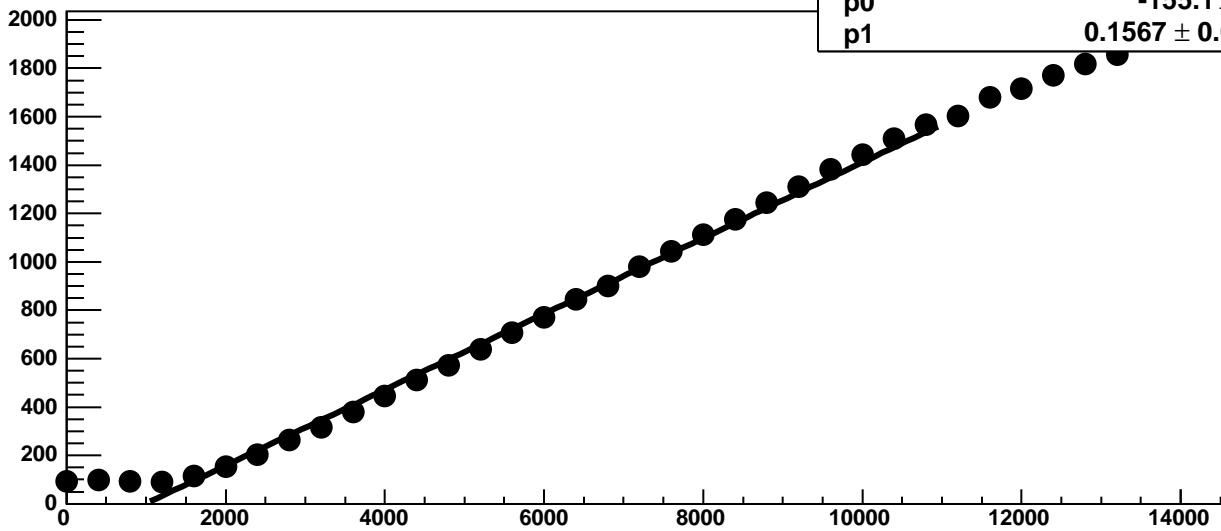
Chip 8, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



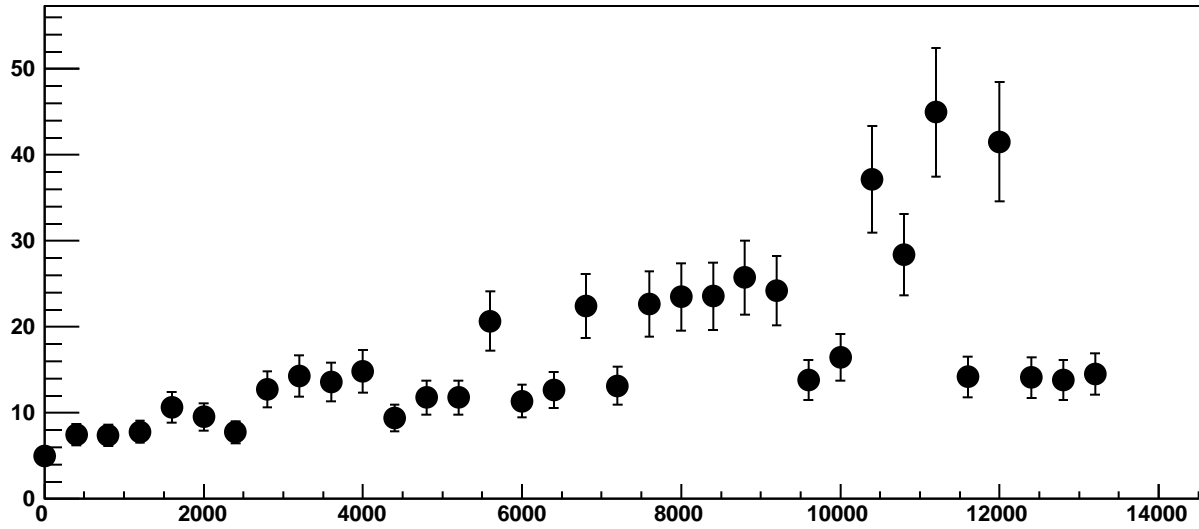
Chip 8, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



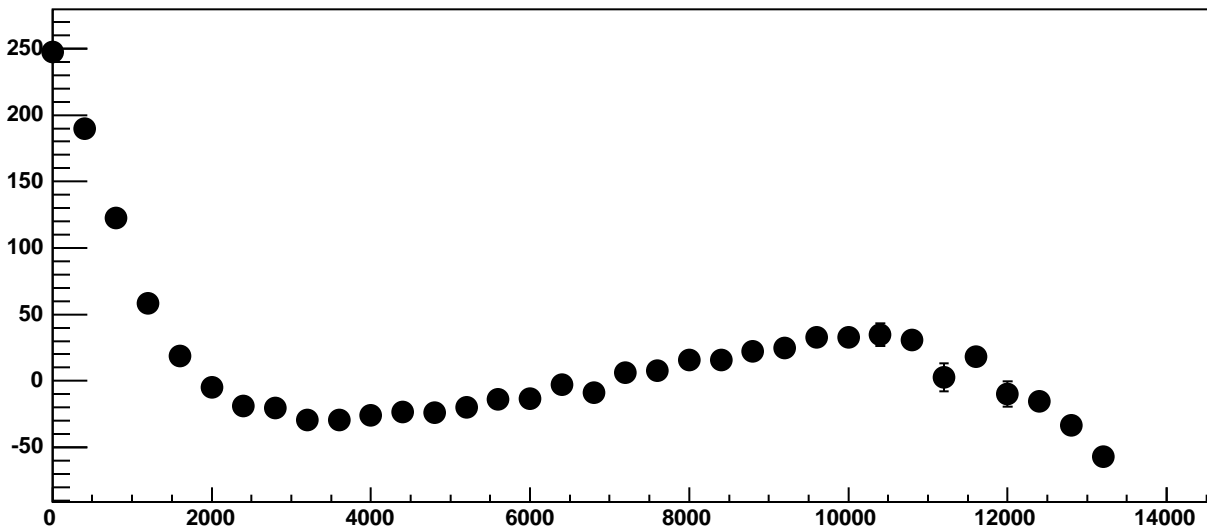
Chip 8, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

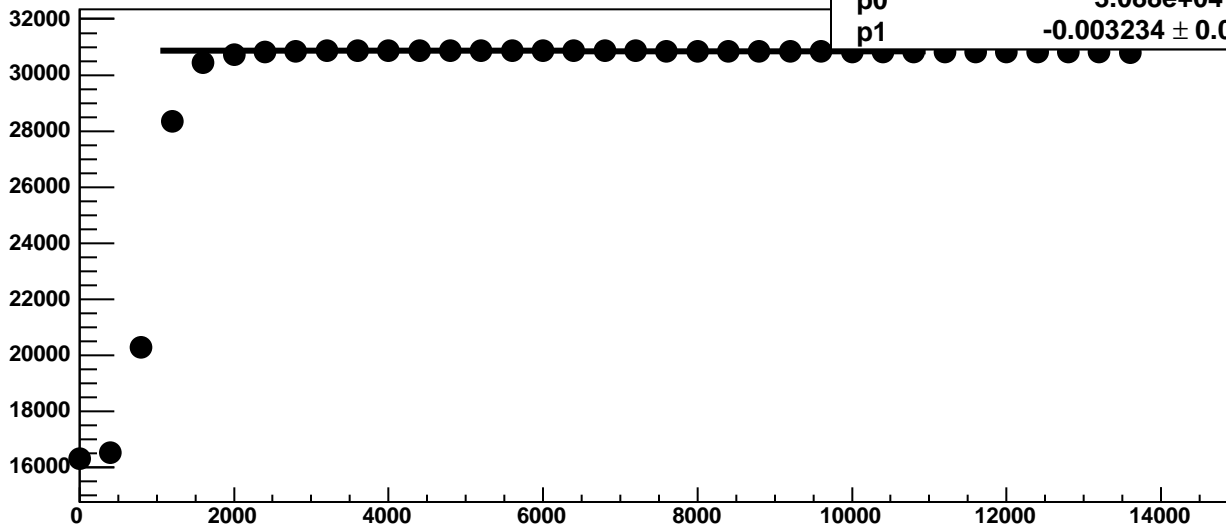
1361 / 23

p0

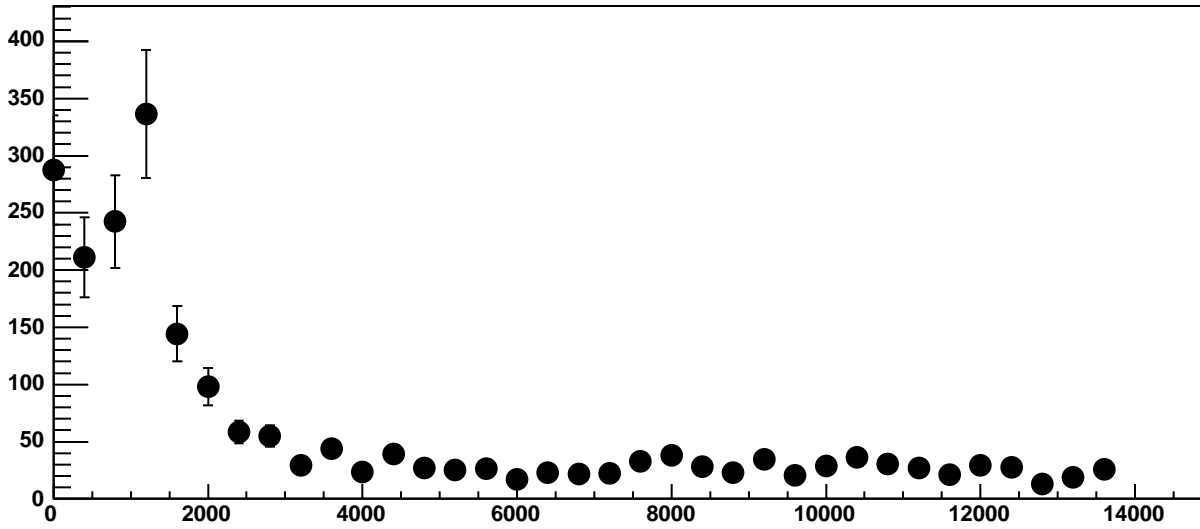
$3.088\text{e}+04 \pm 4.383$

p1

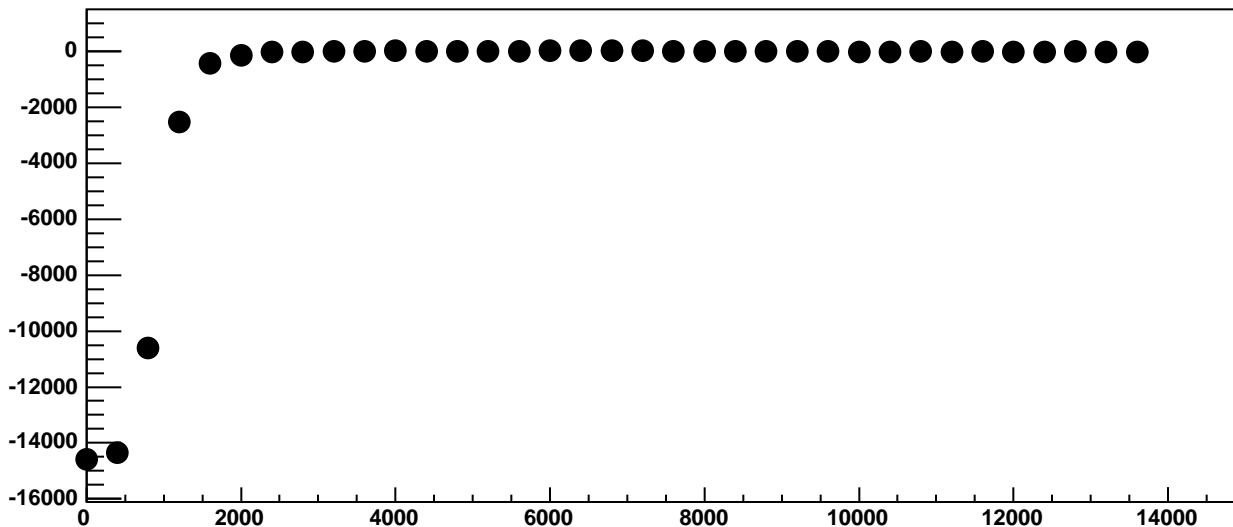
$-0.003234 \pm 0.0006093$



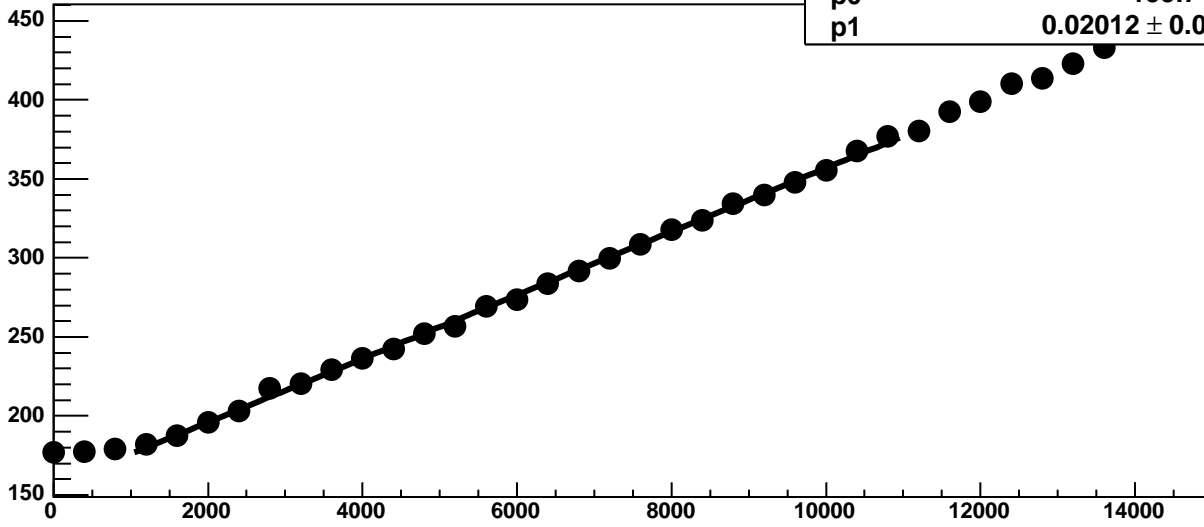
Chip 8, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

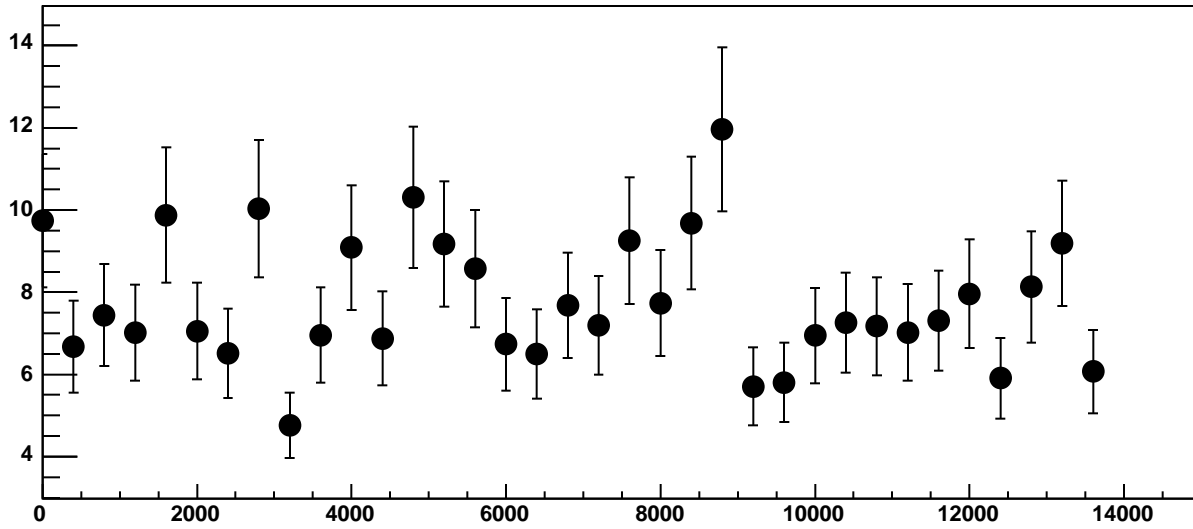


Chip 8, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

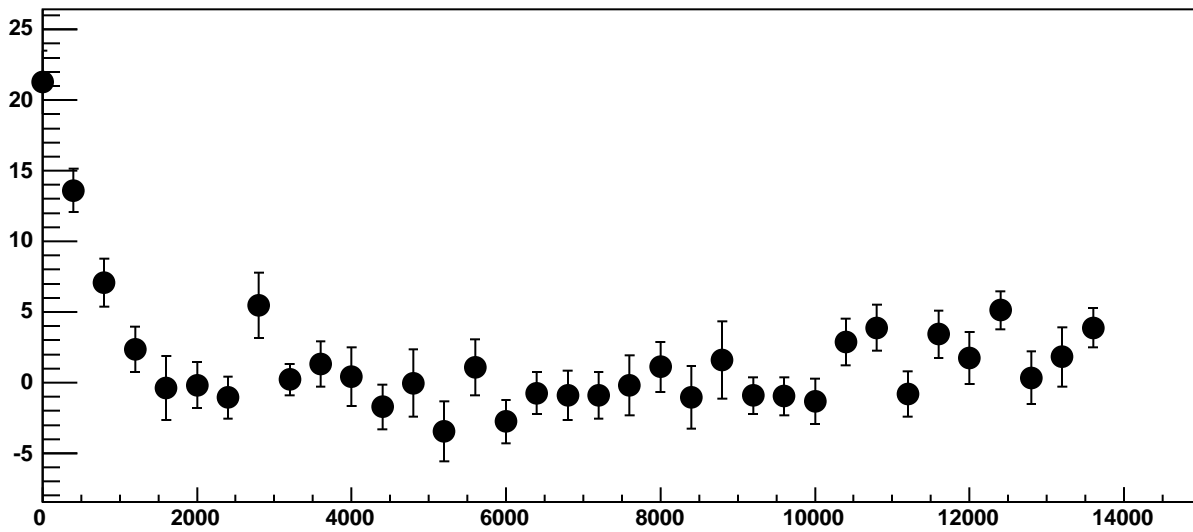


$\chi^2 / \text{ndf}$  28.51 / 23  
p0 155.7 ± 0.762  
p1 0.02012 ± 0.0001135

Chip 8, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

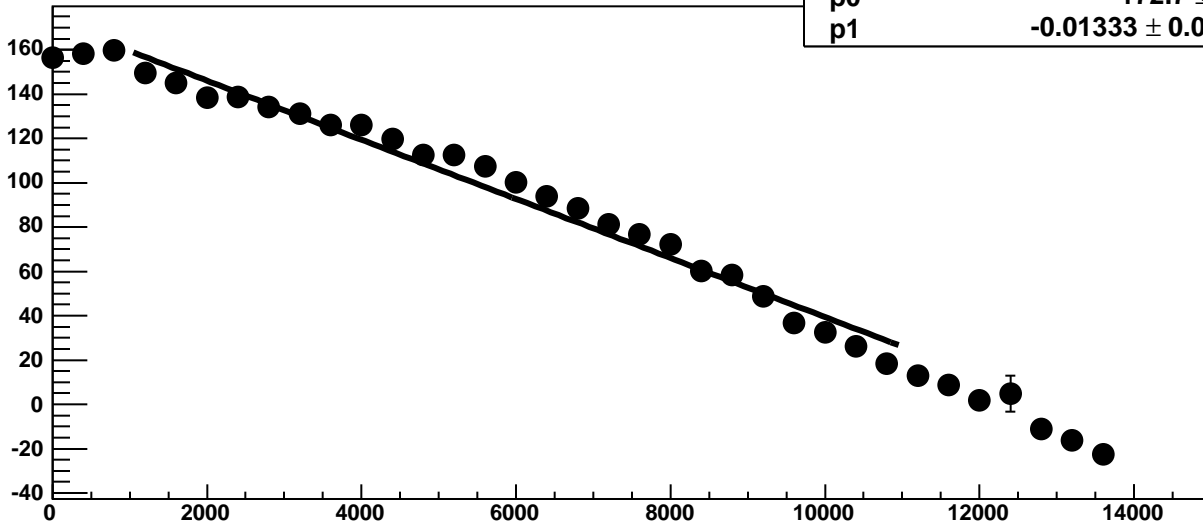


Chip 8, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

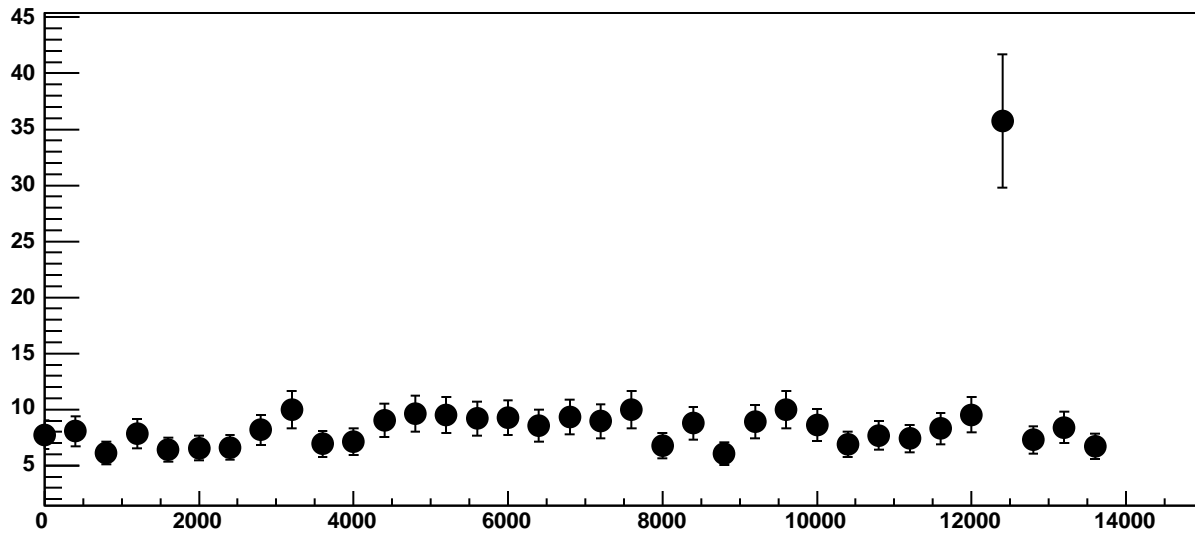


Chip 8, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

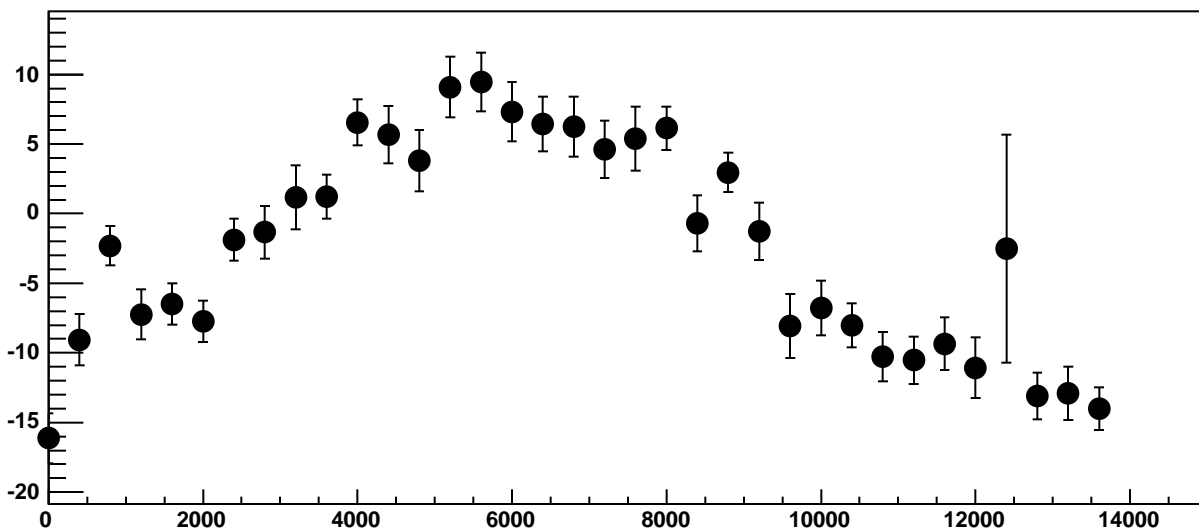
$\chi^2 / \text{ndf}$  275.3 / 23  
p0  $172.7 \pm 0.7851$   
p1  $-0.01333 \pm 0.0001194$



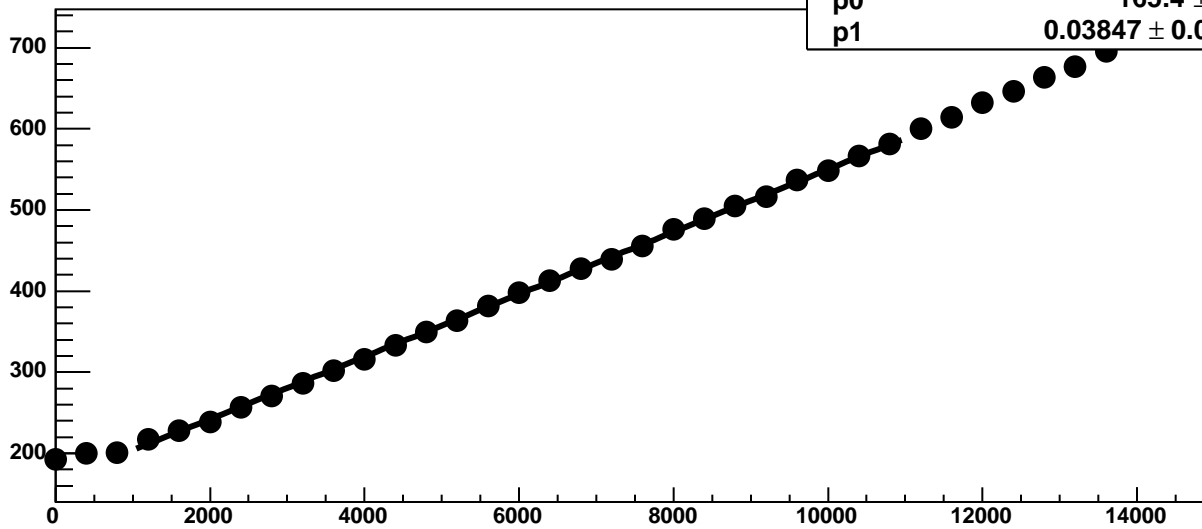
Chip 8, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

37.79 / 23

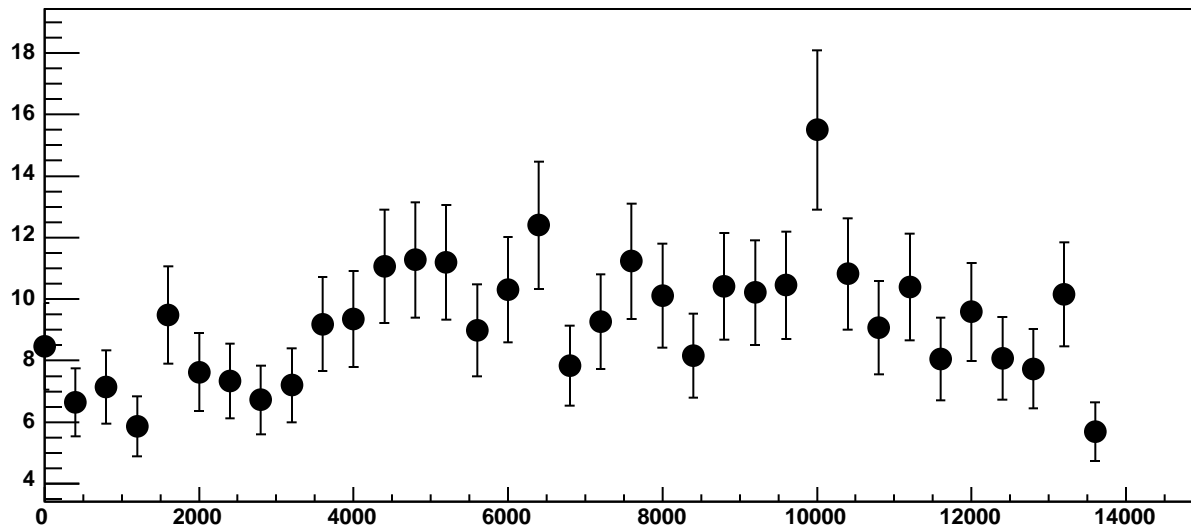
p0

$165.4 \pm 0.8373$

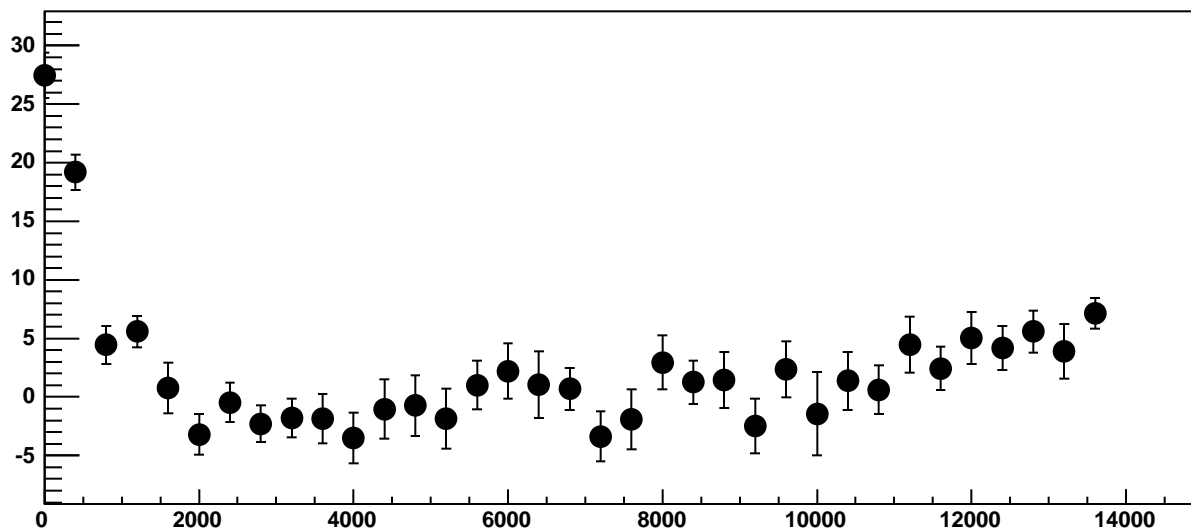
p1

$0.03847 \pm 0.0001389$

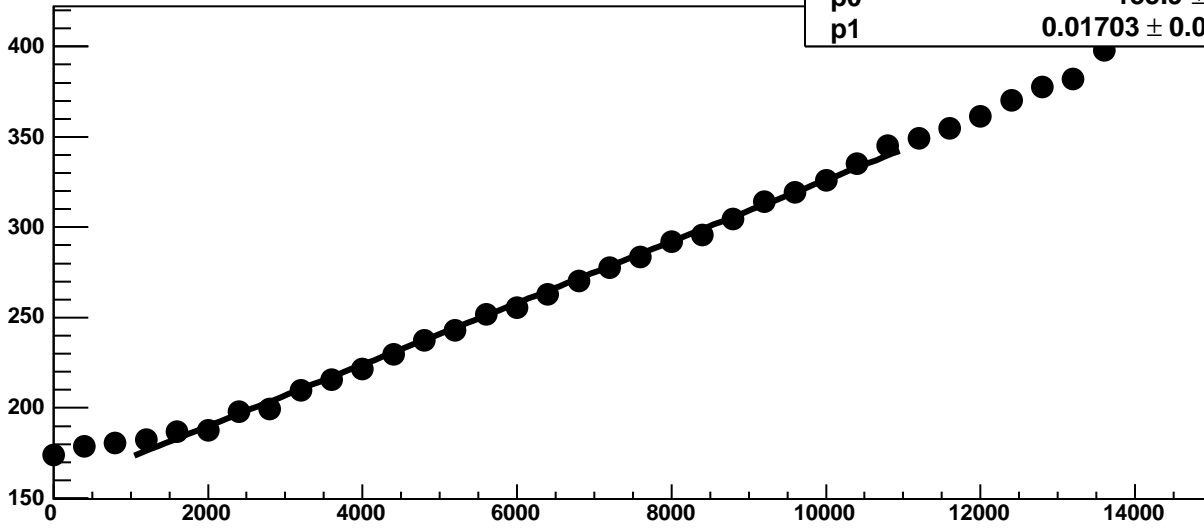
Chip 8, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

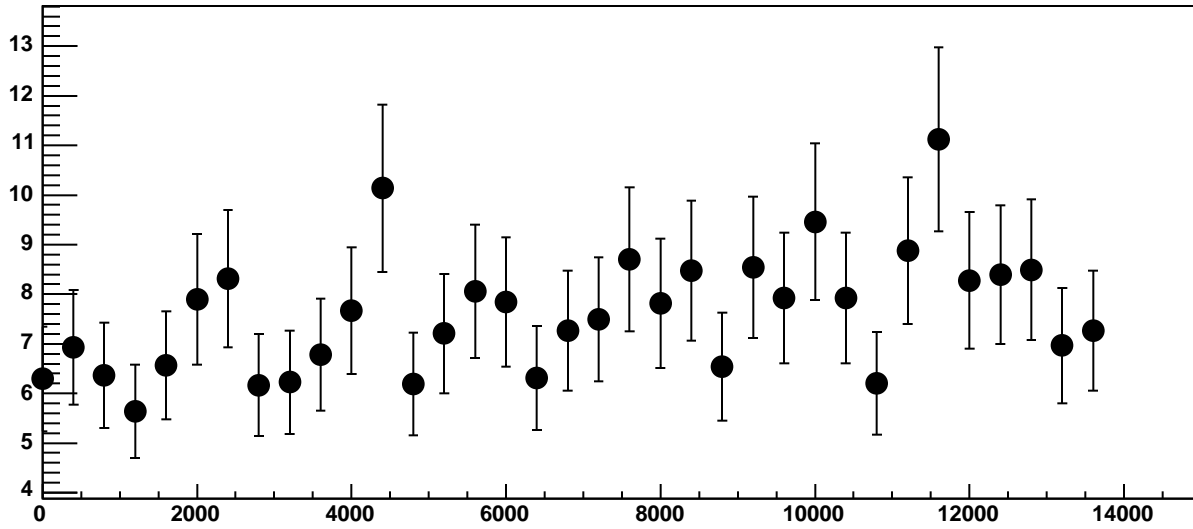


Chip 8, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

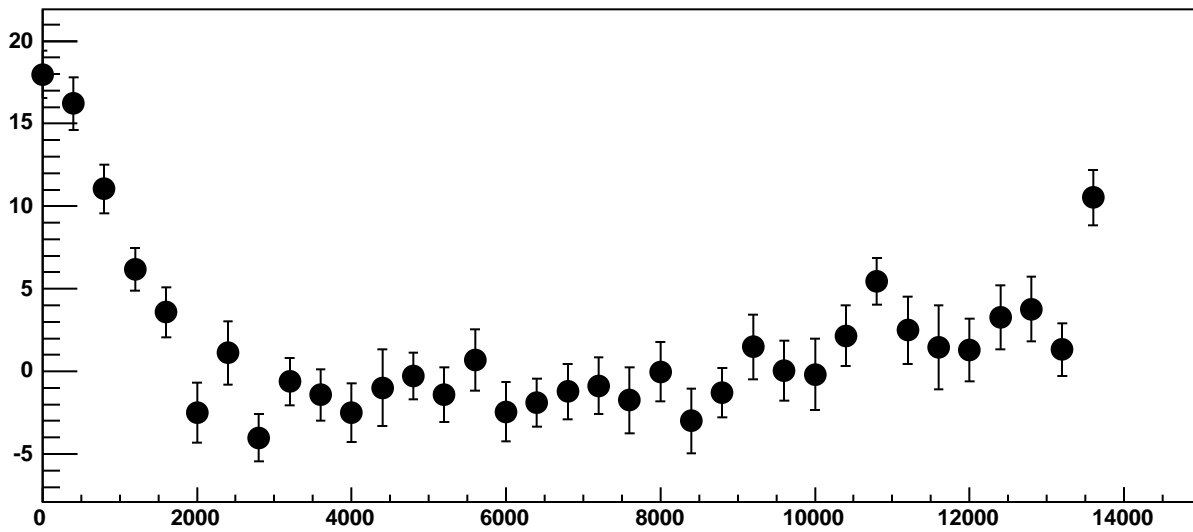


$\chi^2 / \text{ndf}$  67.73 / 23  
p0  $155.9 \pm 0.7233$   
p1  $0.01703 \pm 0.0001125$

Chip 8, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



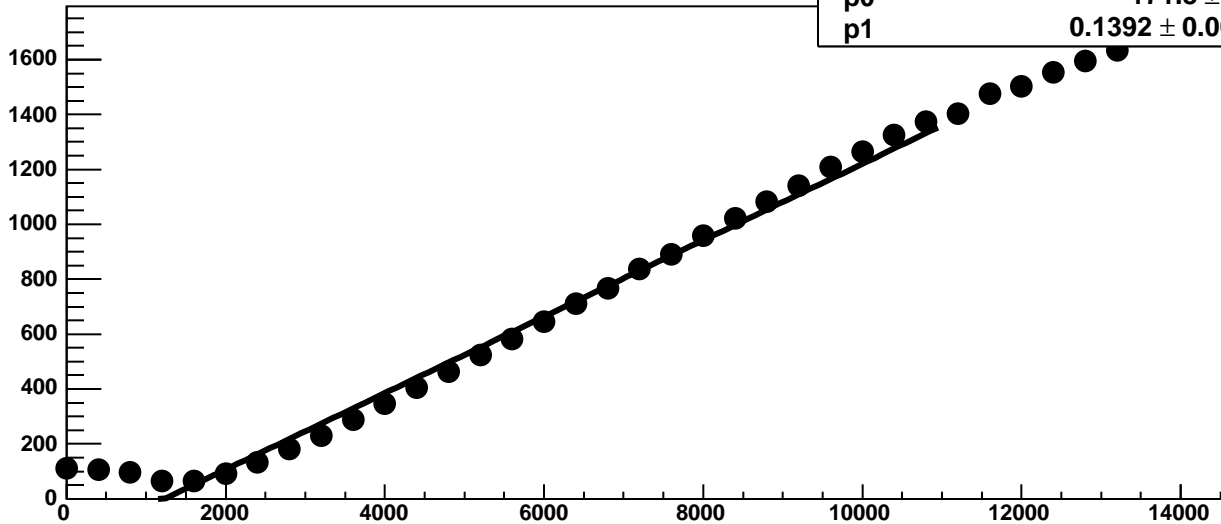
Chip 8, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



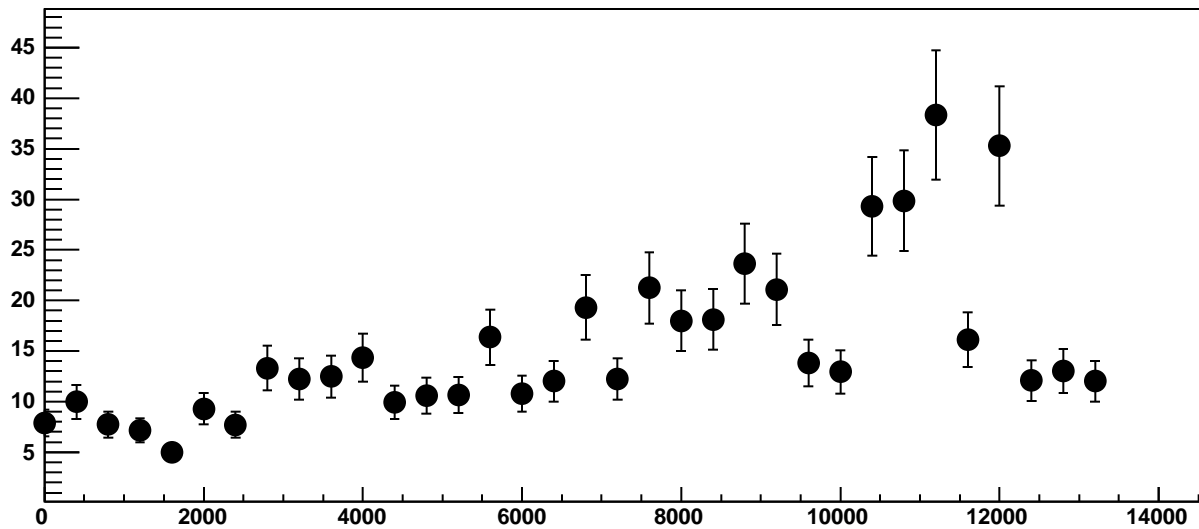


Chip 8, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

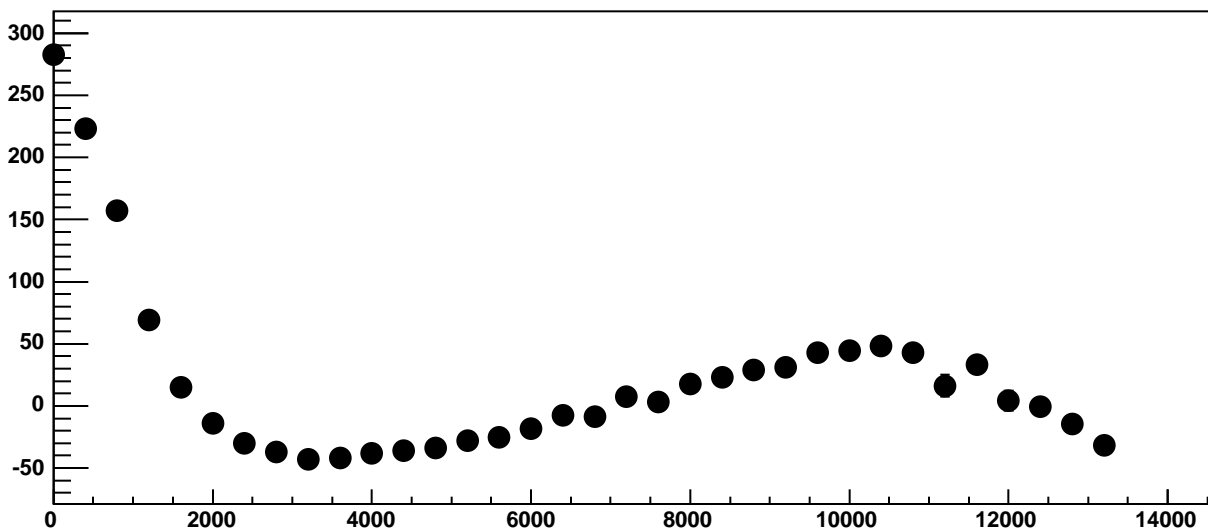
$\chi^2 / \text{ndf}$  4328 / 23  
p0  $-171.8 \pm 0.9399$   
p1  $0.1392 \pm 0.0001938$



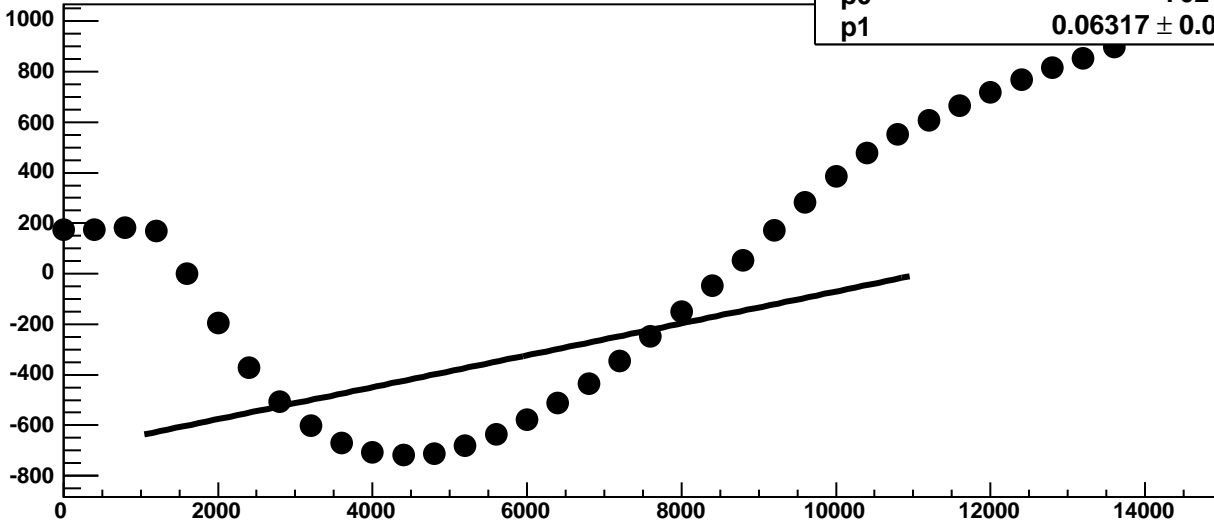
Chip 8, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

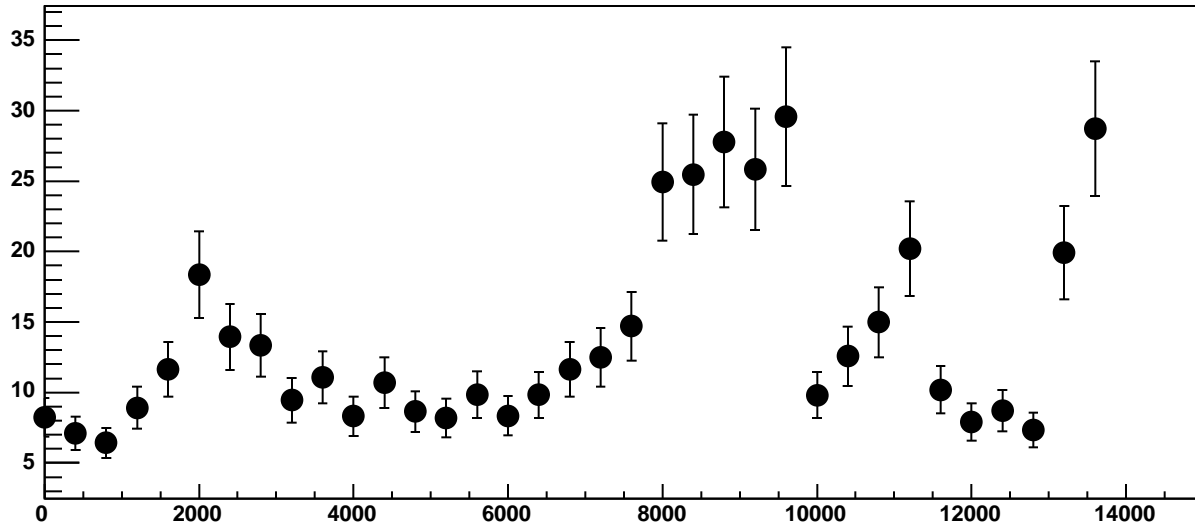


Chip 8, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

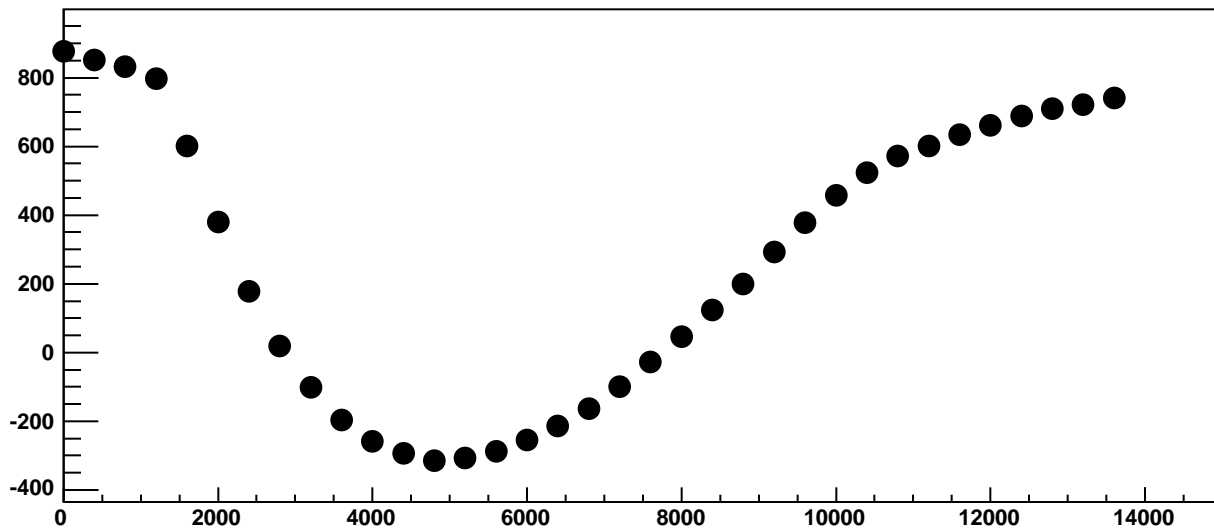


$\chi^2 / \text{ndf}$  4.639e+05 / 23  
p0 -702 ± 1.216  
p1 0.06317 ± 0.0002067

Chip 8, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

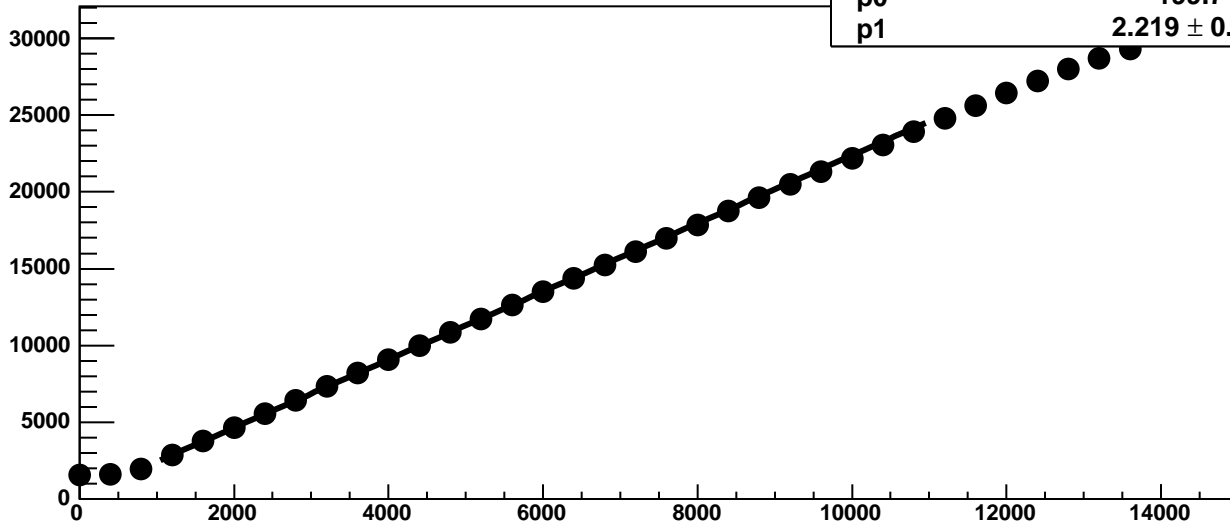


Chip 8, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

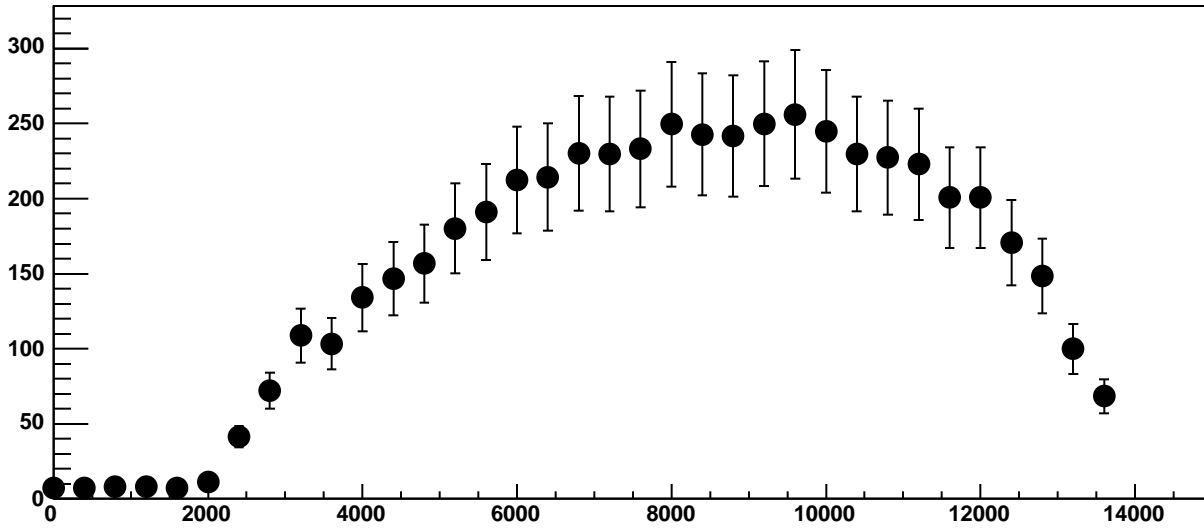


Chip 8, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

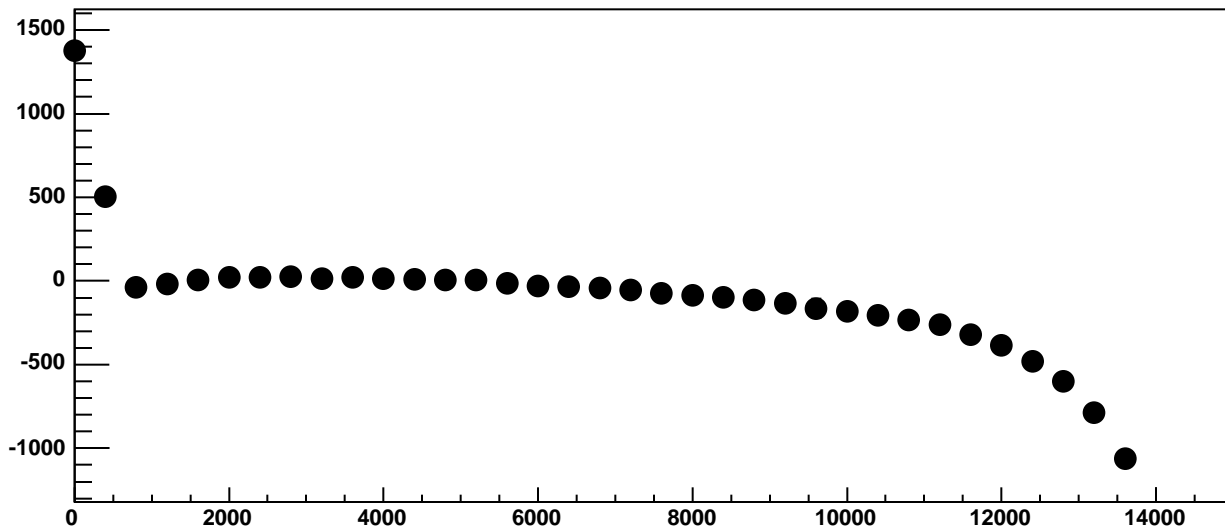
$\chi^2 / \text{ndf}$  245.9 / 23  
p0  $199.7 \pm 2.962$   
p1  $2.219 \pm 0.001706$



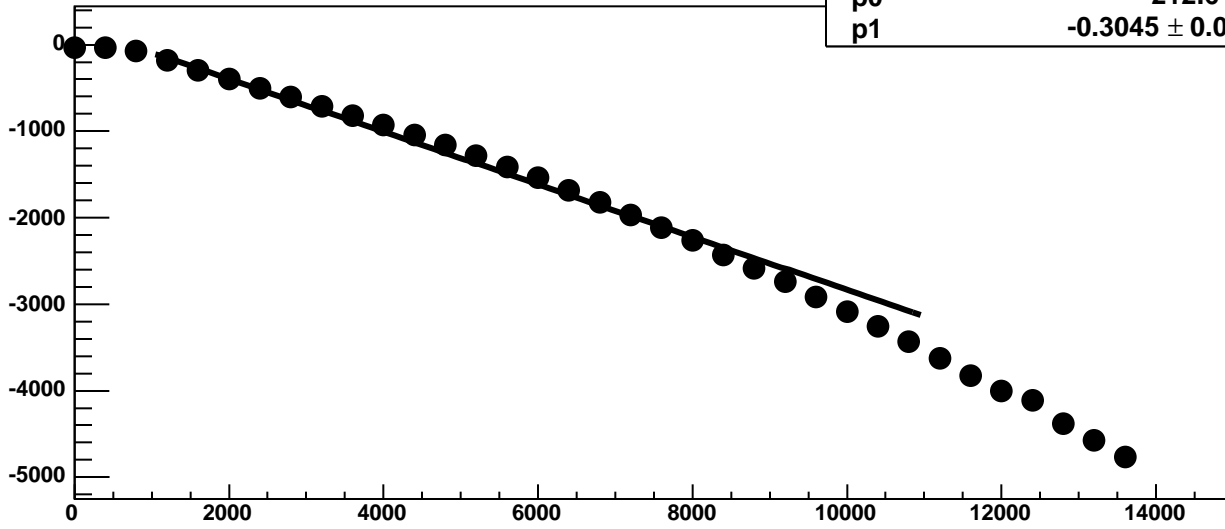
Chip 8, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4045 / 23

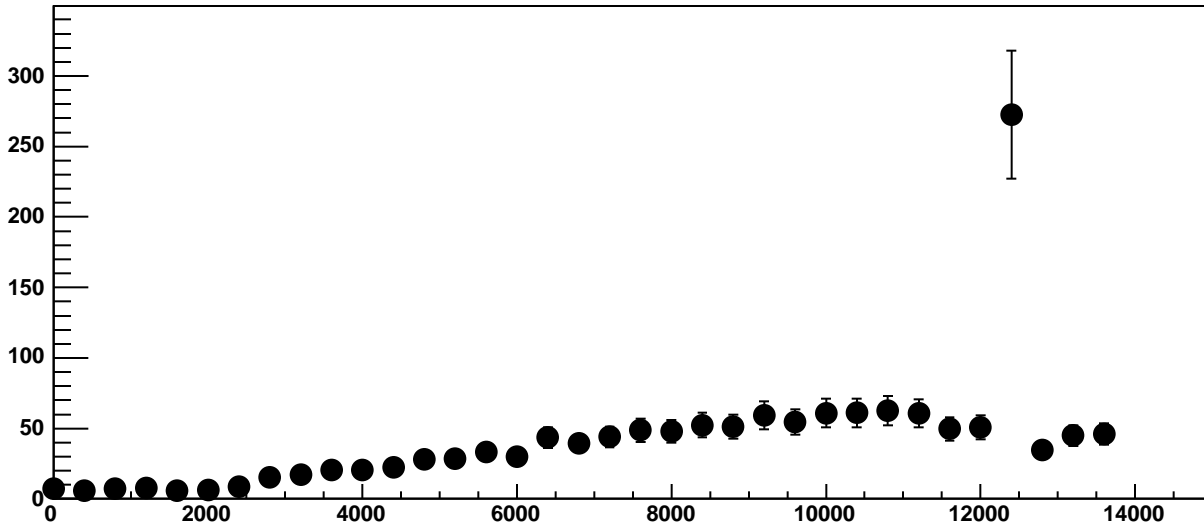
p0

$212.6 \pm 1.277$

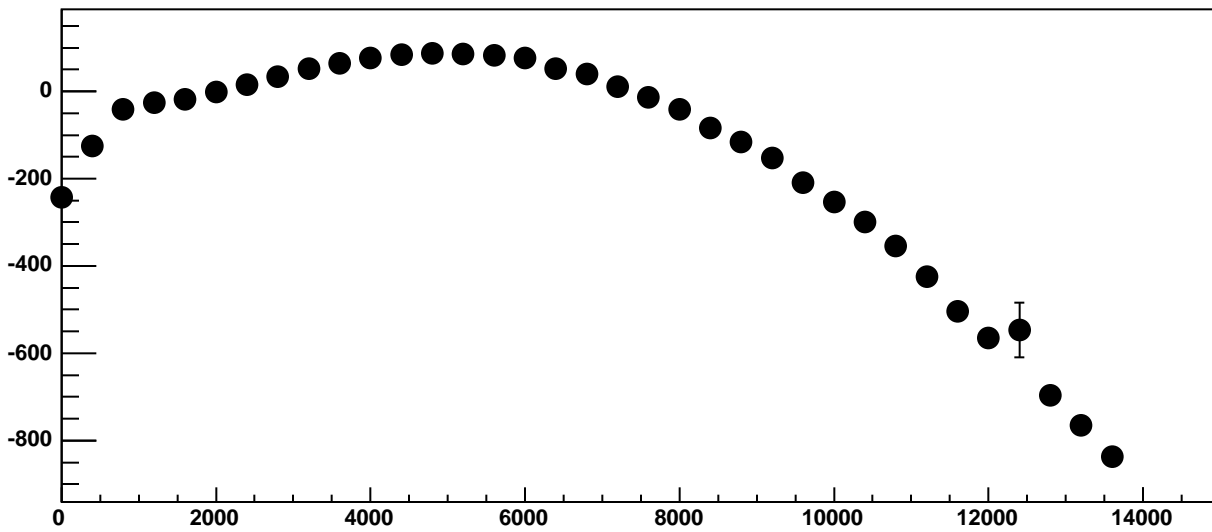
p1

$-0.3045 \pm 0.0004377$

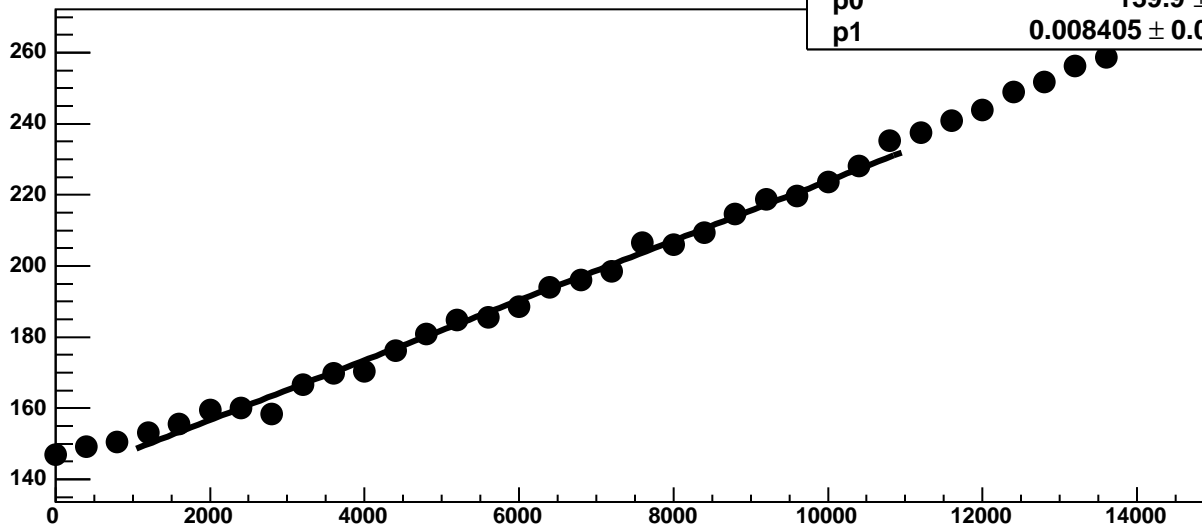
Chip 8, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

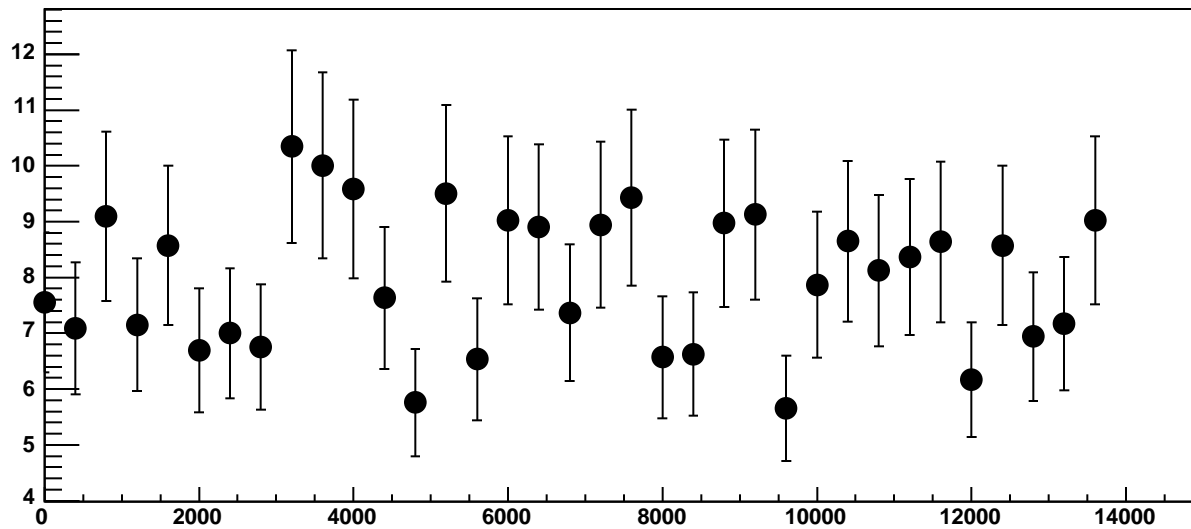


Chip 8, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

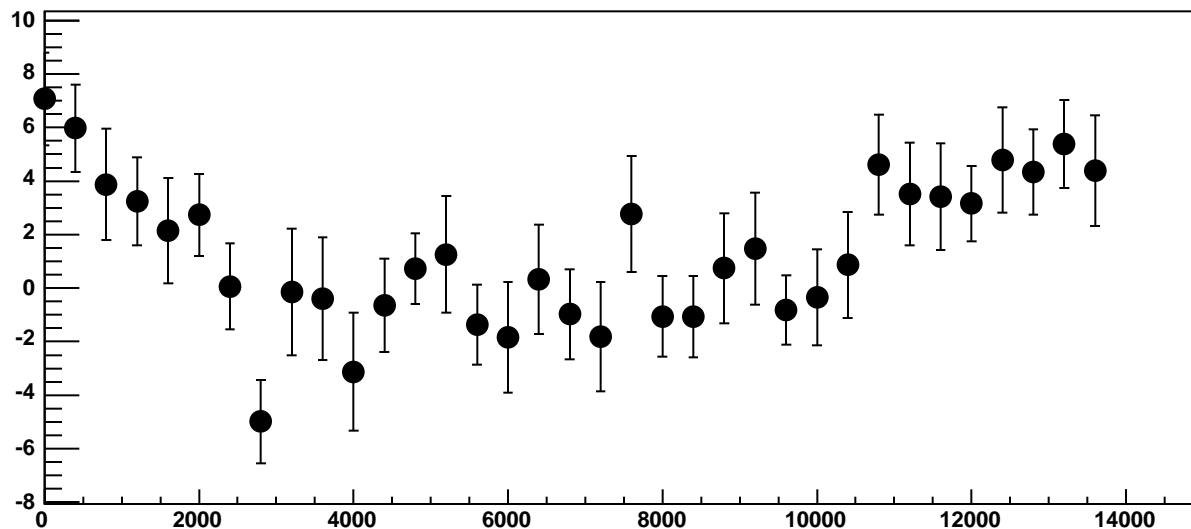


$\chi^2 / \text{ndf}$  34.26 / 23  
p0  $139.9 \pm 0.8047$   
p1  $0.008405 \pm 0.0001208$

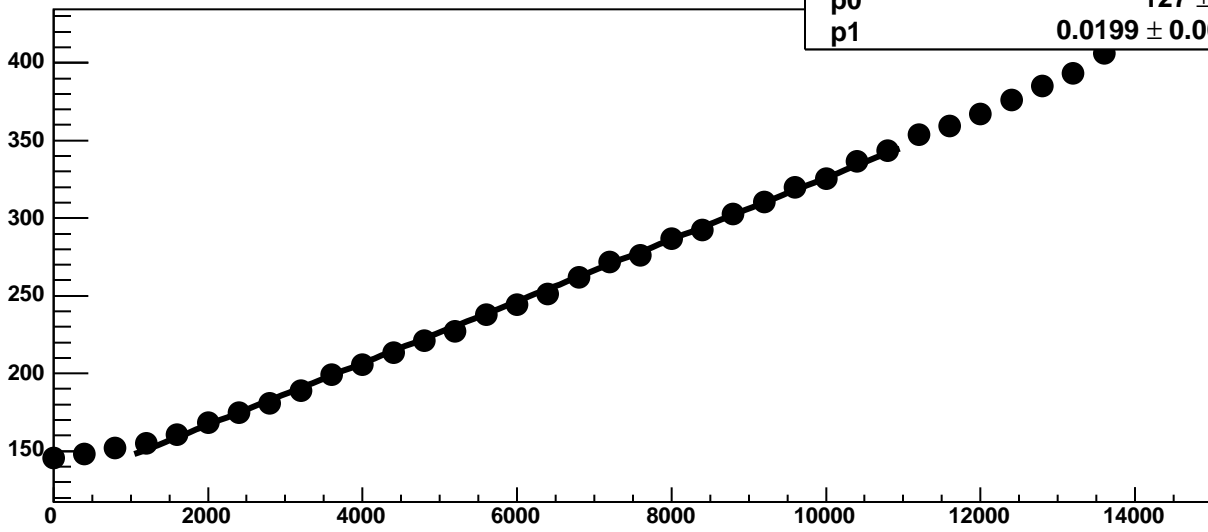
Chip 8, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



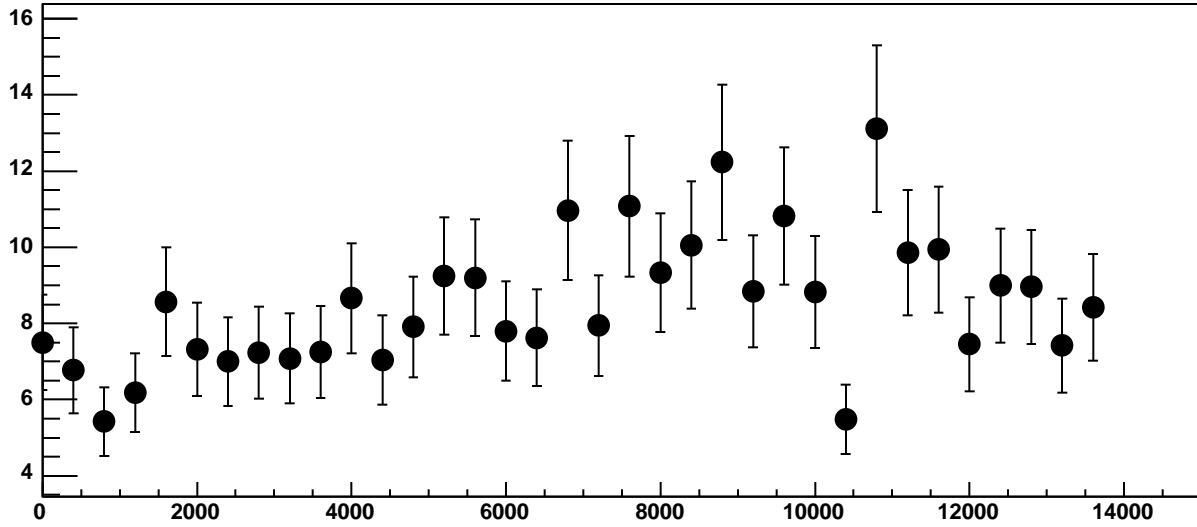
Chip 8, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



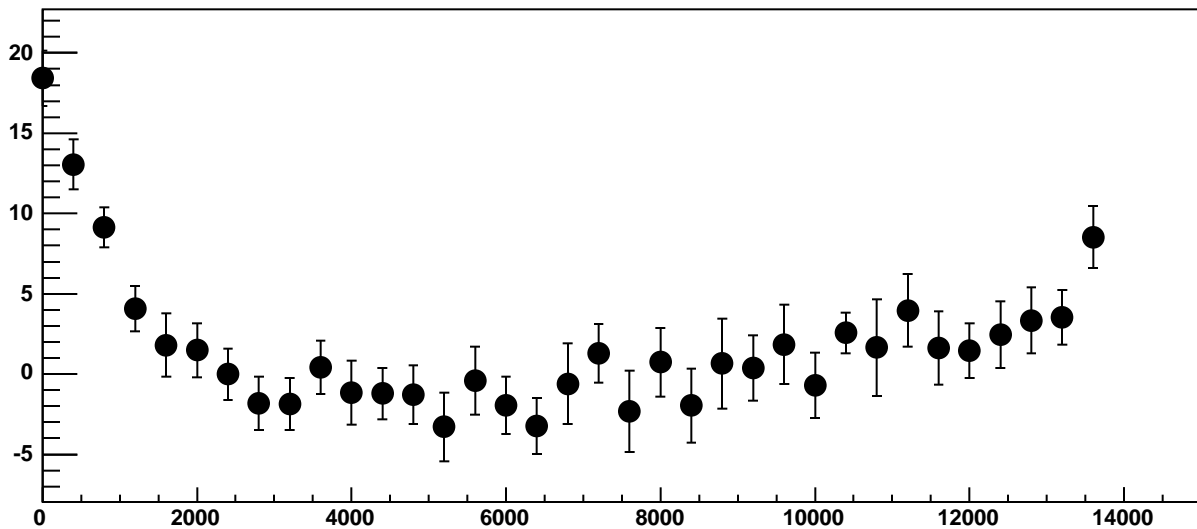
Chip 8, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



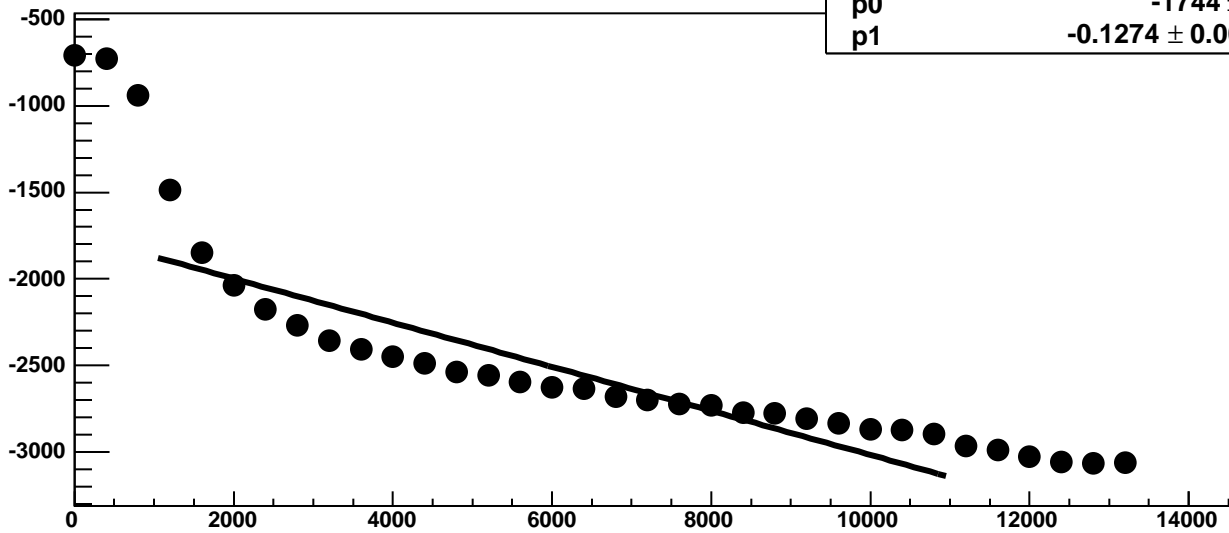
Chip 8, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

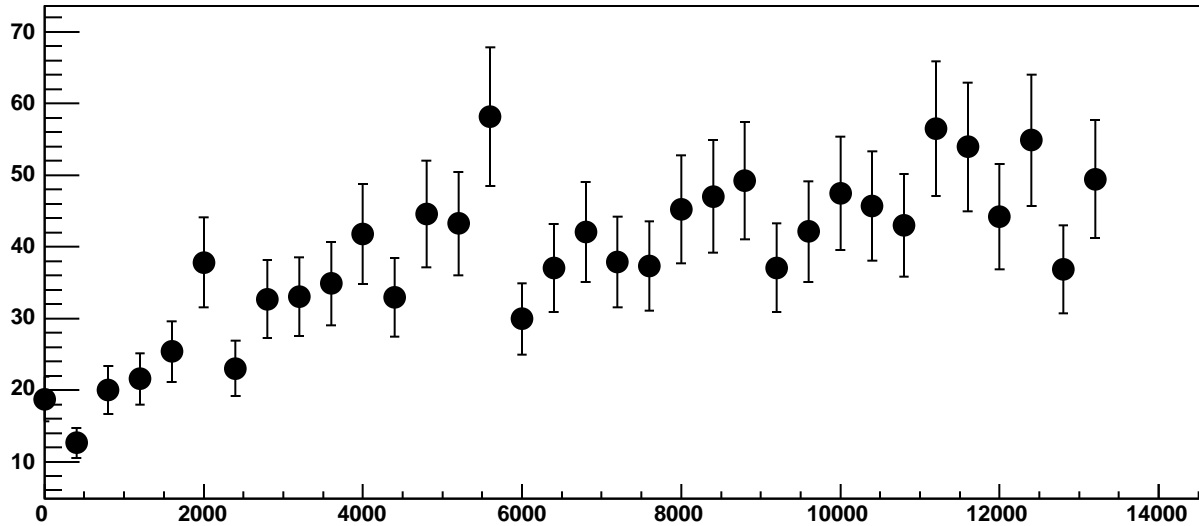


Chip 8, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

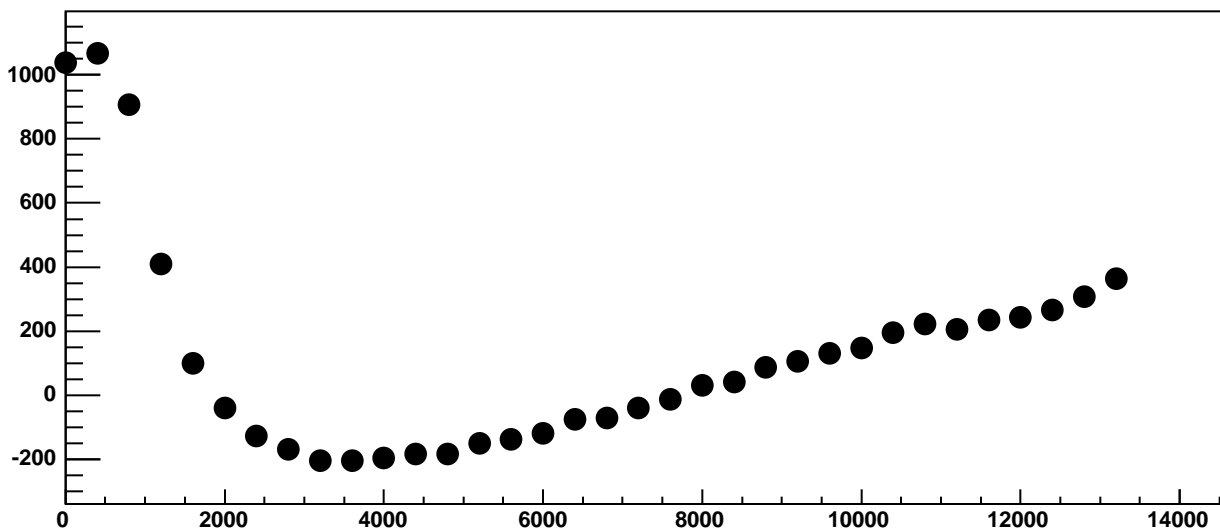


$\chi^2 / \text{ndf}$  1.323e+04 / 23  
p0 -1744 ± 3.159  
p1 -0.1274 ± 0.0005495

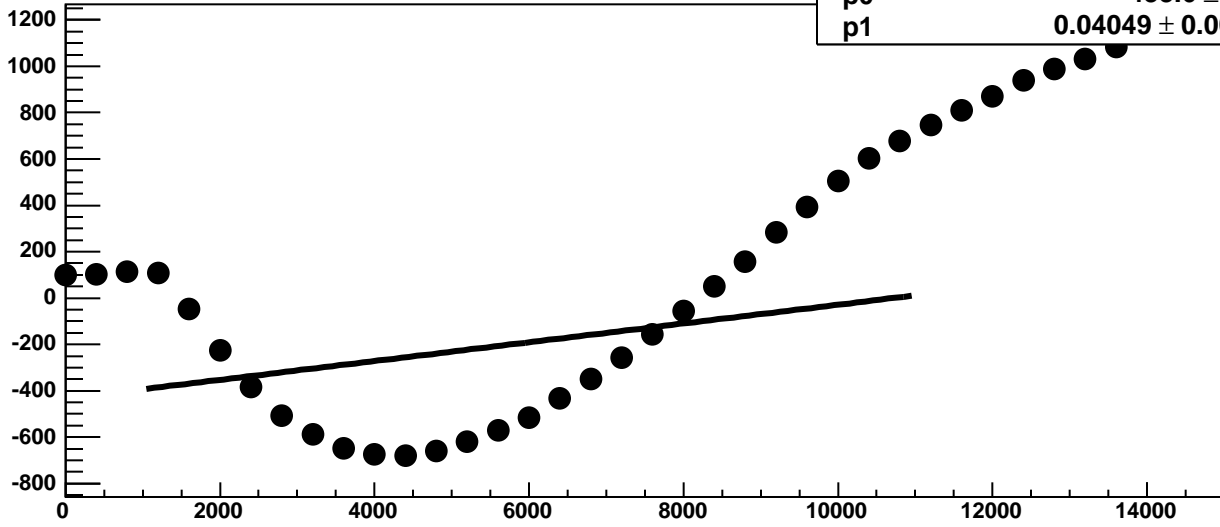
Chip 8, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

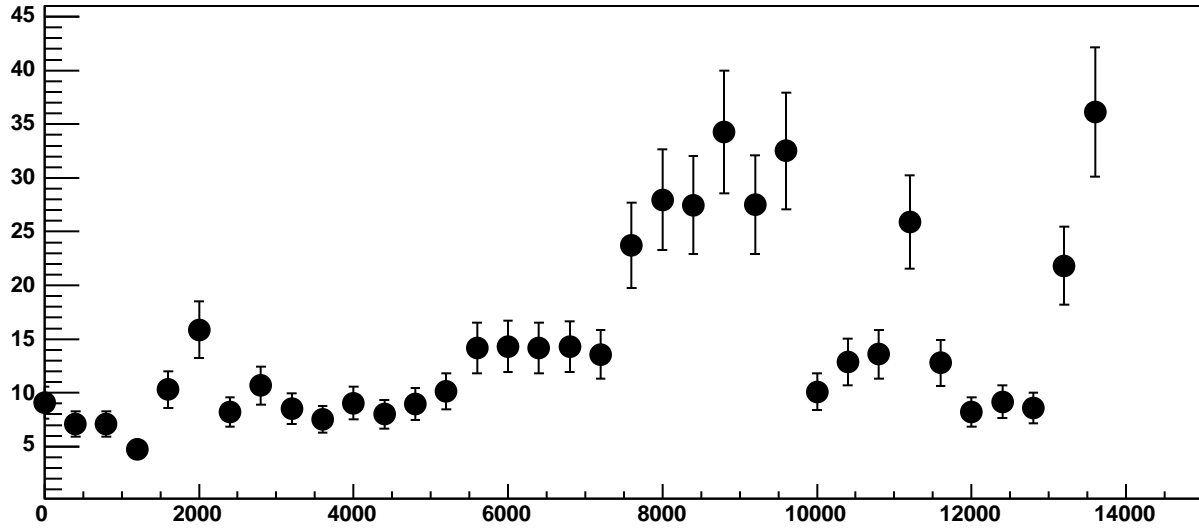


Chip 8, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

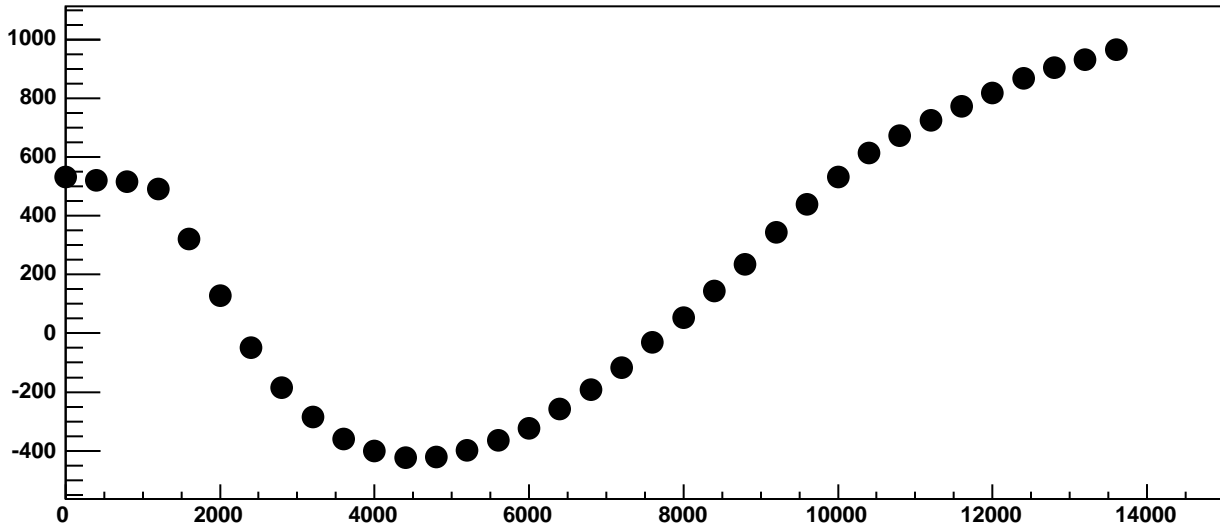


$\chi^2 / \text{ndf}$  6.427e+05 / 23  
p0 -433.6 ± 0.8999  
p1 0.04049 ± 0.0001783

Chip 8, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

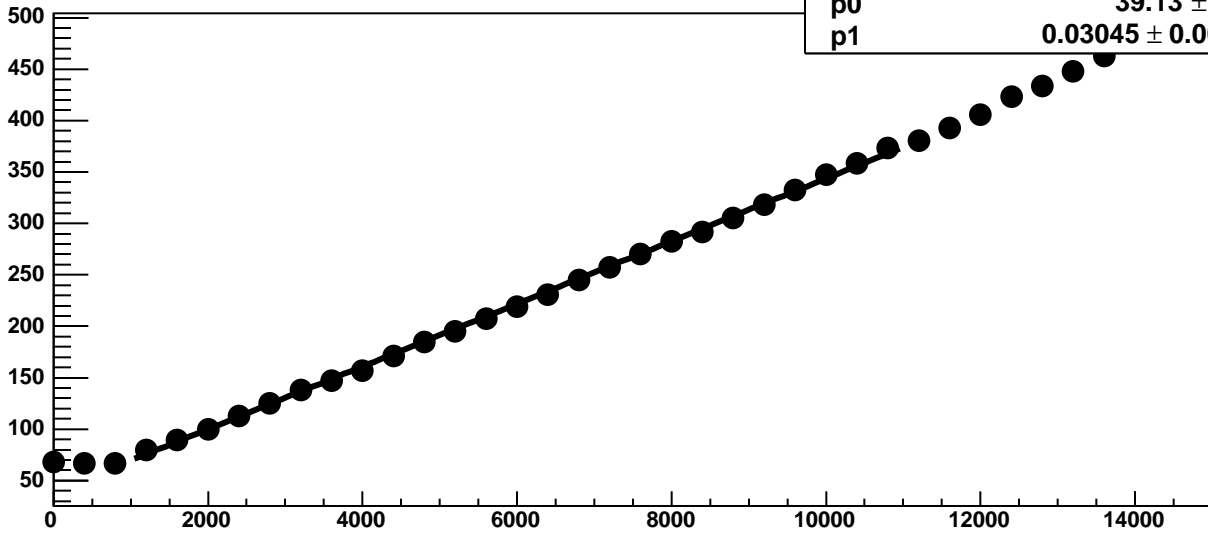


Chip 8, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



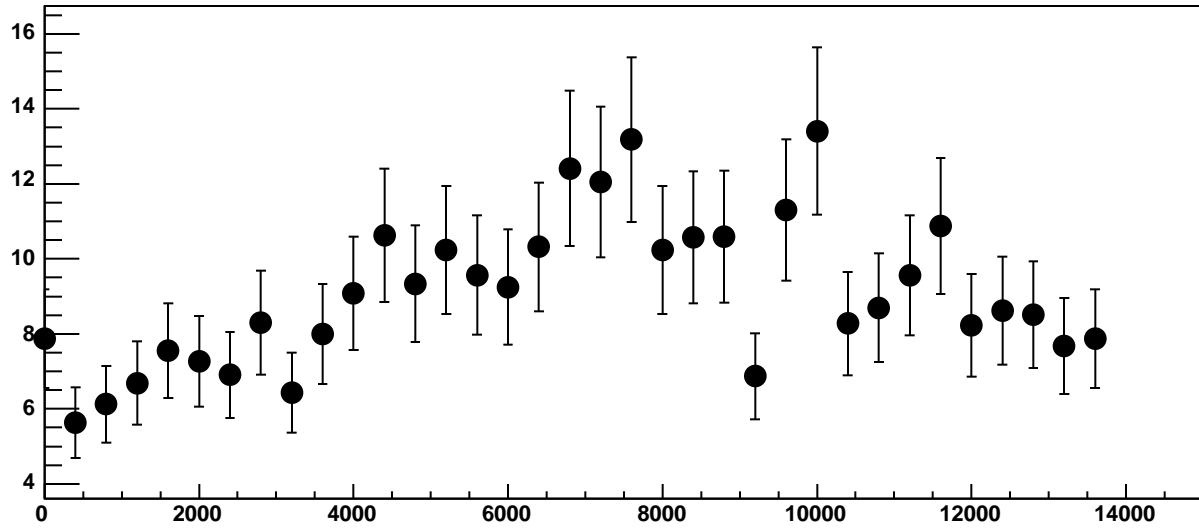


Chip 8, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

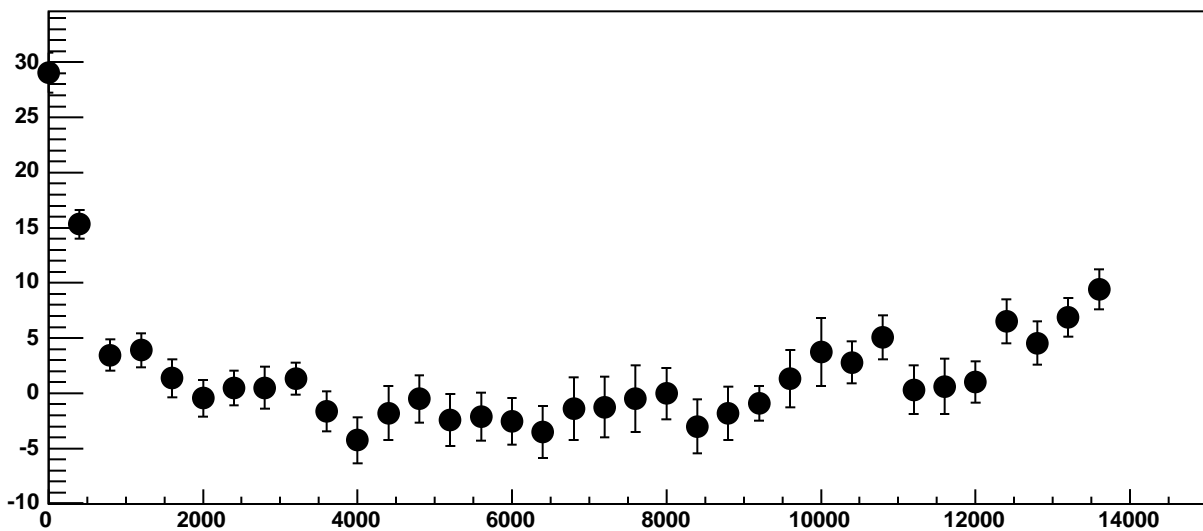


$\chi^2 / \text{ndf}$  32.6 / 23  
p0 39.13 ± 0.8152  
p1 0.03045 ± 0.0001326

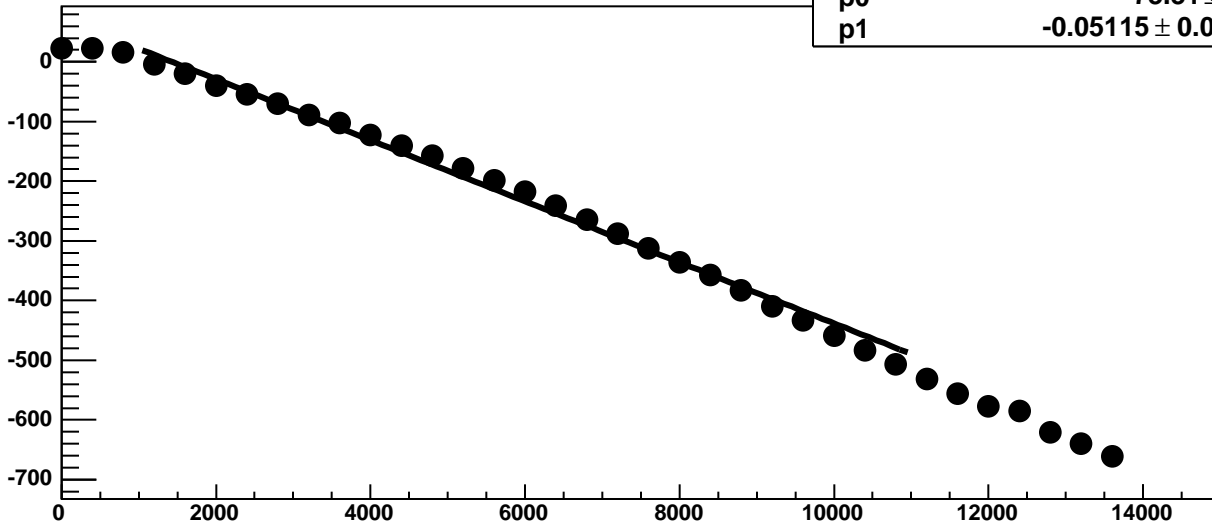
Chip 8, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

765.6 / 23

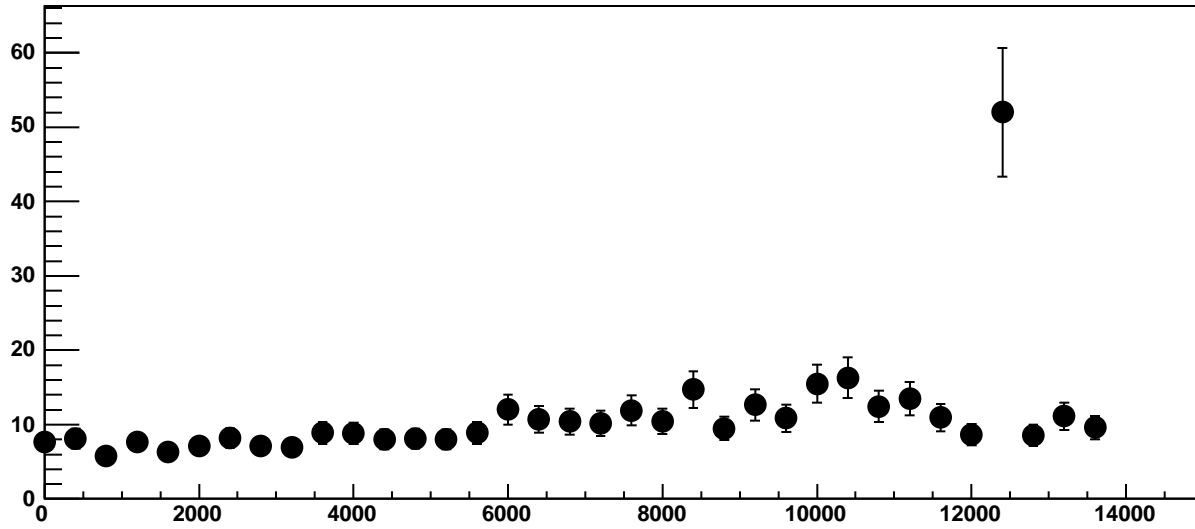
p0

$73.31 \pm 0.8601$

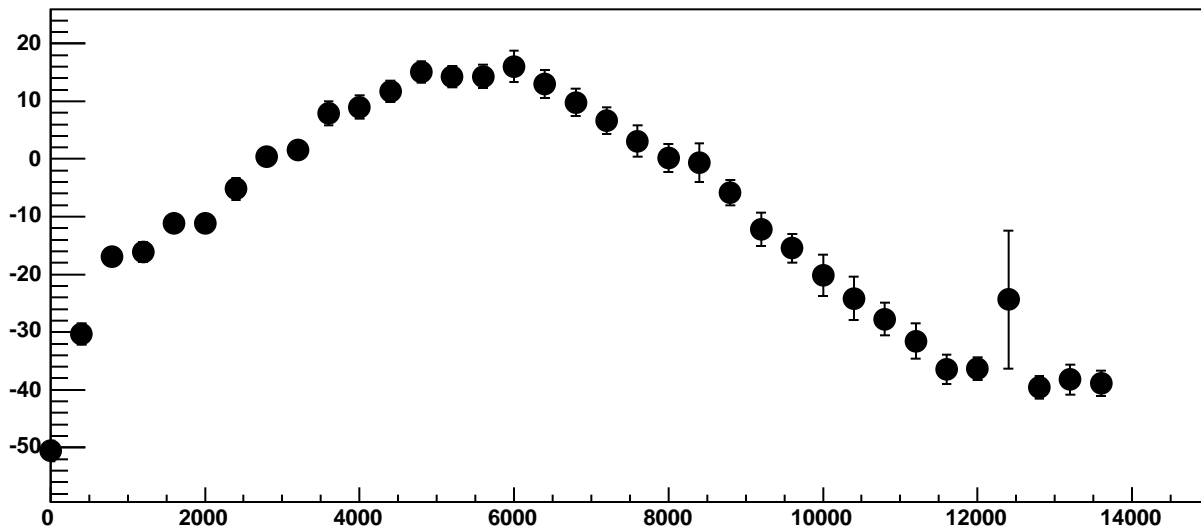
p1

$-0.05115 \pm 0.0001556$

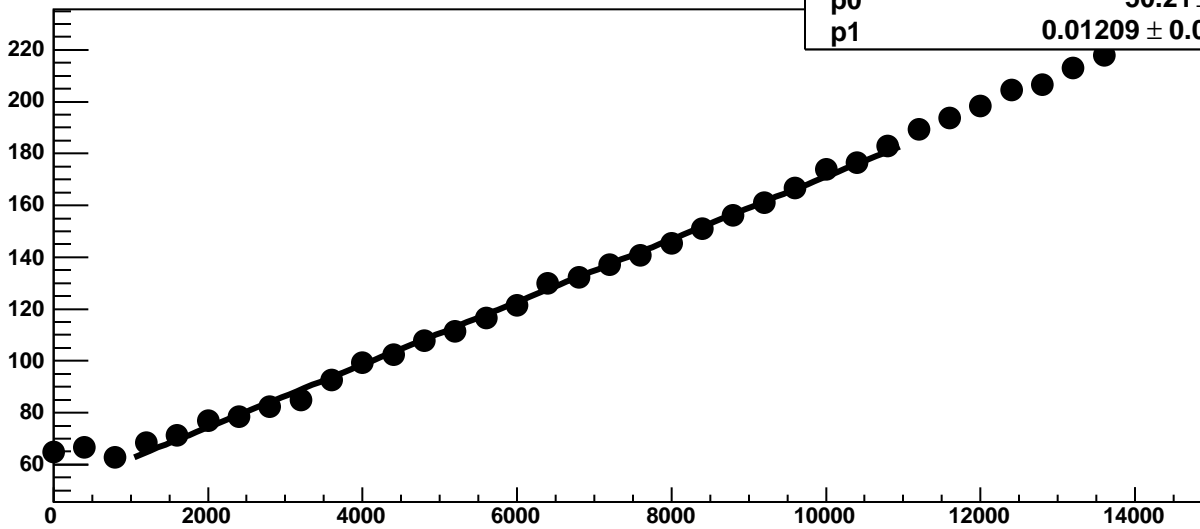
Chip 8, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

25.91 / 23

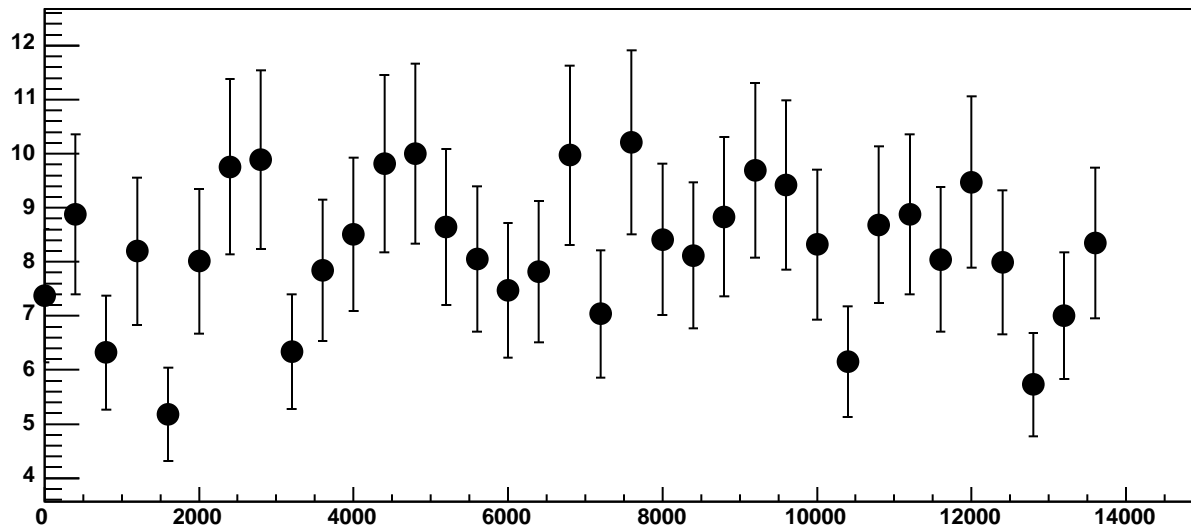
p0

$50.21 \pm 0.7961$

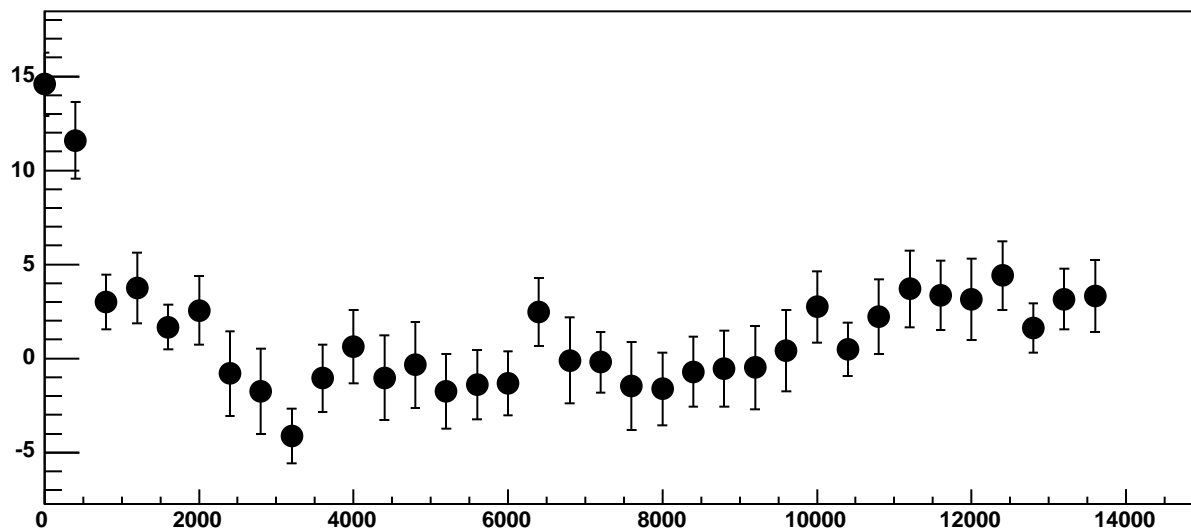
p1

$0.01209 \pm 0.0001219$

Chip 8, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

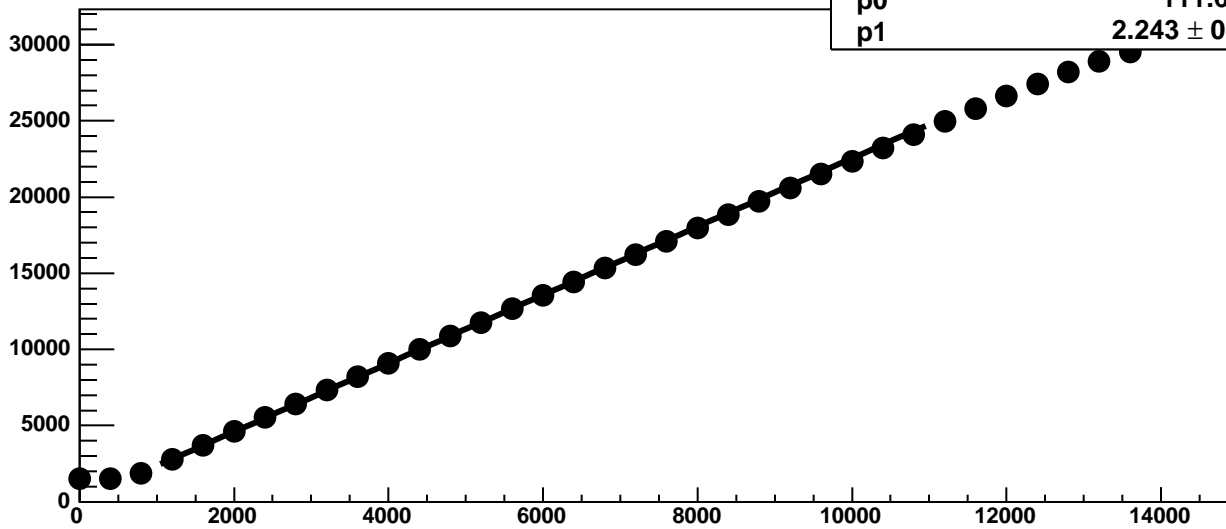


Chip 8, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

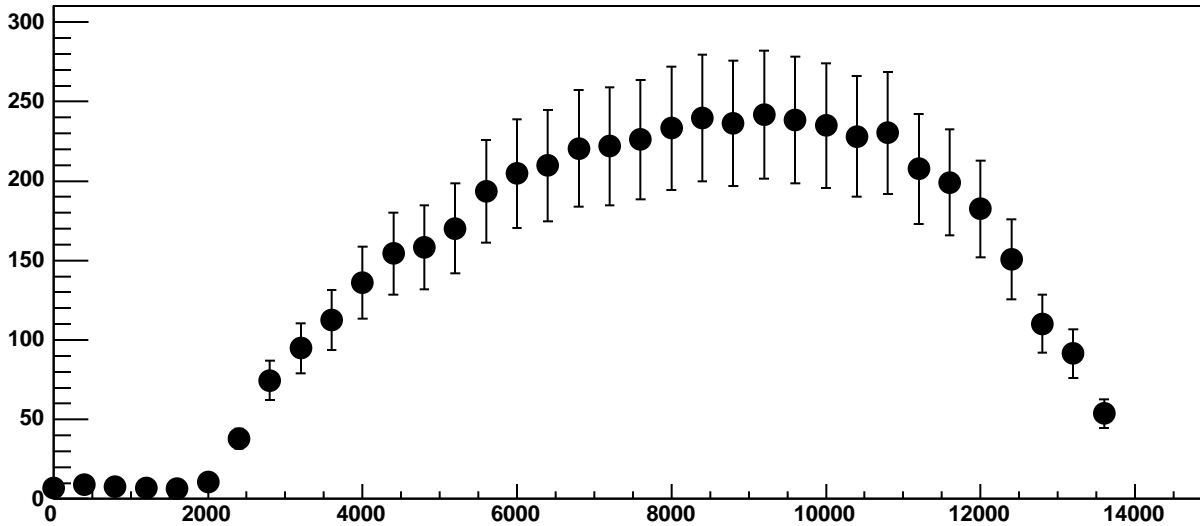


Chip 8, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

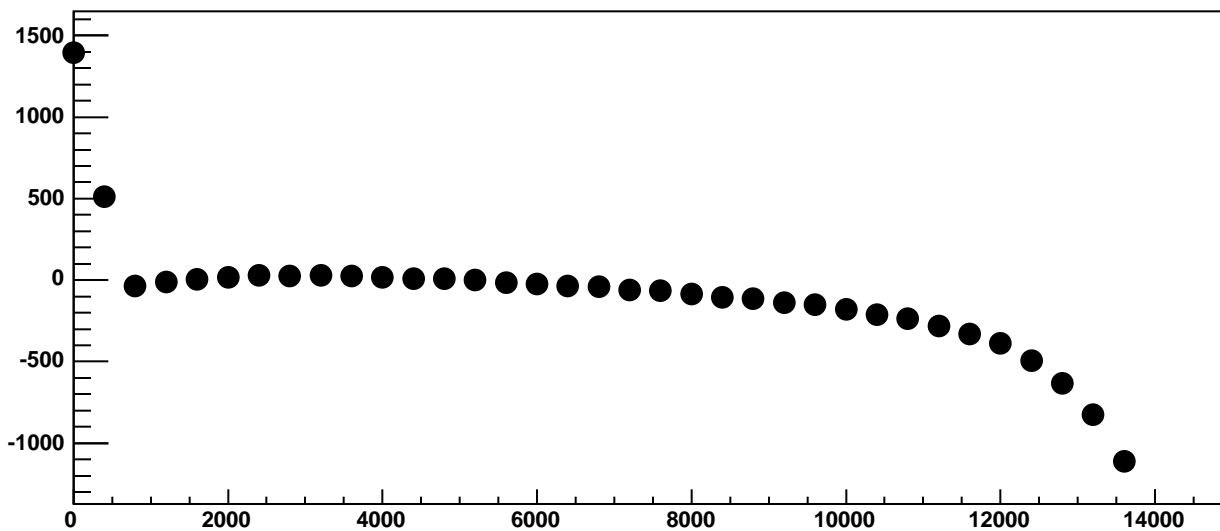
$\chi^2 / \text{ndf}$  229.4 / 23  
p0 111.6 ± 2.754  
p1 2.243 ± 0.001634



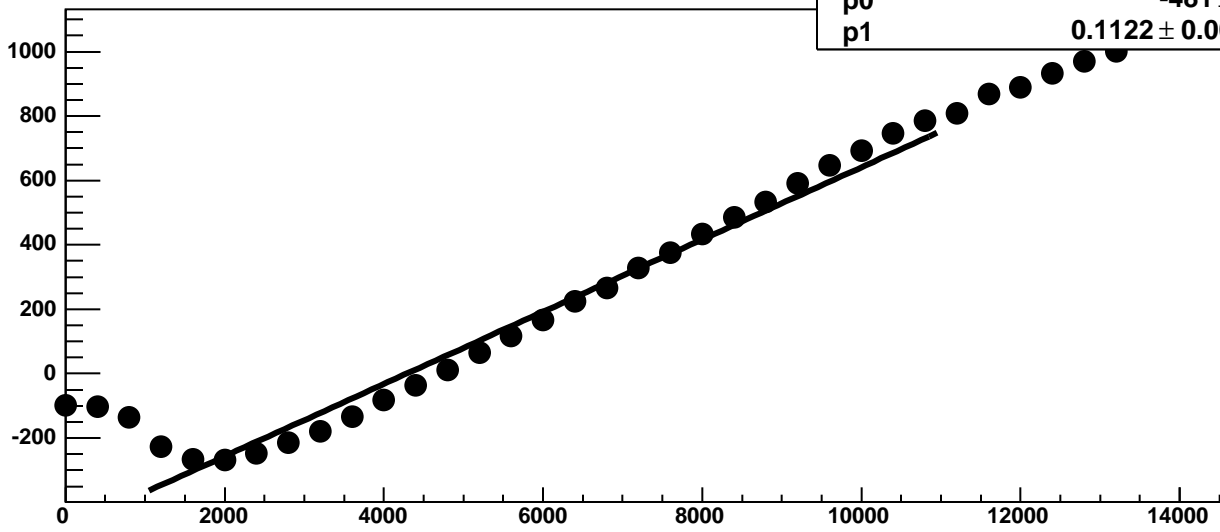
Chip 8, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



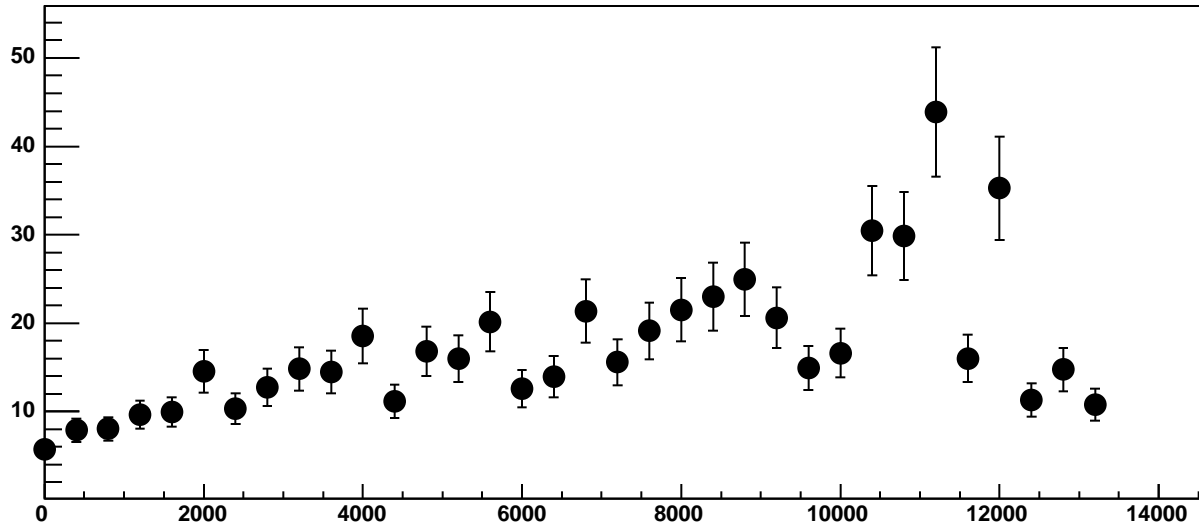
Chip 8, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



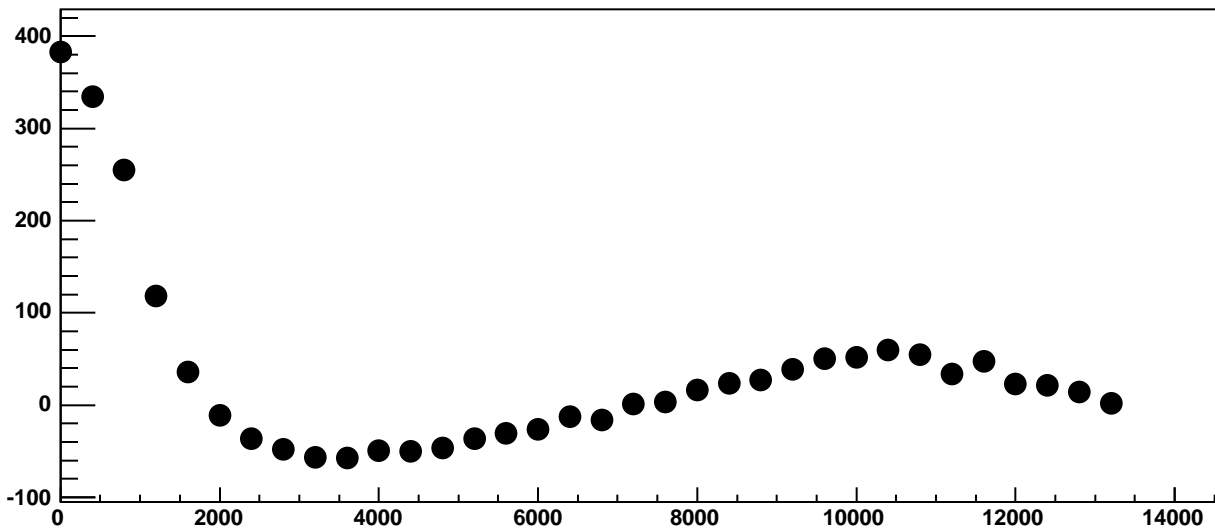
Chip 8, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



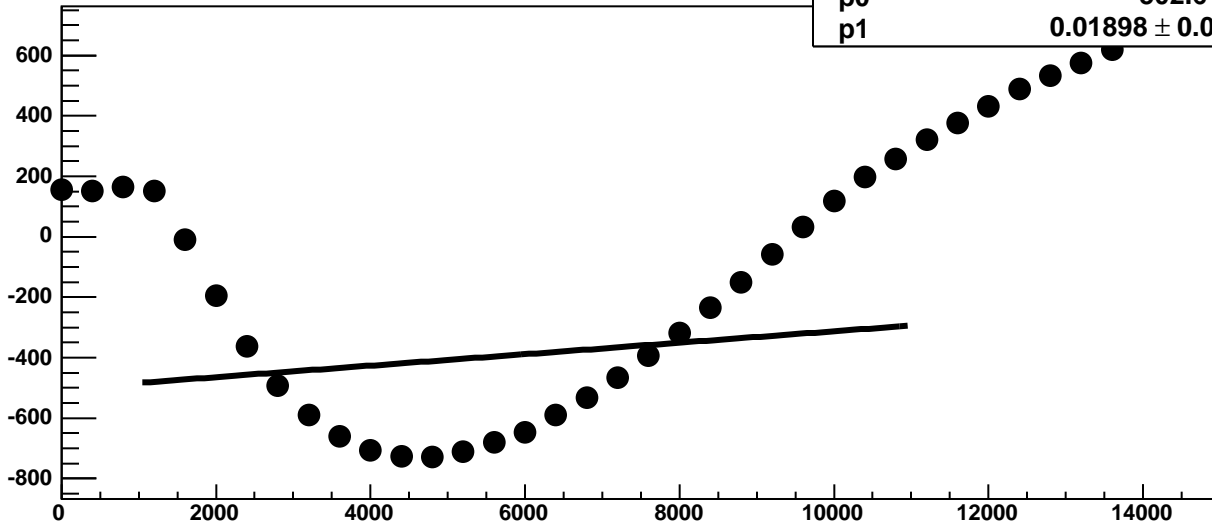
Chip 8, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

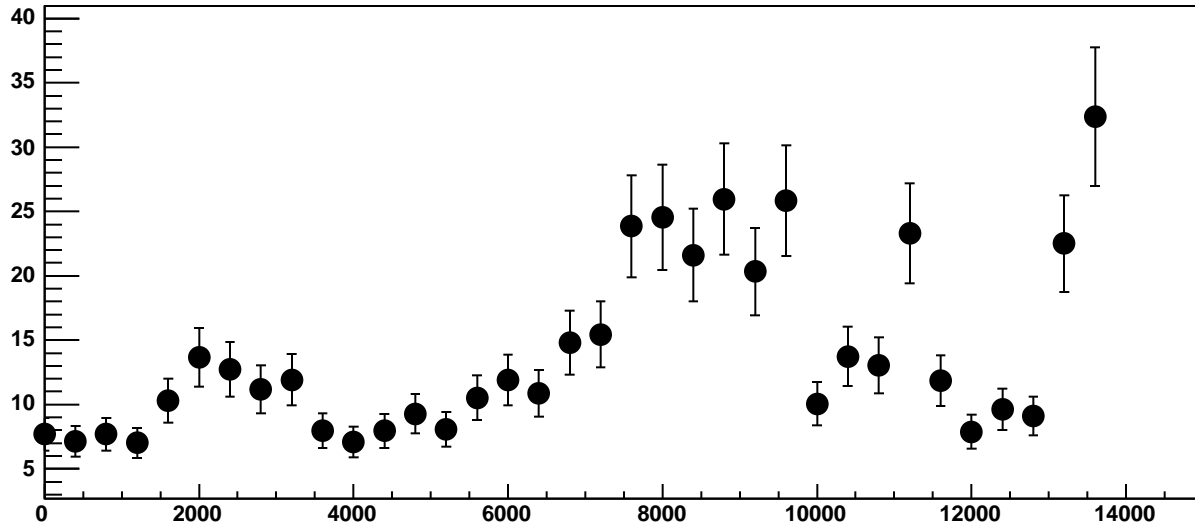


Chip 8, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

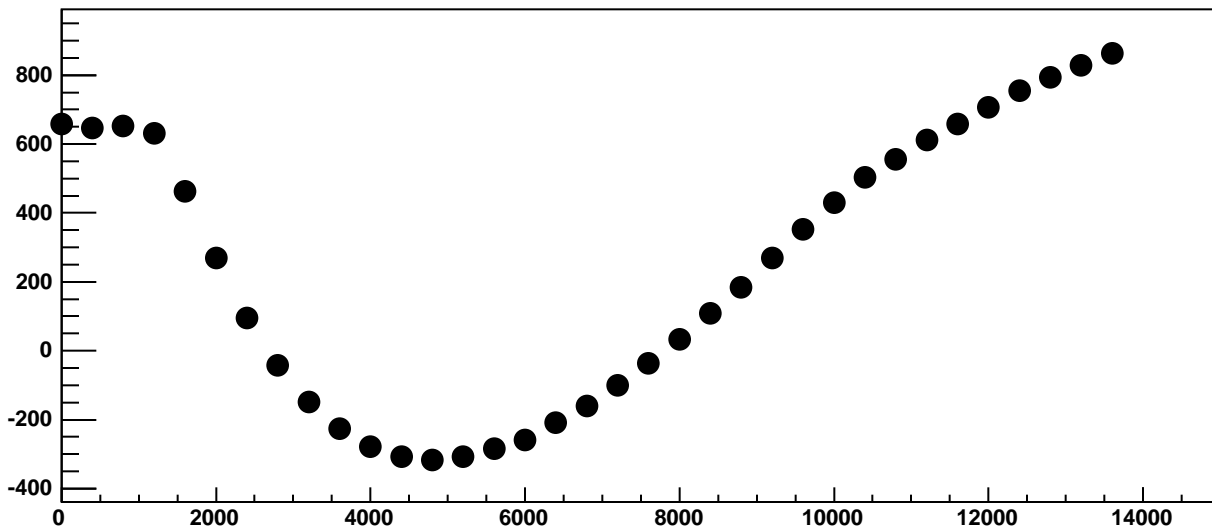


$\chi^2 / \text{ndf}$  4.623e+05 / 23  
p0 -502.6 ± 1.068  
p1 0.01898 ± 0.0001928

Chip 8, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

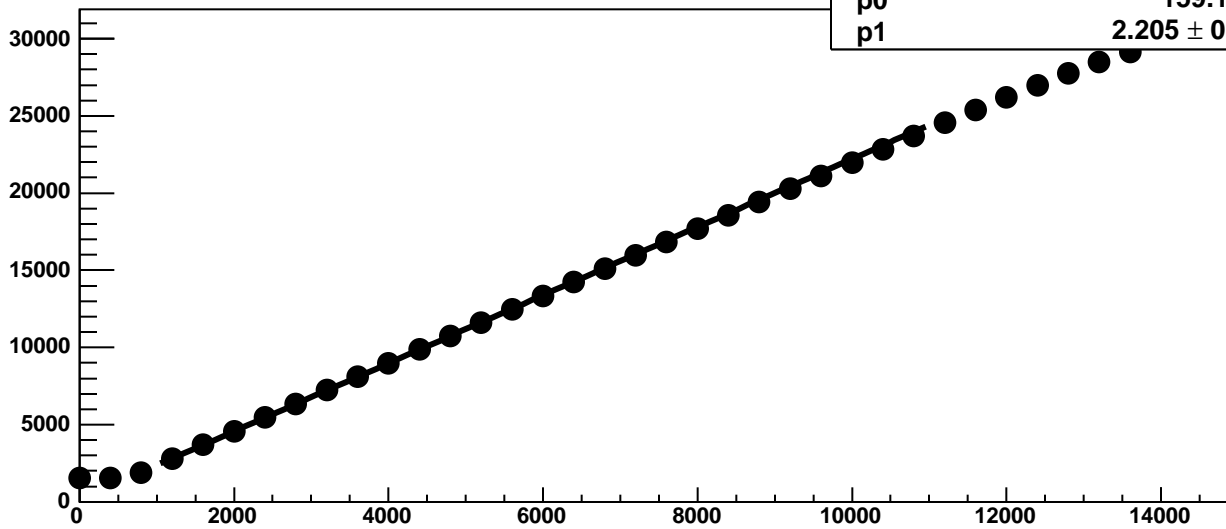


Chip 8, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

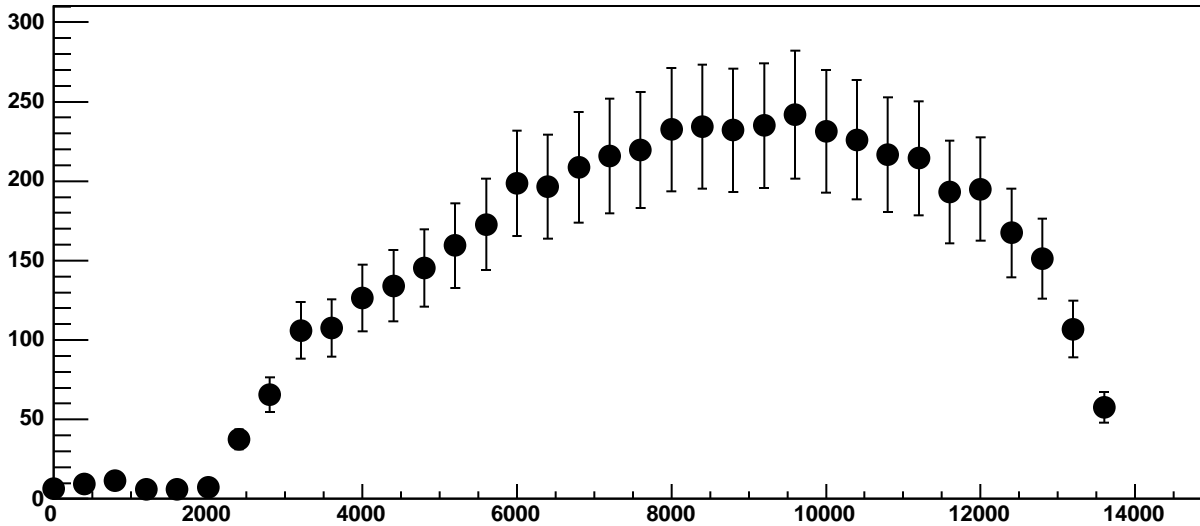


Chip 8, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

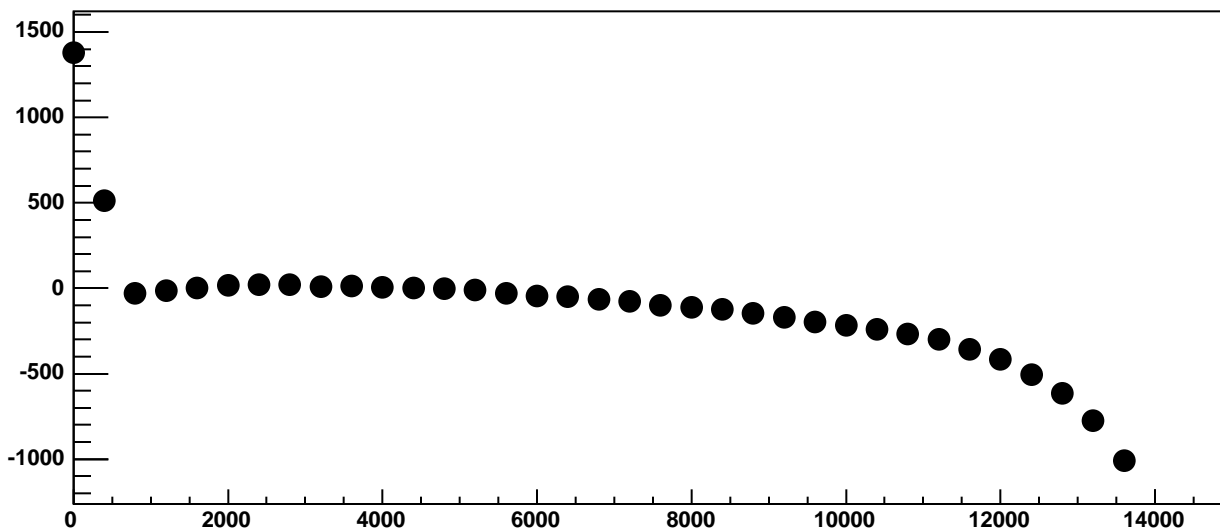
$\chi^2 / \text{ndf}$  299.4 / 23  
p0 159.1 ± 2.574  
p1 2.205 ± 0.001508



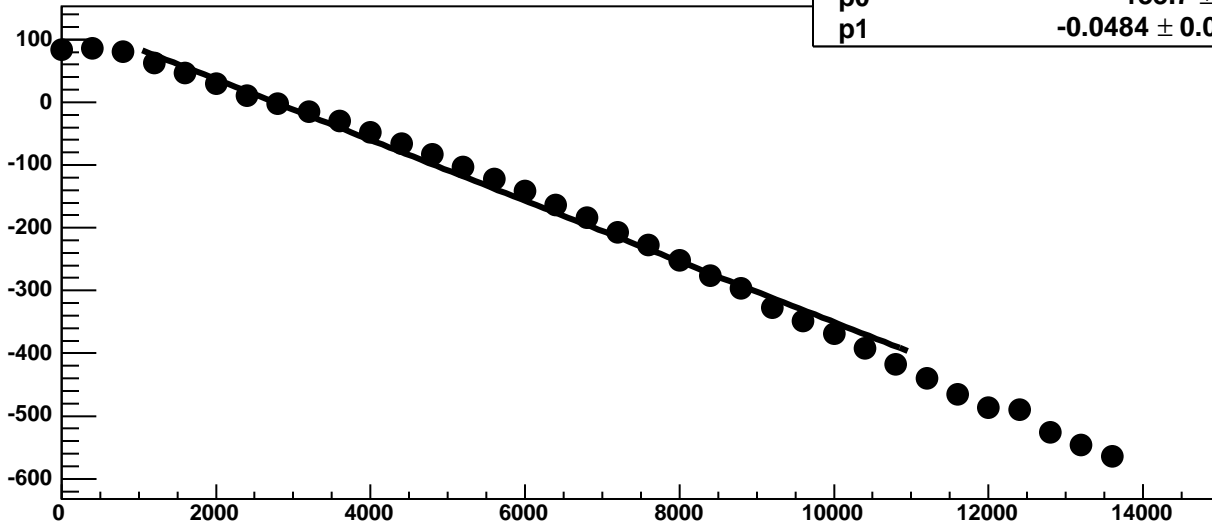
Chip 8, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

697.3 / 23

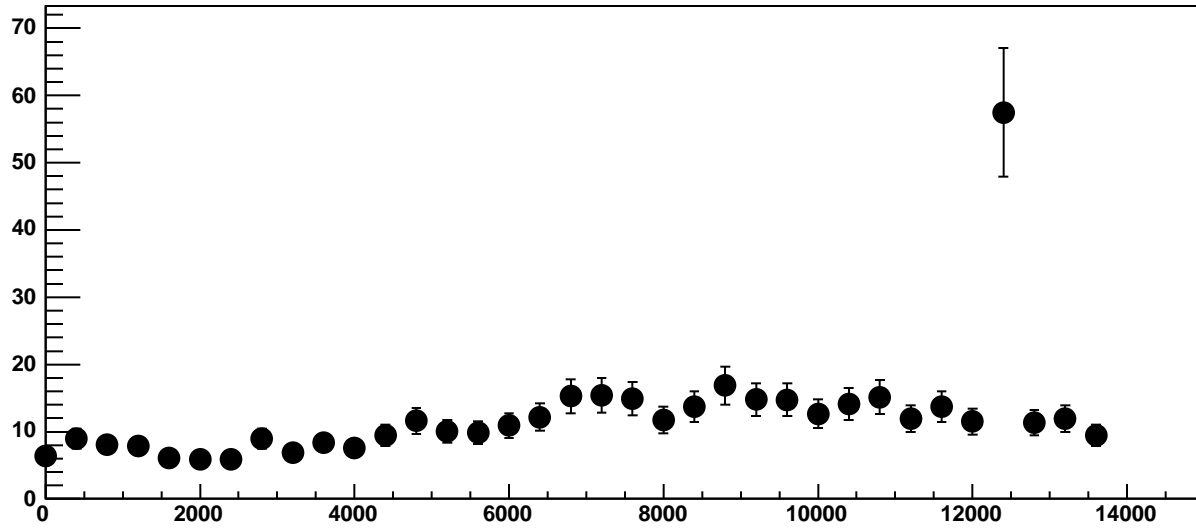
p0

$133.7 \pm 0.8354$

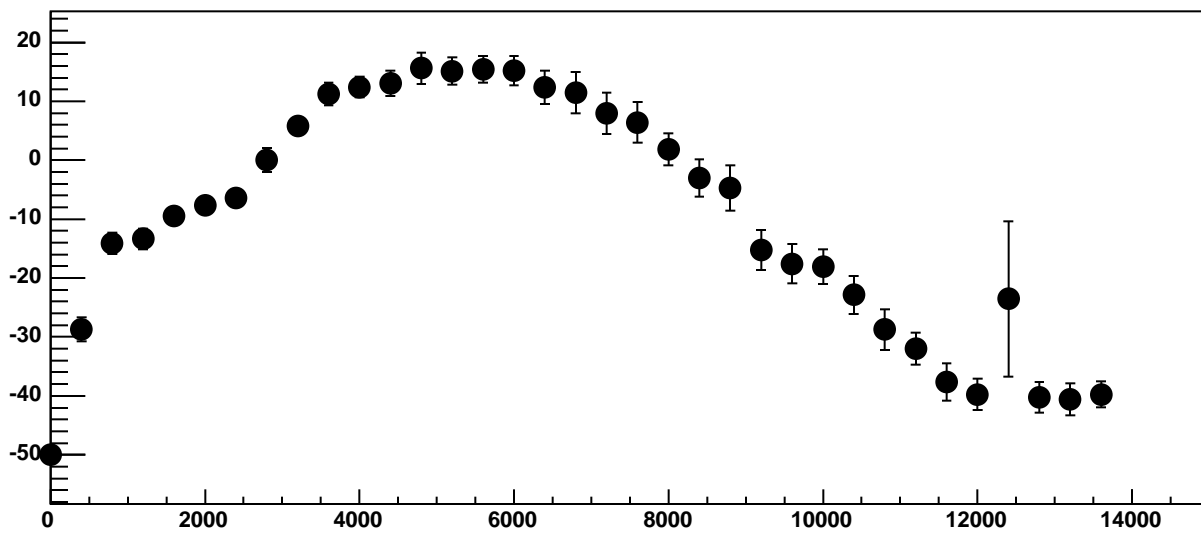
p1

$-0.0484 \pm 0.0001651$

Chip 8, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

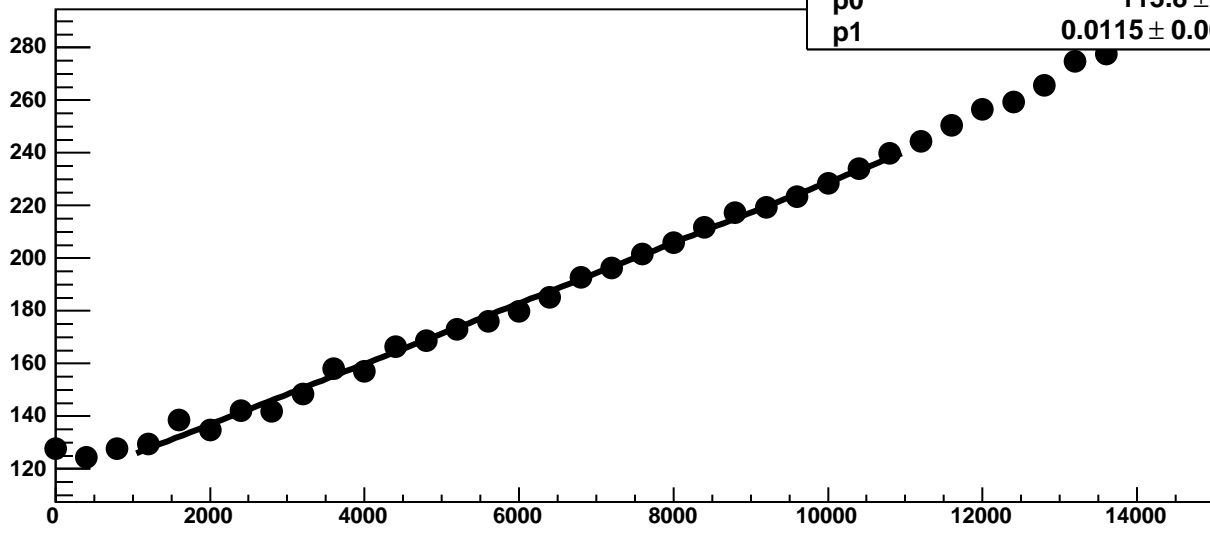


Chip 8, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

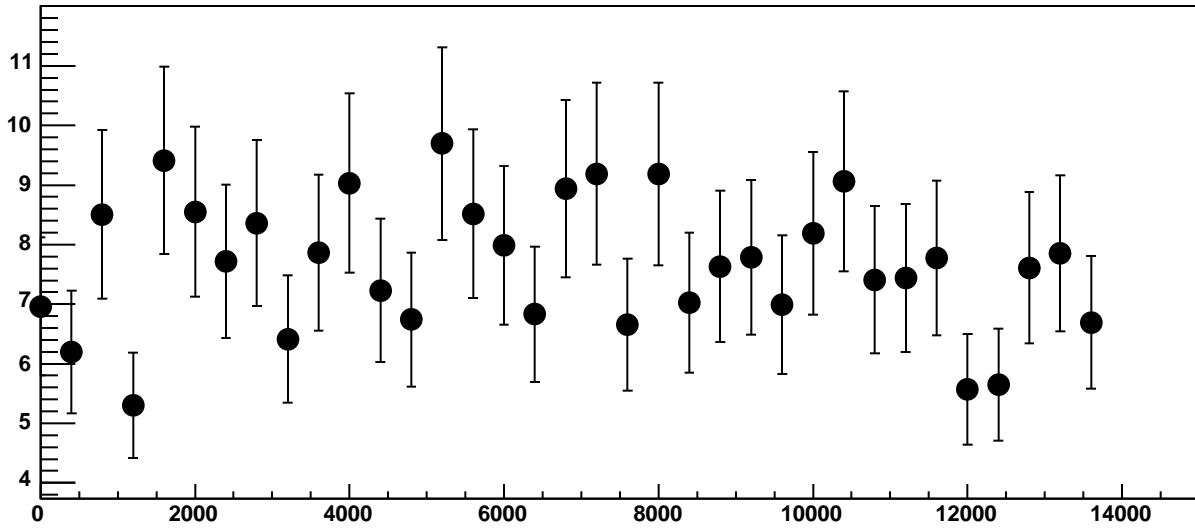




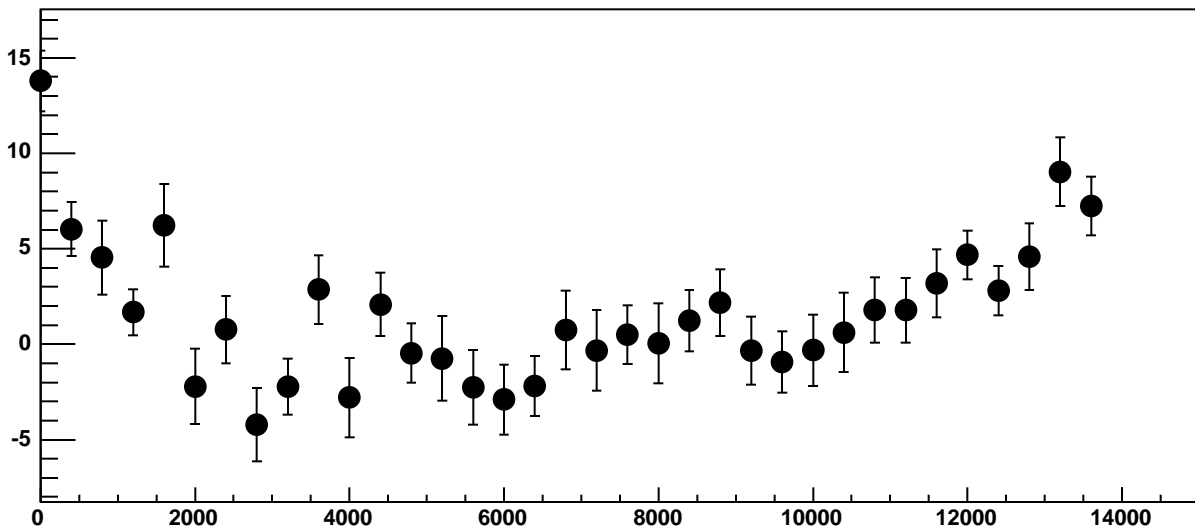
Chip 8, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



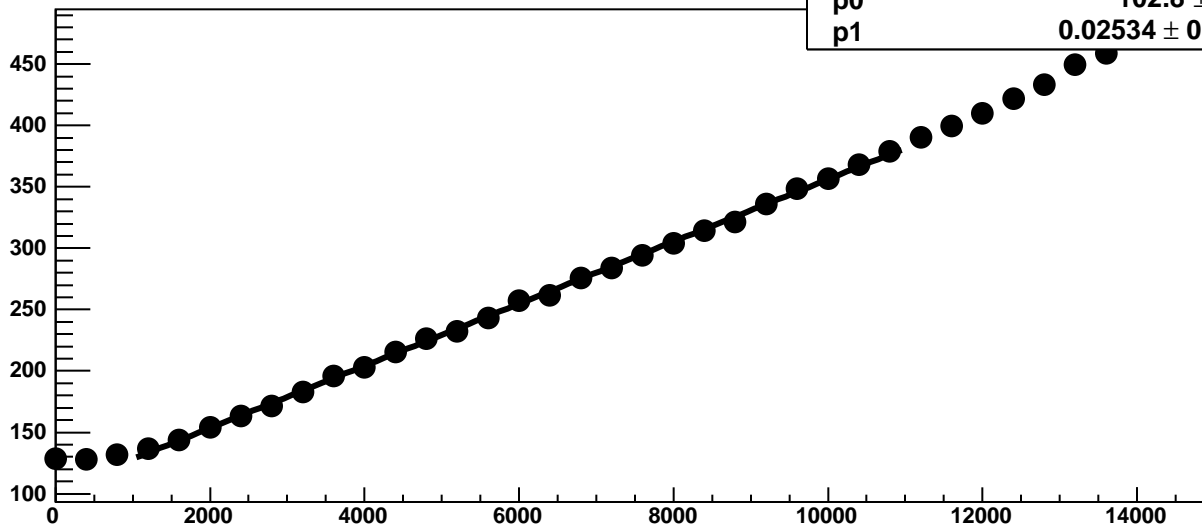
Chip 8, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

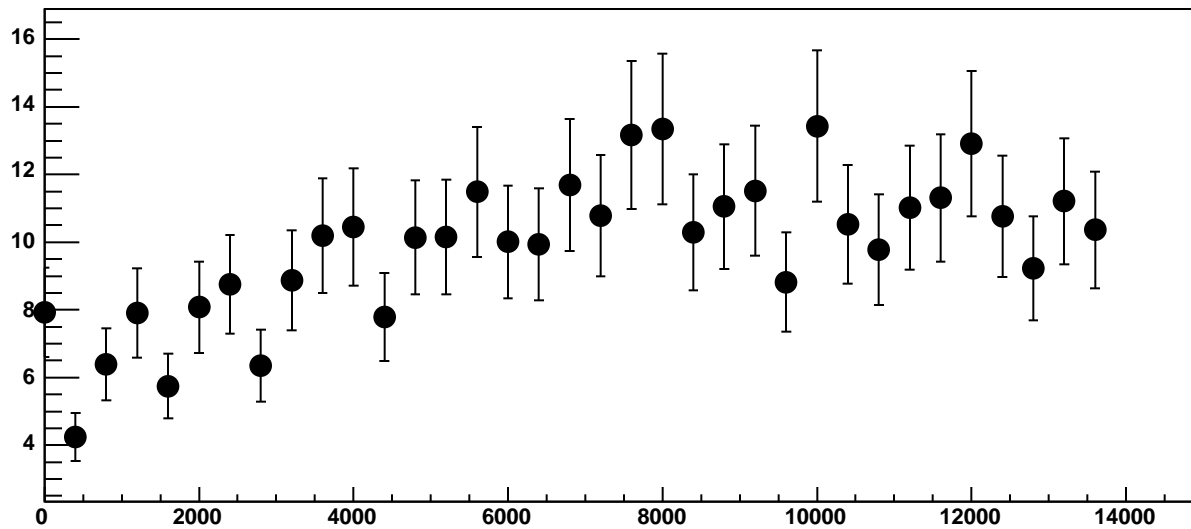


Chip 8, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

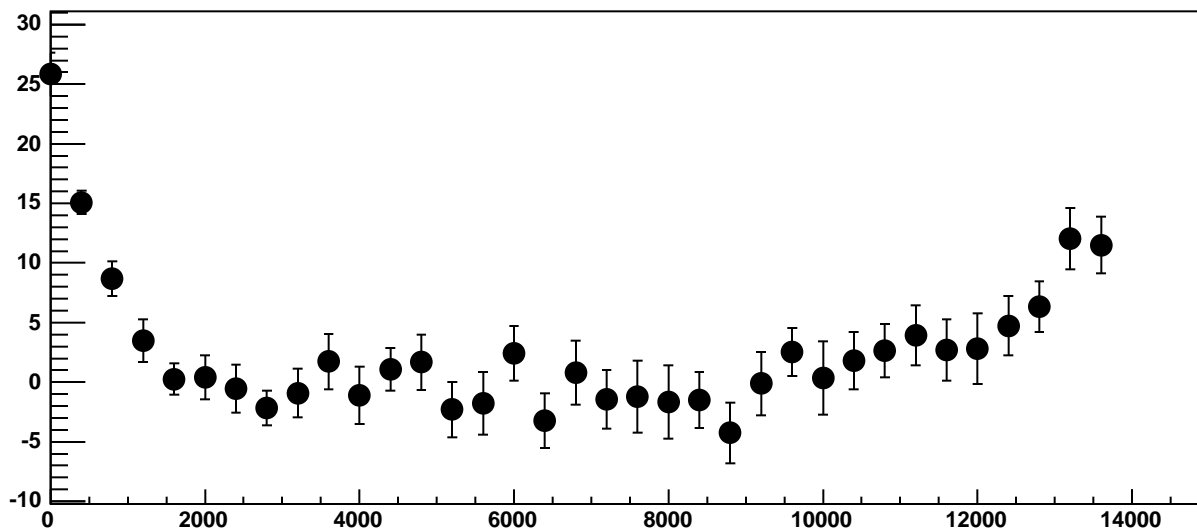


$\chi^2 / \text{ndf}$  20.08 / 23  
p0  $102.8 \pm 0.8474$   
p1  $0.02534 \pm 0.000143$

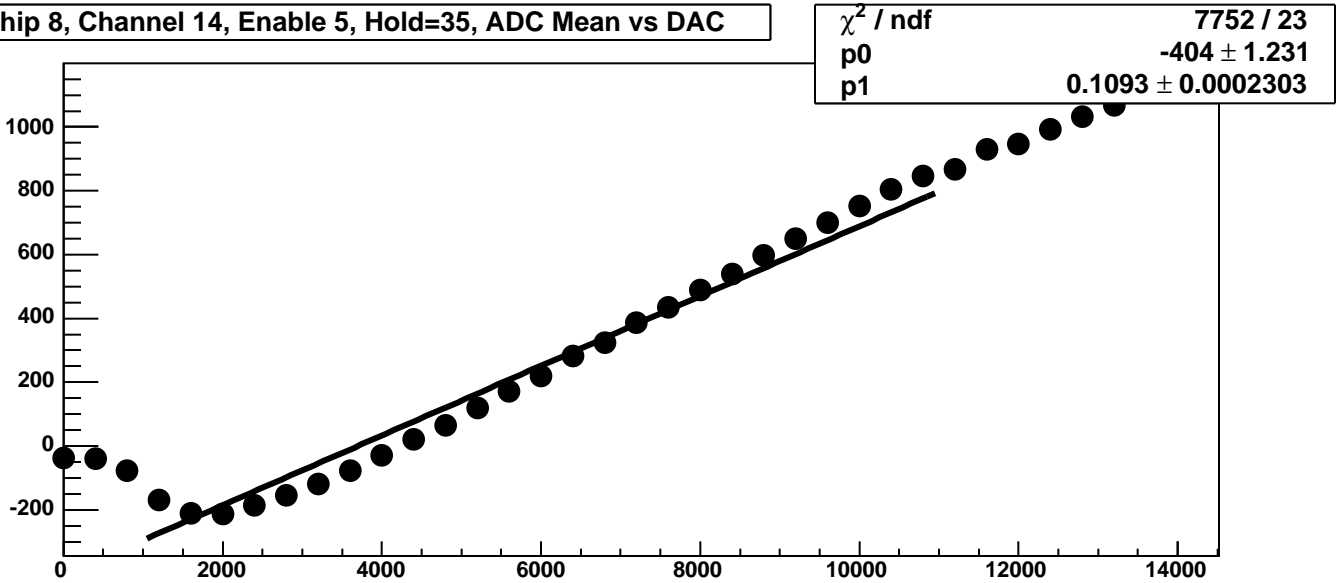
Chip 8, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



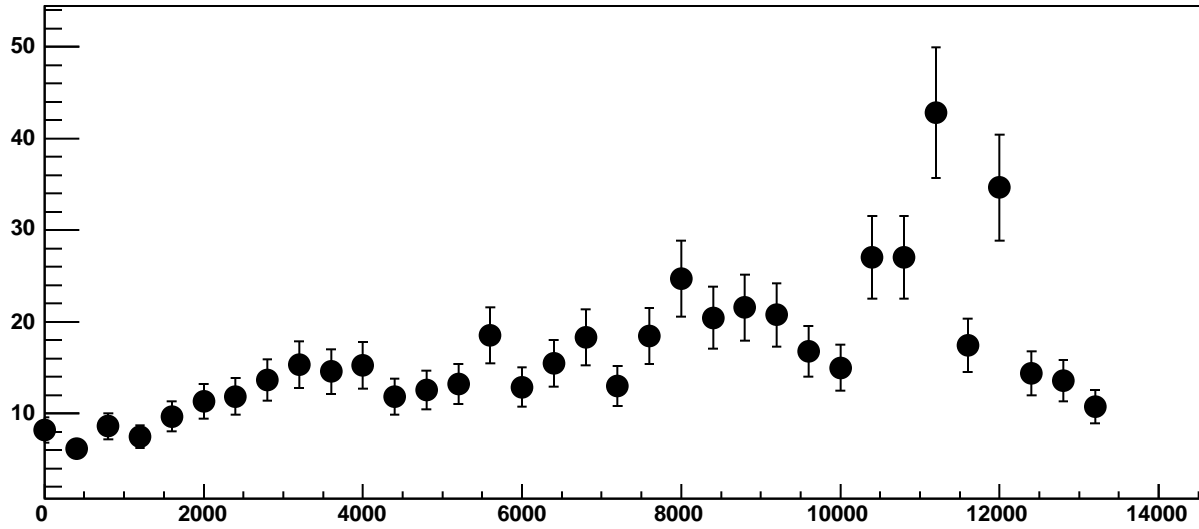
Chip 8, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



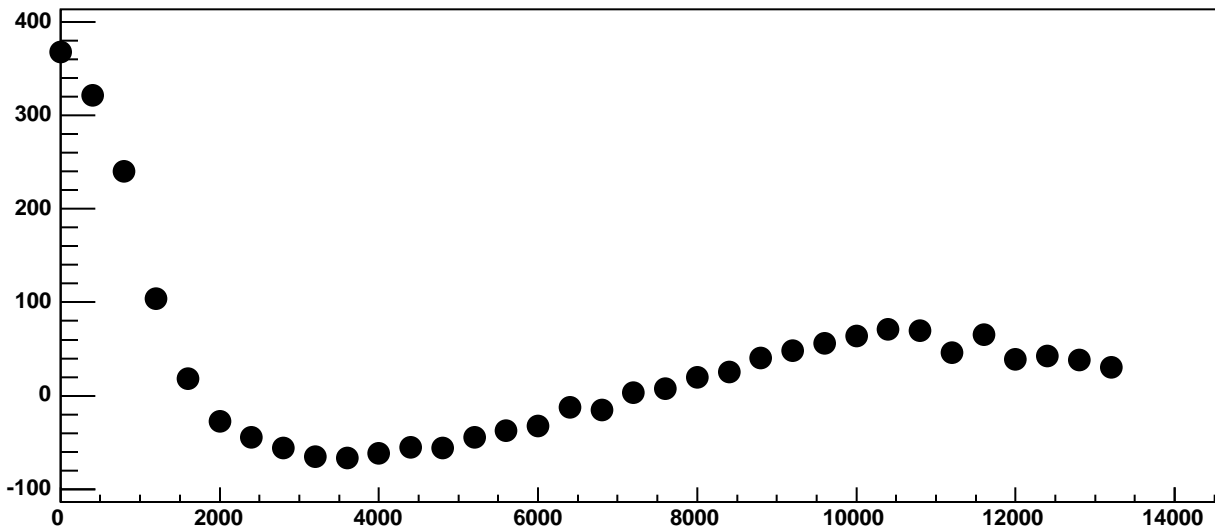
Chip 8, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

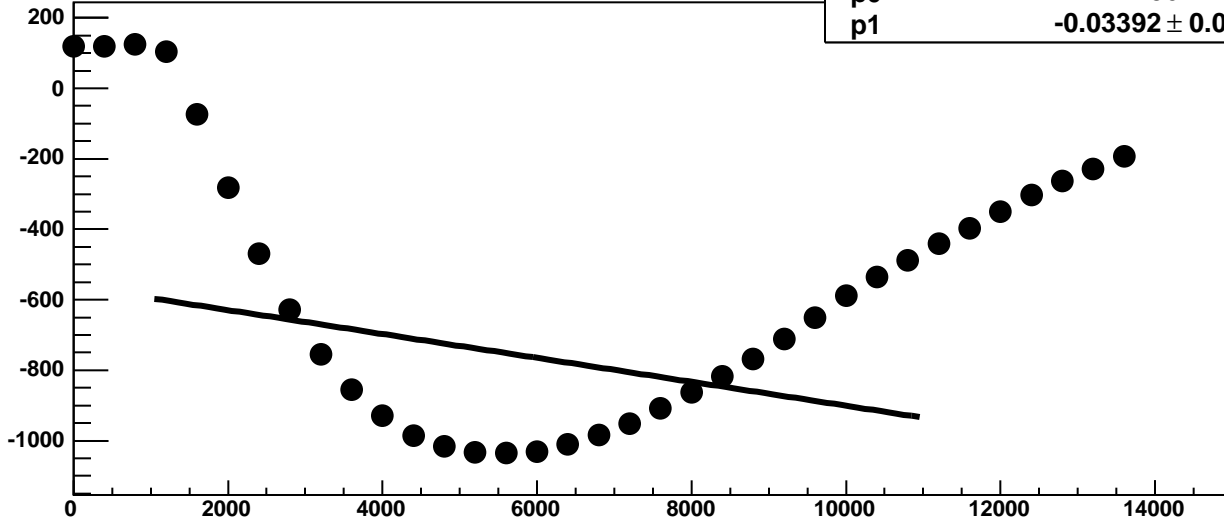


Chip 8, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

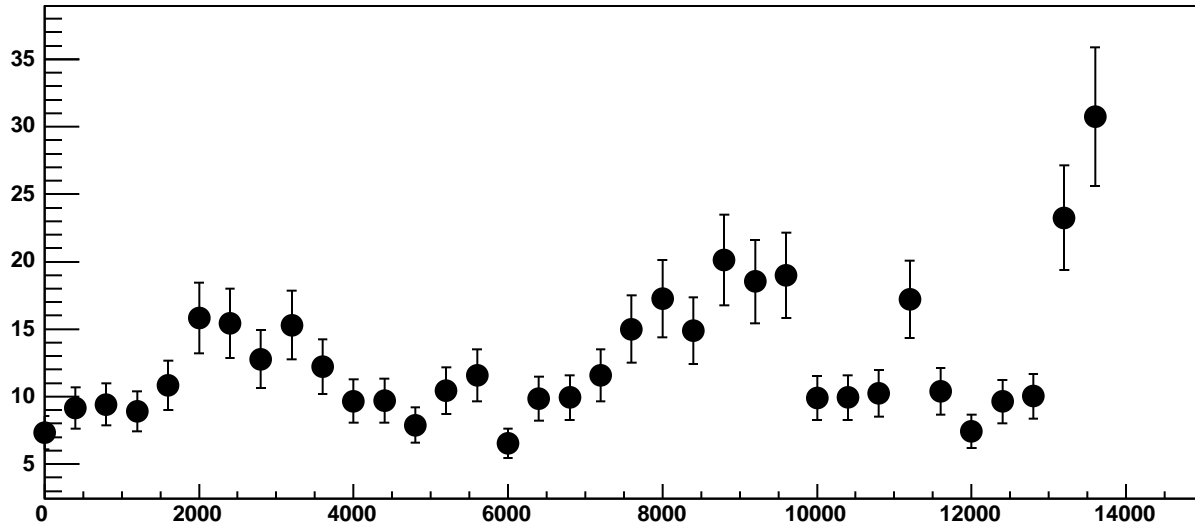


Chip 8, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

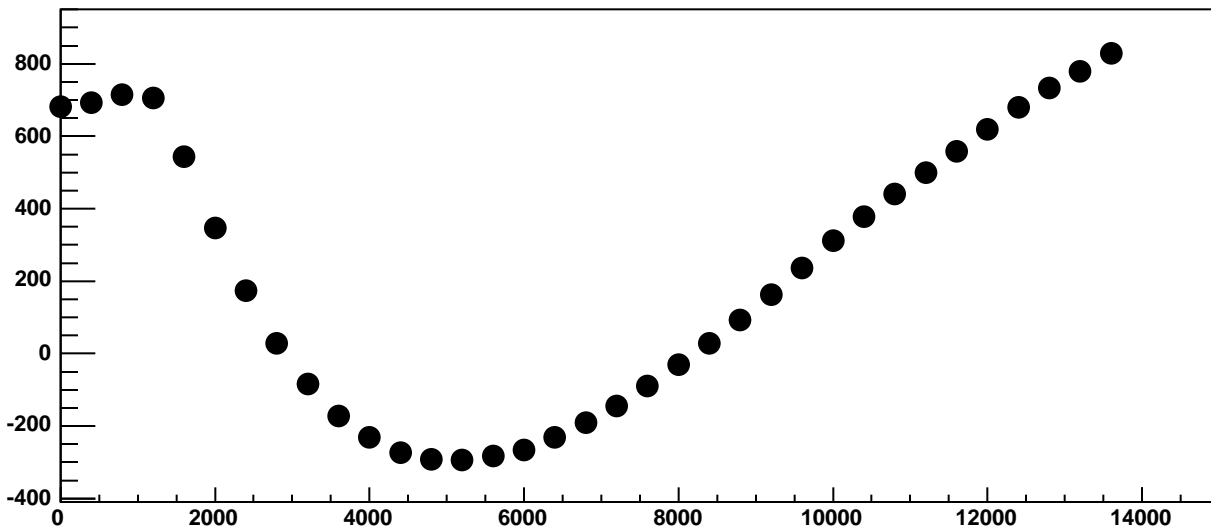
$\chi^2 / \text{ndf}$  4.006e+05 / 23  
p0 -561.4  $\pm$  1.188  
p1 -0.03392  $\pm$  0.0001852



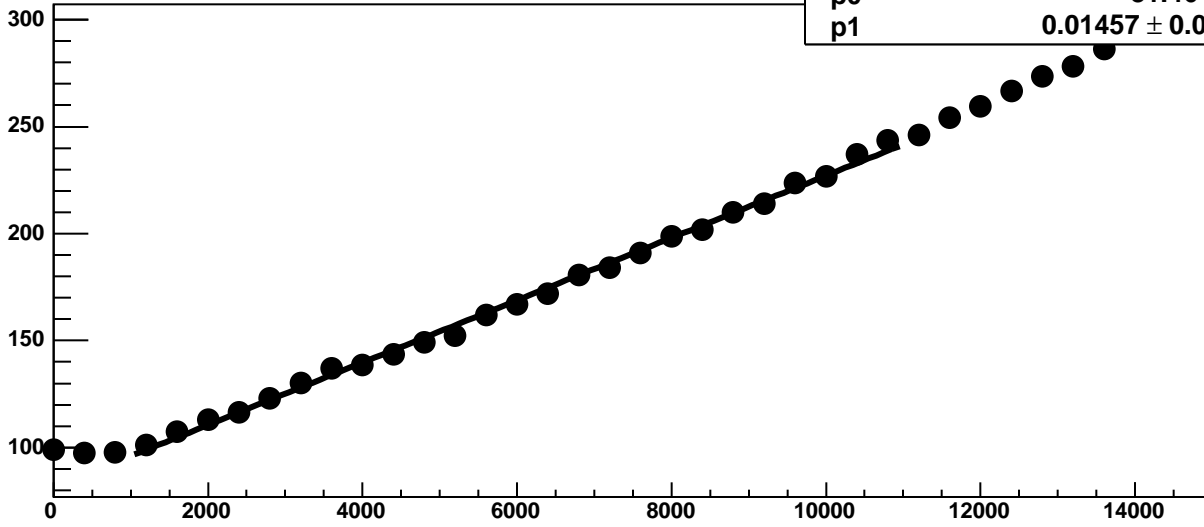
Chip 8, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

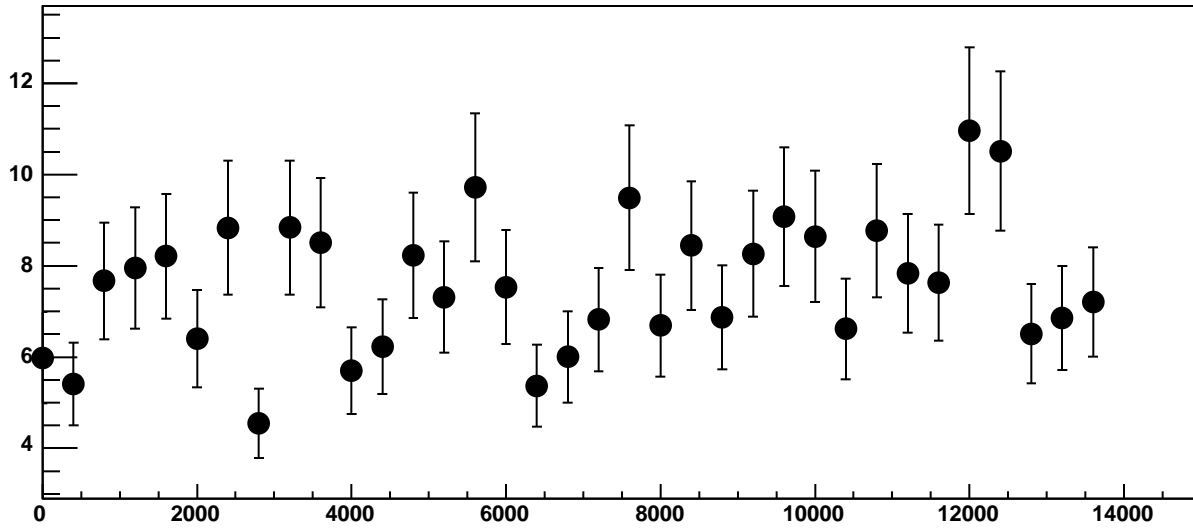


Chip 8, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

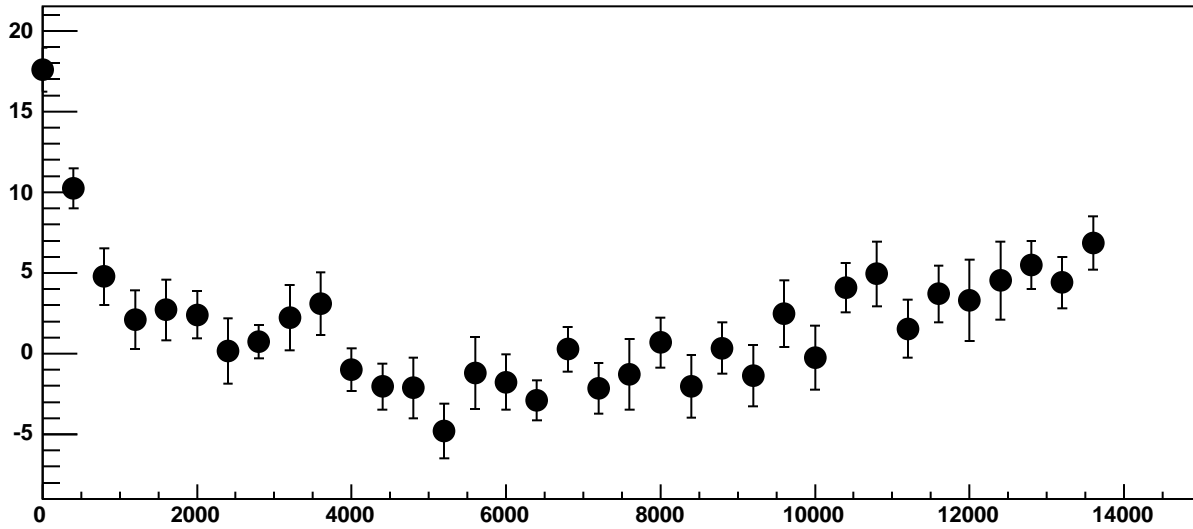


$\chi^2 / \text{ndf}$  48.1 / 23  
p0  $81.46 \pm 0.755$   
p1  $0.01457 \pm 0.0001184$

Chip 8, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

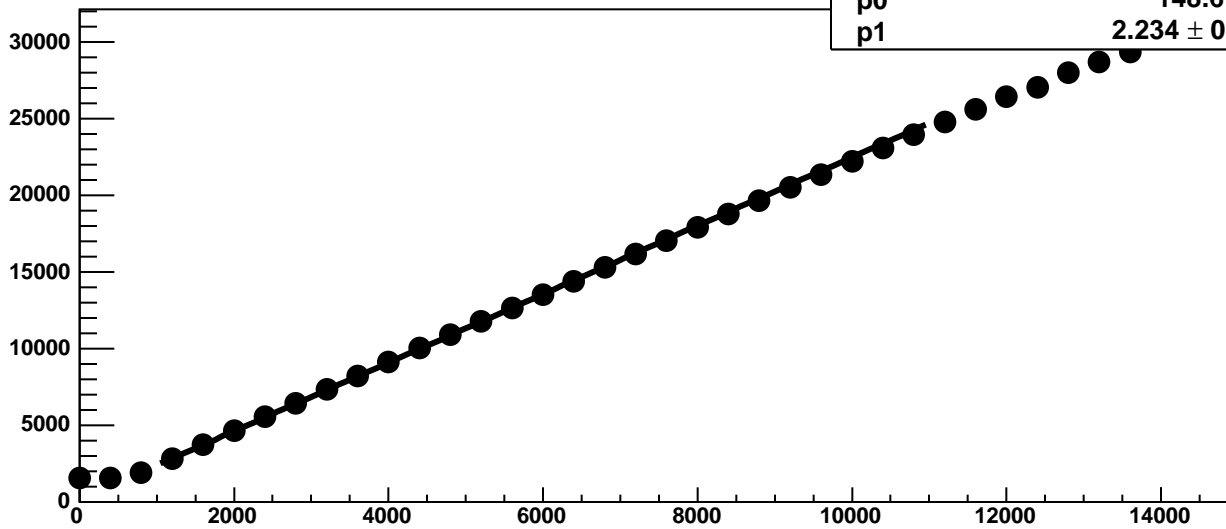


Chip 8, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

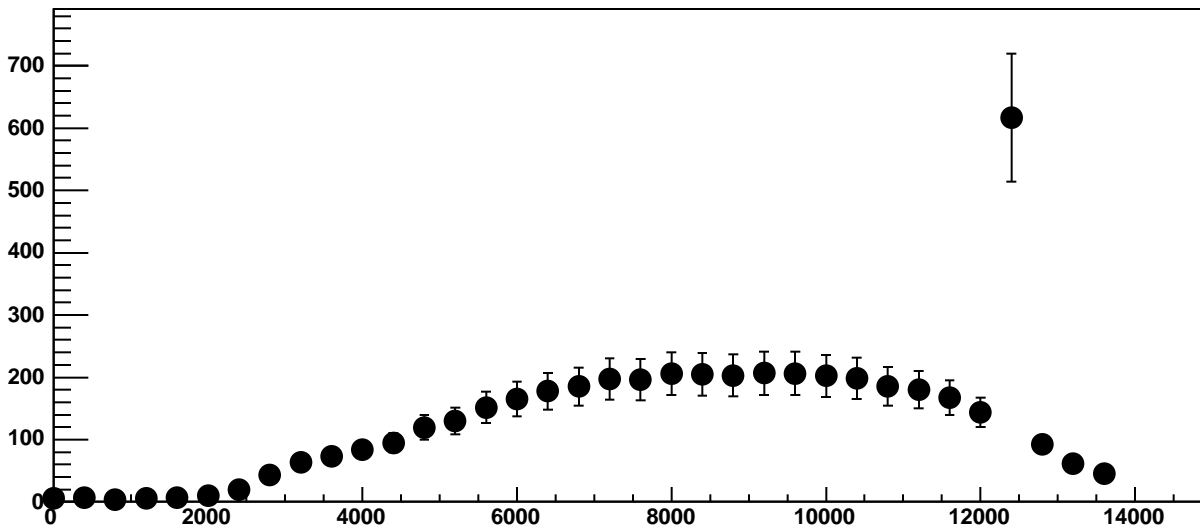


Chip 8, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

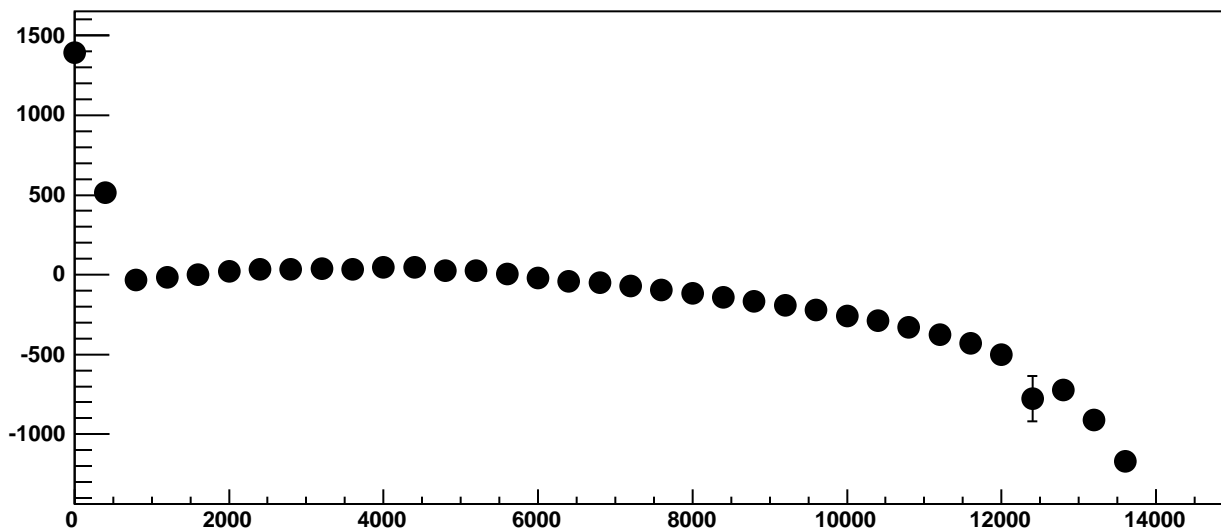
$\chi^2 / \text{ndf}$  485.4 / 23  
p0  $148.6 \pm 2.342$   
p1  $2.234 \pm 0.001333$



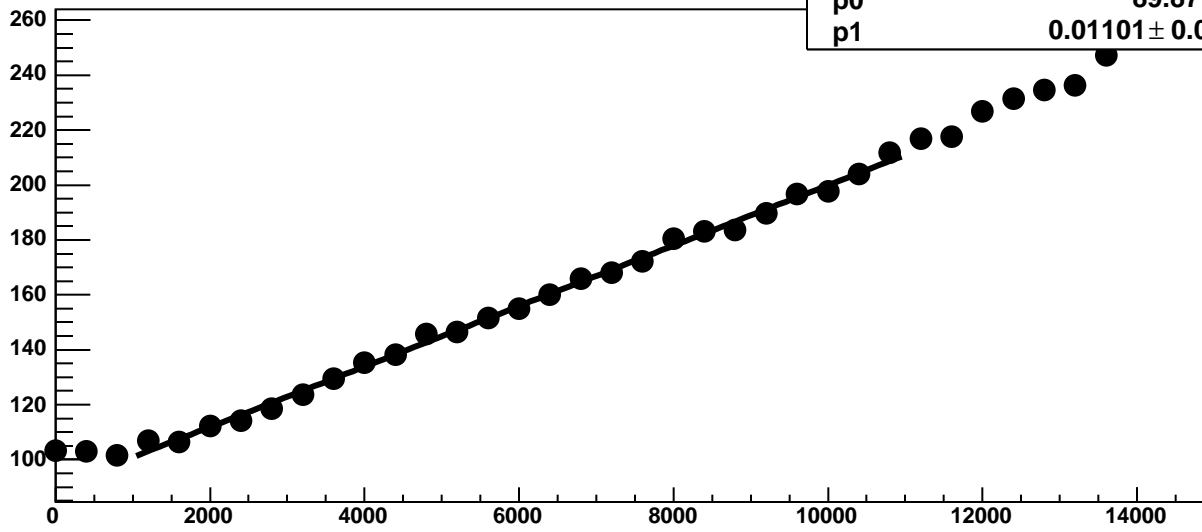
Chip 8, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

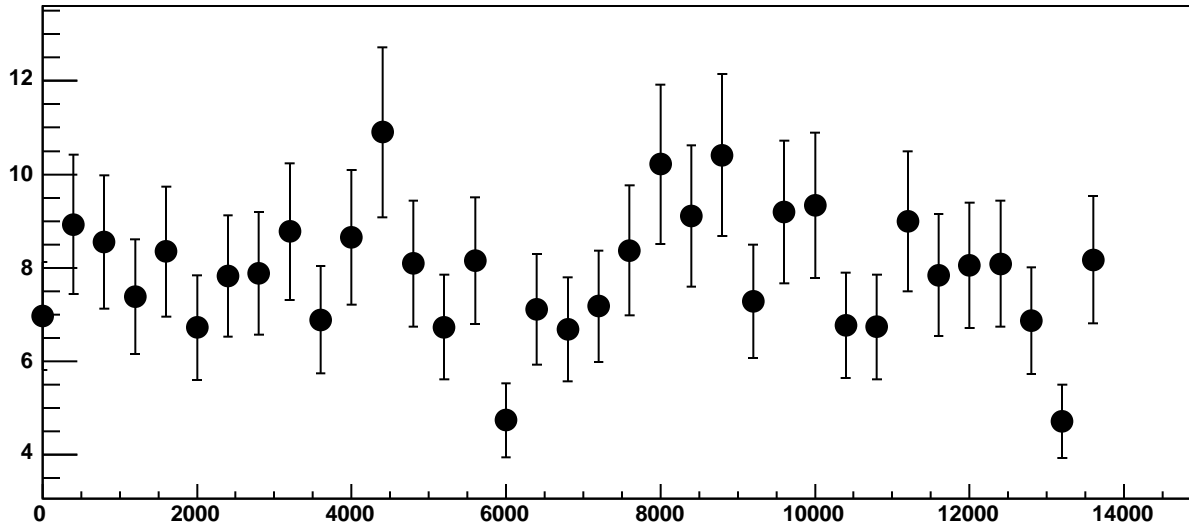


Chip 8, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

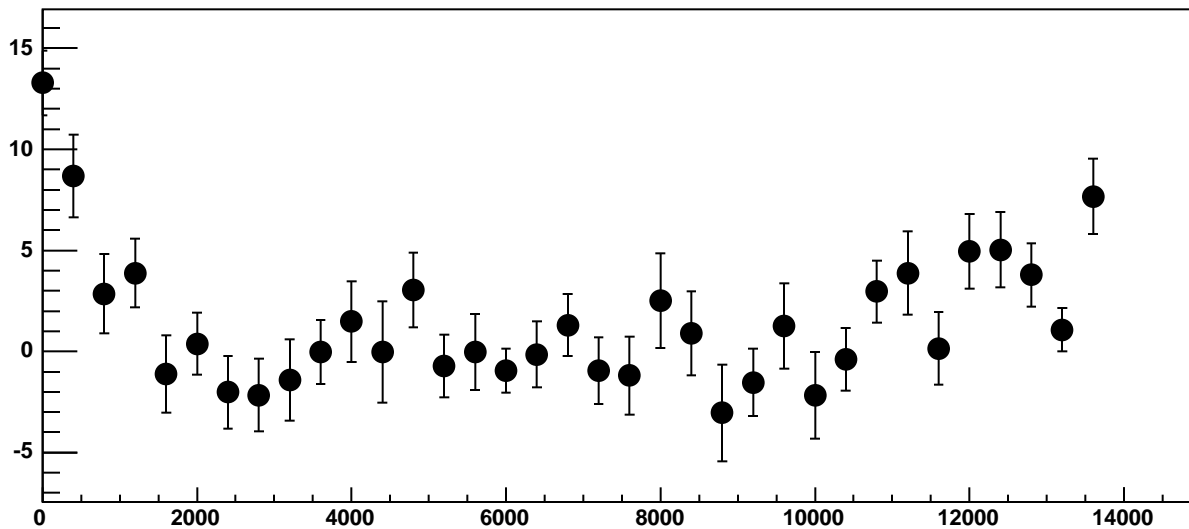


$\chi^2 / \text{ndf}$  23.41 / 23  
p0 89.87 ± 0.809  
p1 0.01101 ± 0.0001228

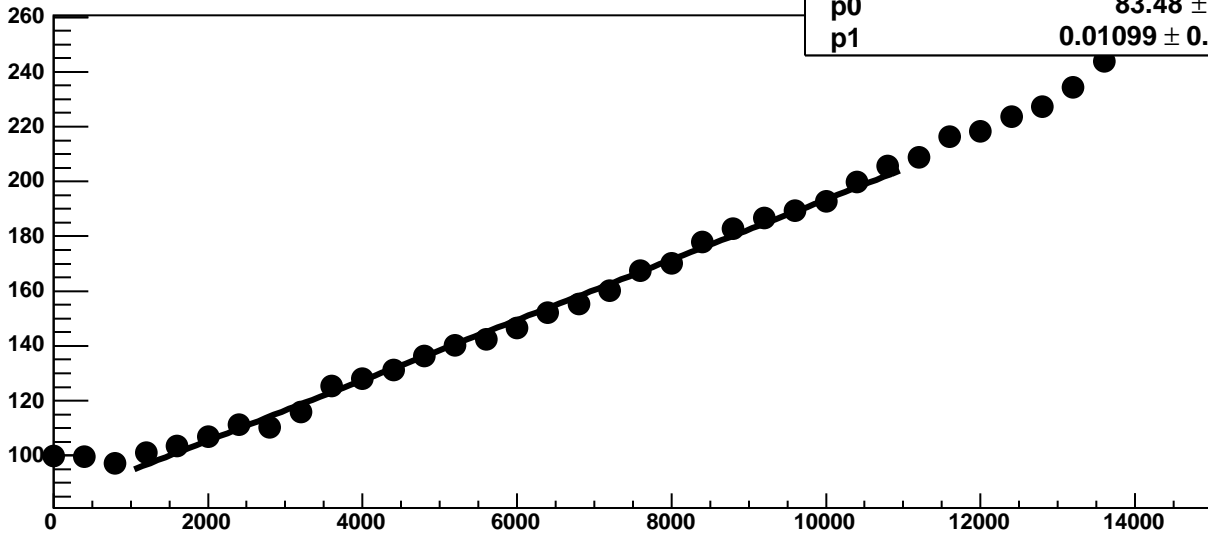
Chip 8, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



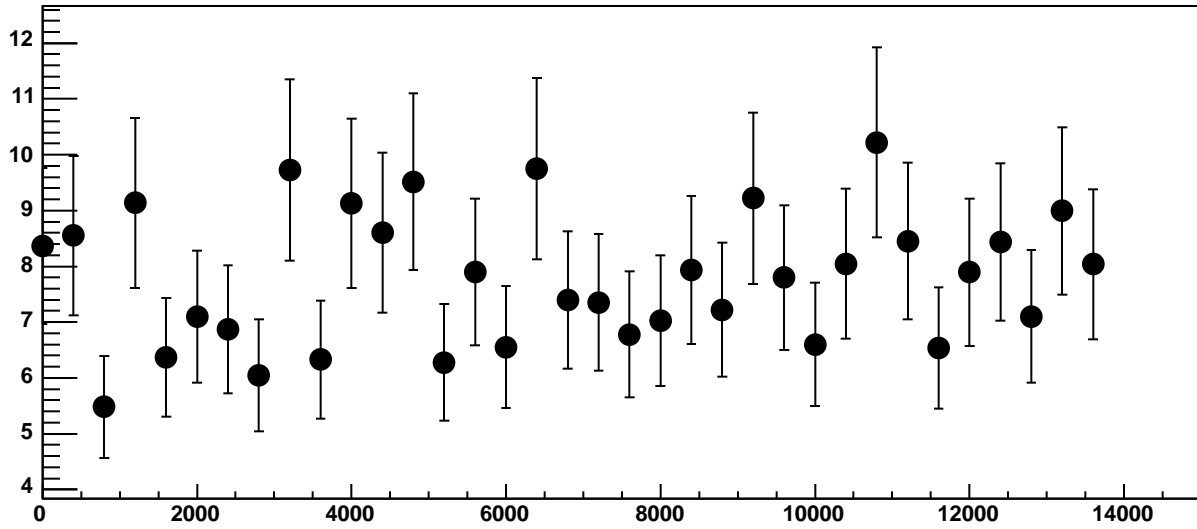
Chip 8, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



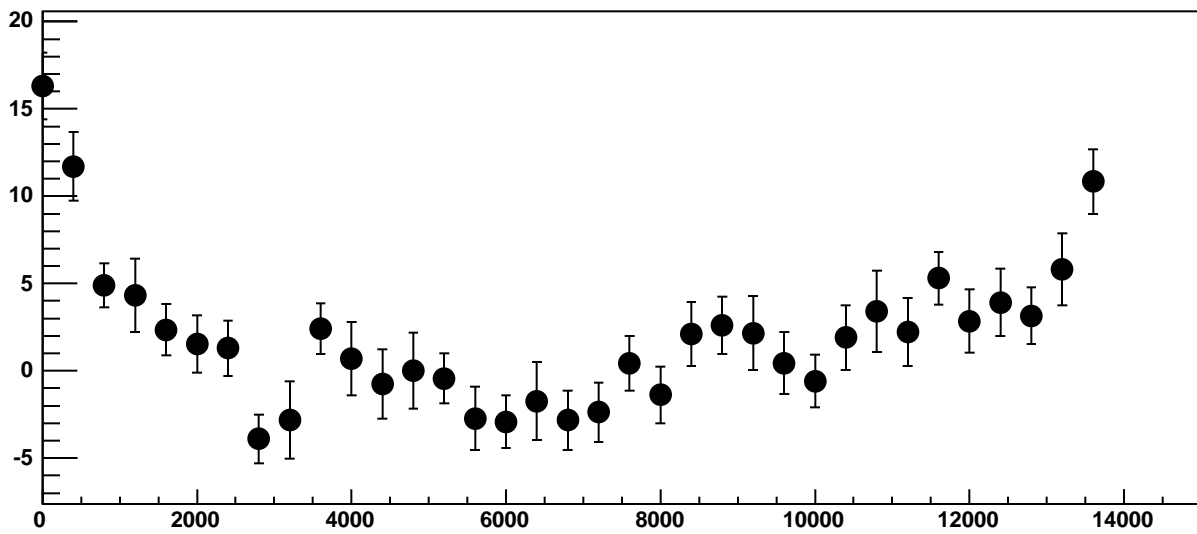
Chip 8, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



Chip 8, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

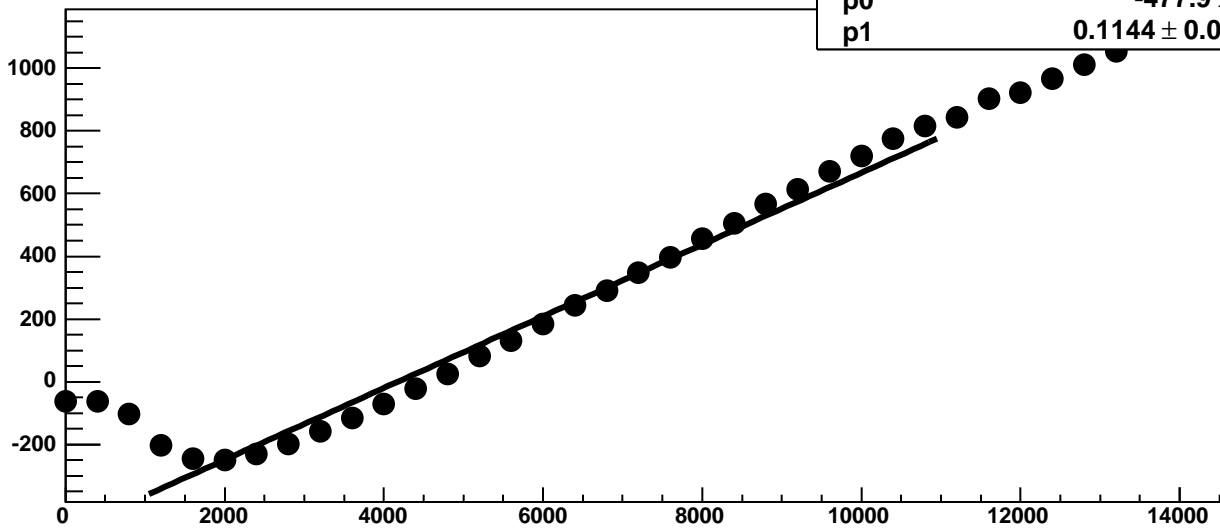


Chip 8, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

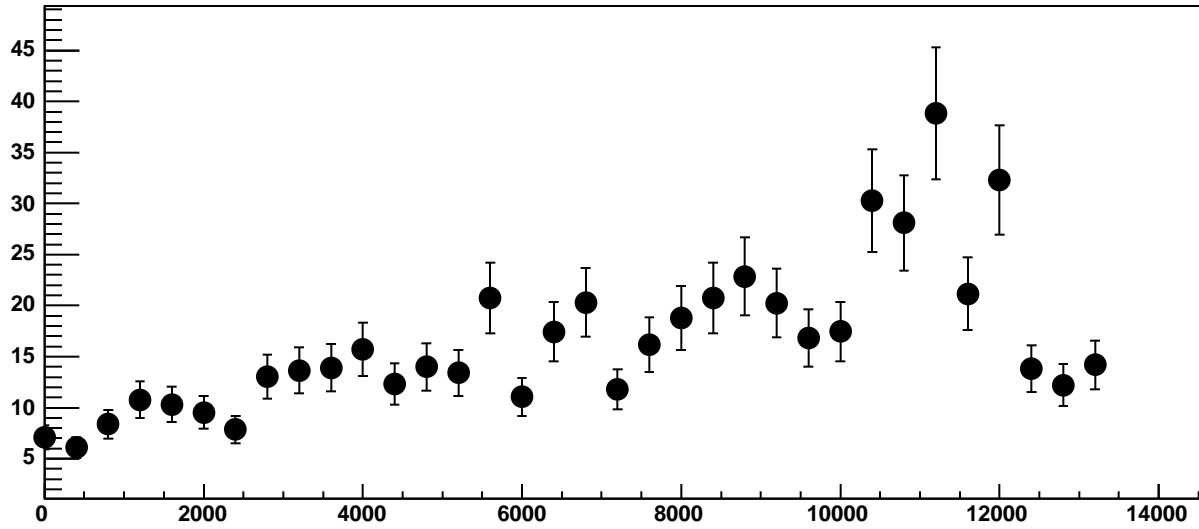




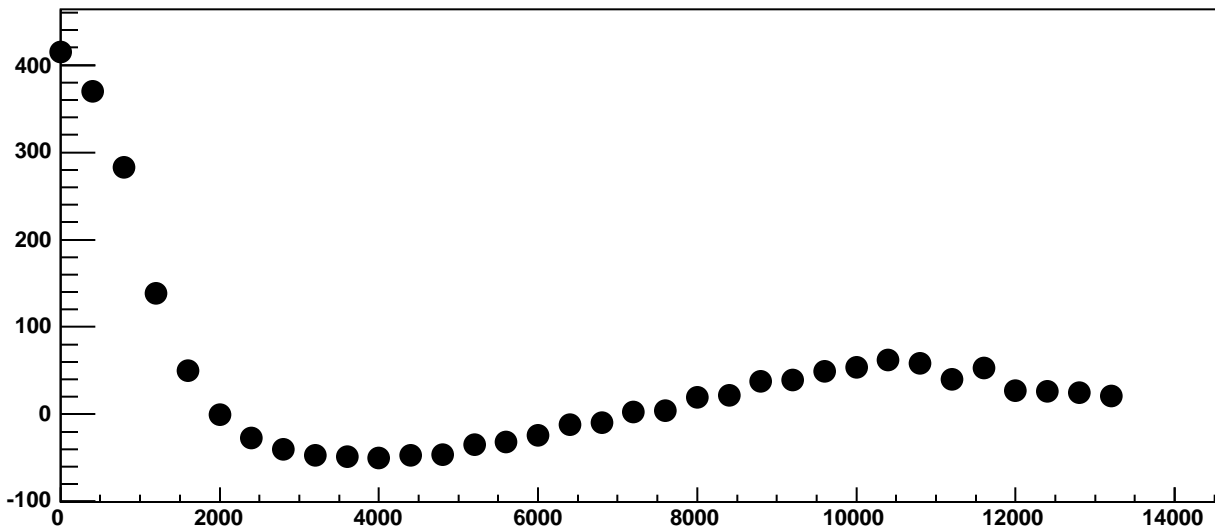
Chip 8, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

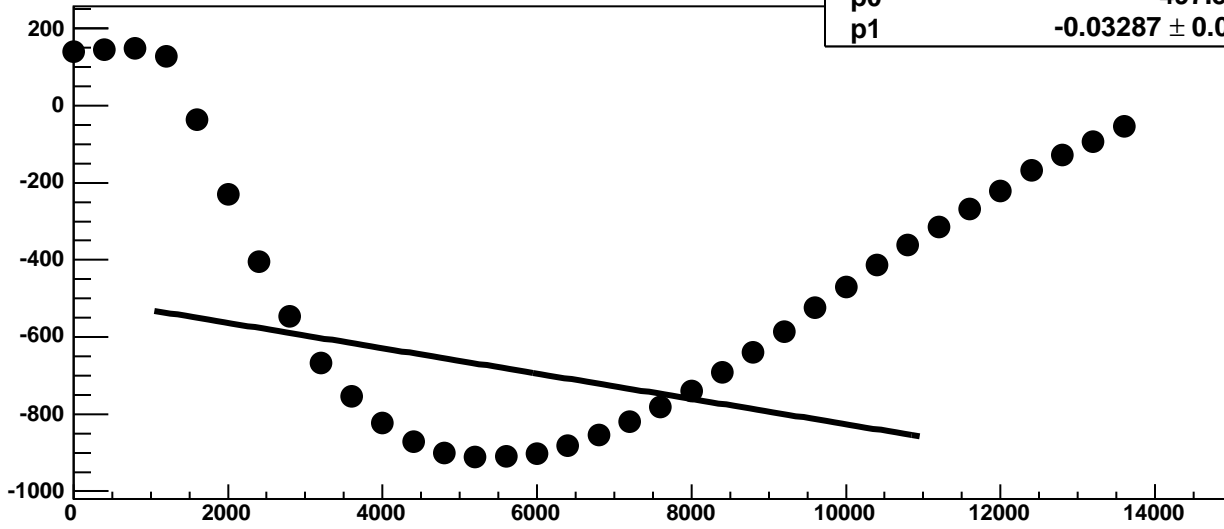


Chip 8, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

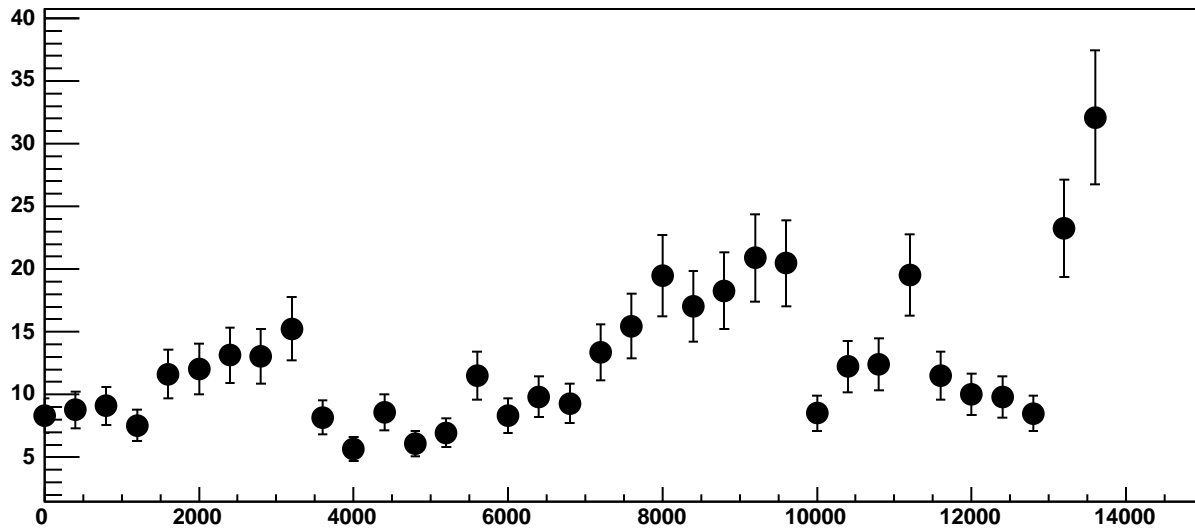


Chip 8, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

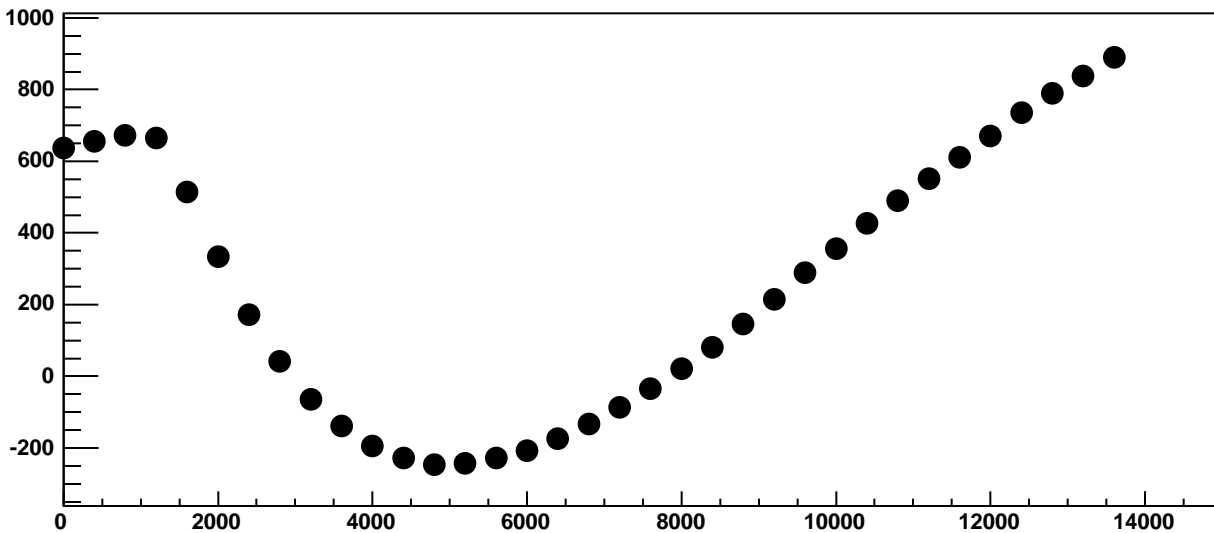
$\chi^2 / \text{ndf}$  4.233e+05 / 23  
p0 -497.5 ± 1.05  
p1 -0.03287 ± 0.0001801



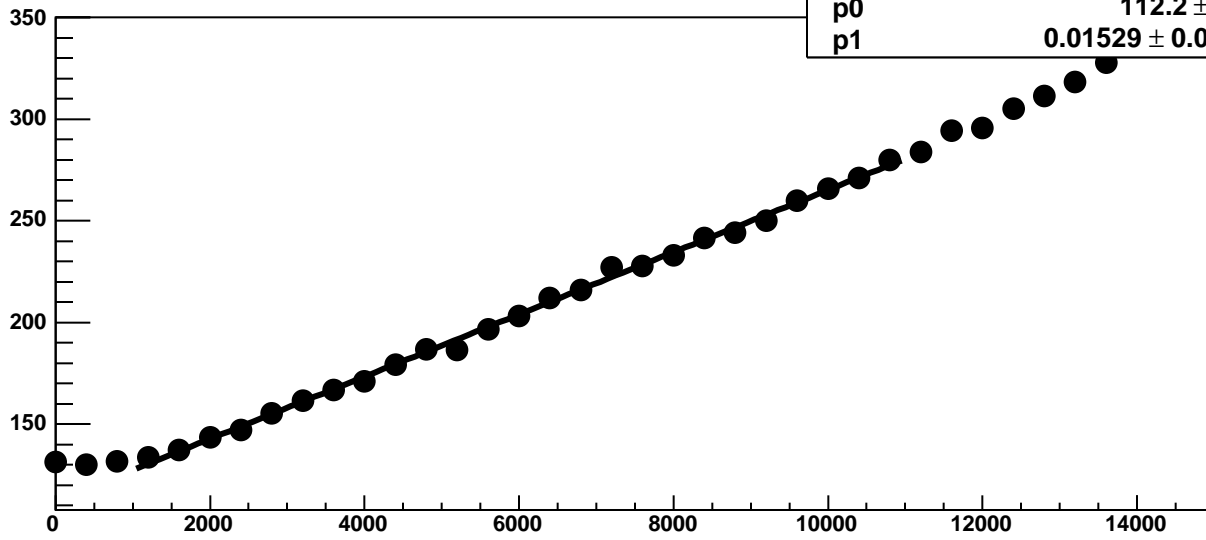
Chip 8, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 8, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

35.96 / 23

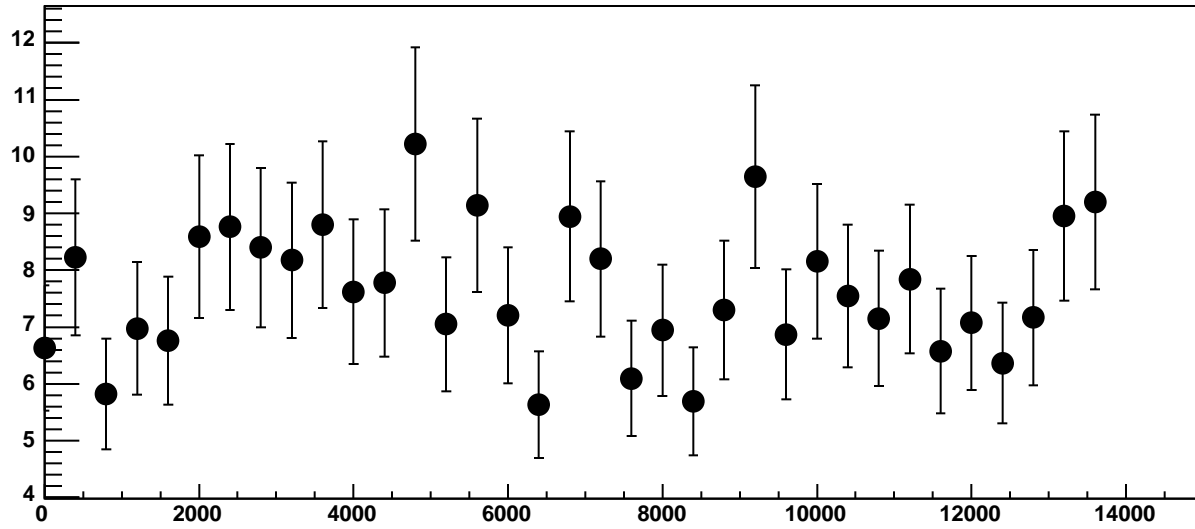
p0

$112.2 \pm 0.8172$

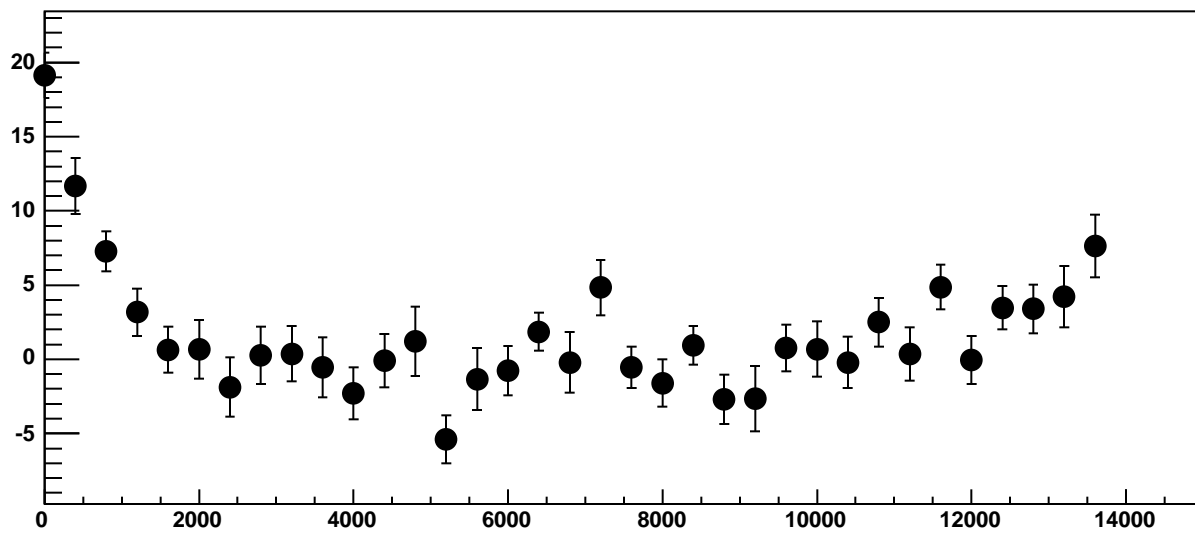
p1

$0.01529 \pm 0.0001197$

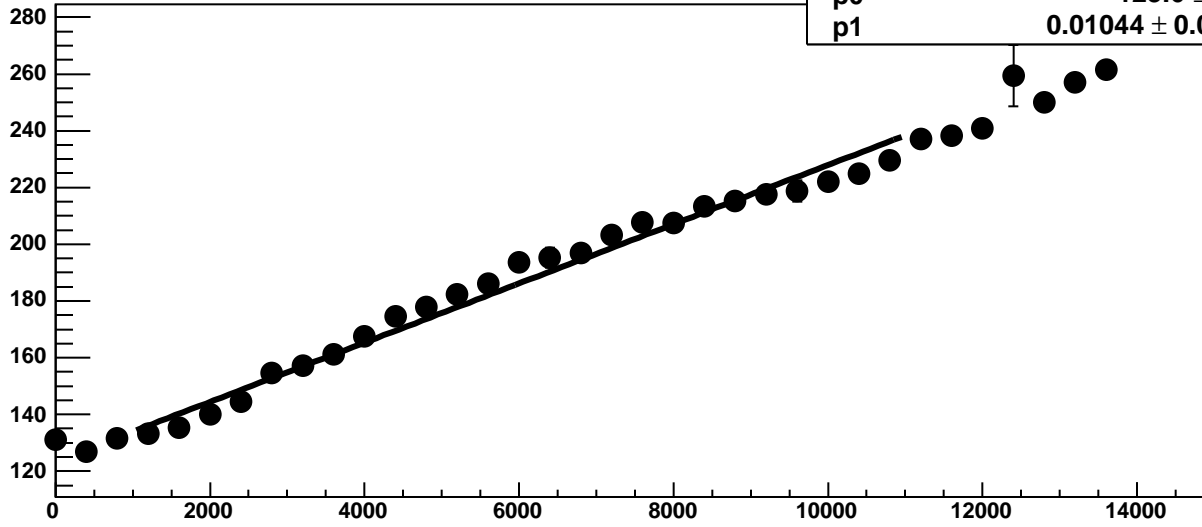
Chip 8, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



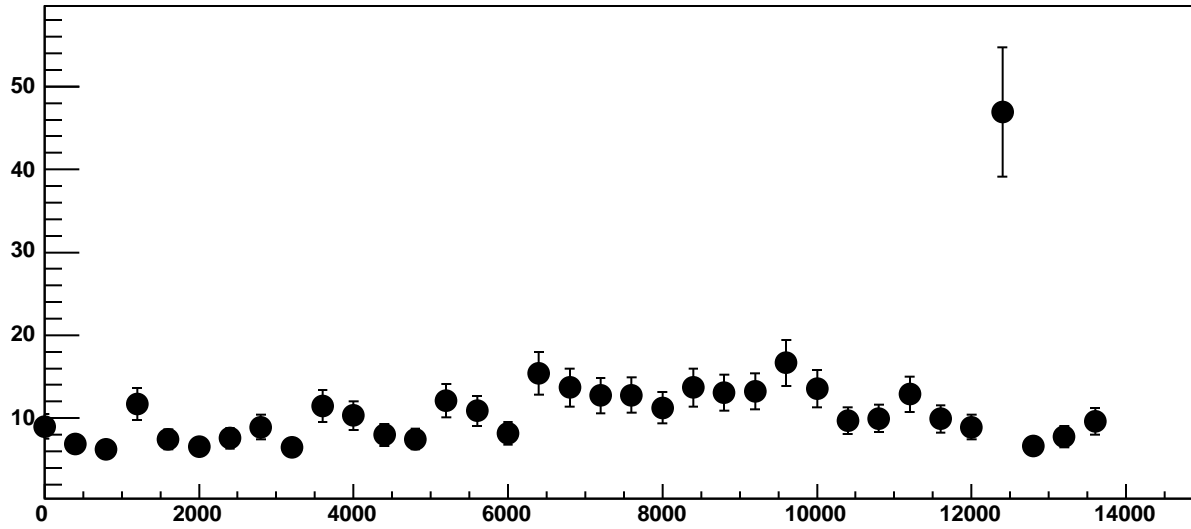
Chip 8, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



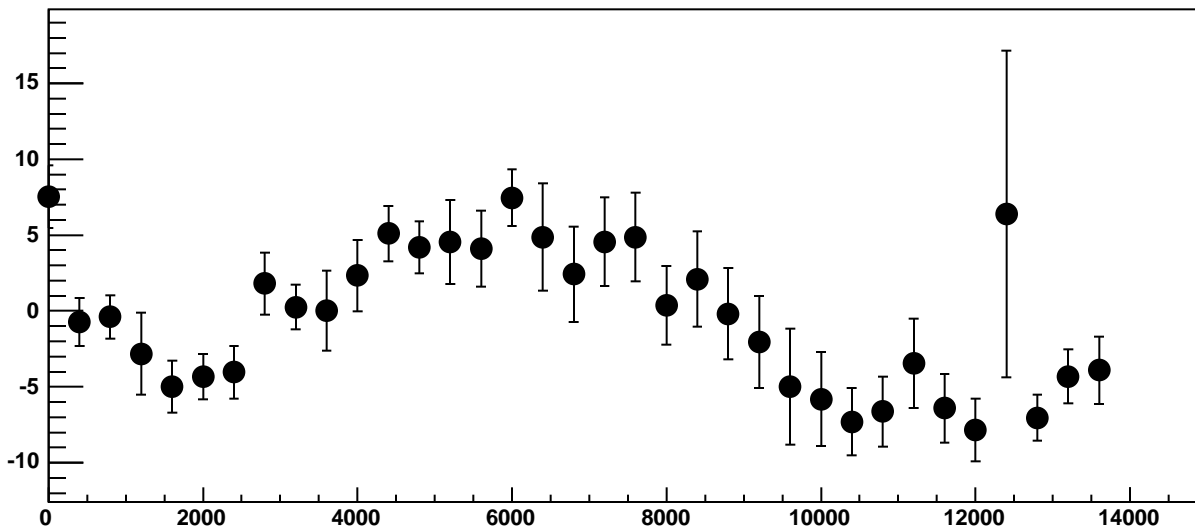
Chip 8, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



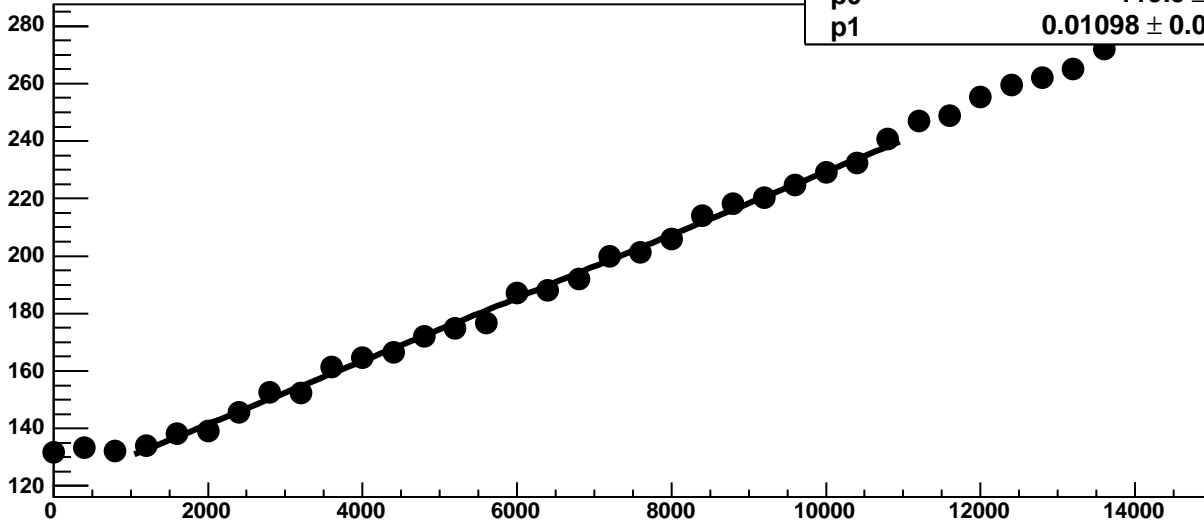
Chip 8, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

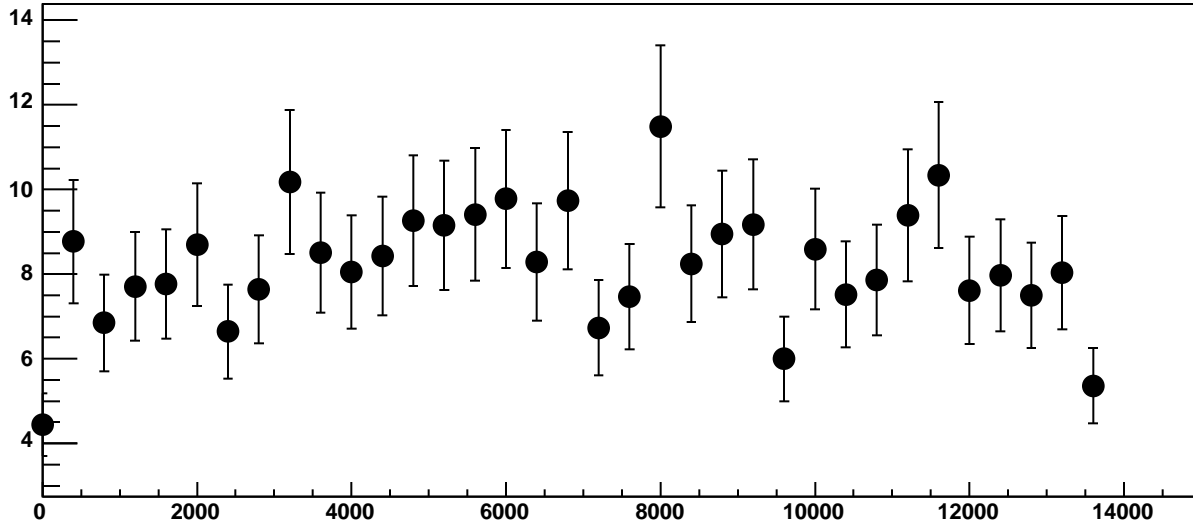


Chip 8, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

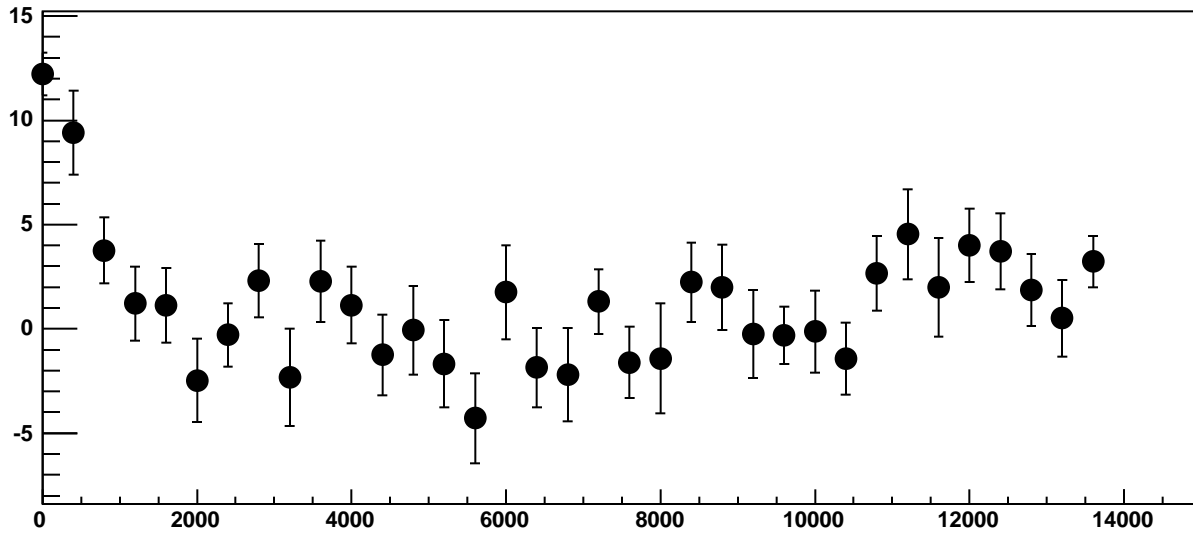


$\chi^2 / \text{ndf}$  21.59 / 23  
p0 119.5 ± 0.8433  
p1 0.01098 ± 0.0001248

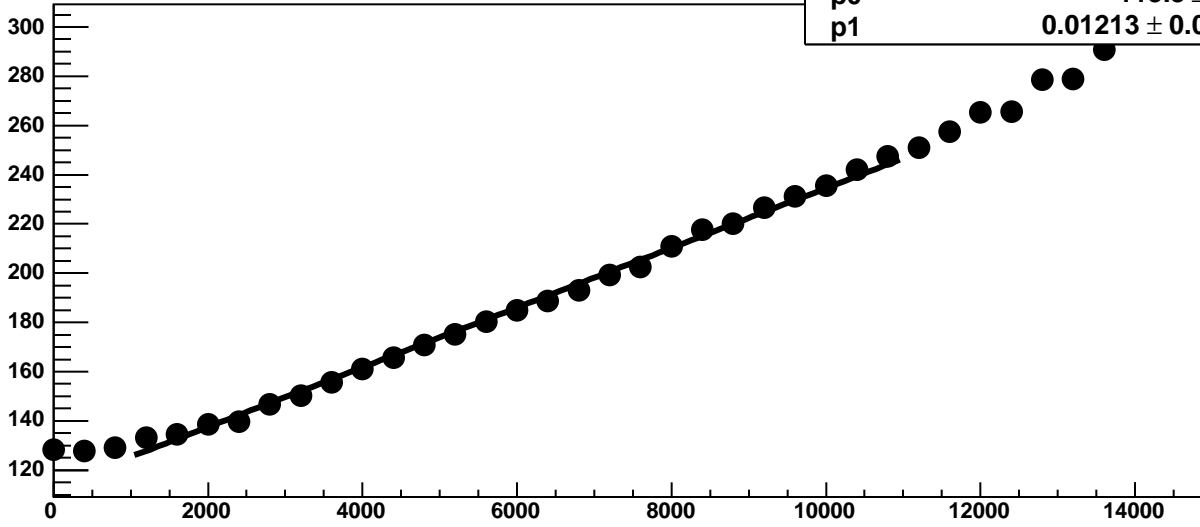
Chip 8, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

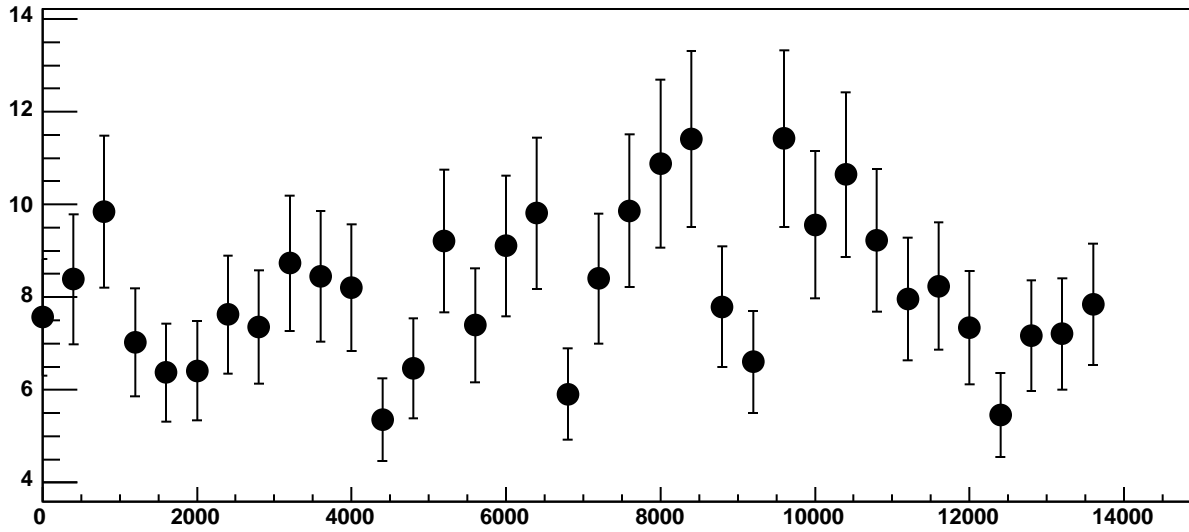


Chip 8, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

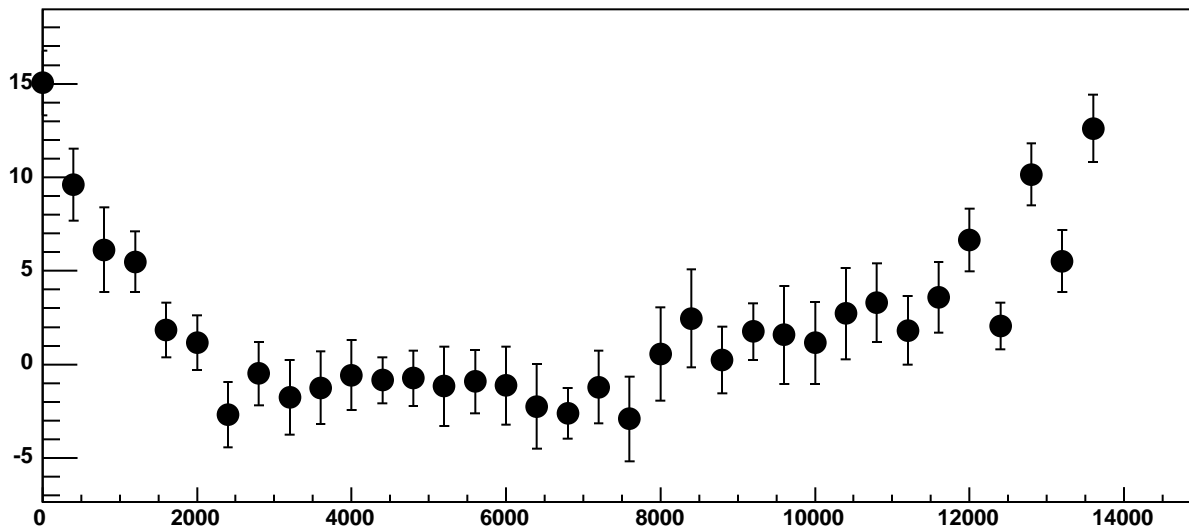


$\chi^2 / \text{ndf}$  32.54 / 23  
p0 113.3 ± 0.7805  
p1 0.01213 ± 0.0001282

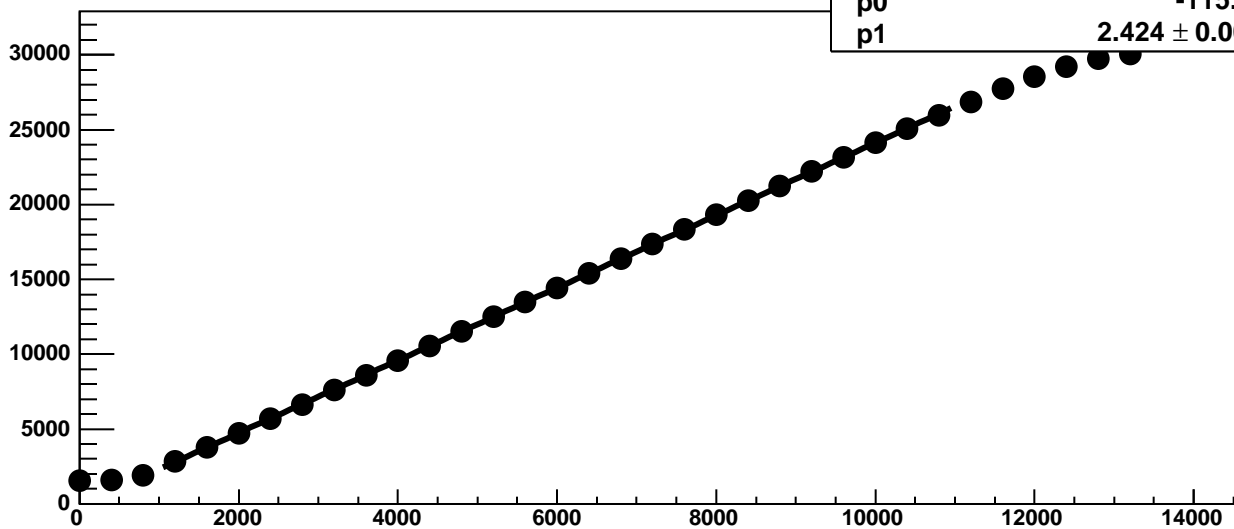
Chip 8, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

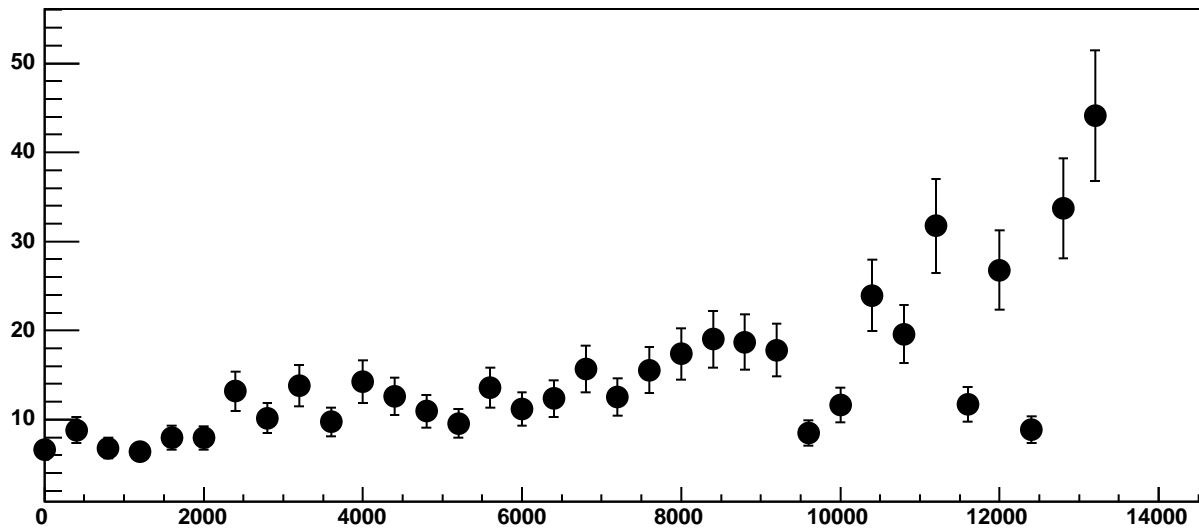


Chip 8, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

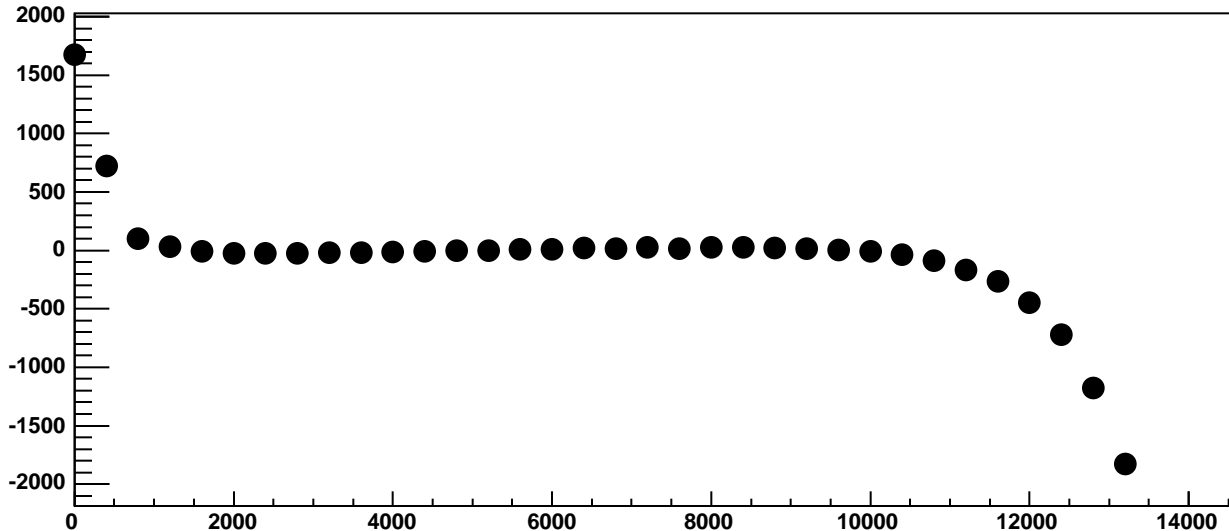


$\chi^2 / \text{ndf}$  1739 / 23  
p0  $-115.9 \pm 1$   
p1  $2.424 \pm 0.0001777$

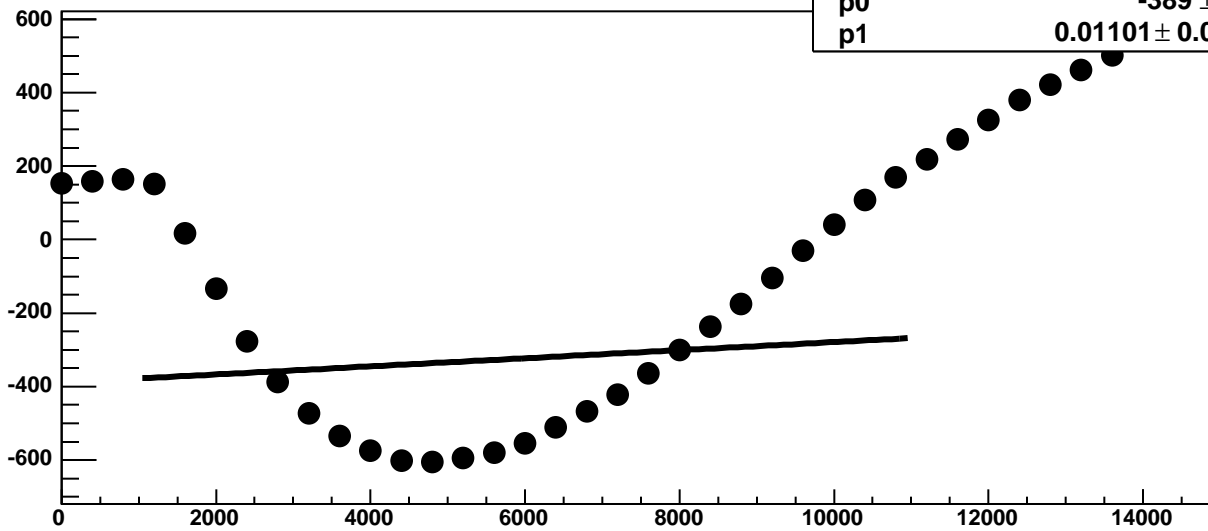
Chip 8, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

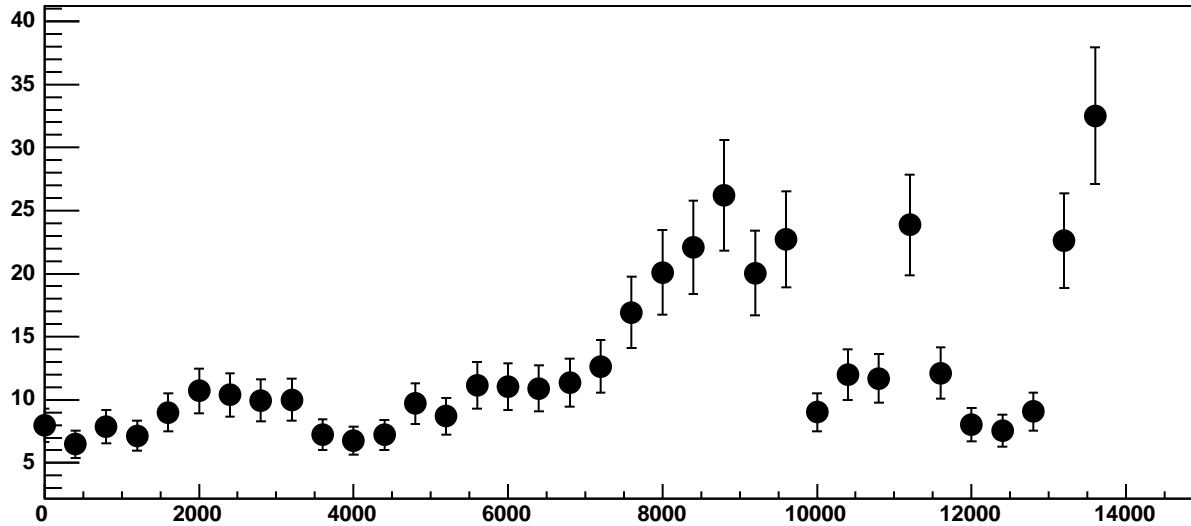


Chip 8, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

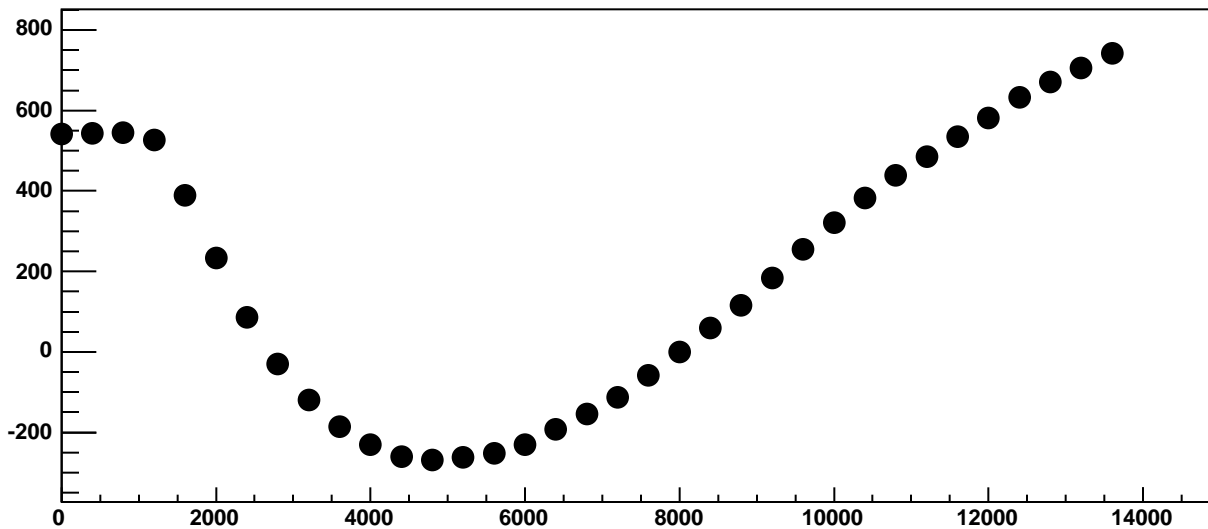


$\chi^2 / \text{ndf}$  3.47e+05 / 23  
p0 -389 ± 0.9778  
p1 0.01101 ± 0.0001746

Chip 8, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

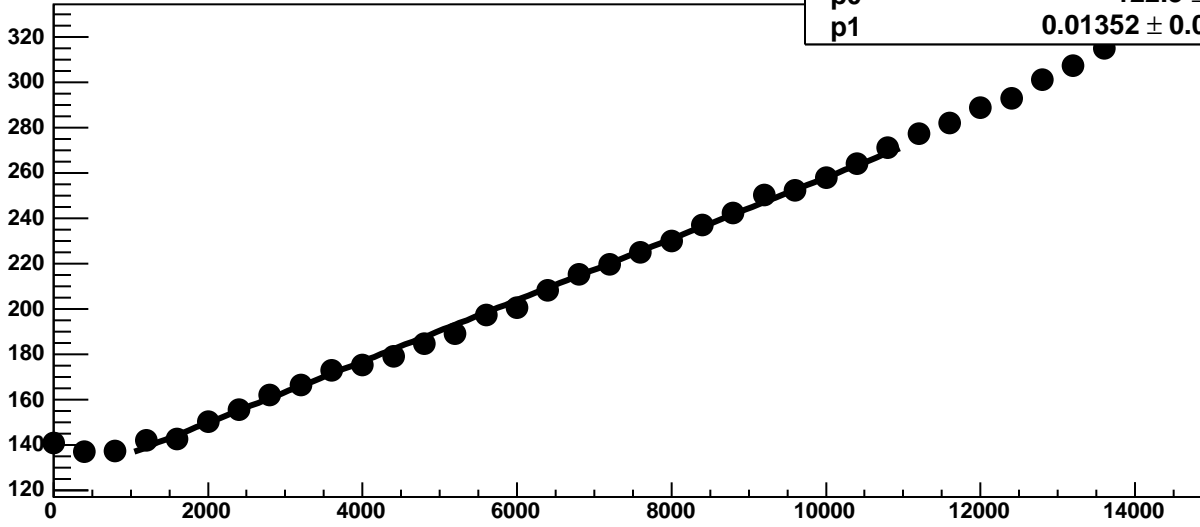


Chip 8, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



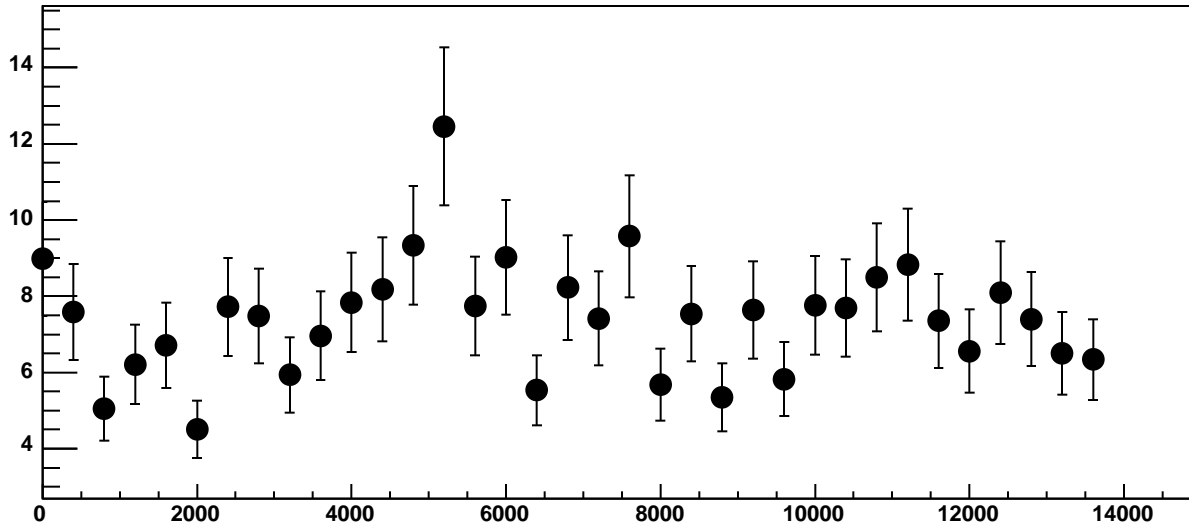


Chip 8, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

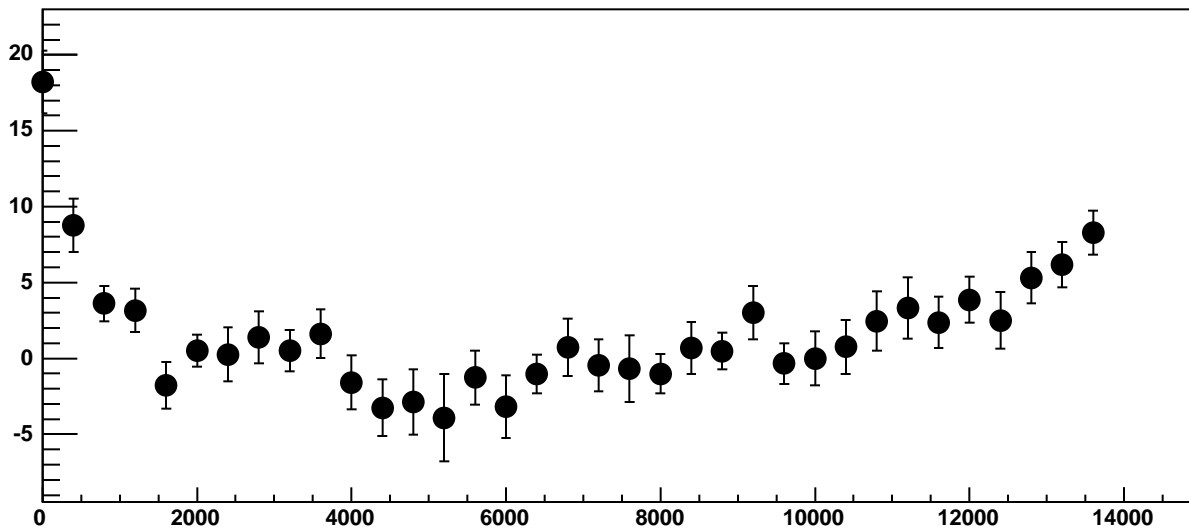


$\chi^2 / \text{ndf}$  25.31 / 23  
p0  $122.8 \pm 0.6908$   
p1  $0.01352 \pm 0.0001059$

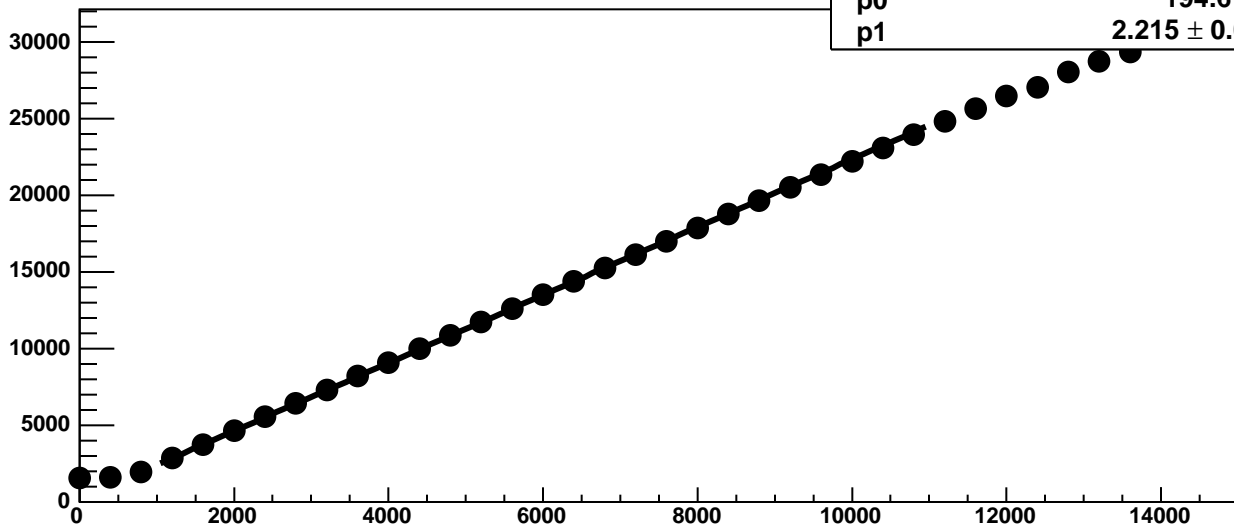
Chip 8, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



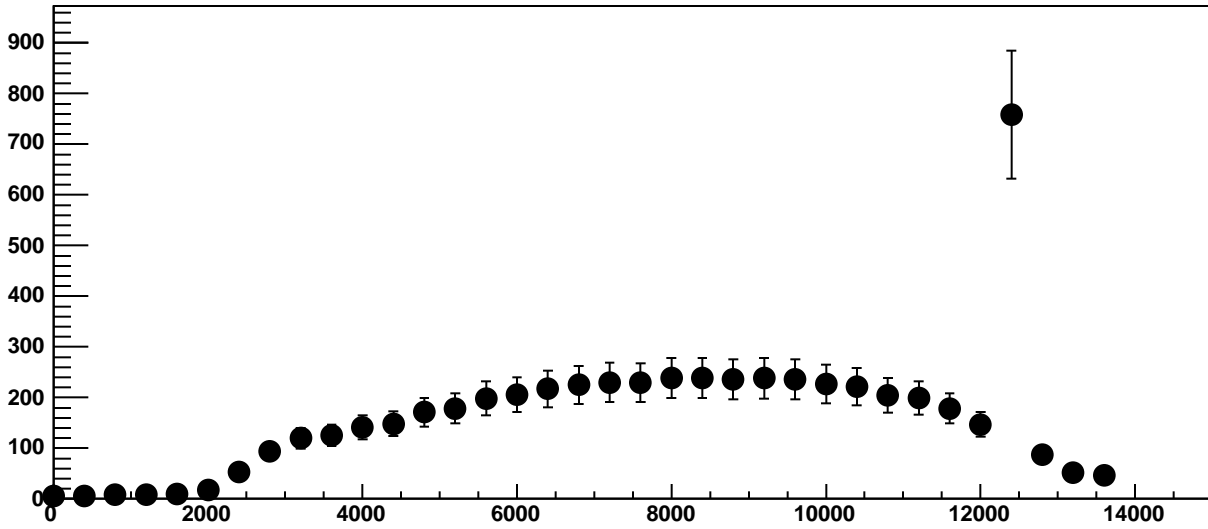
Chip 8, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



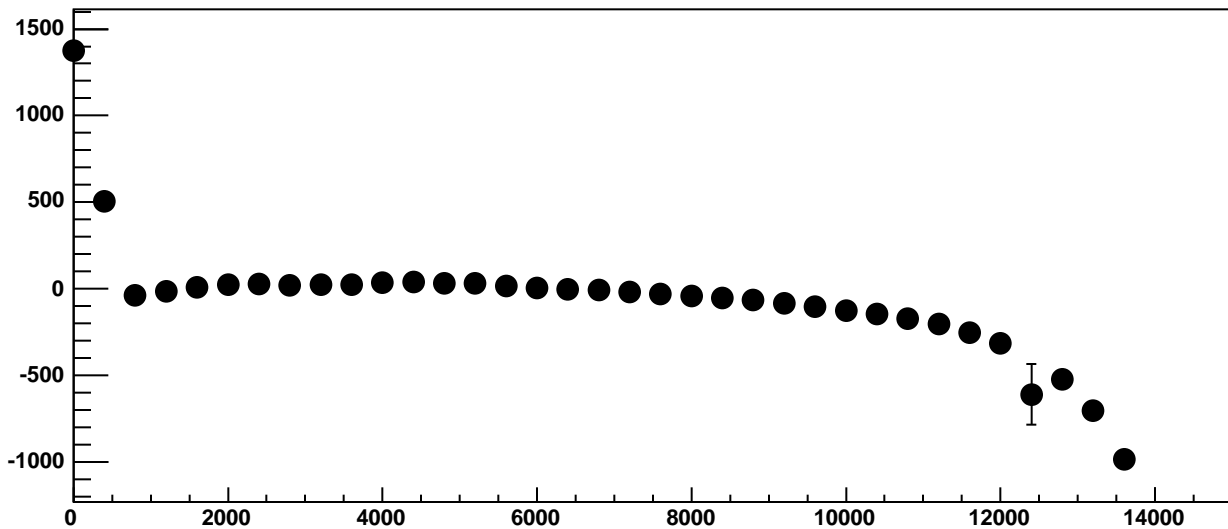
Chip 8, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



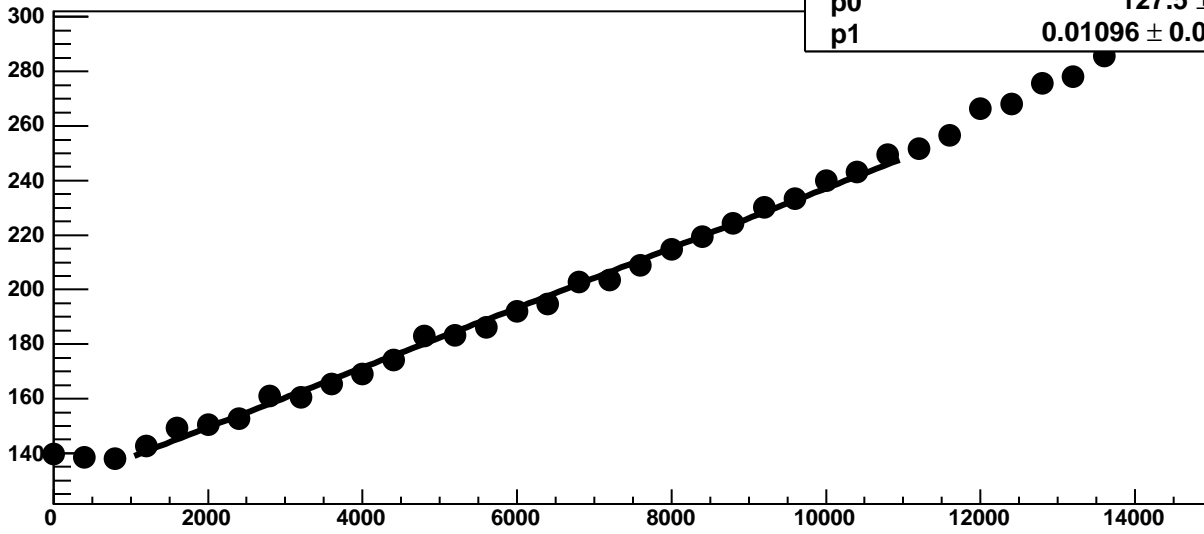
Chip 8, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

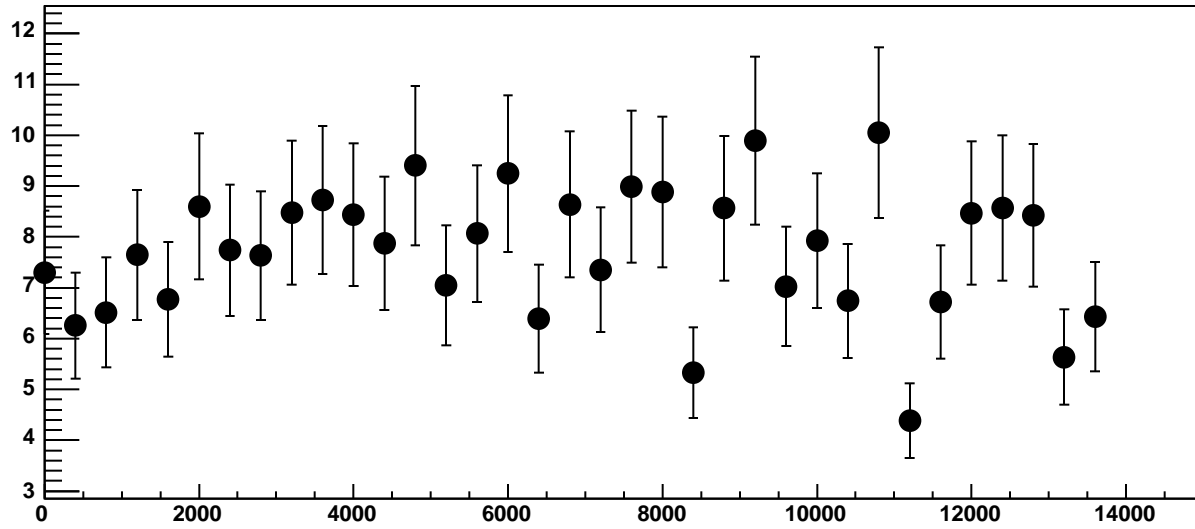


Chip 8, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

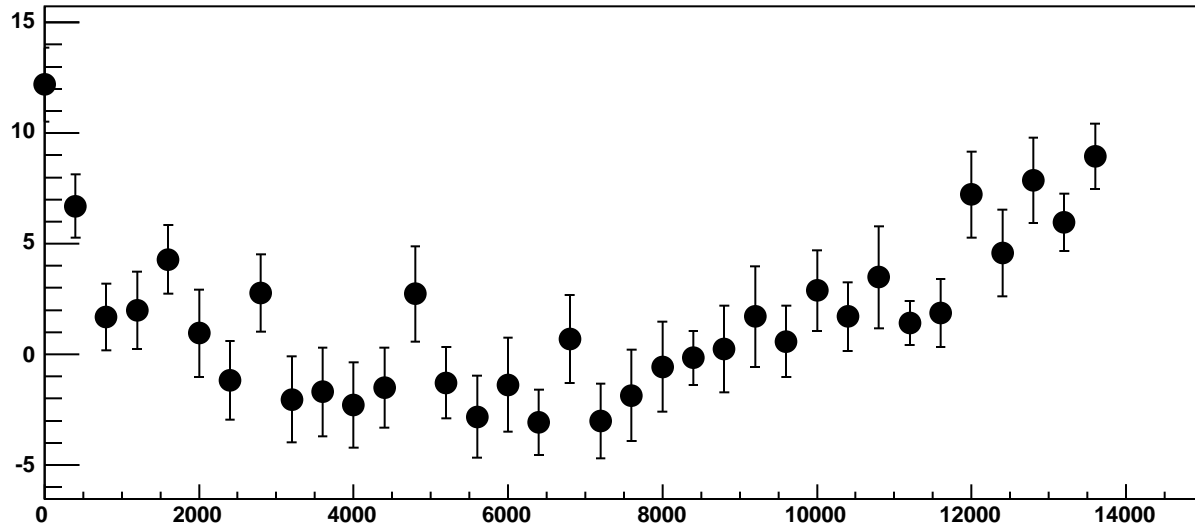


$\chi^2 / \text{ndf}$  36.26 / 23  
p0 127.5 ± 0.8281  
p1 0.01096 ± 0.0001236

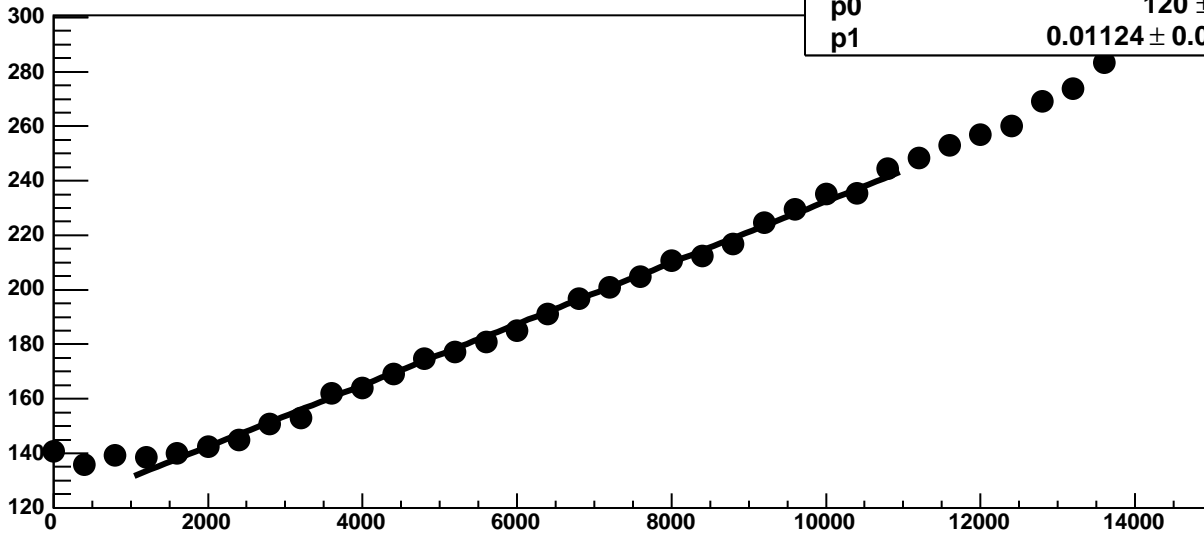
Chip 8, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

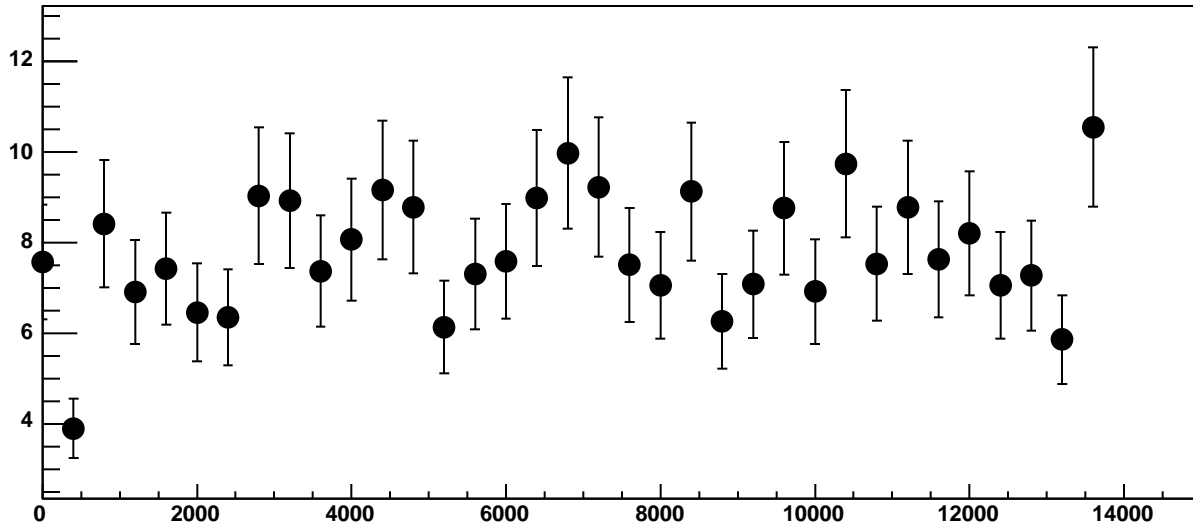


Chip 8, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

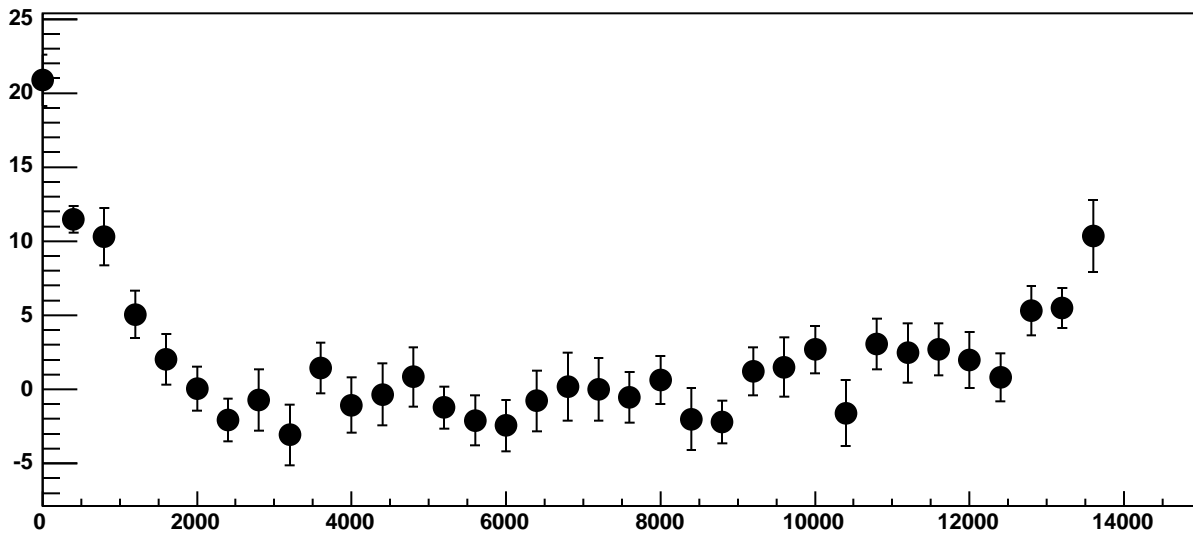


$\chi^2 / \text{ndf}$  32.77 / 23  
p0  $120 \pm 0.7811$   
p1  $0.01124 \pm 0.0001189$

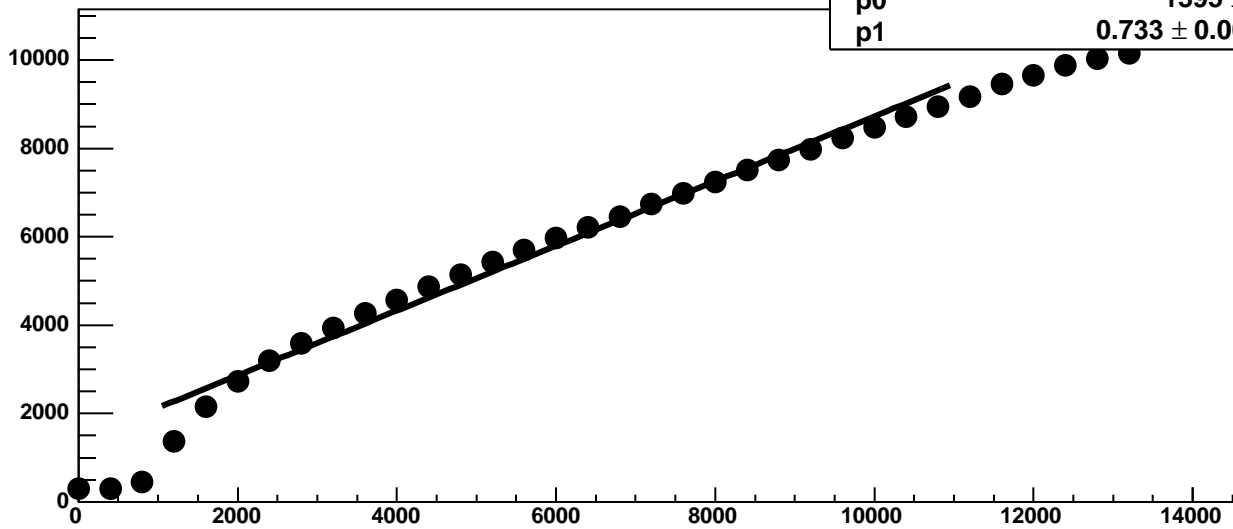
Chip 8, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



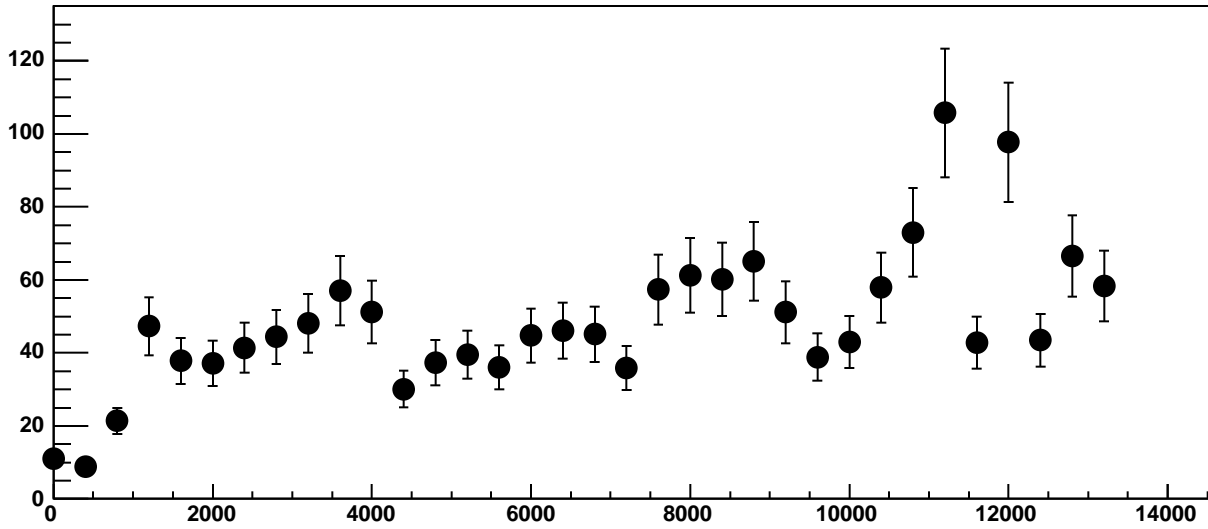
Chip 8, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC



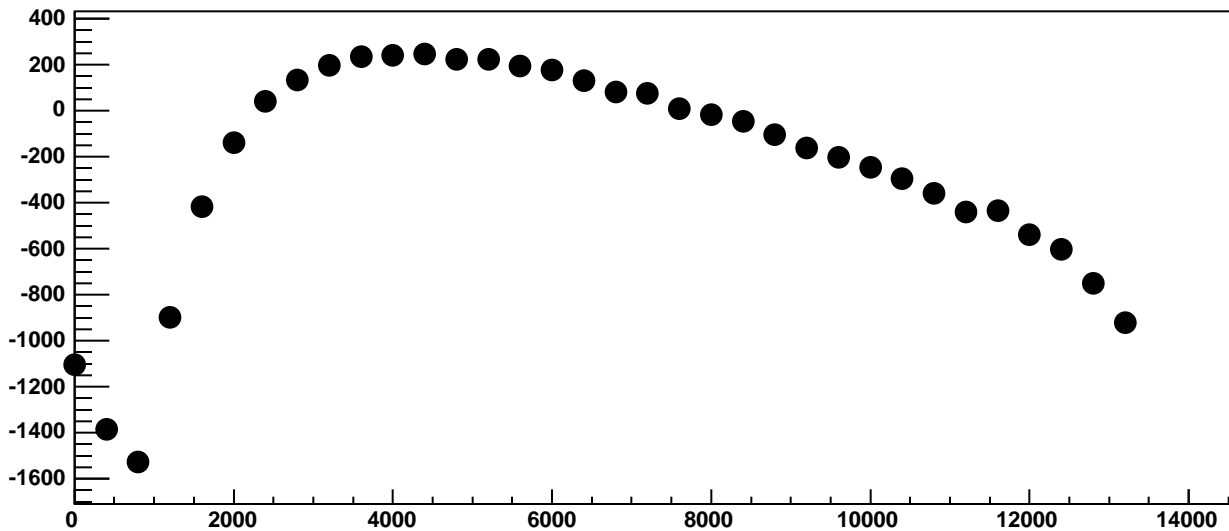
Chip 8, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC



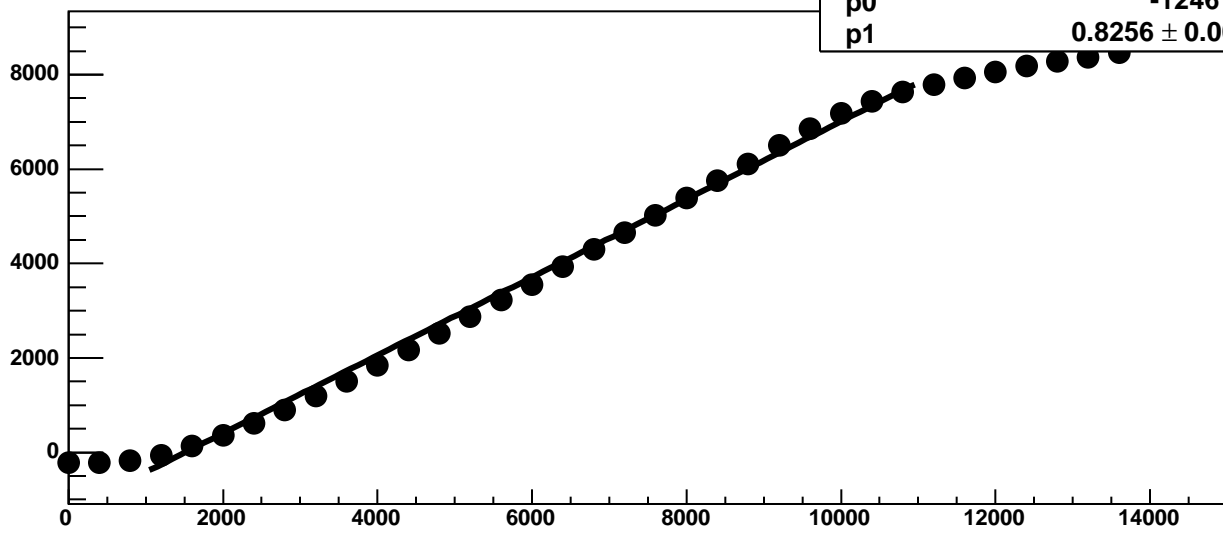
Chip 8, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC



Chip 8, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

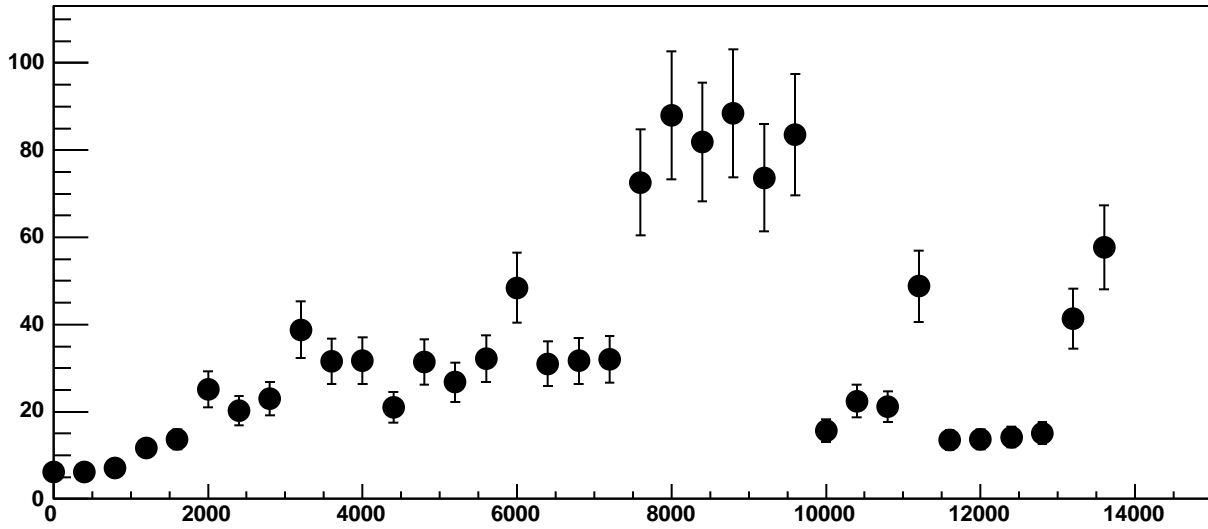


Chip 9, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

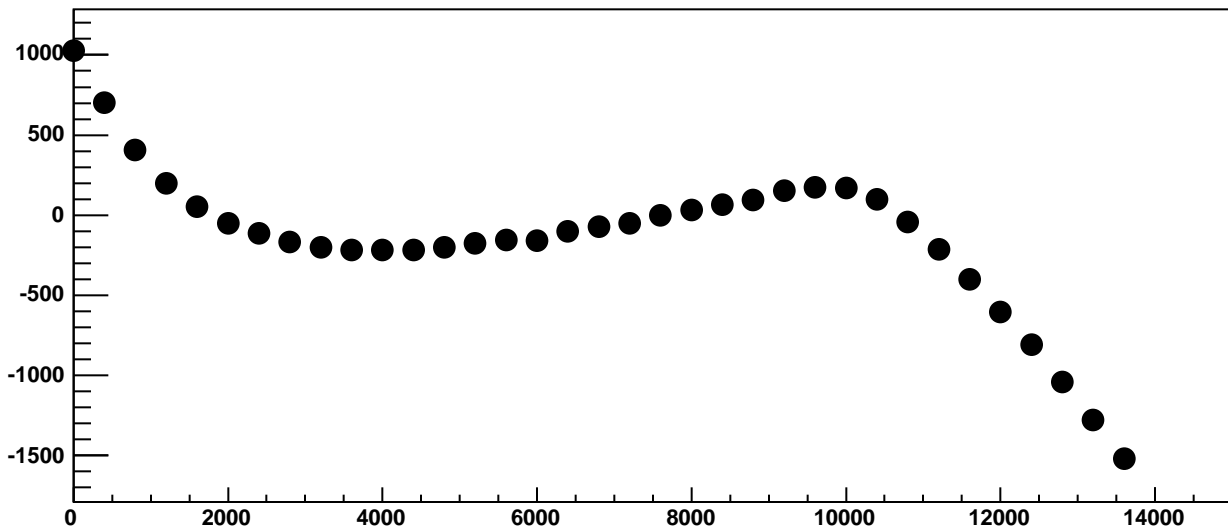


$\chi^2 / \text{ndf}$  1.721e+04 / 23  
p0 -1246 ± 1.951  
p1 0.8256 ± 0.0003357

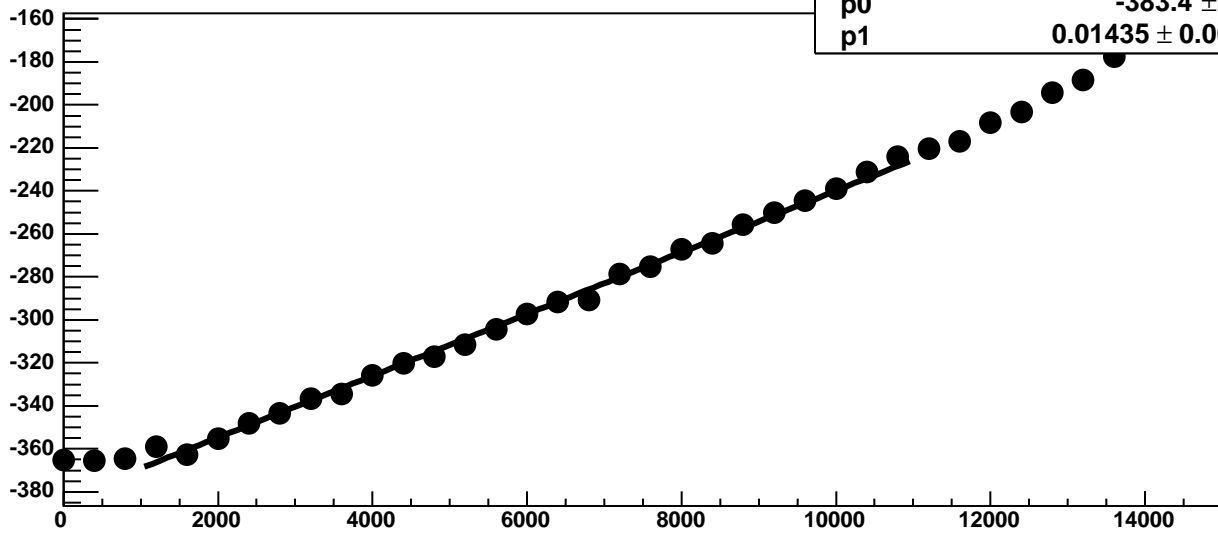
Chip 9, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

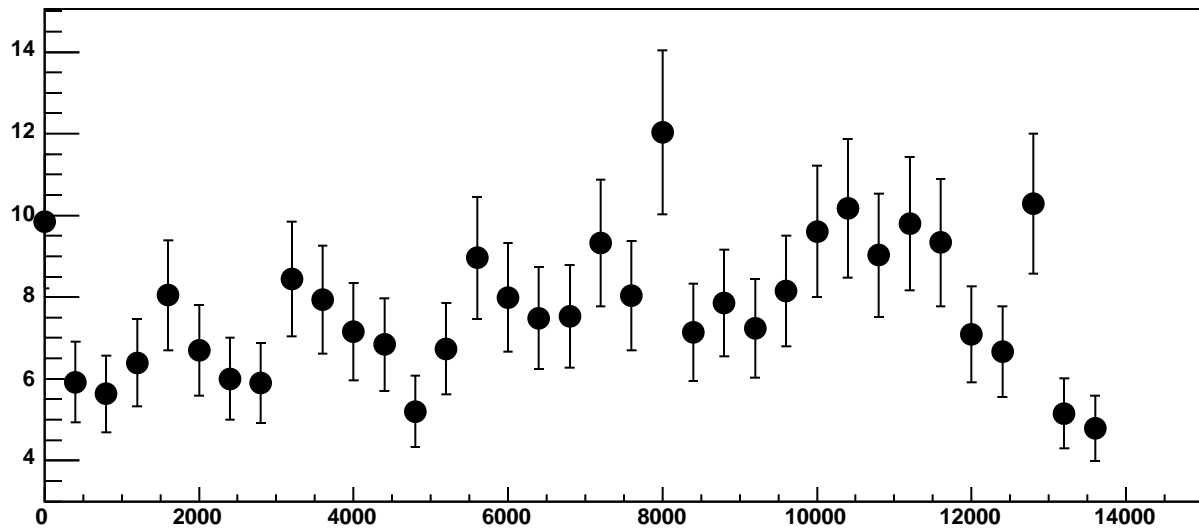


Chip 9, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

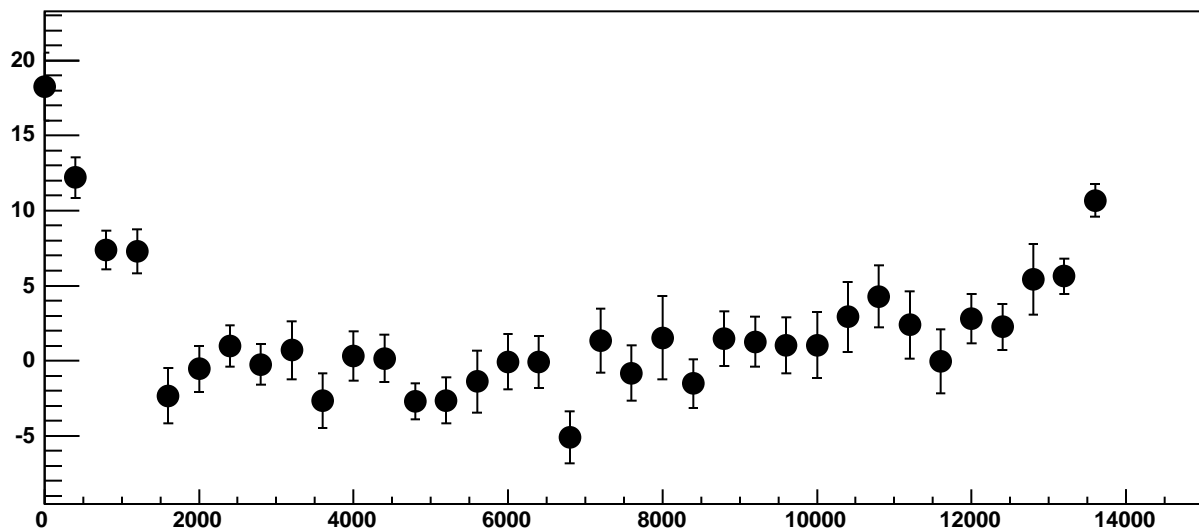


$\chi^2 / \text{ndf}$  55.88 / 23  
p0 -383.4 ± 0.7482  
p1 0.01435 ± 0.0001226

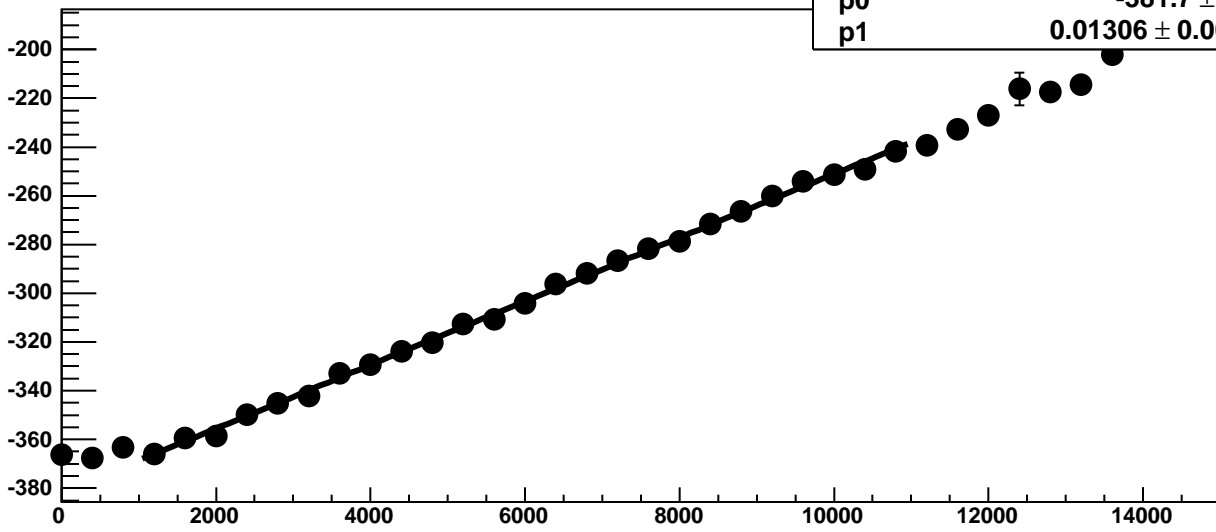
Chip 9, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



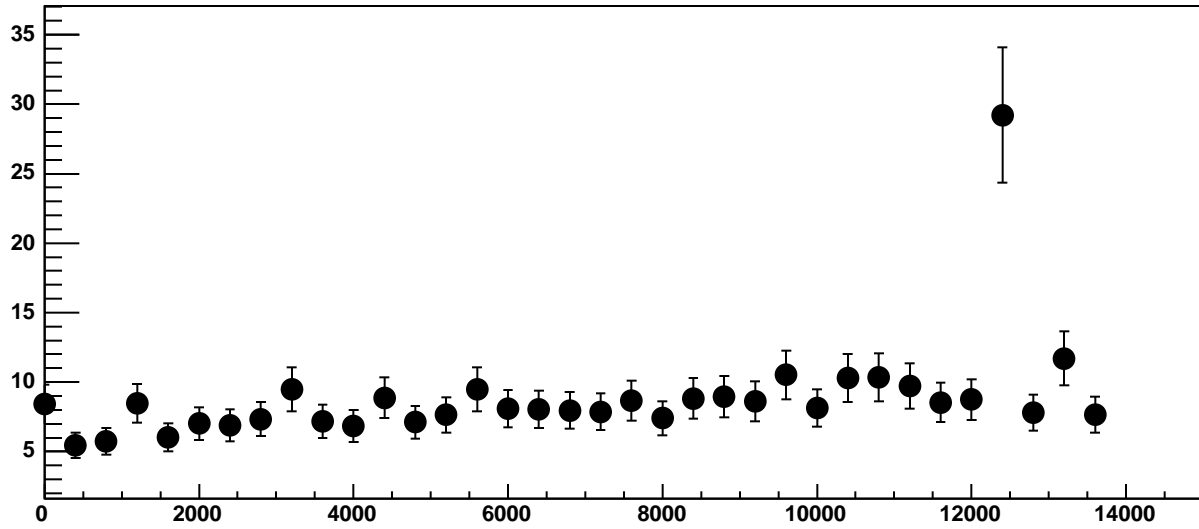
Chip 9, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



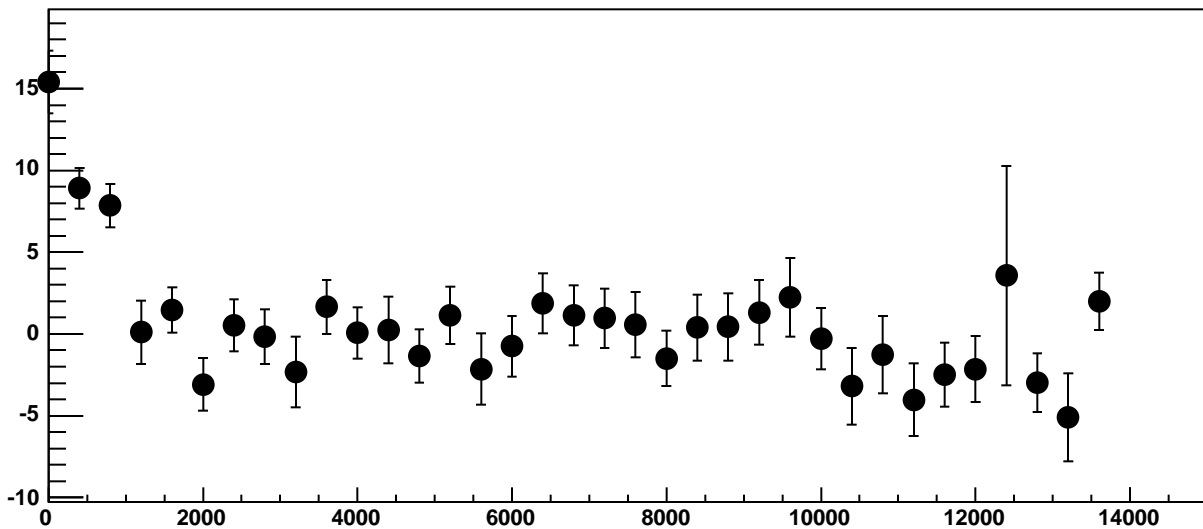
Chip 9, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



Chip 9, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

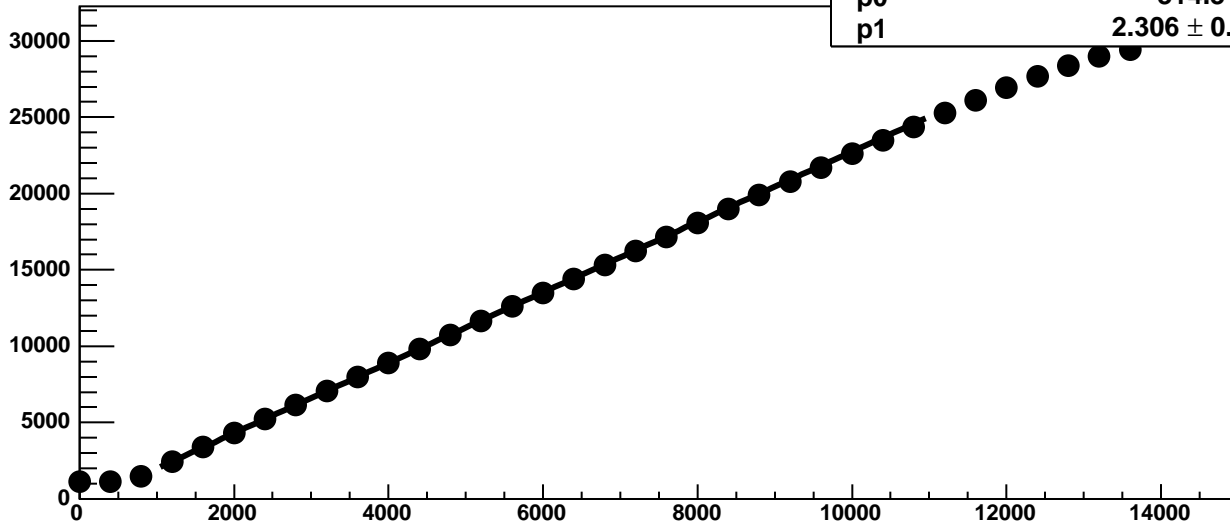


Chip 9, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC



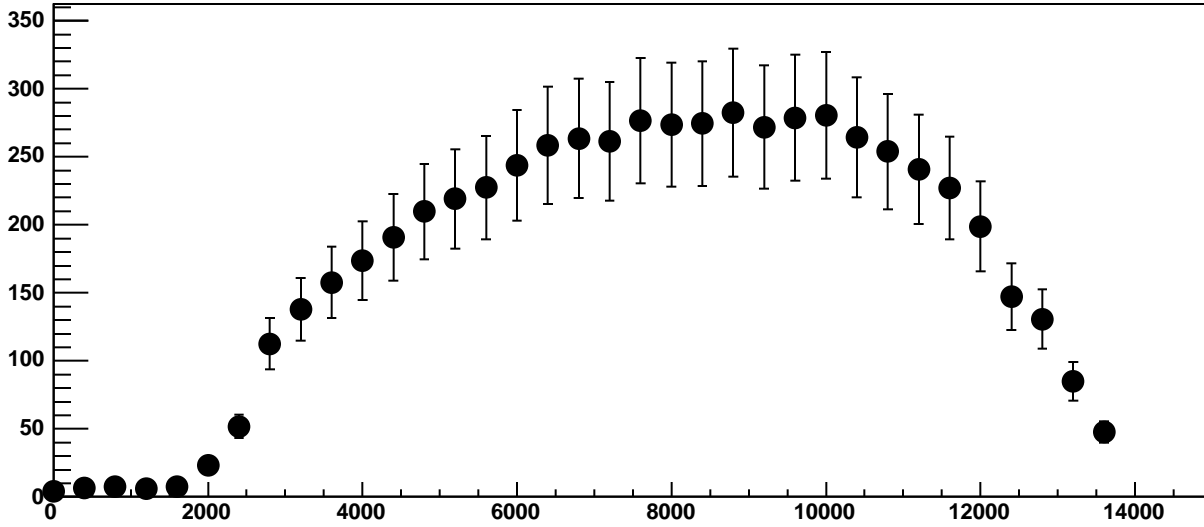


Chip 9, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC

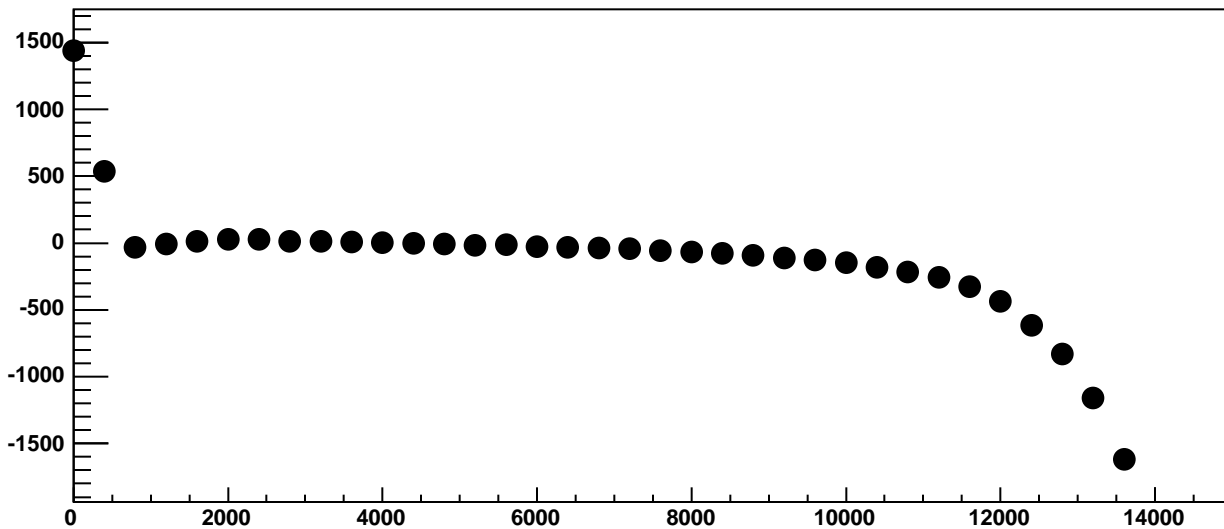


$\chi^2 / \text{ndf}$  161.4 / 23  
p0  $-314.3 \pm 2.979$   
p1  $2.306 \pm 0.001944$

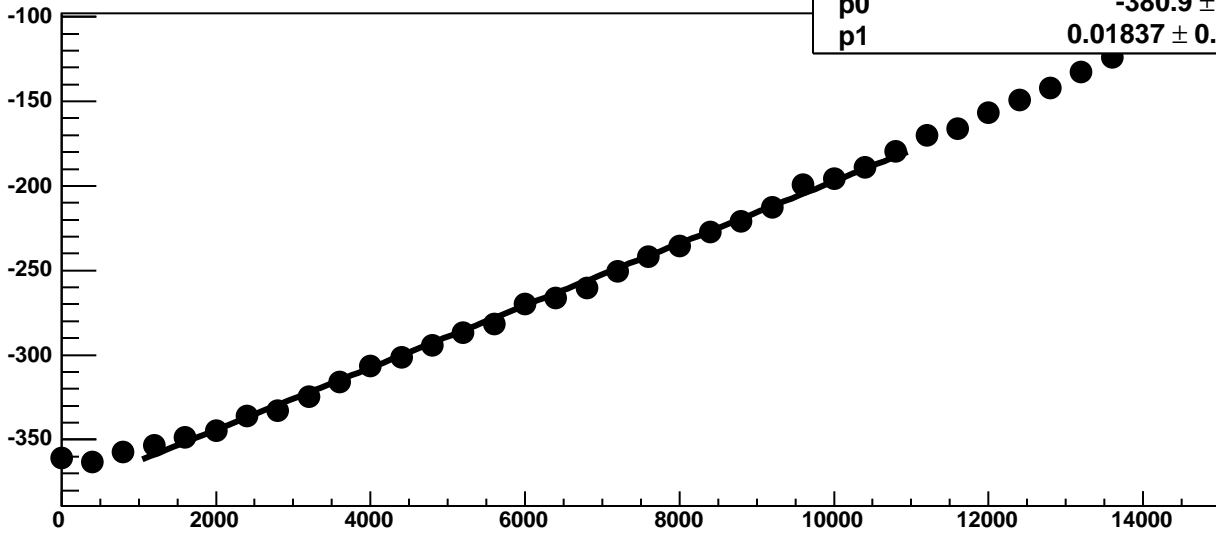
Chip 9, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



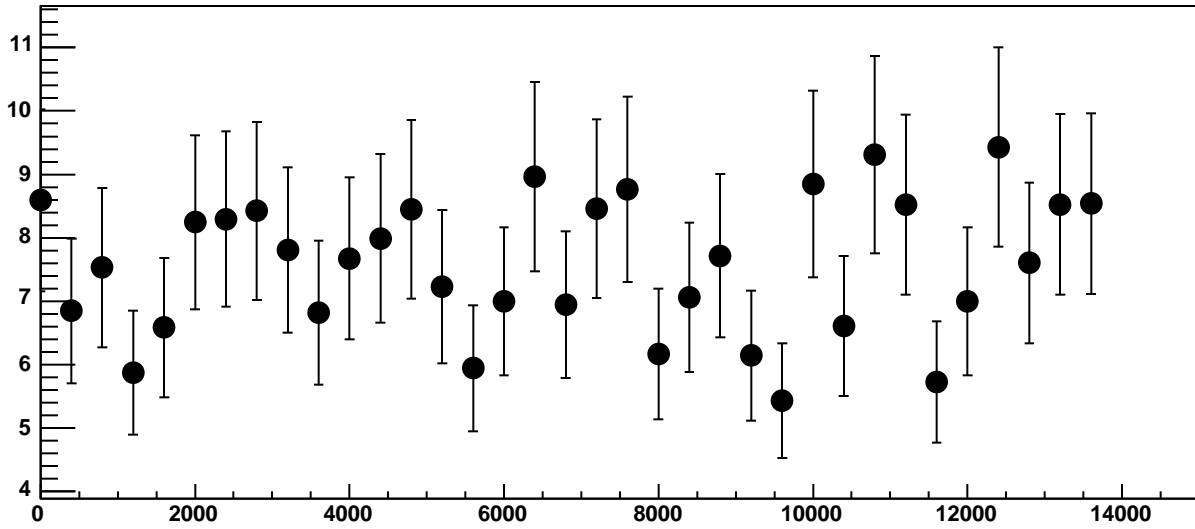
Chip 9, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC



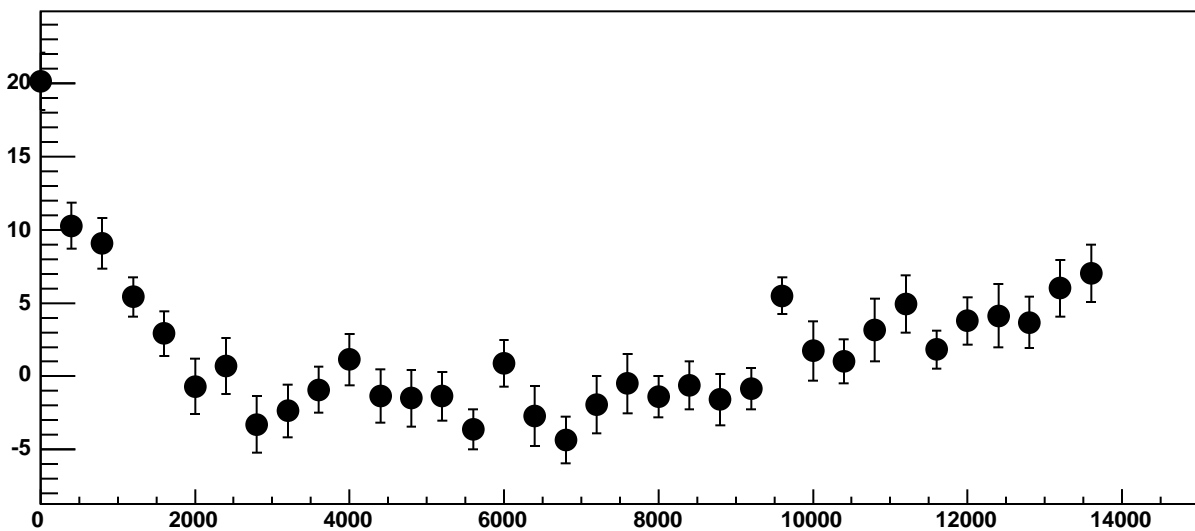
Chip 9, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



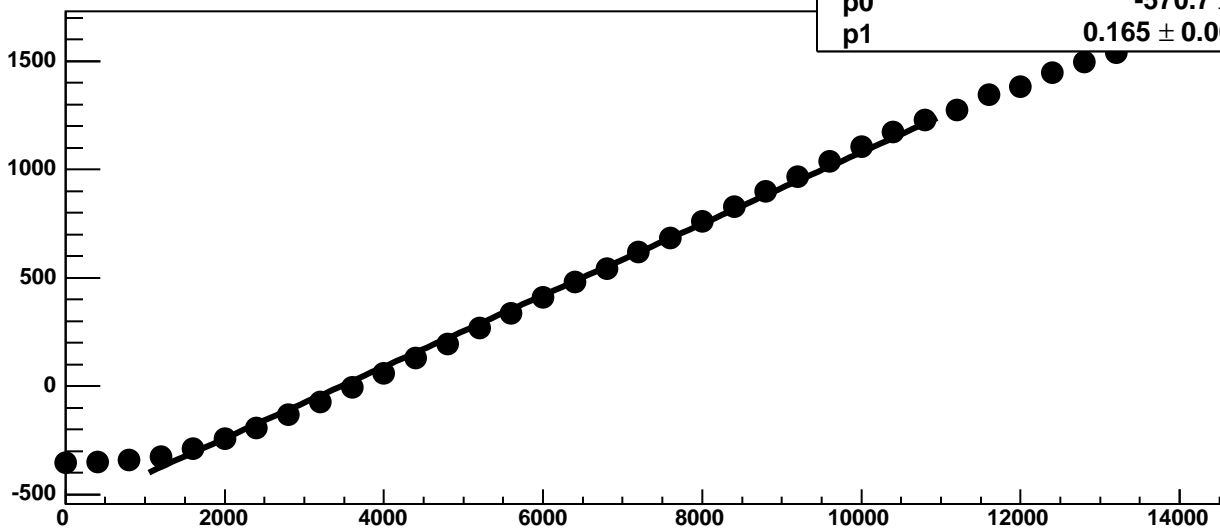
Chip 9, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



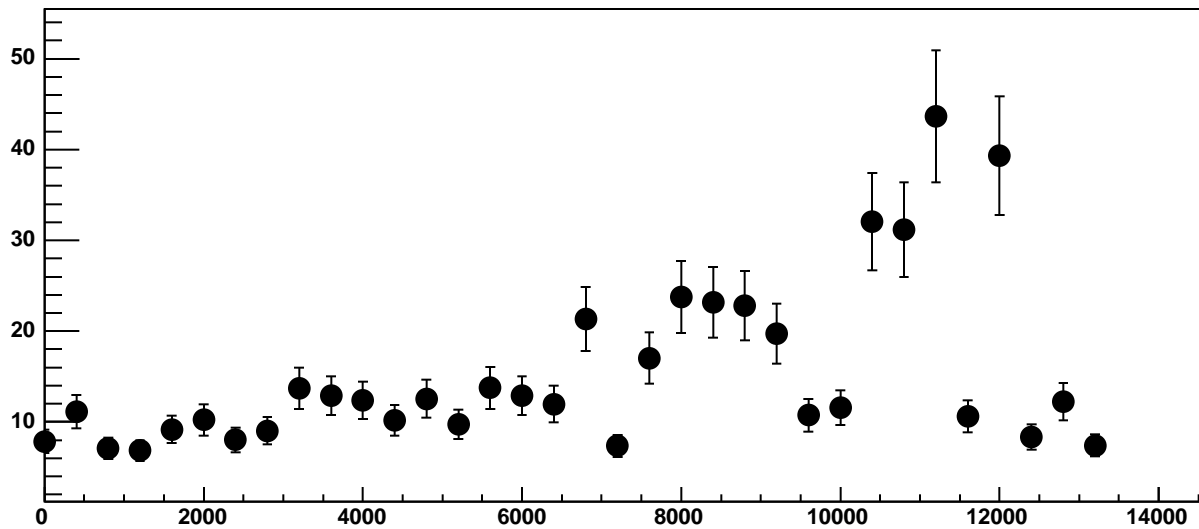
Chip 9, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



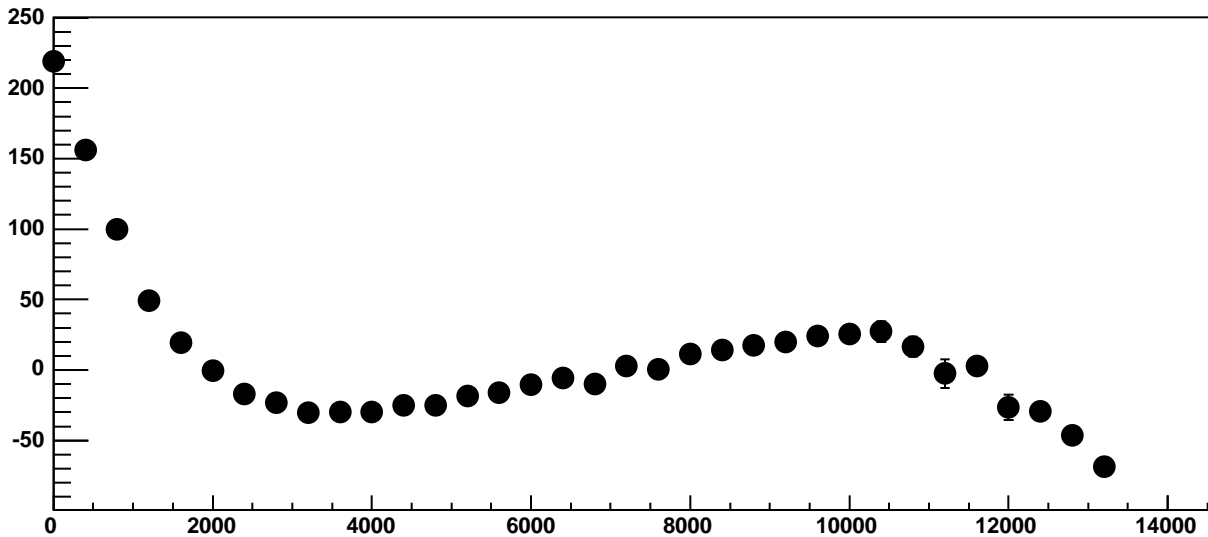
Chip 9, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

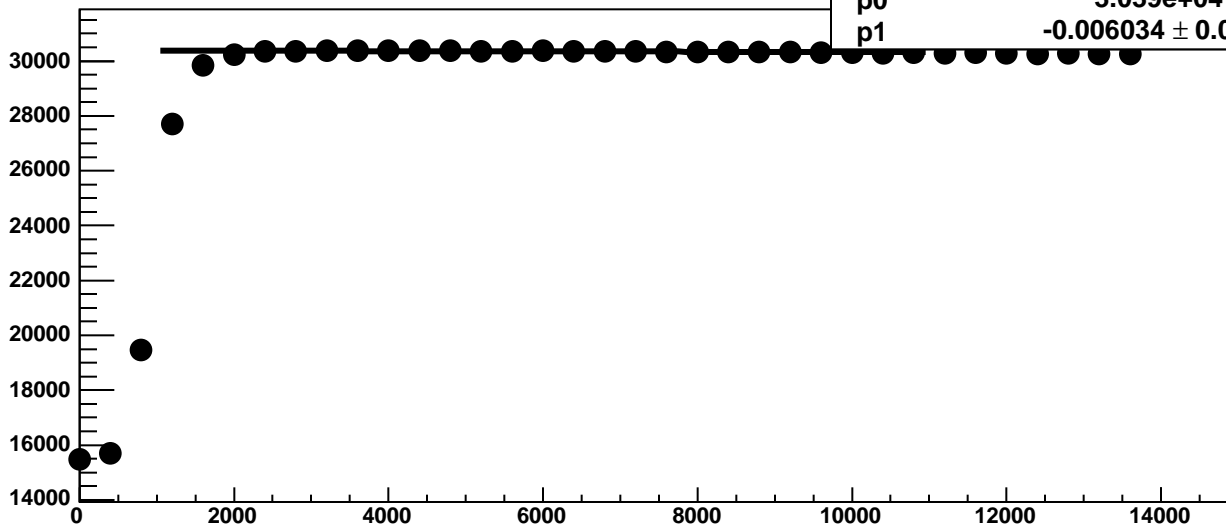


Chip 9, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

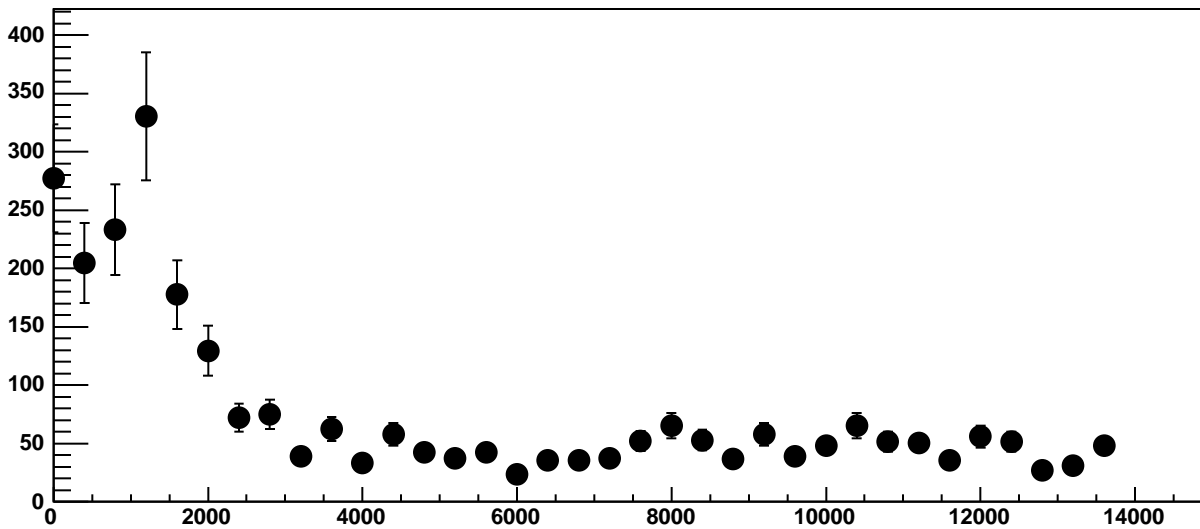


Chip 9, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC

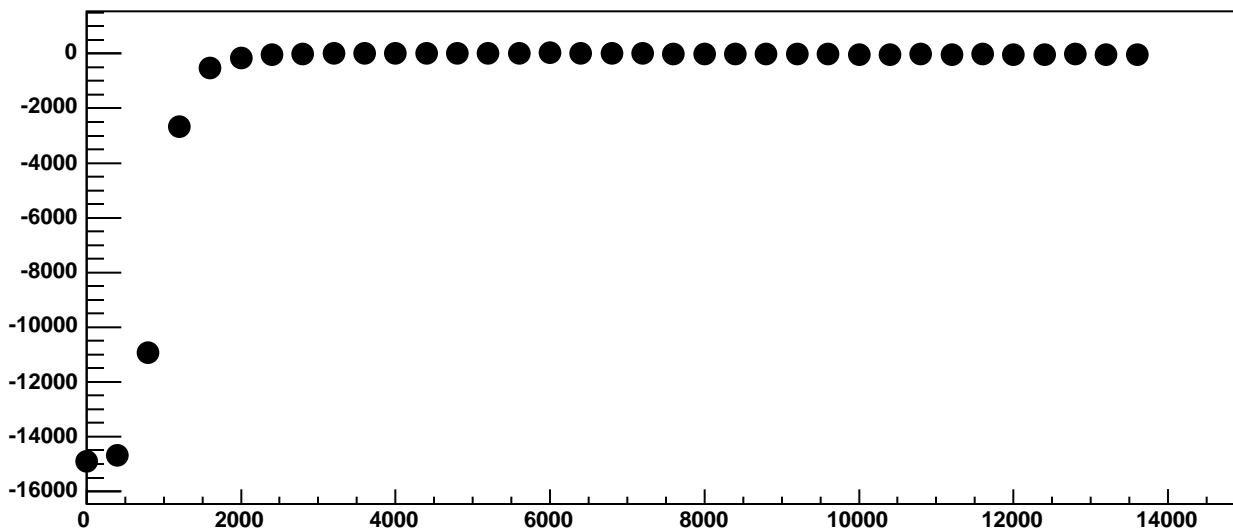
$\chi^2 / \text{ndf}$  1536 / 23  
p0  $3.039\text{e}+04 \pm 6.489$   
p1  $-0.006034 \pm 0.0009493$



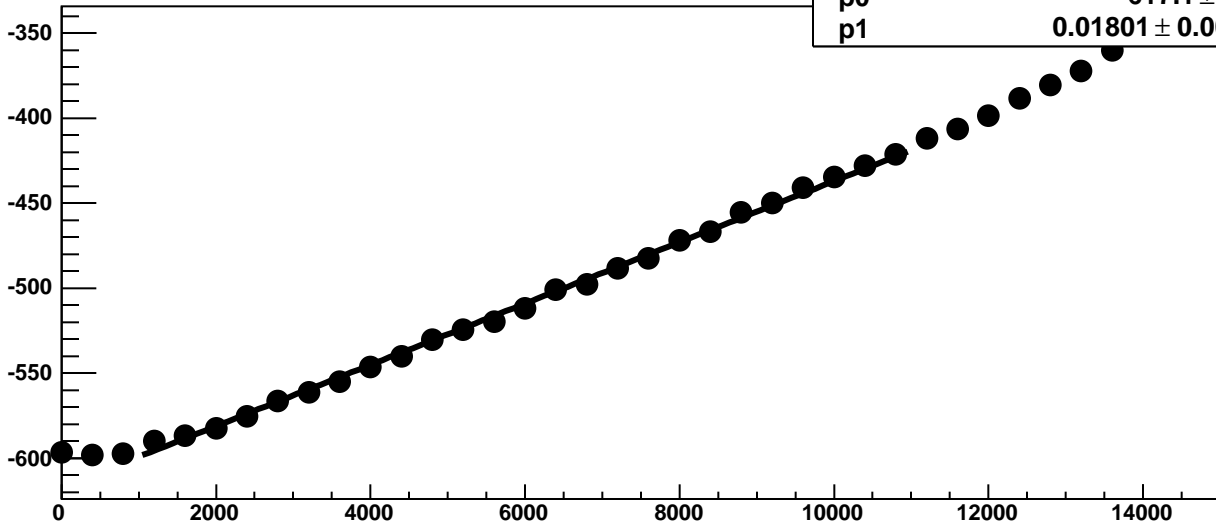
Chip 9, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

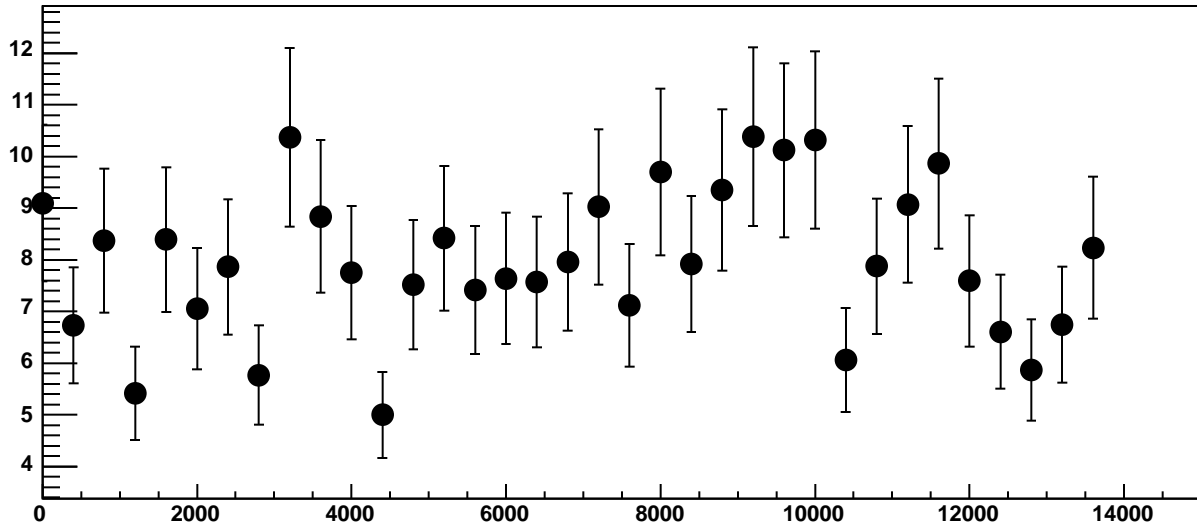


Chip 9, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

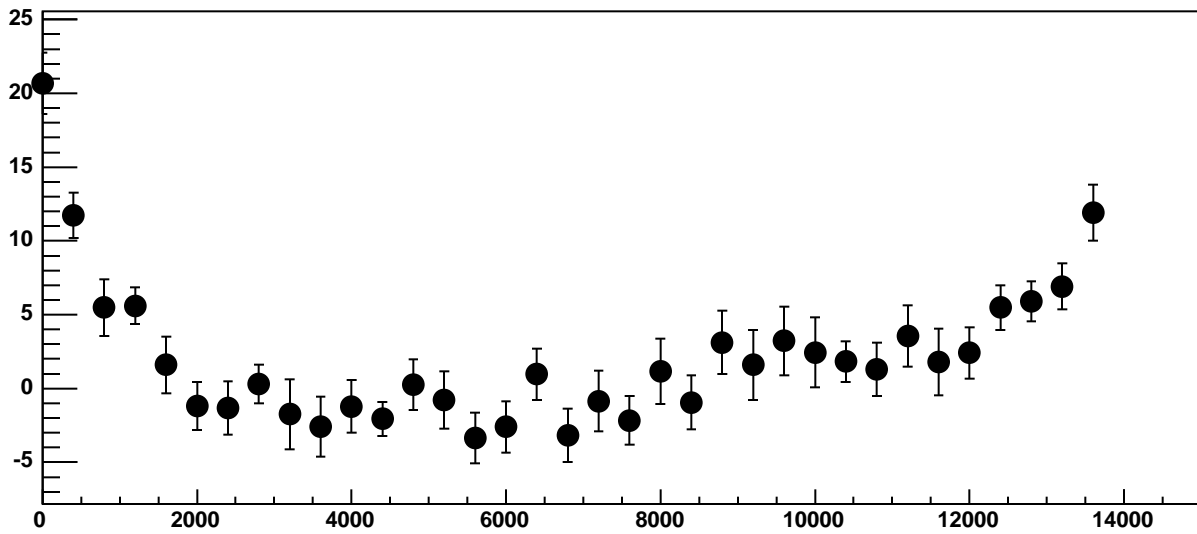


$\chi^2 / \text{ndf}$  47.79 / 23  
p0 -617.1 ± 0.7442  
p1 0.01801 ± 0.0001186

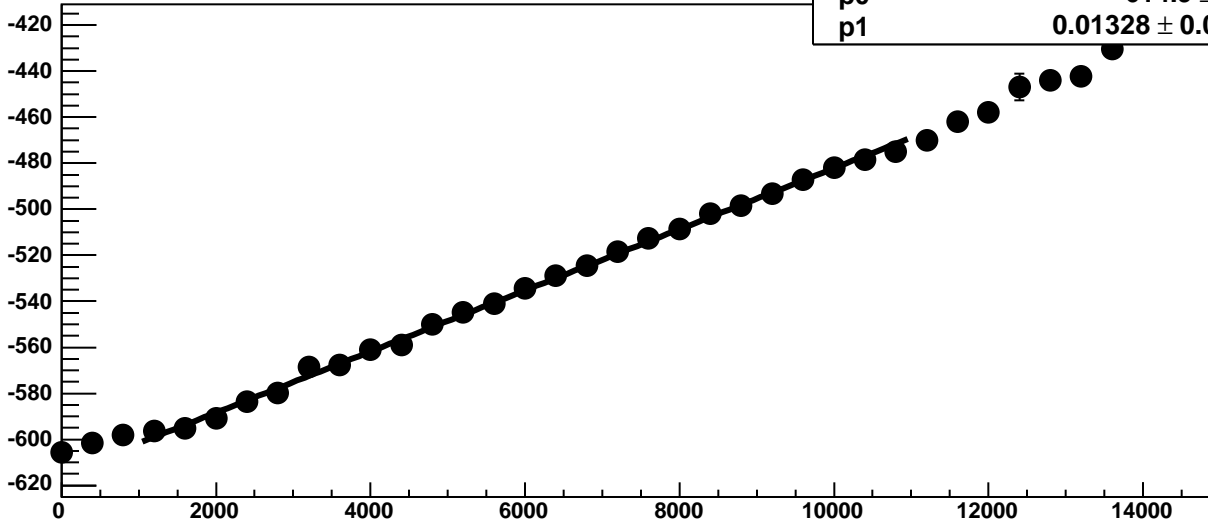
Chip 9, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



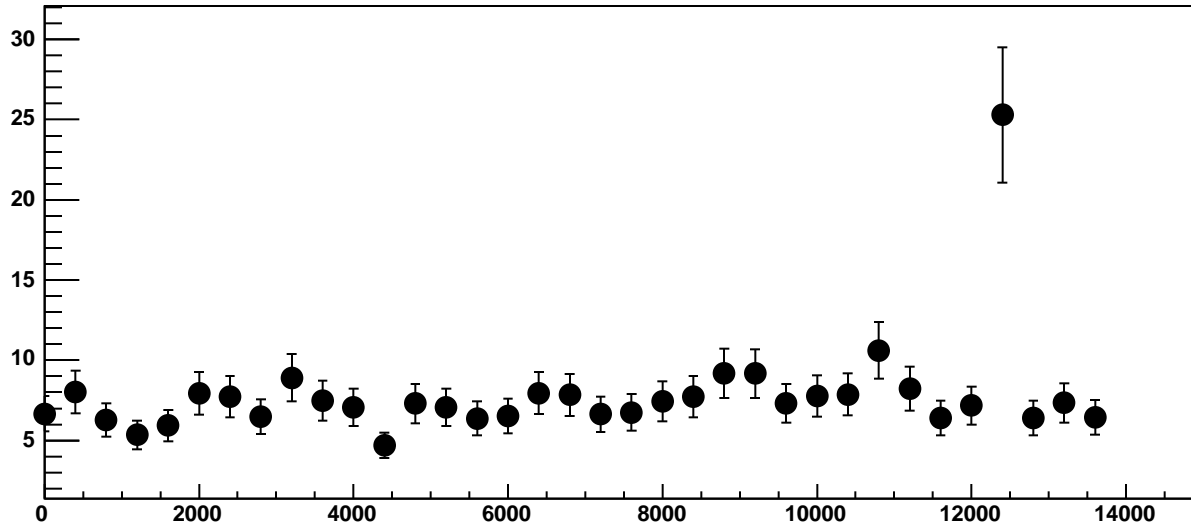
Chip 9, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



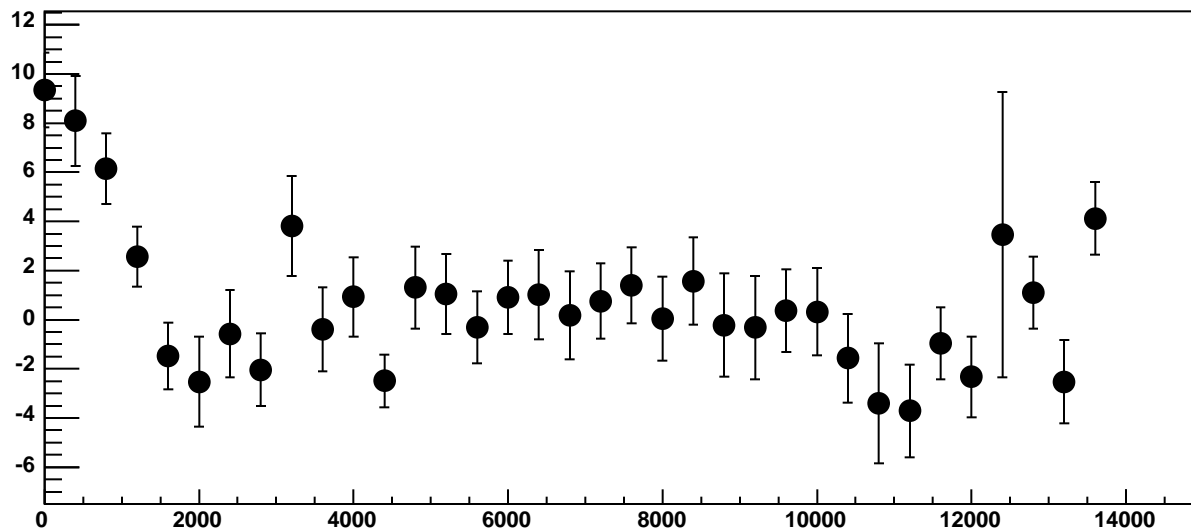
Chip 9, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



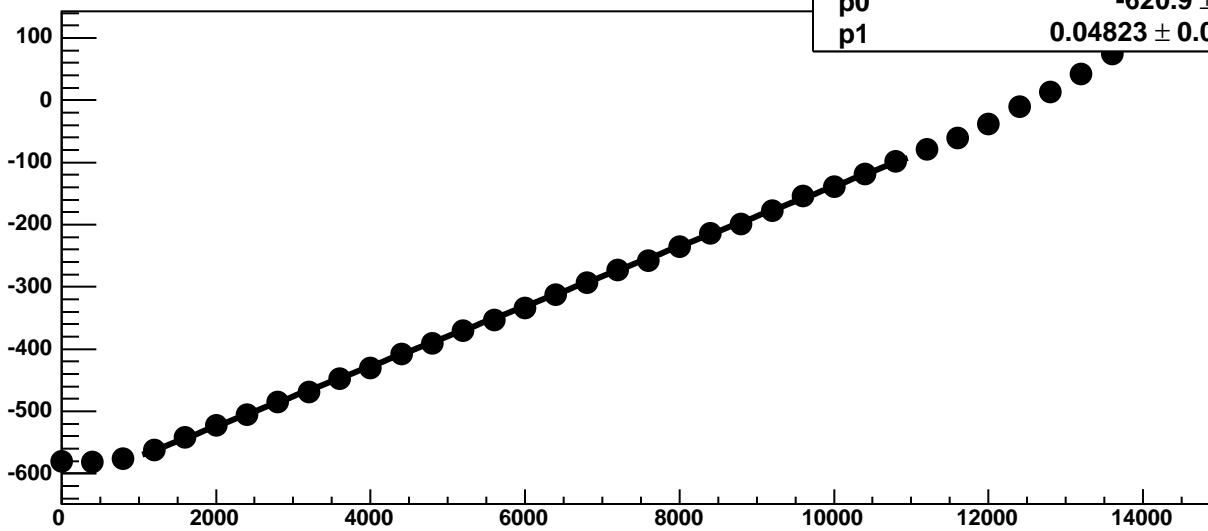
Chip 9, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

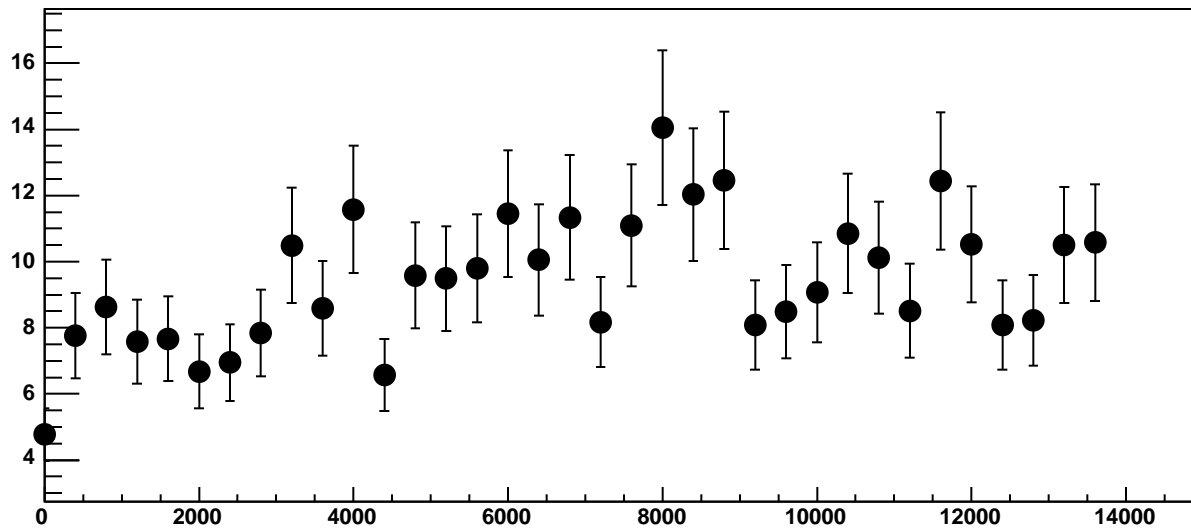


Chip 9, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

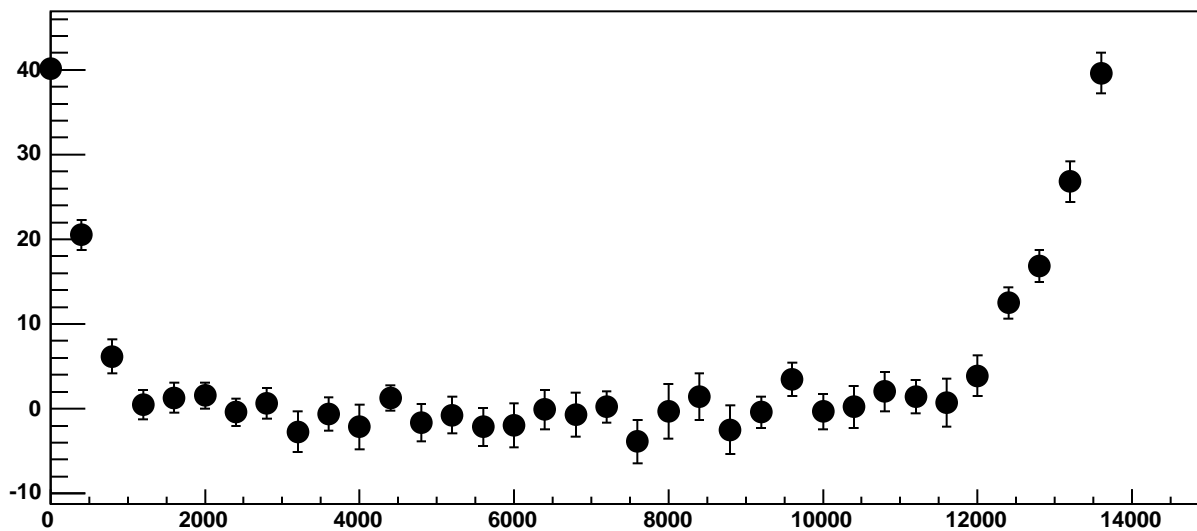


$\chi^2 / \text{ndf}$  14.14 / 23  
p0  $-620.9 \pm 0.8515$   
p1  $0.04823 \pm 0.0001385$

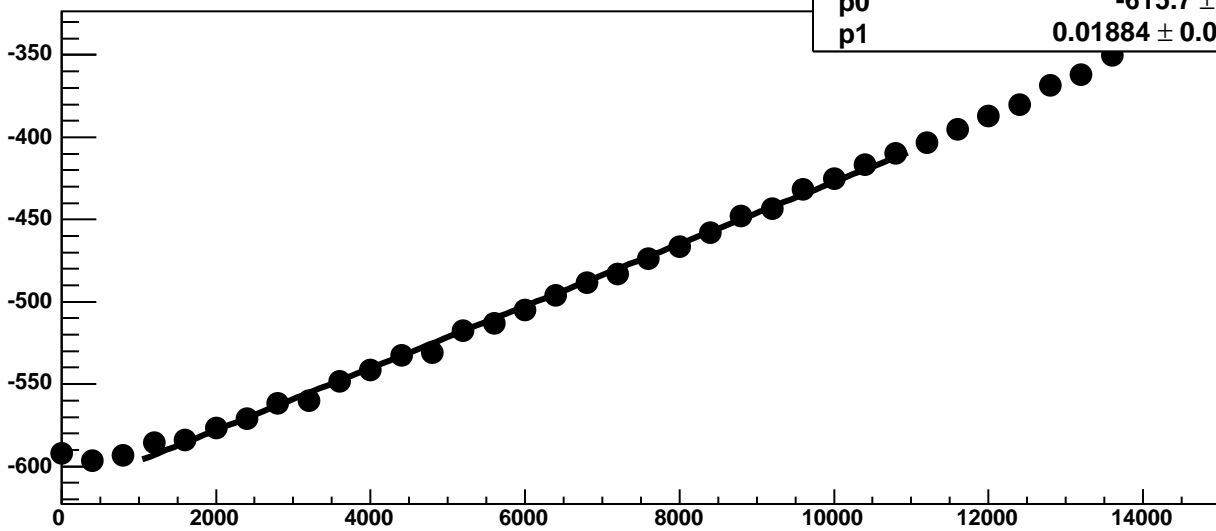
Chip 9, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



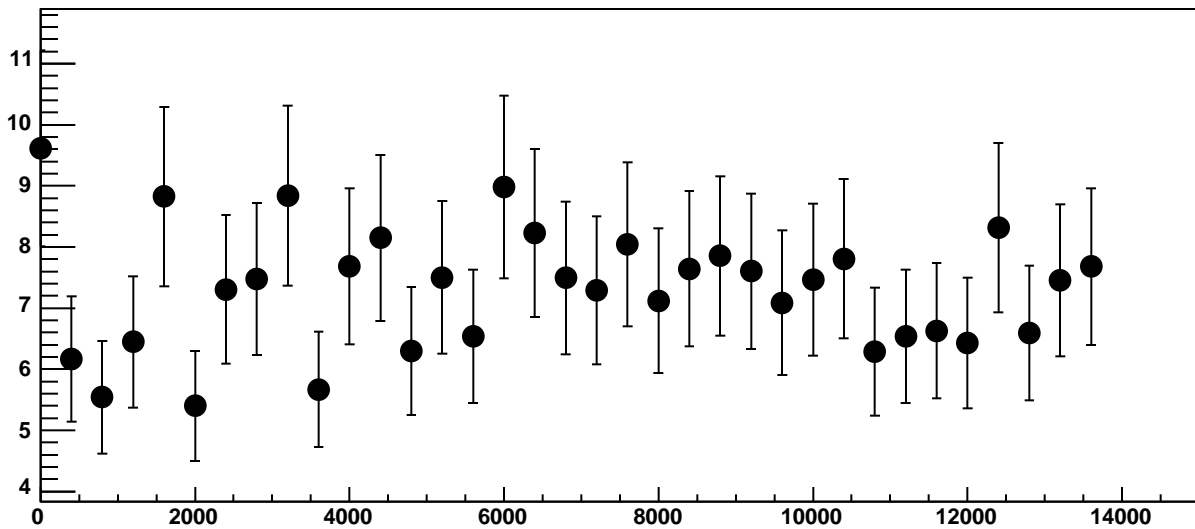
Chip 9, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC



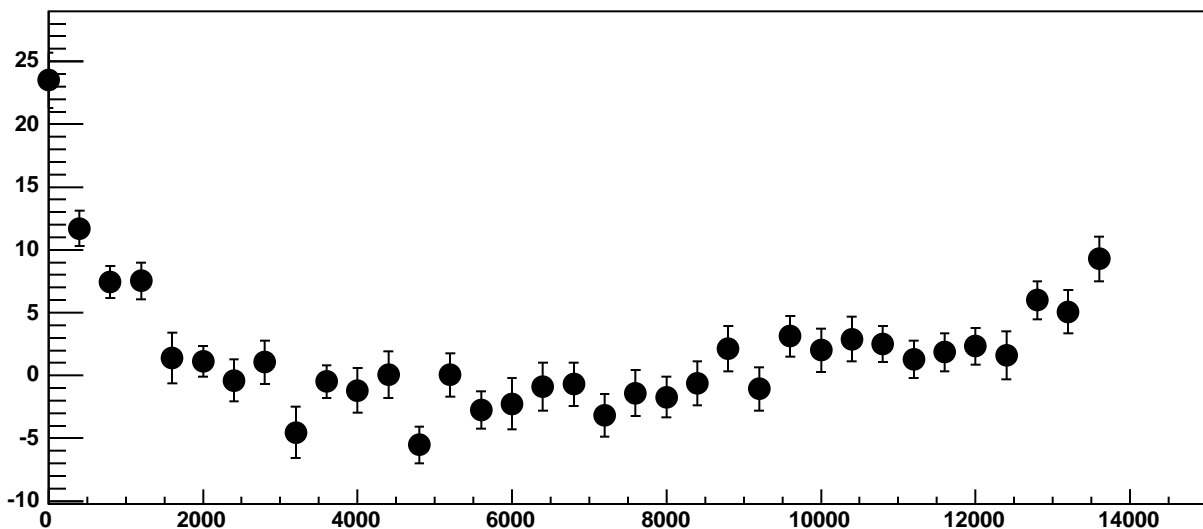
Chip 9, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



Chip 9, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

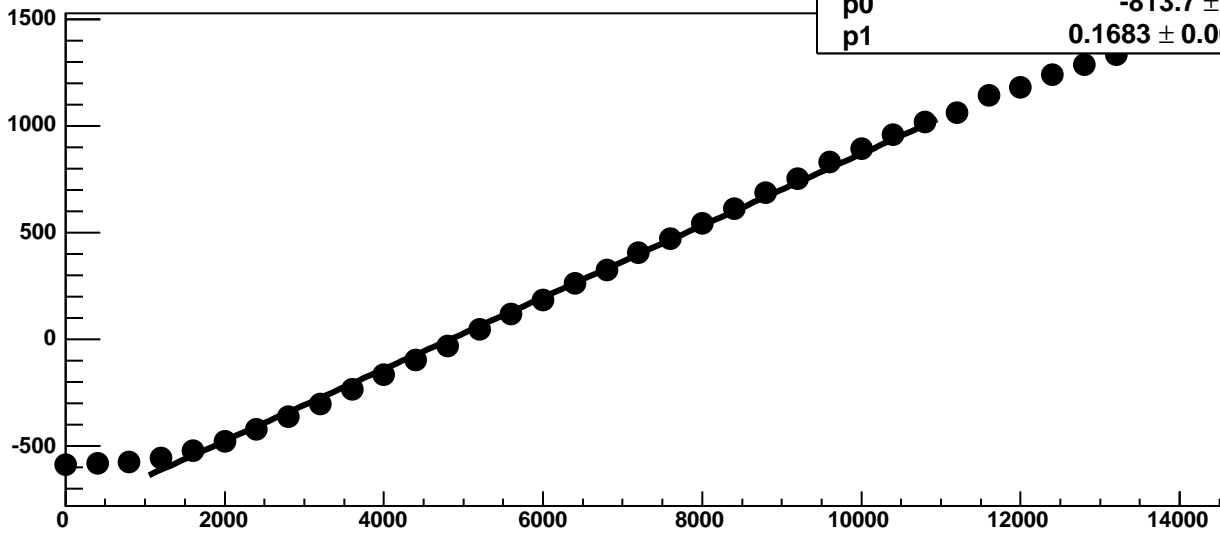


Chip 9, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

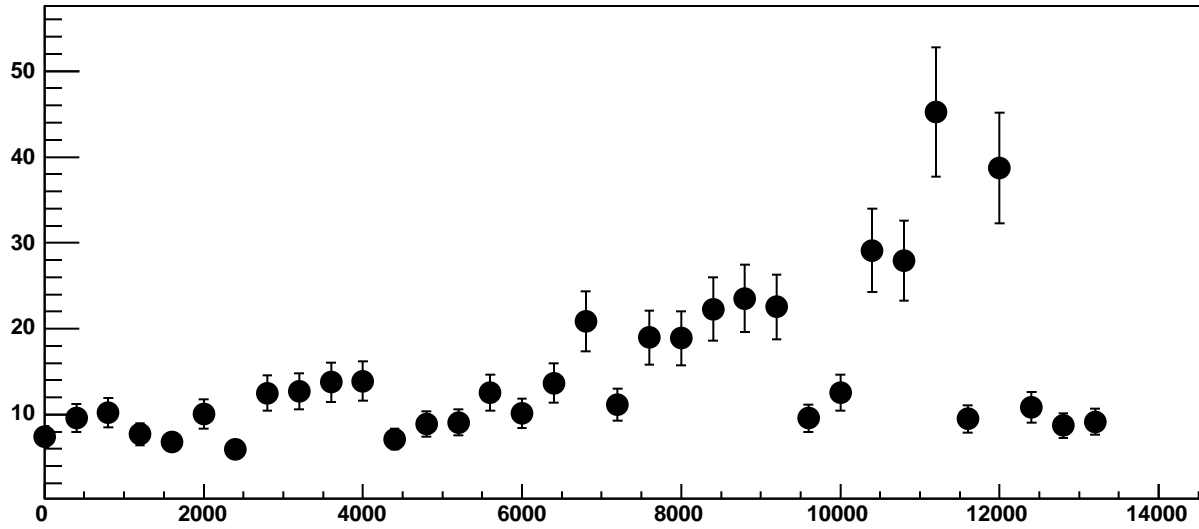




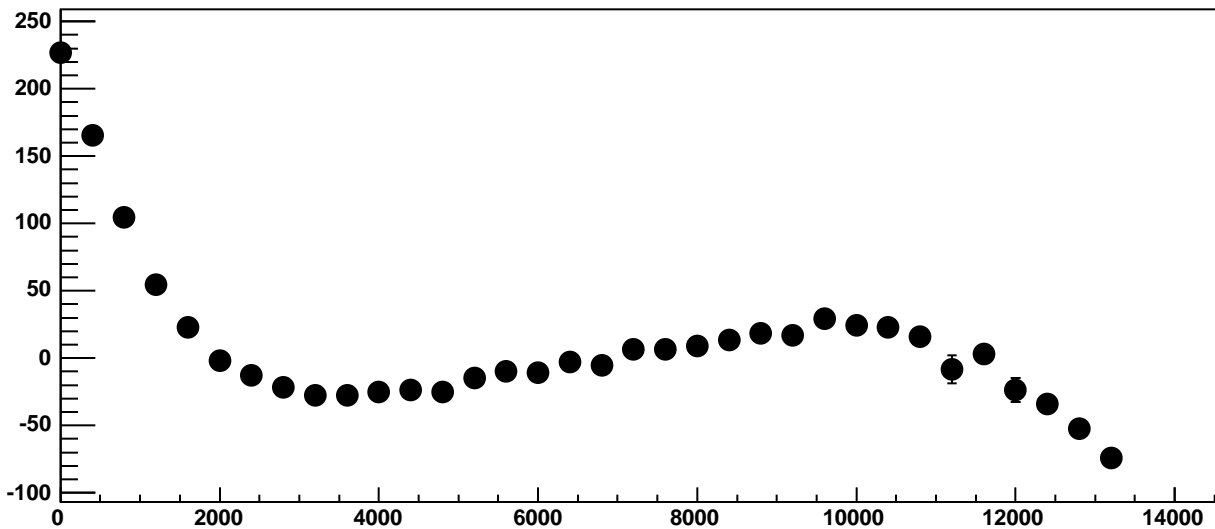
Chip 9, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



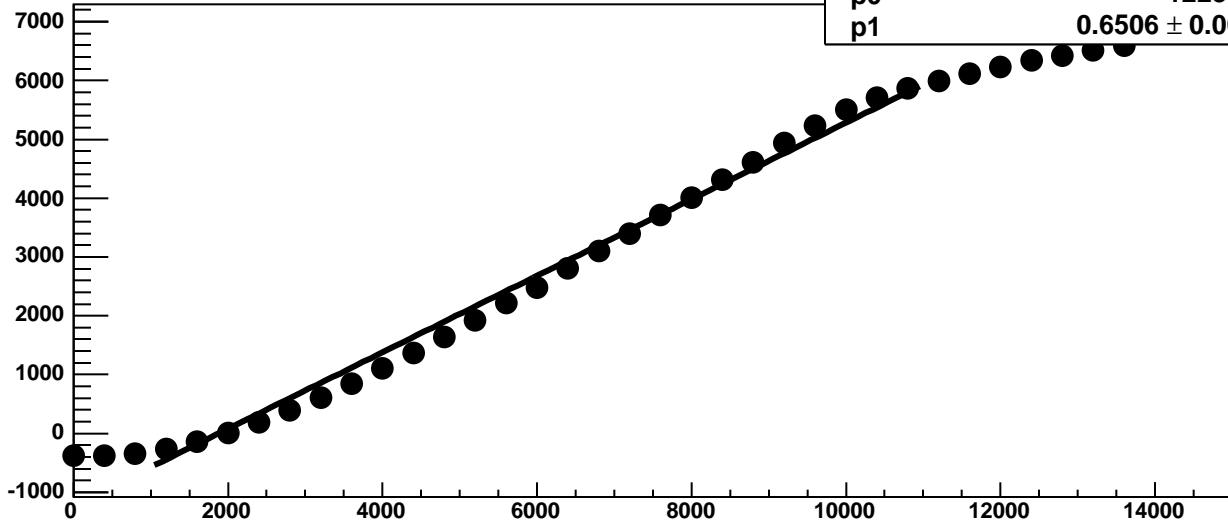
Chip 9, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



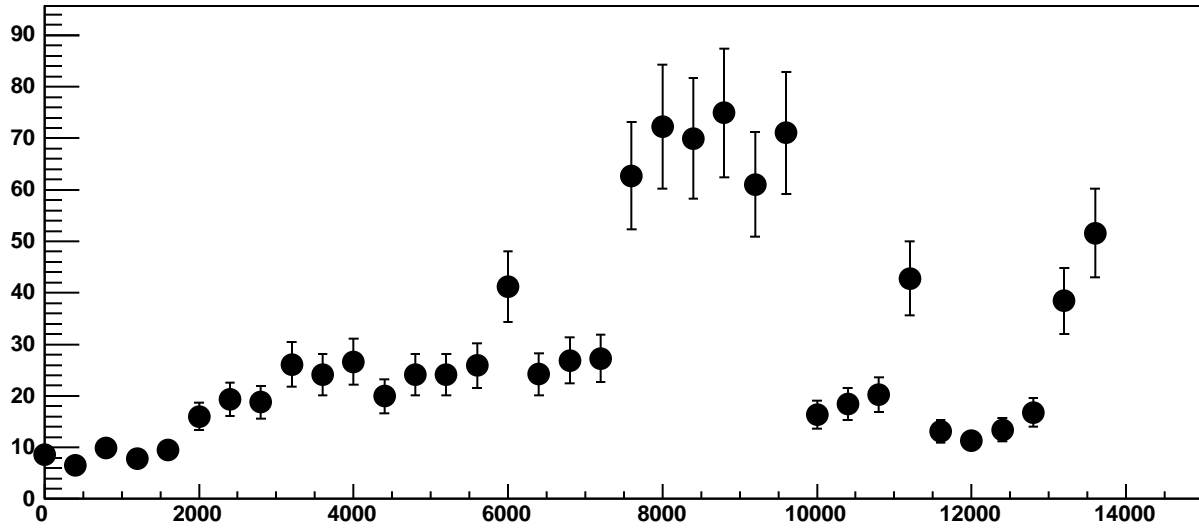
Chip 9, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



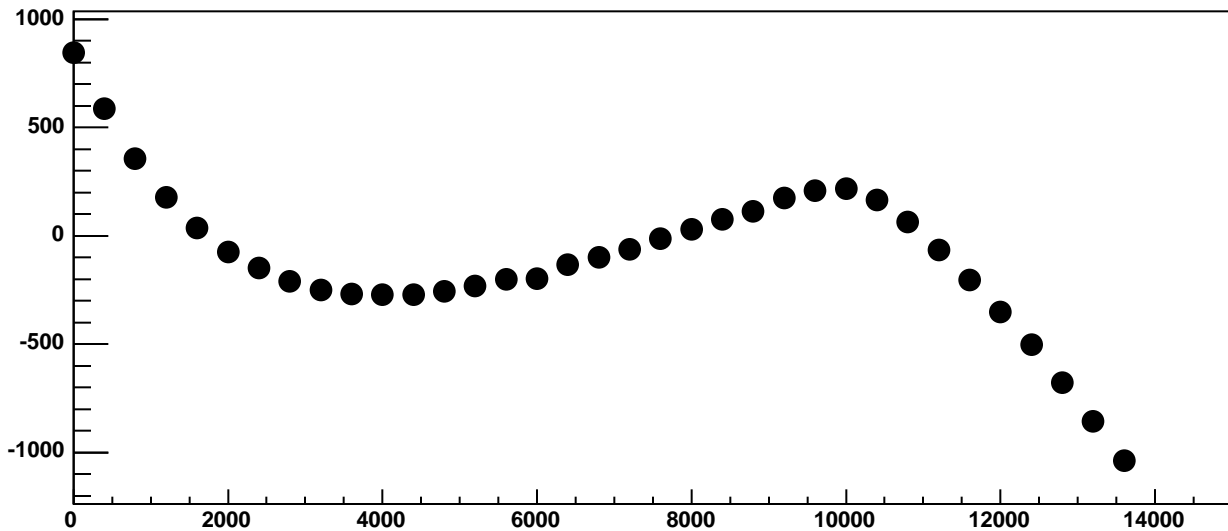
Chip 9, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



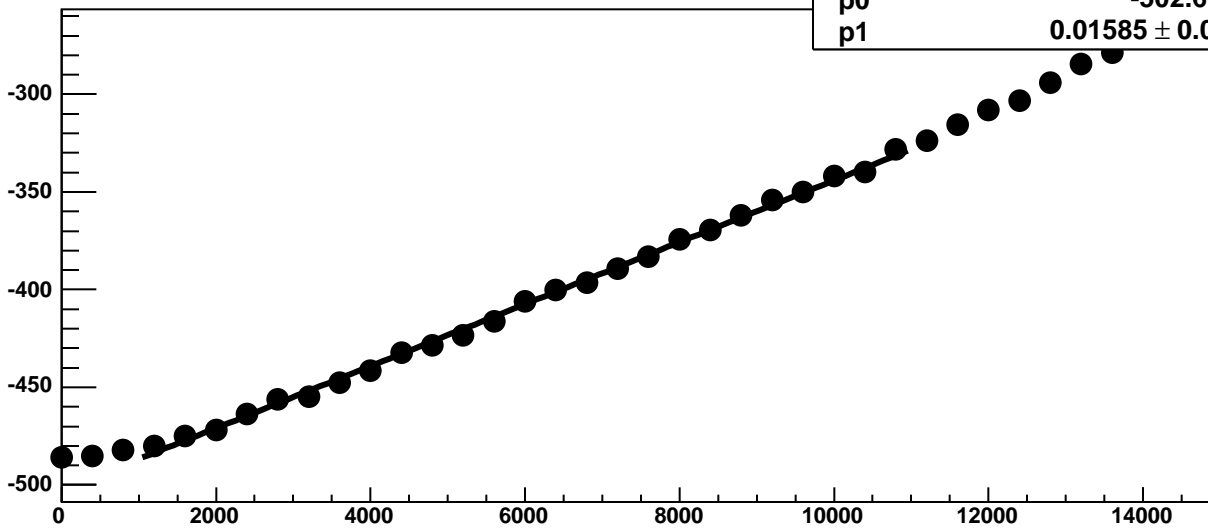
Chip 9, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

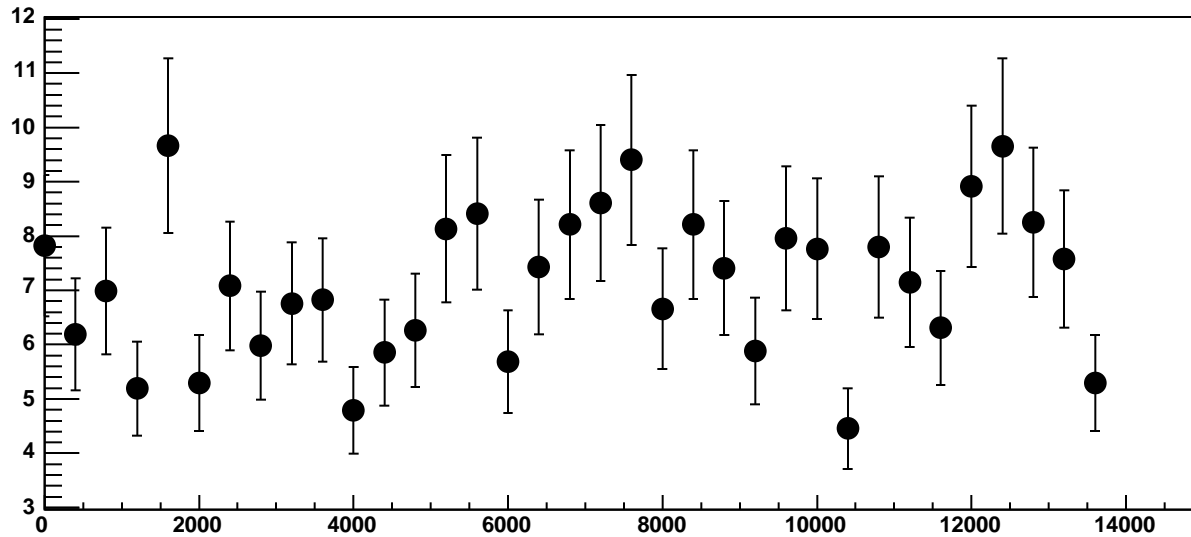


Chip 9, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

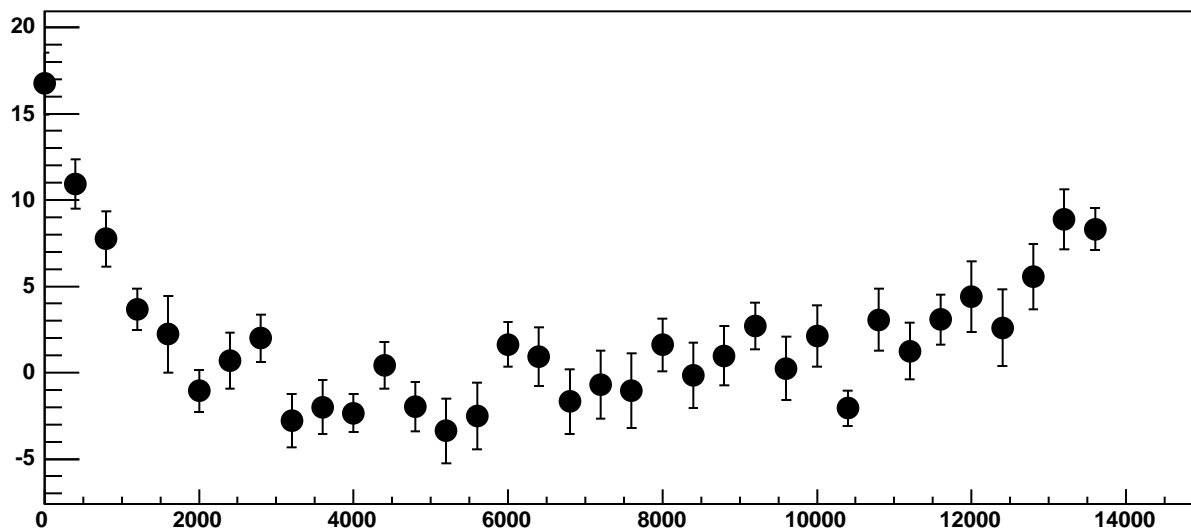


$\chi^2 / \text{ndf}$  46.79 / 23  
p0  $-502.6 \pm 0.654$   
p1  $0.01585 \pm 0.0001002$

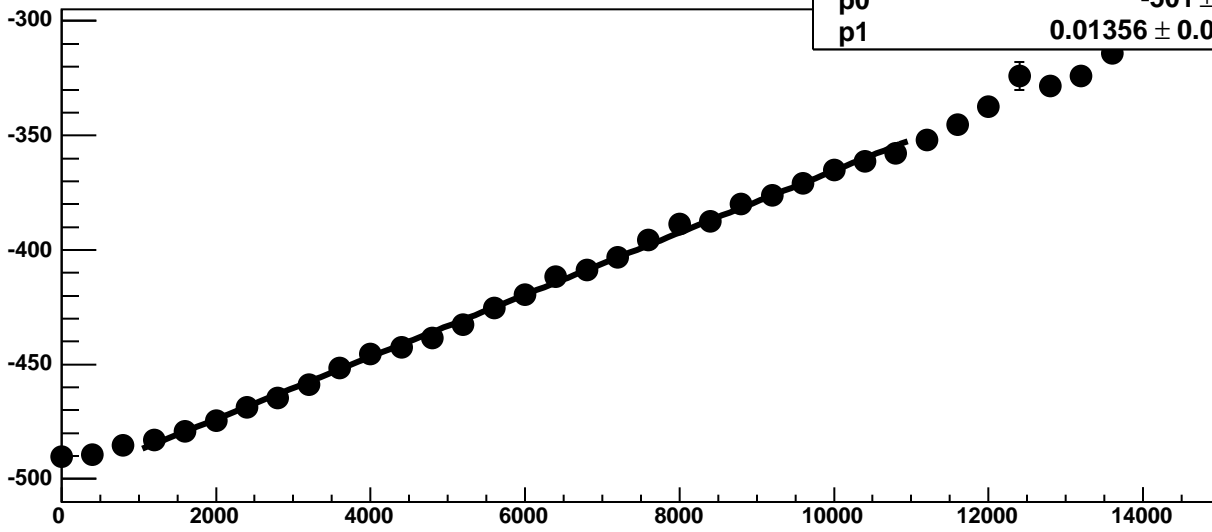
Chip 9, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

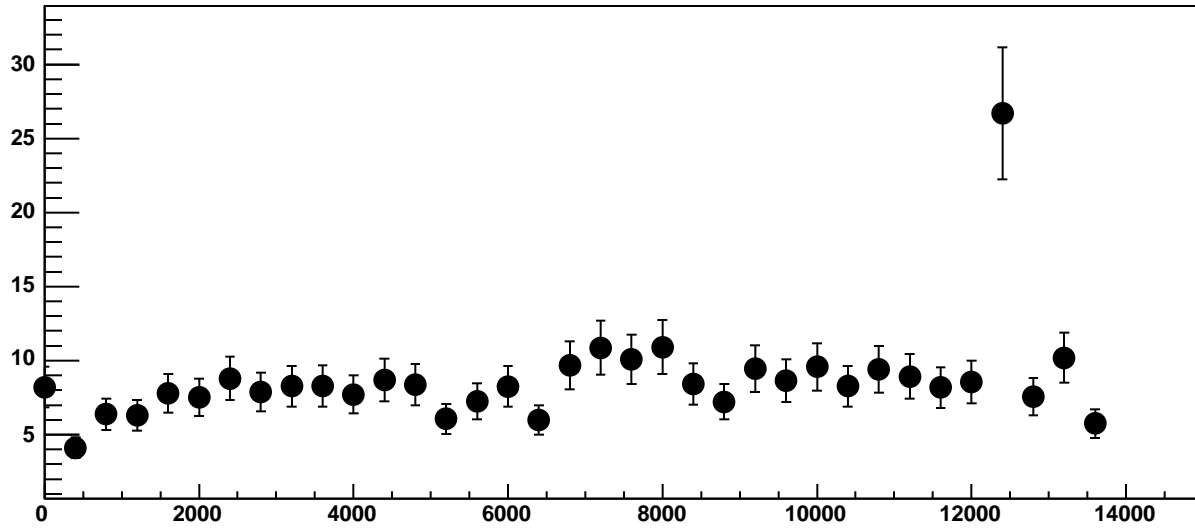


Chip 9, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

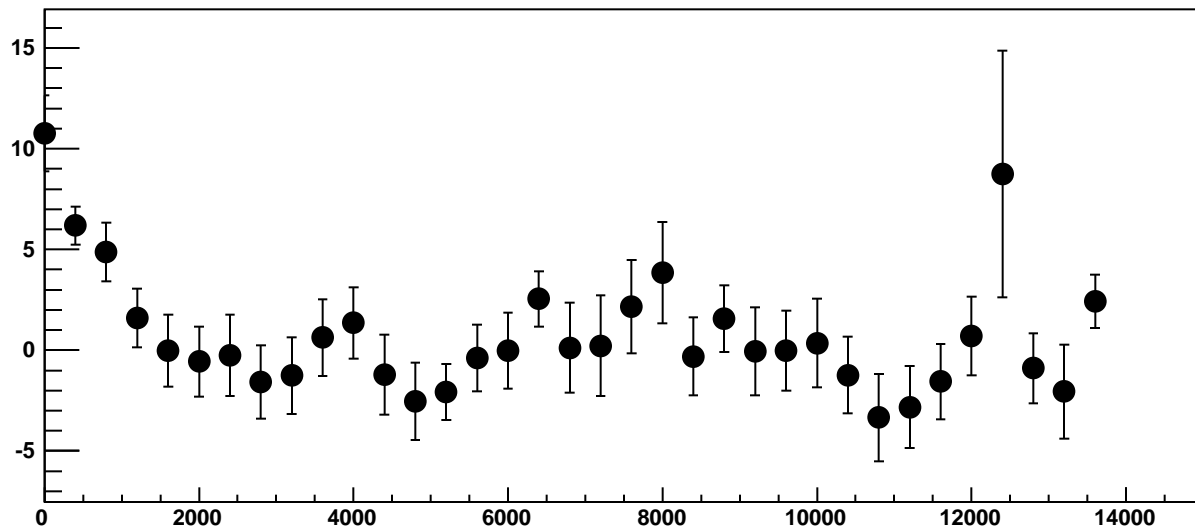


$\chi^2 / \text{ndf}$  18.11 / 23  
p0  $-501 \pm 0.8197$   
p1  $0.01356 \pm 0.0001299$

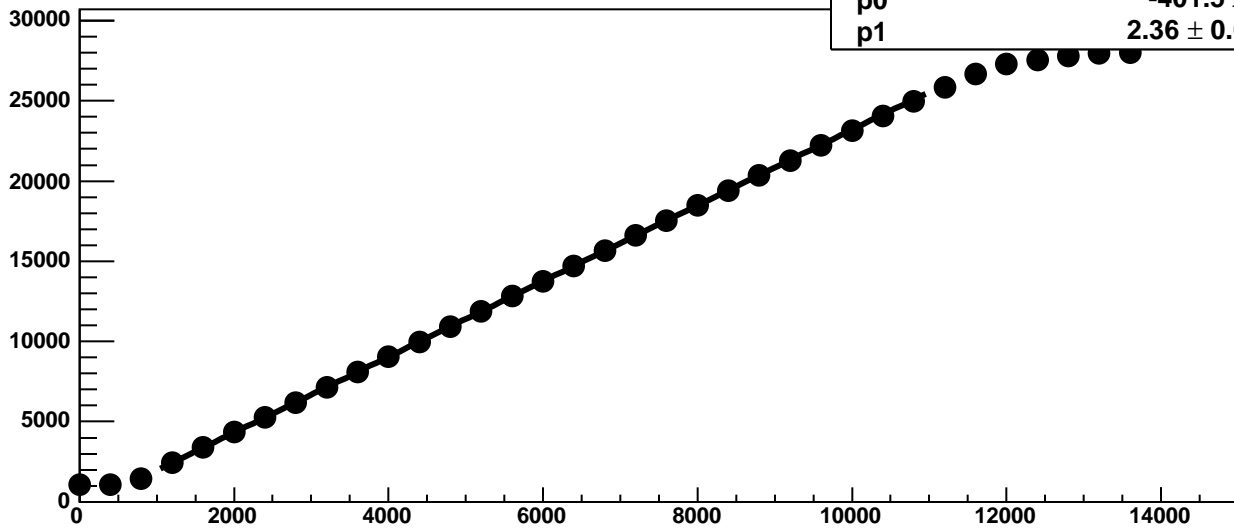
Chip 9, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

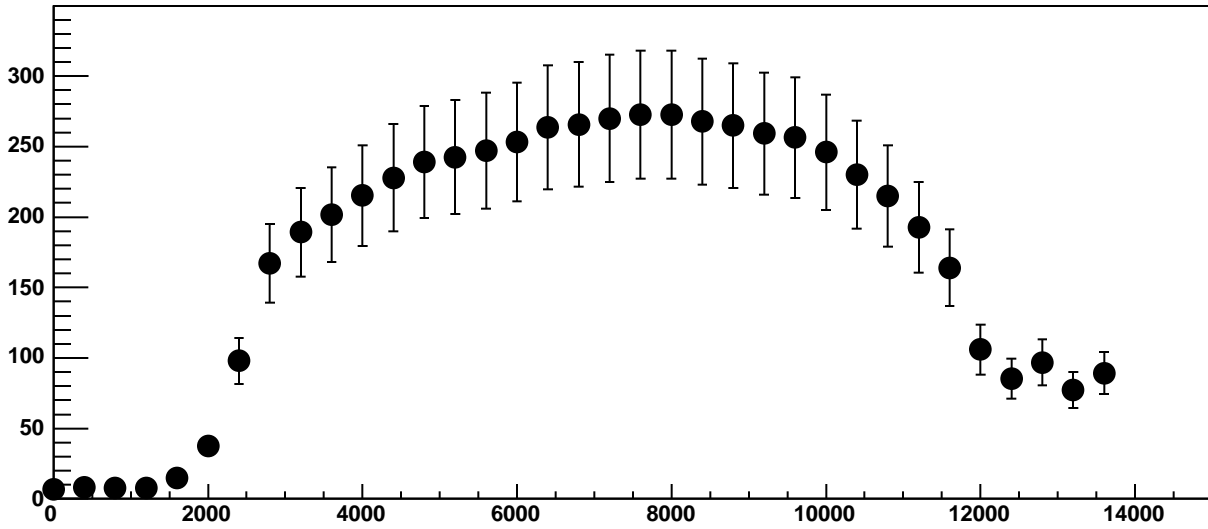


Chip 9, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC

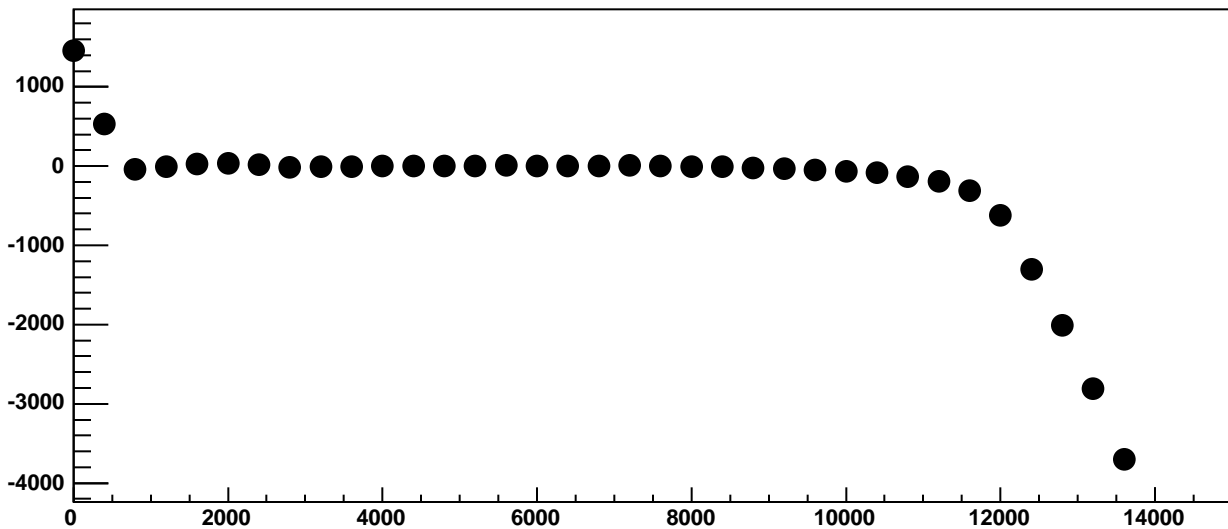


$\chi^2 / \text{ndf}$  99 / 23  
p0  $-401.5 \pm 3.178$   
p1  $2.36 \pm 0.001992$

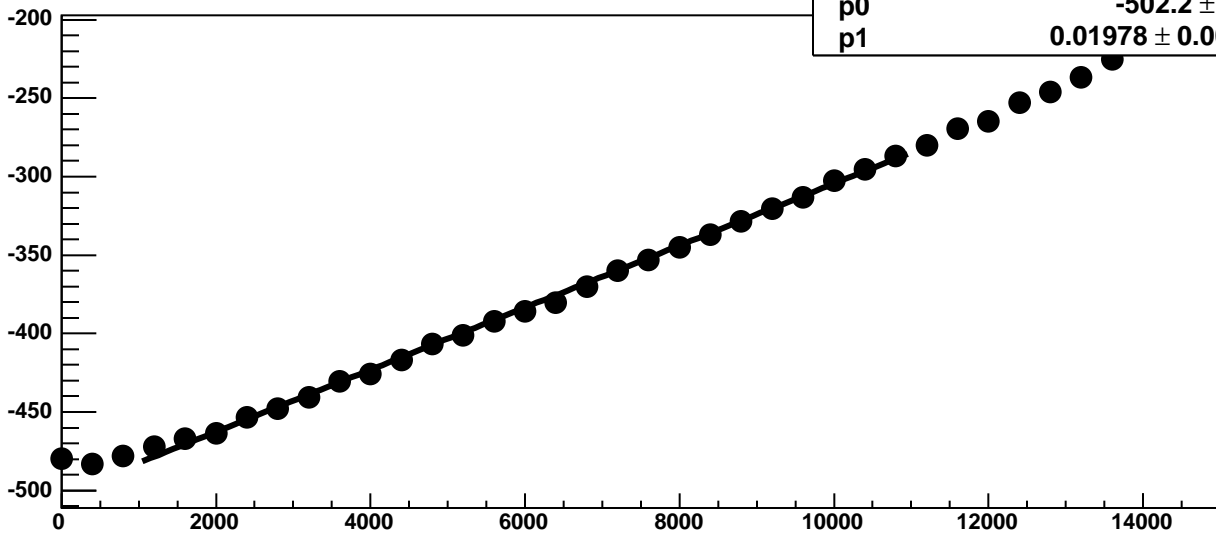
Chip 9, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC

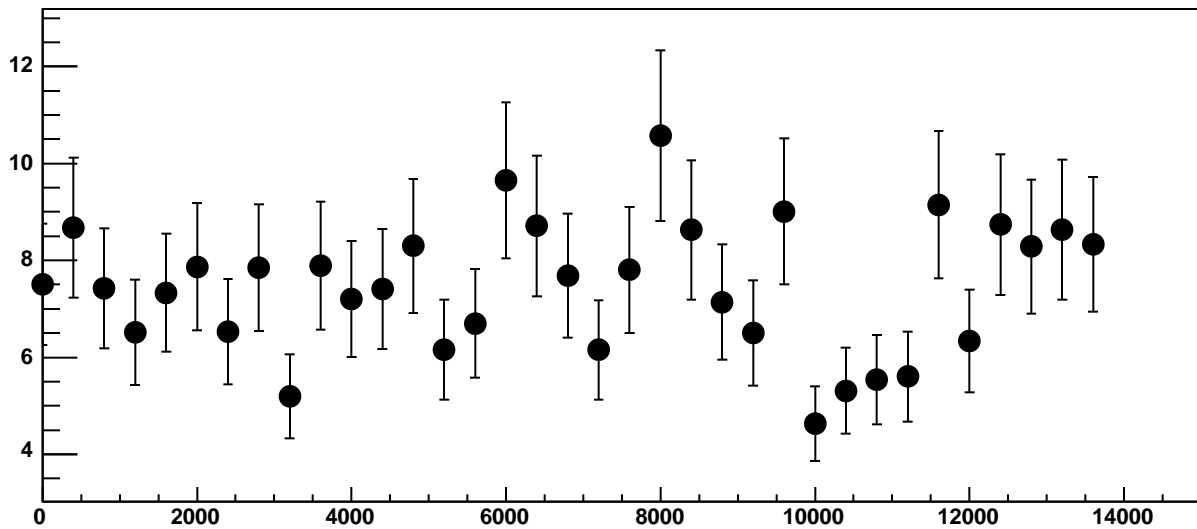


Chip 9, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

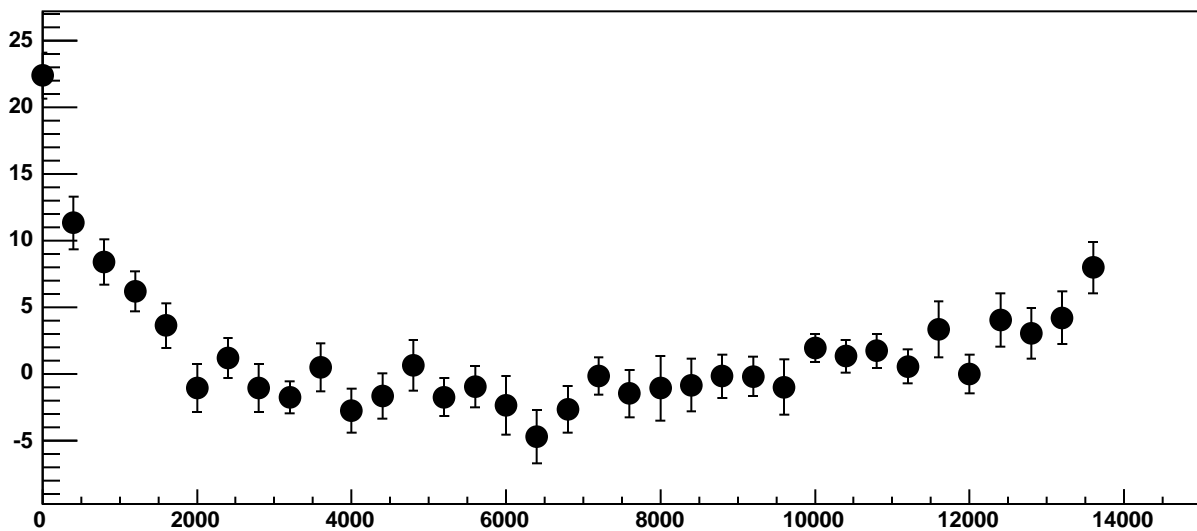


$\chi^2 / \text{ndf}$  47.62 / 23  
p0  $-502.2 \pm 0.7129$   
p1  $0.01978 \pm 0.0001018$

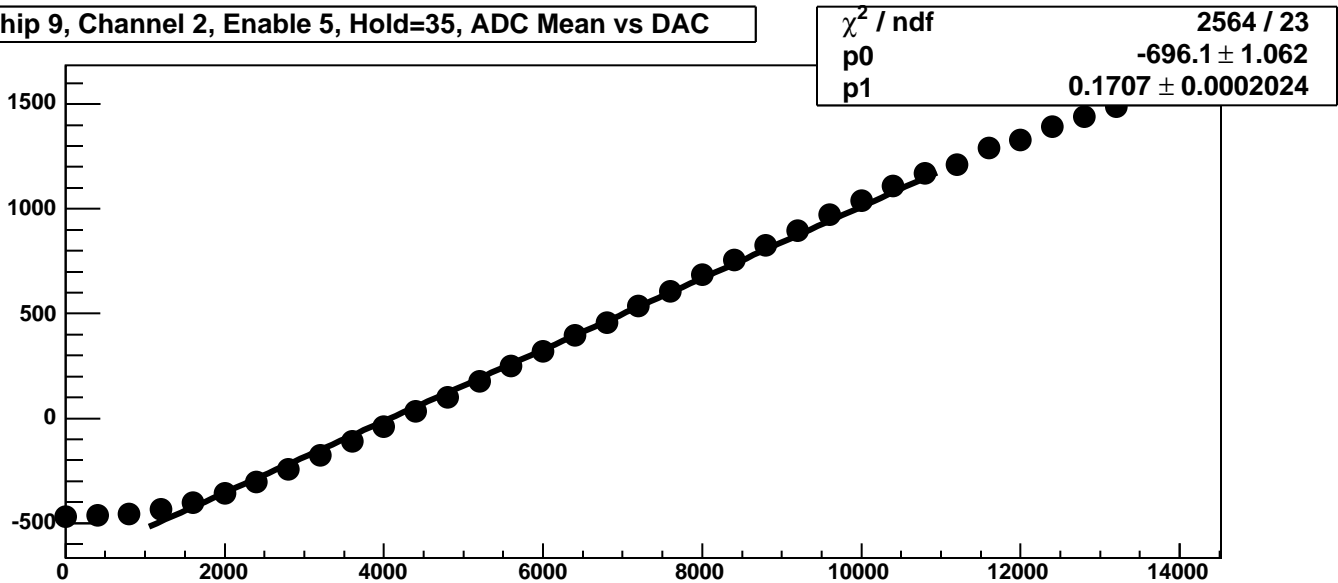
Chip 9, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



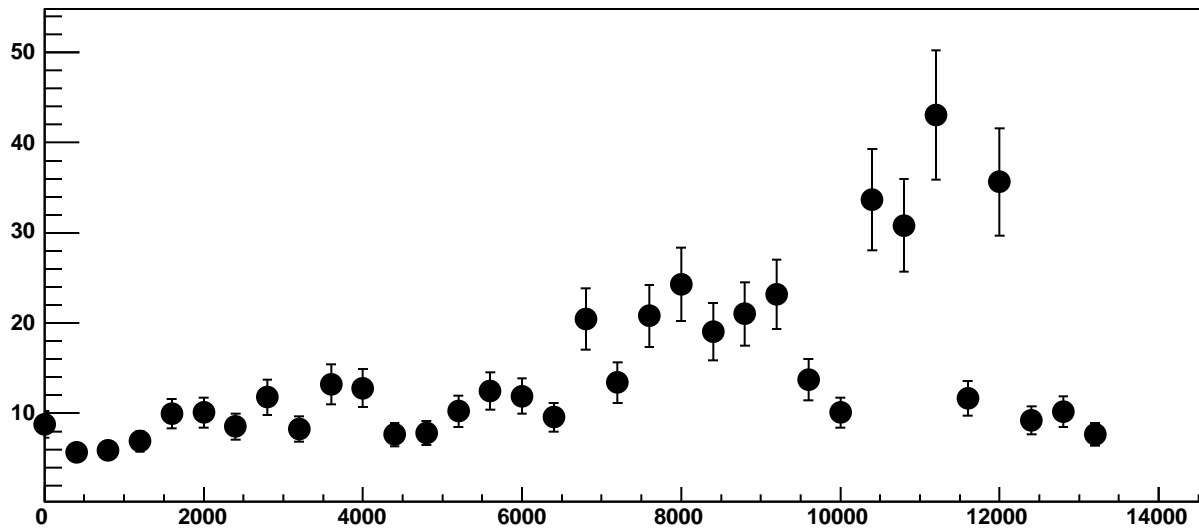
Chip 9, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



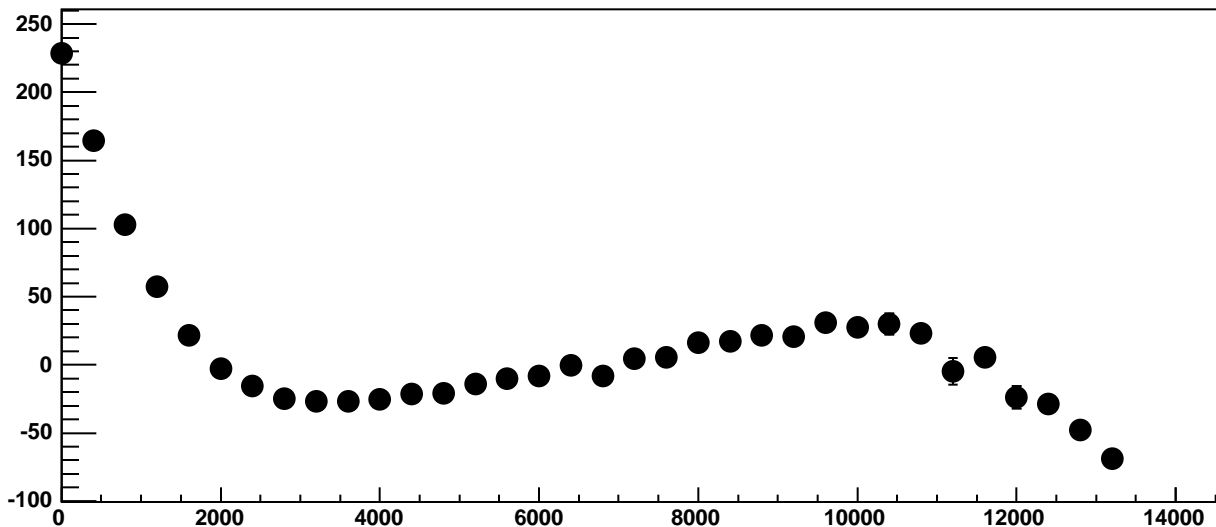
Chip 9, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



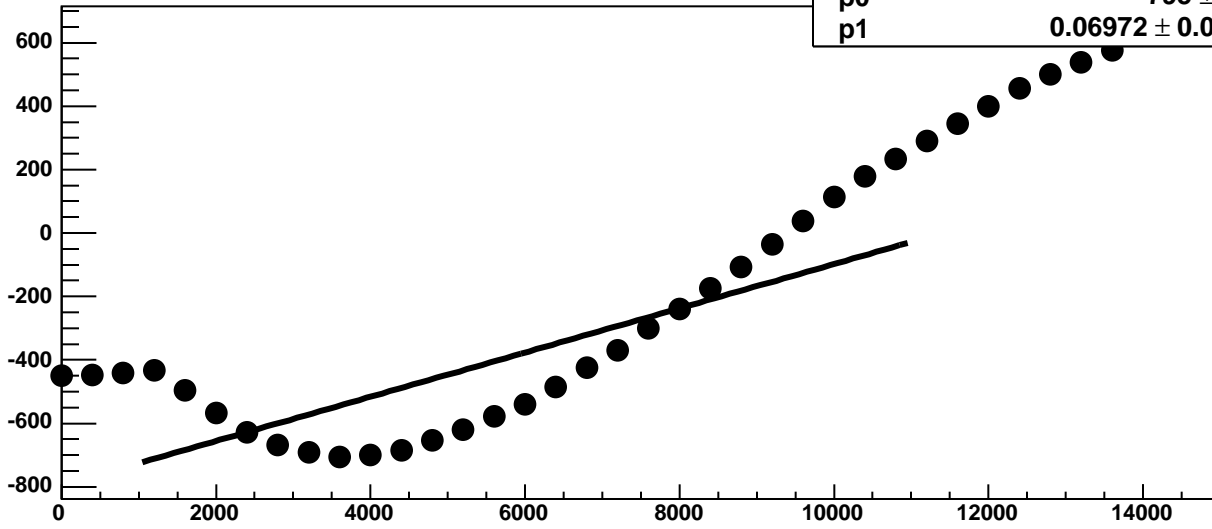
Chip 9, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



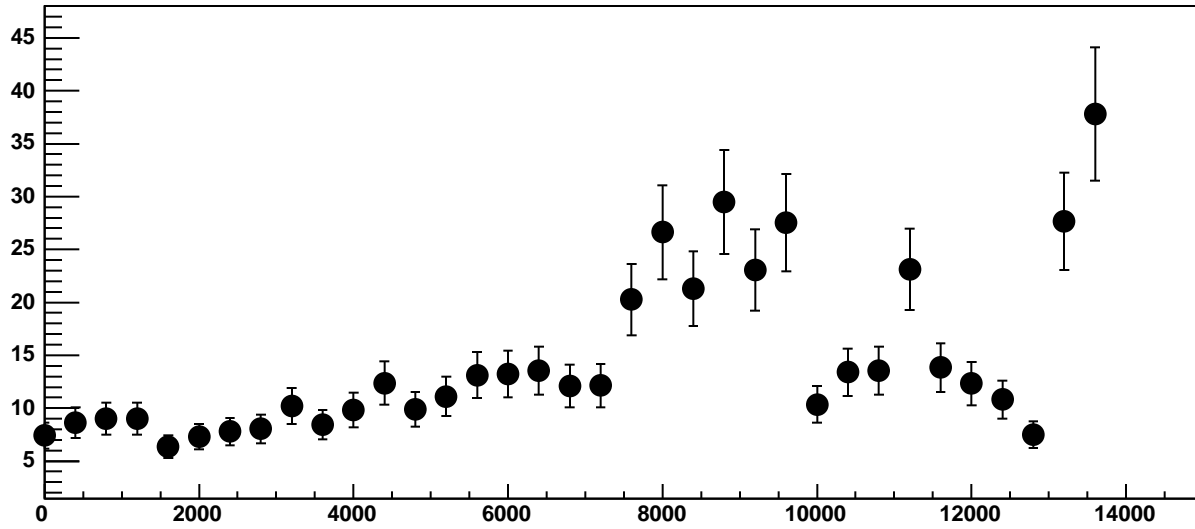
Chip 9, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



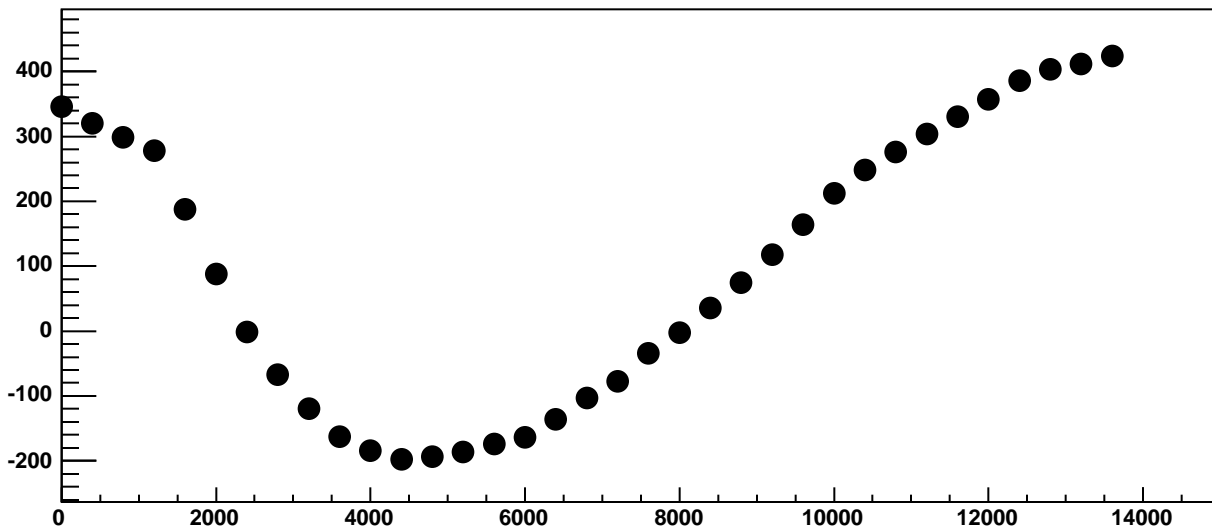
Chip 9, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



Chip 9, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

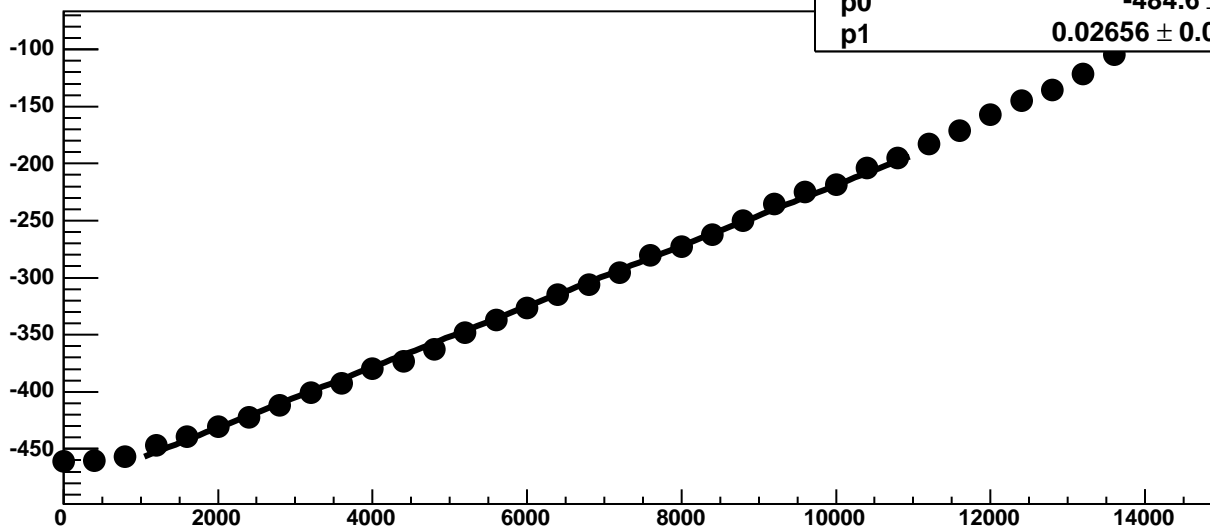


Chip 9, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC





Chip 9, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

60 / 23

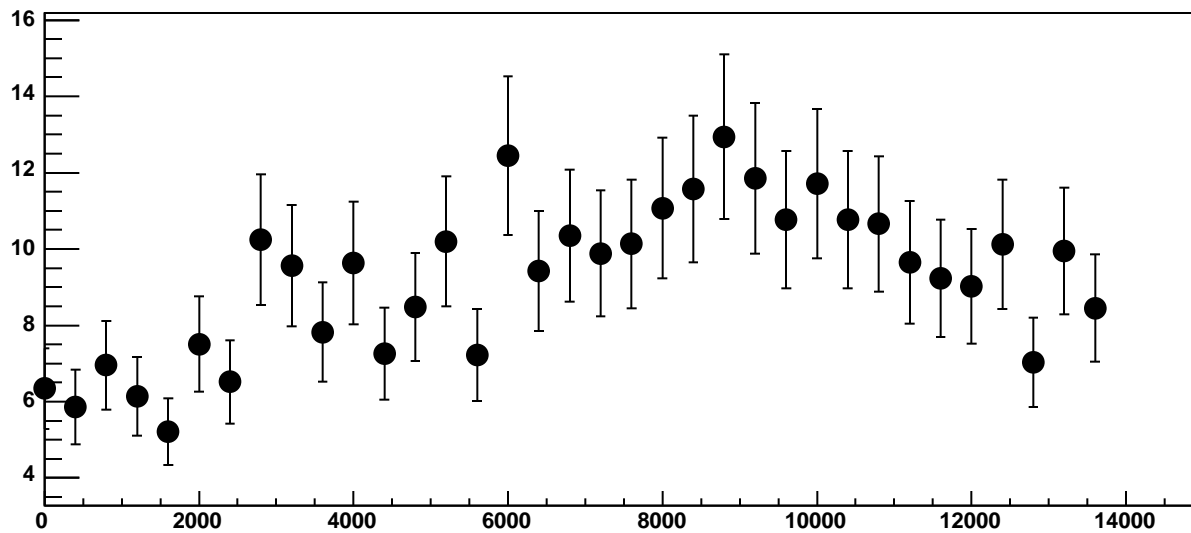
p0

$-484.6 \pm 0.7851$

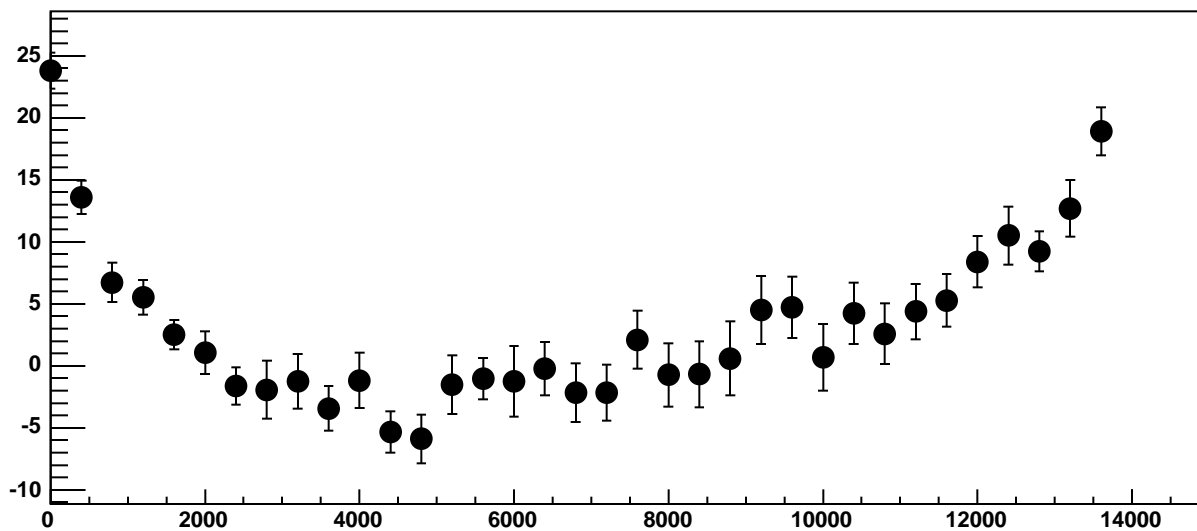
p1

$0.02656 \pm 0.0001392$

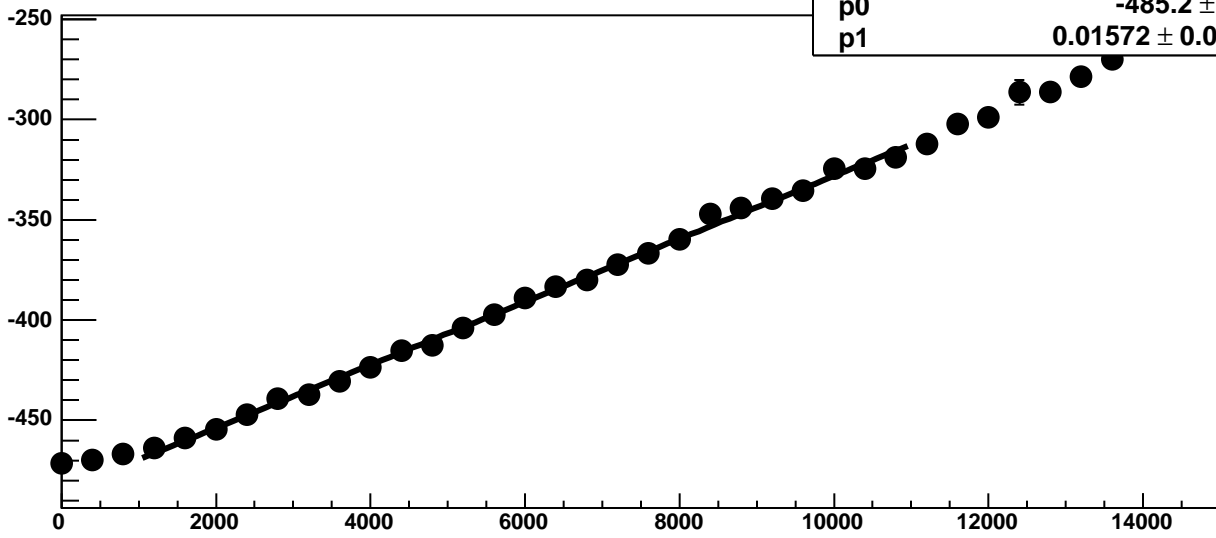
Chip 9, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



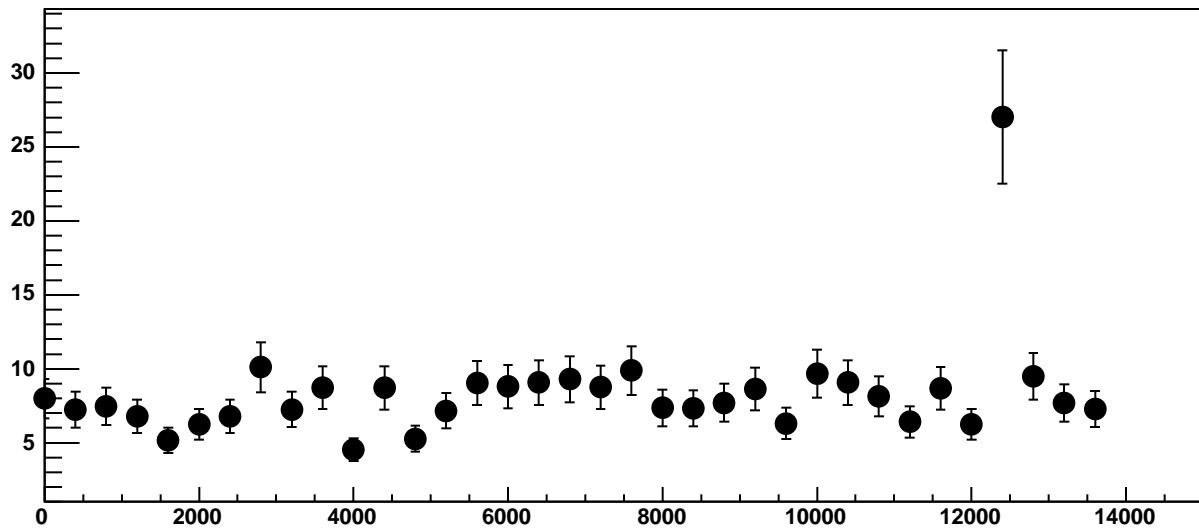
Chip 9, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



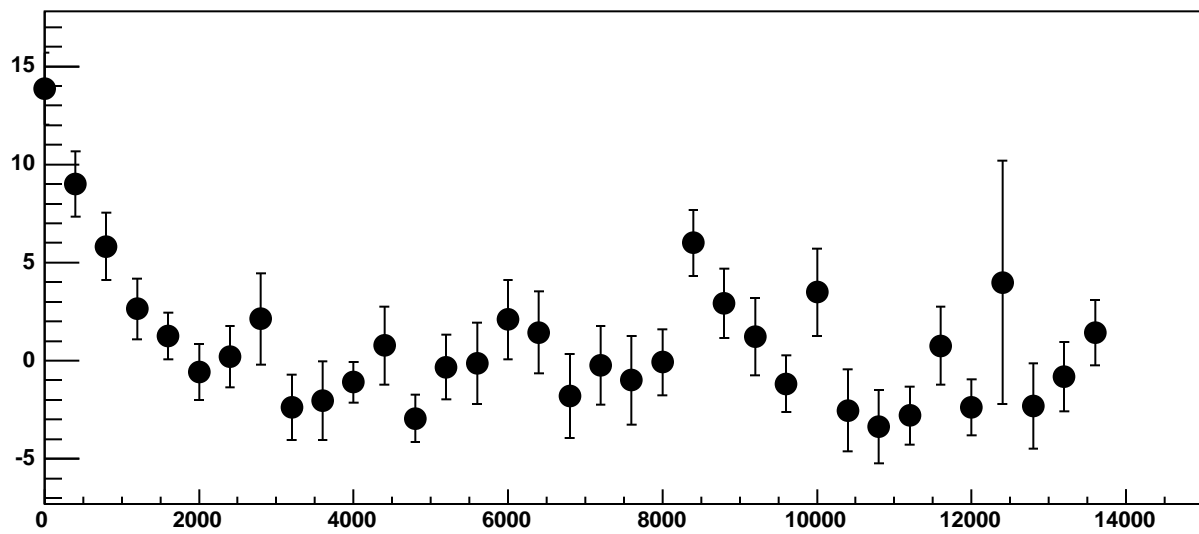
Chip 9, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



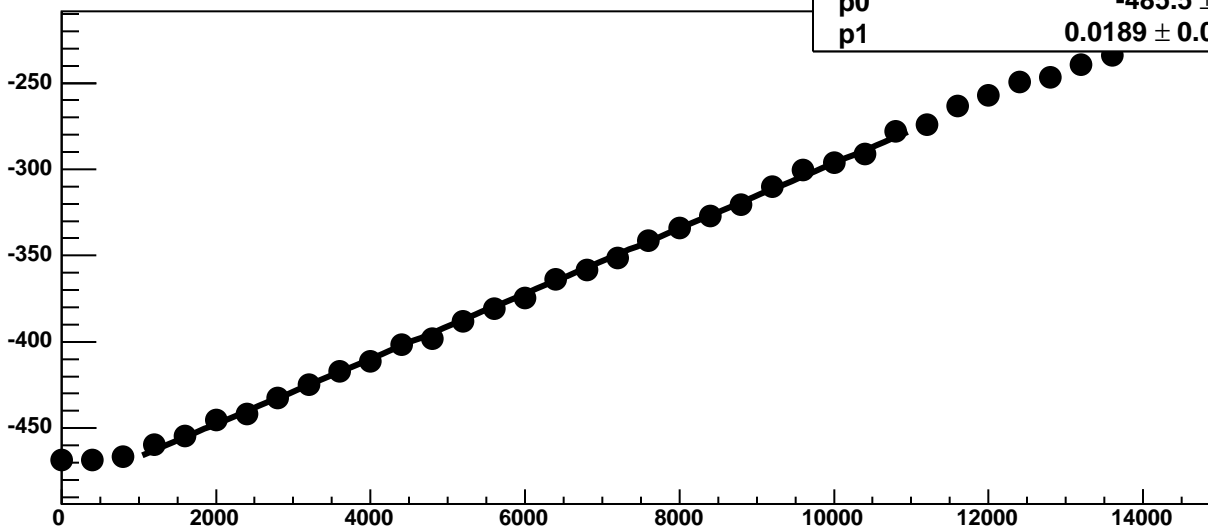
Chip 9, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

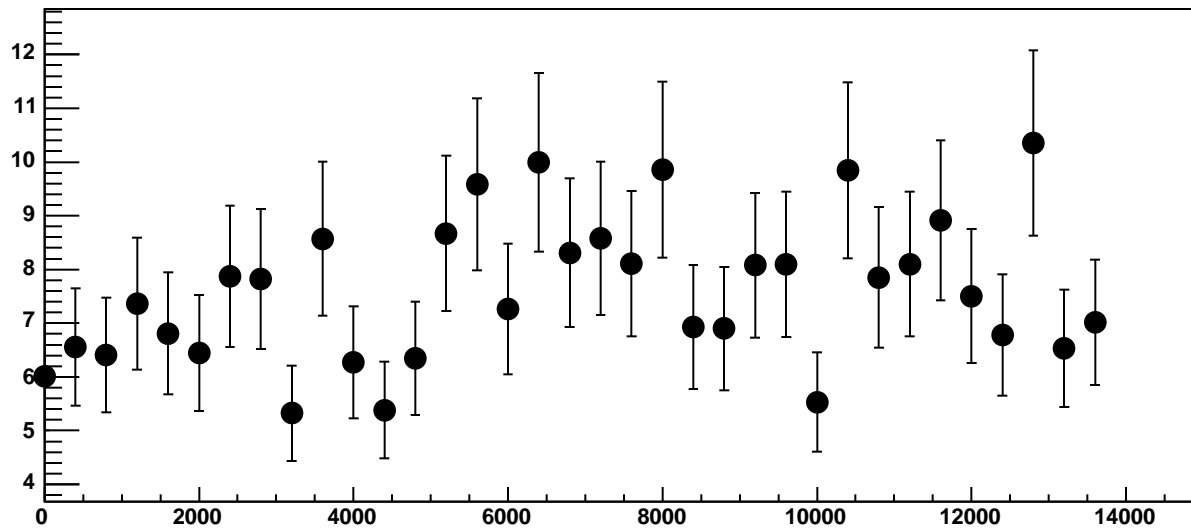


Chip 9, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

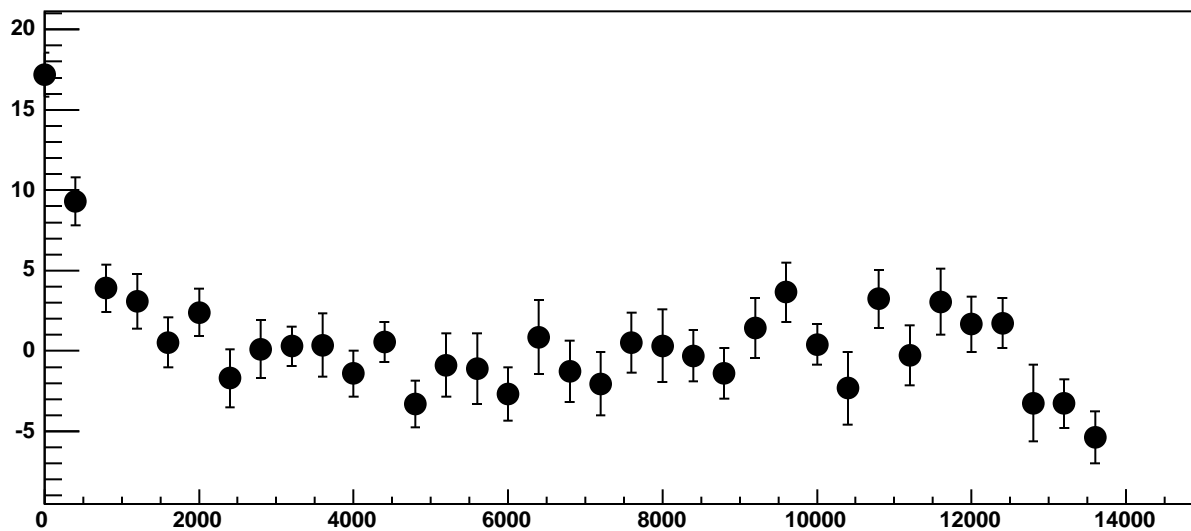


$\chi^2 / \text{ndf}$  27.83 / 23  
p0  $-485.5 \pm 0.7368$   
p1  $0.0189 \pm 0.0001146$

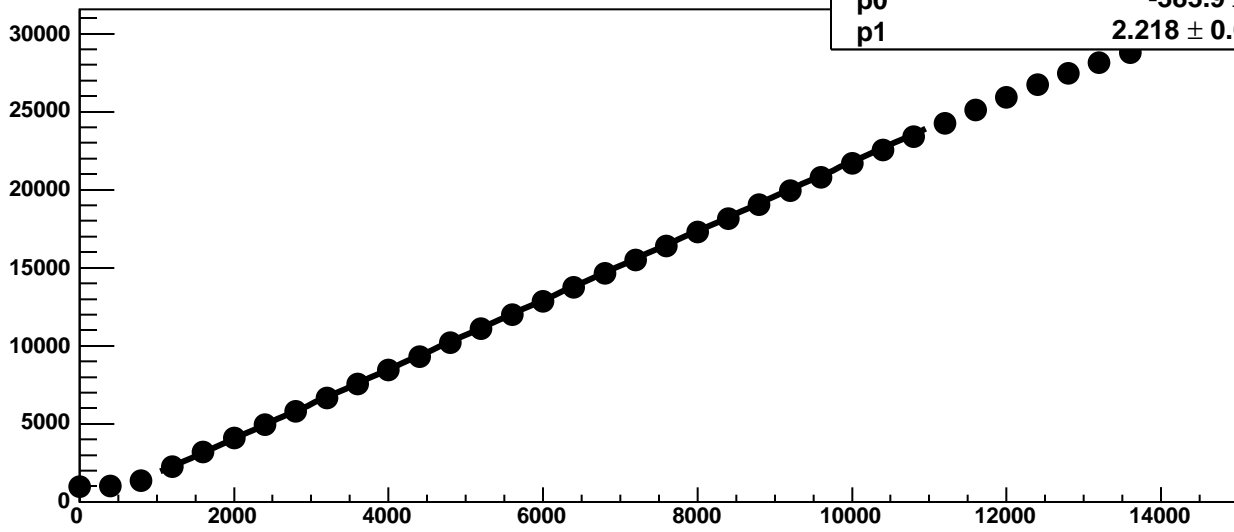
Chip 9, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

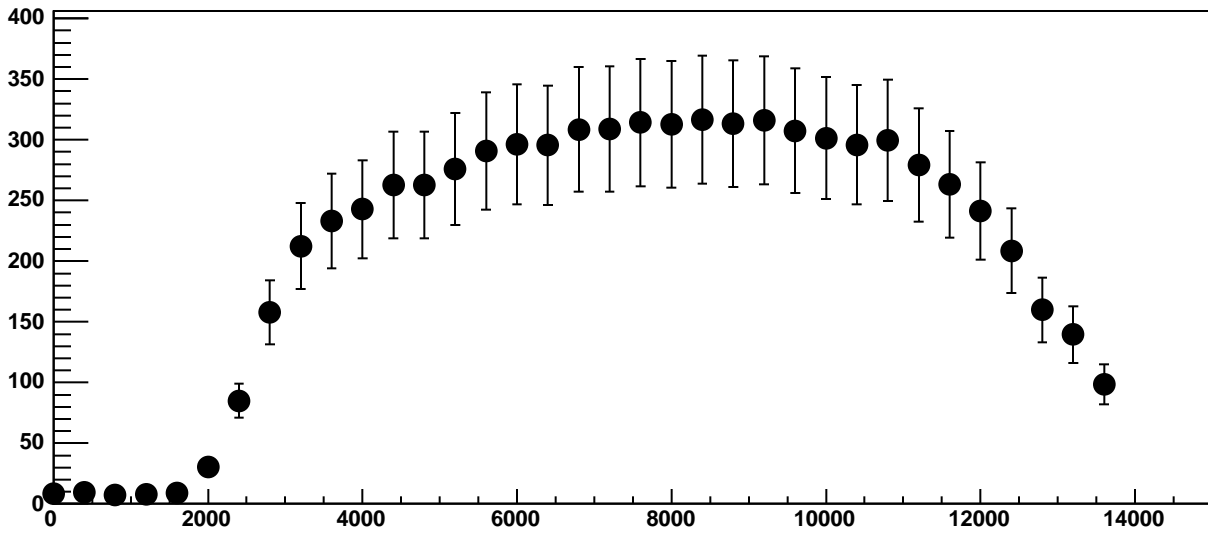


Chip 9, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

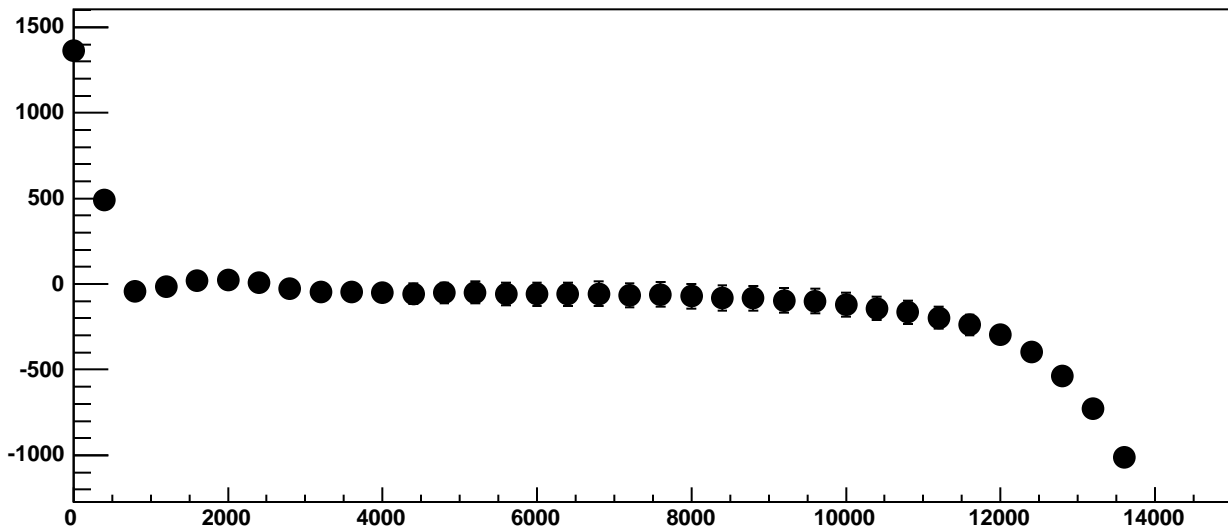


$\chi^2 / \text{ndf}$	191.9 / 23
p0	$-383.9 \pm 3.619$
p1	$2.218 \pm 0.002323$

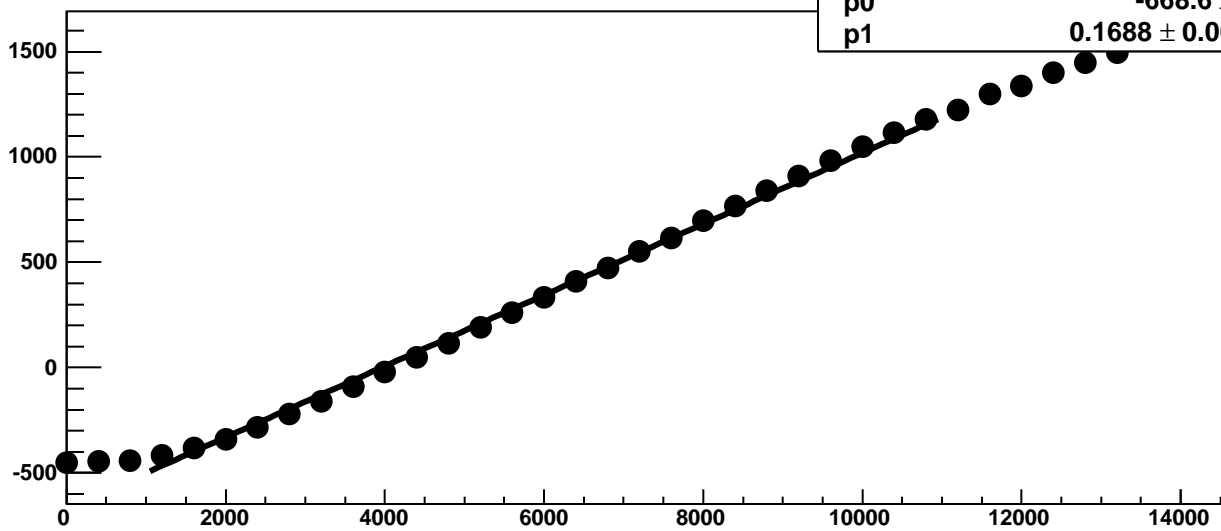
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



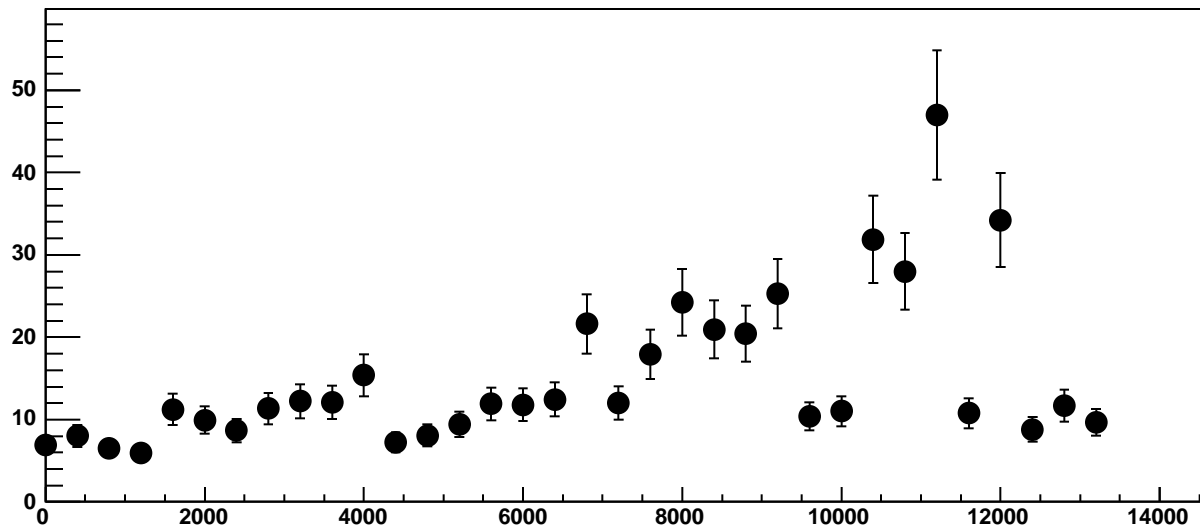
Chip 9, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC



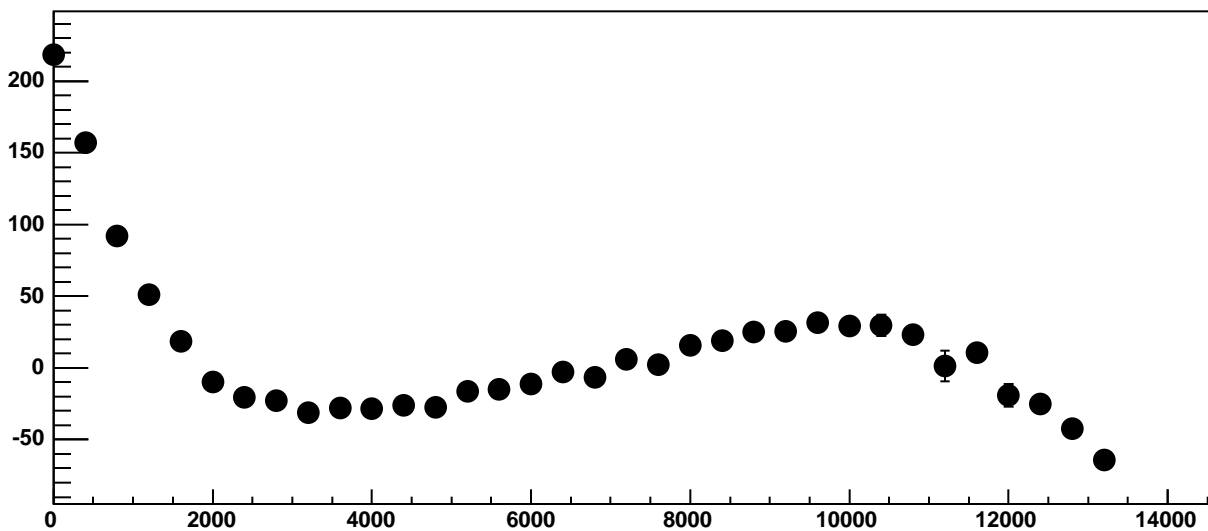
Chip 9, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

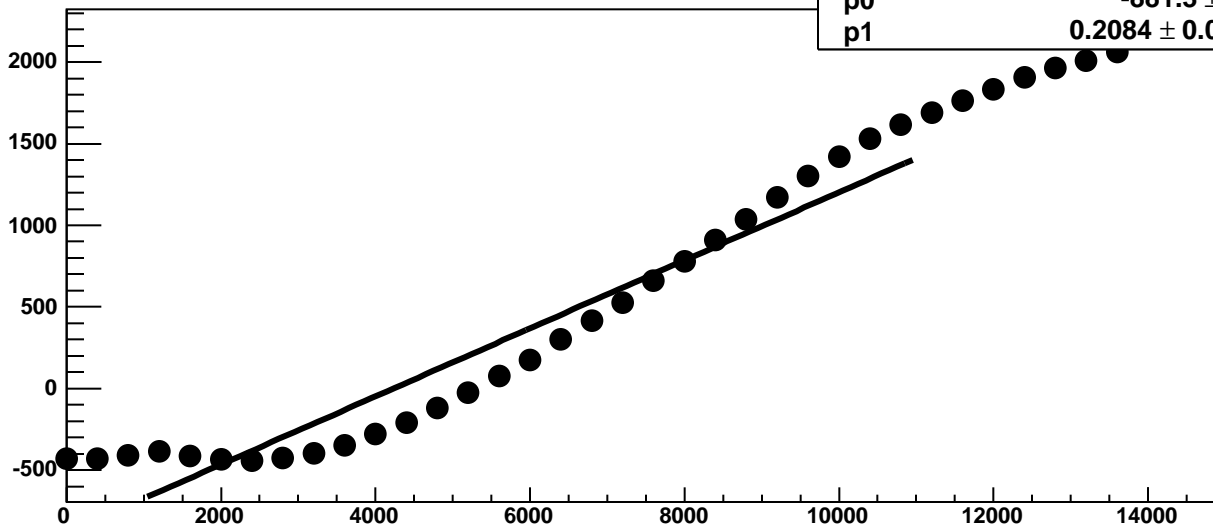


Chip 9, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

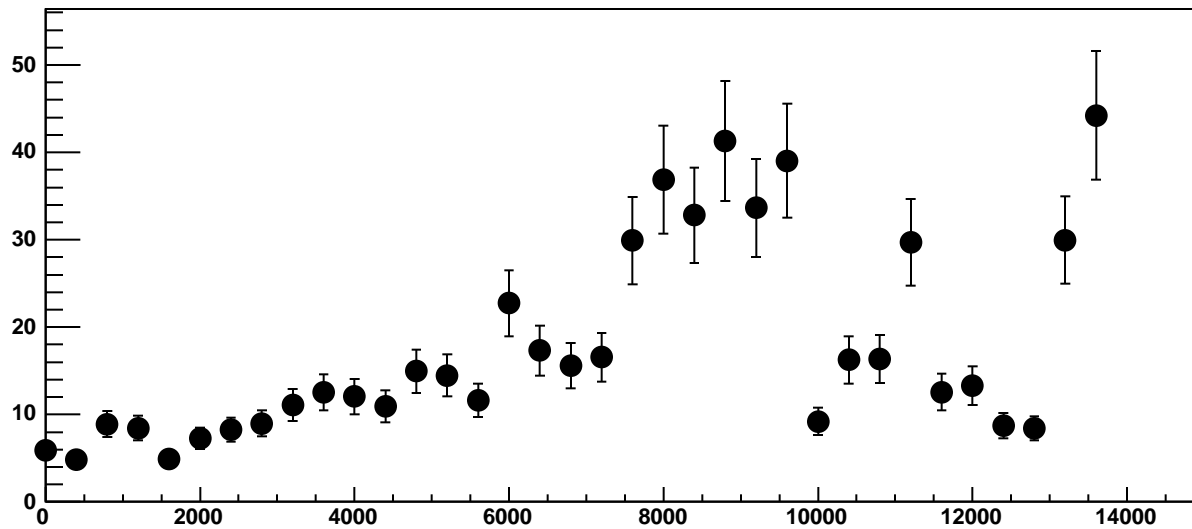


Chip 9, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

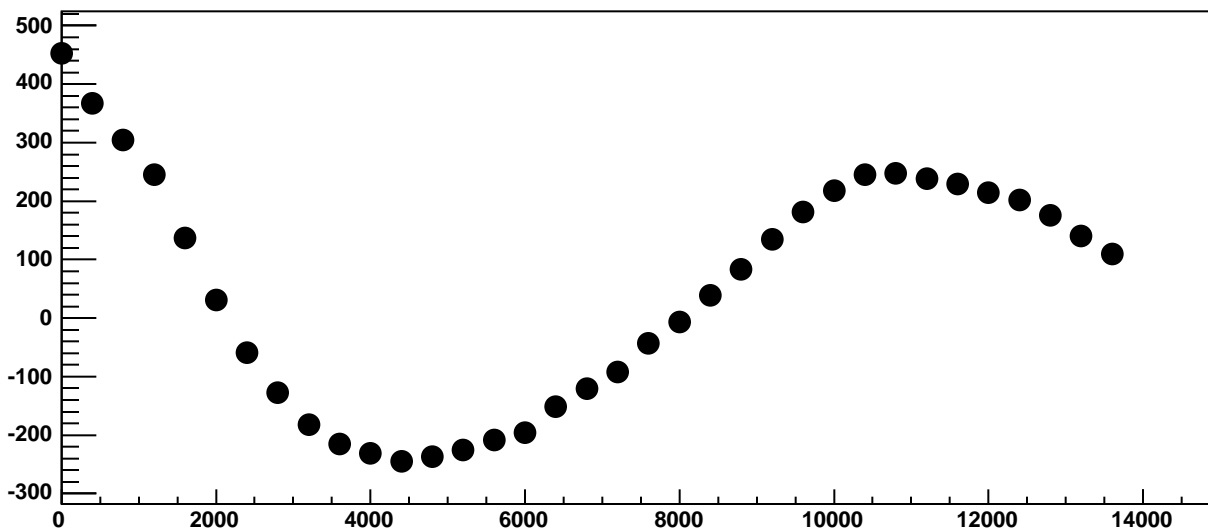
$\chi^2 / \text{ndf}$  1.031e+05 / 23  
p0 -881.3 ± 0.9102  
p1 0.2084 ± 0.0001898



Chip 9, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

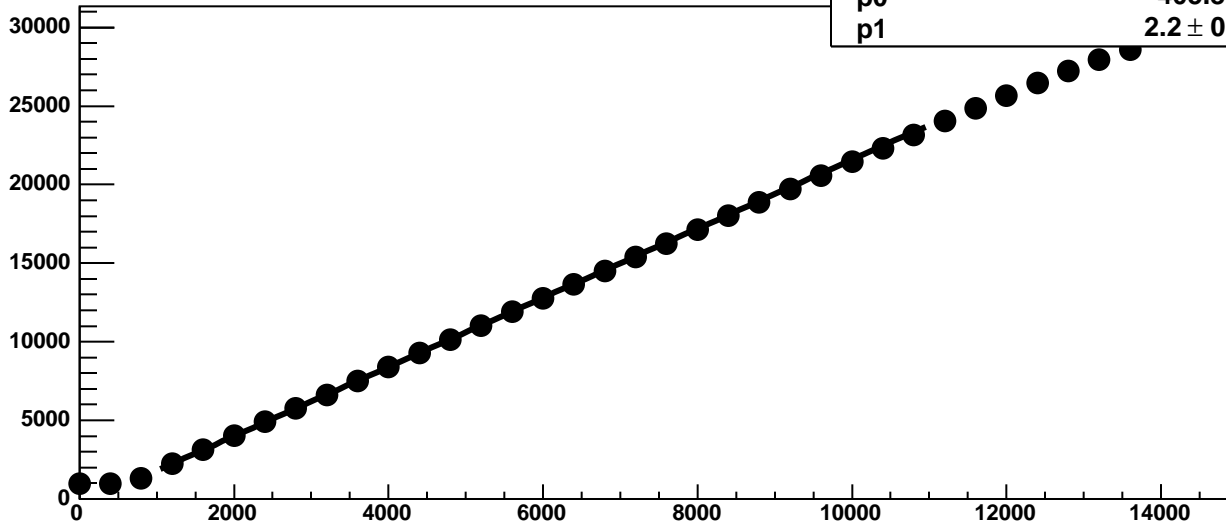


Chip 9, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

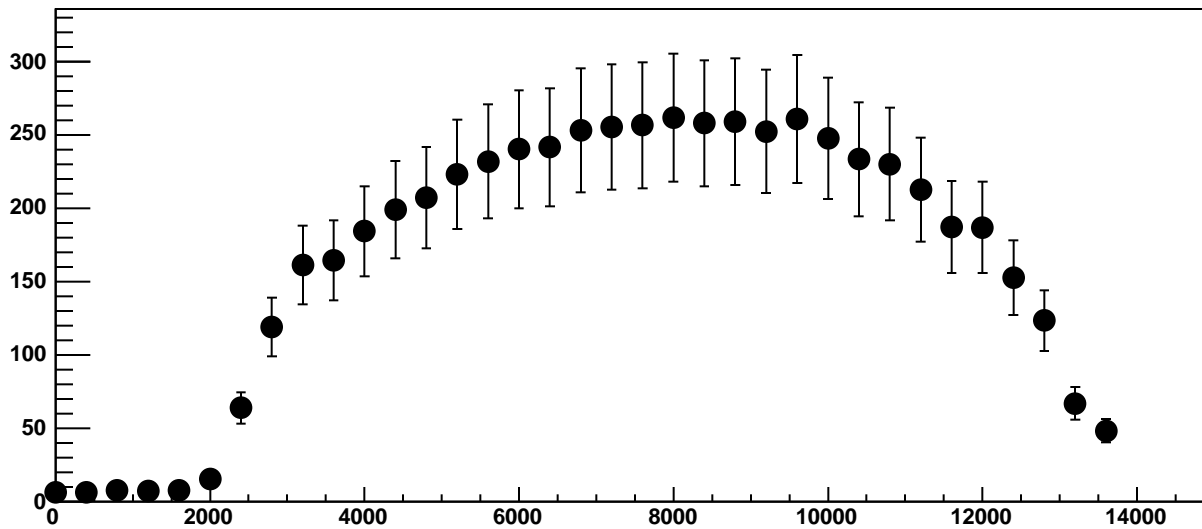


Chip 9, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

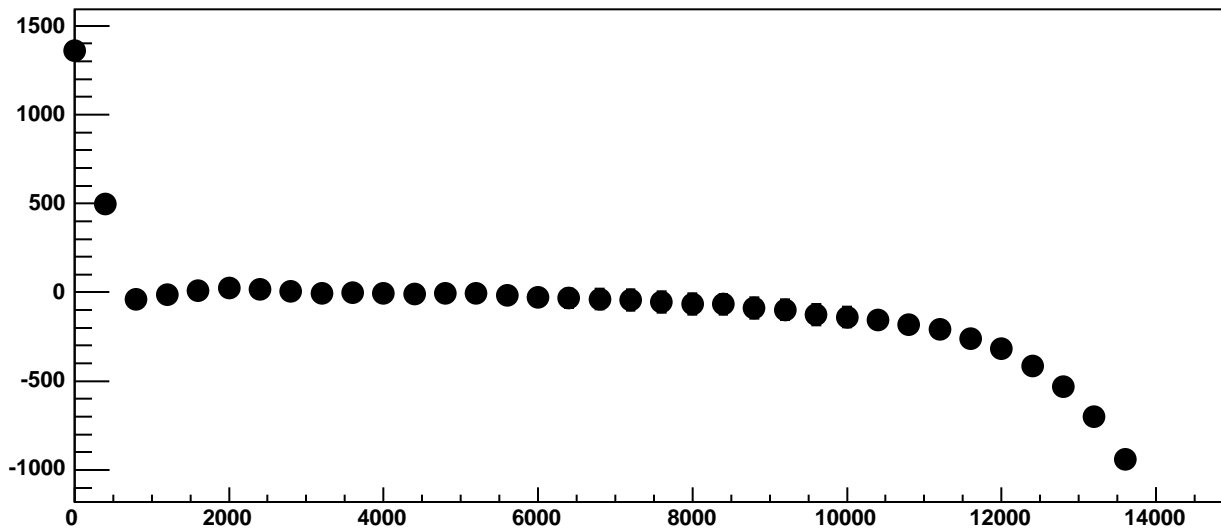
$\chi^2 / \text{ndf}$  192.9 / 23  
p0  $-405.8 \pm 3.059$   
p1  $2.2 \pm 0.001866$



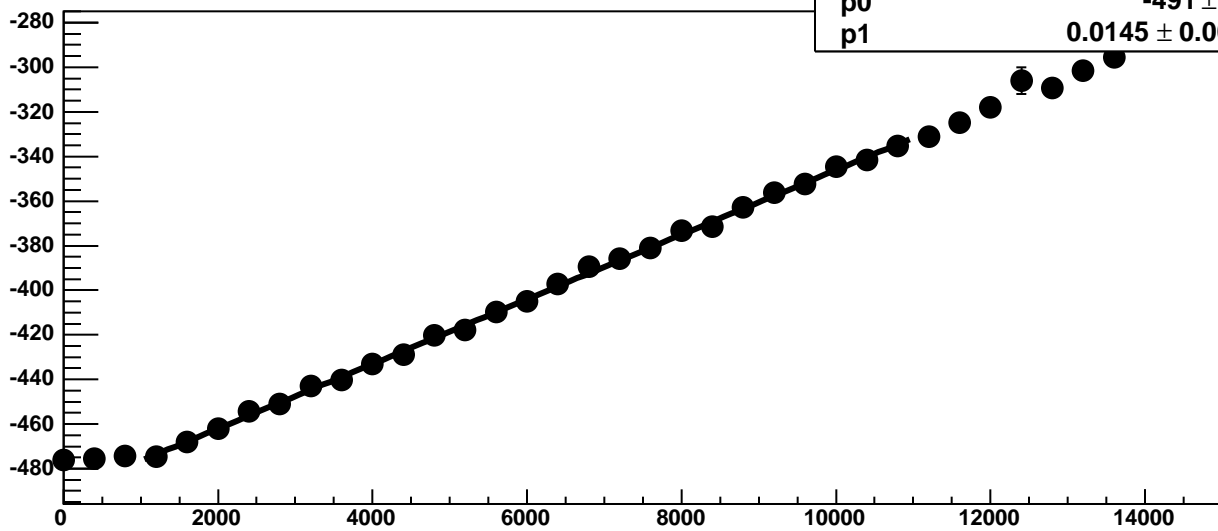
Chip 9, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC

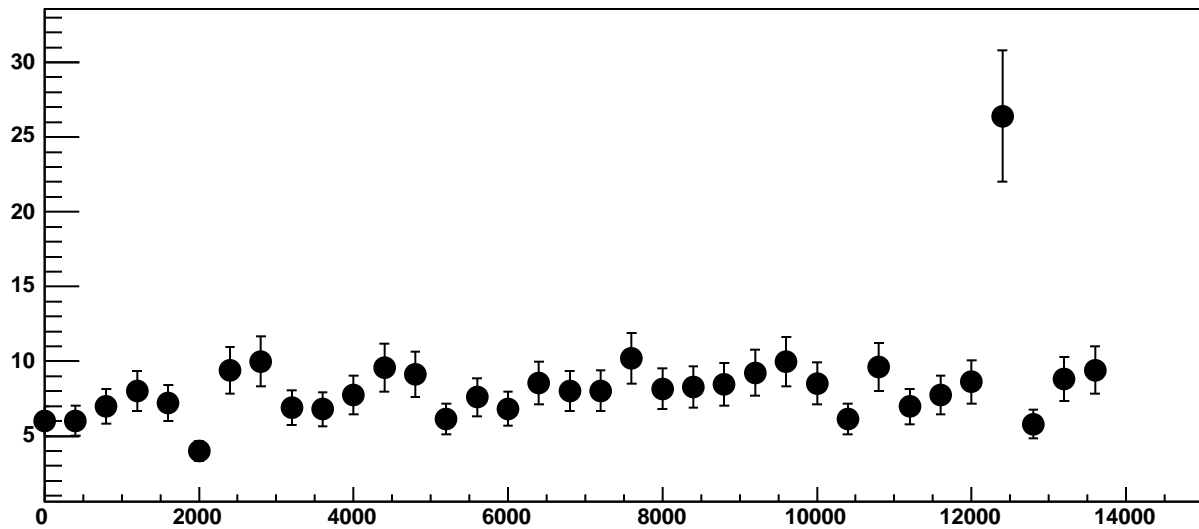


Chip 9, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

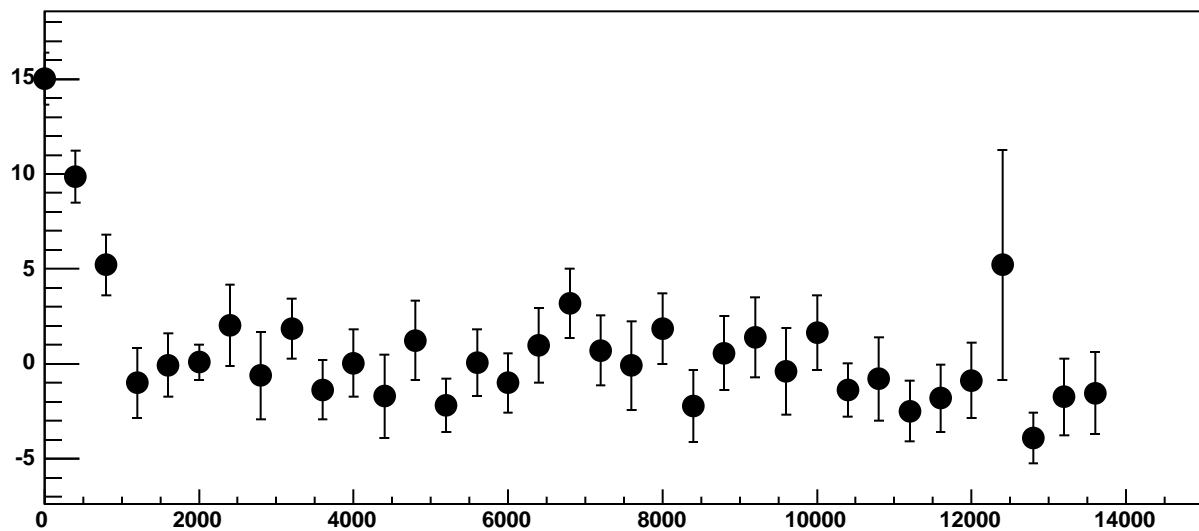


$\chi^2 / \text{ndf}$  15.26 / 23  
p0  $-491 \pm 0.7302$   
p1  $0.0145 \pm 0.0001174$

Chip 9, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

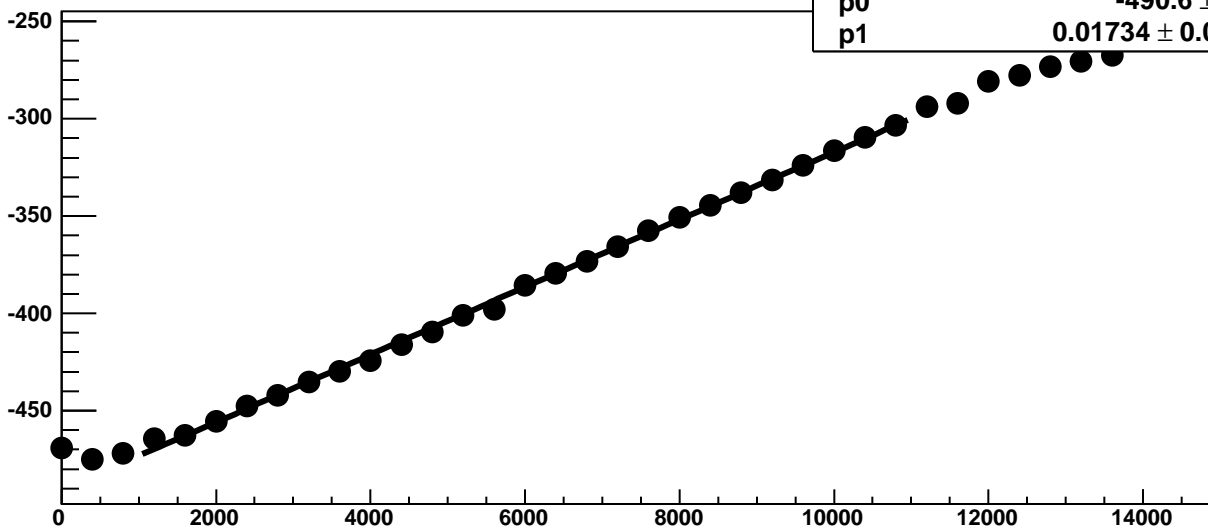


Chip 9, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

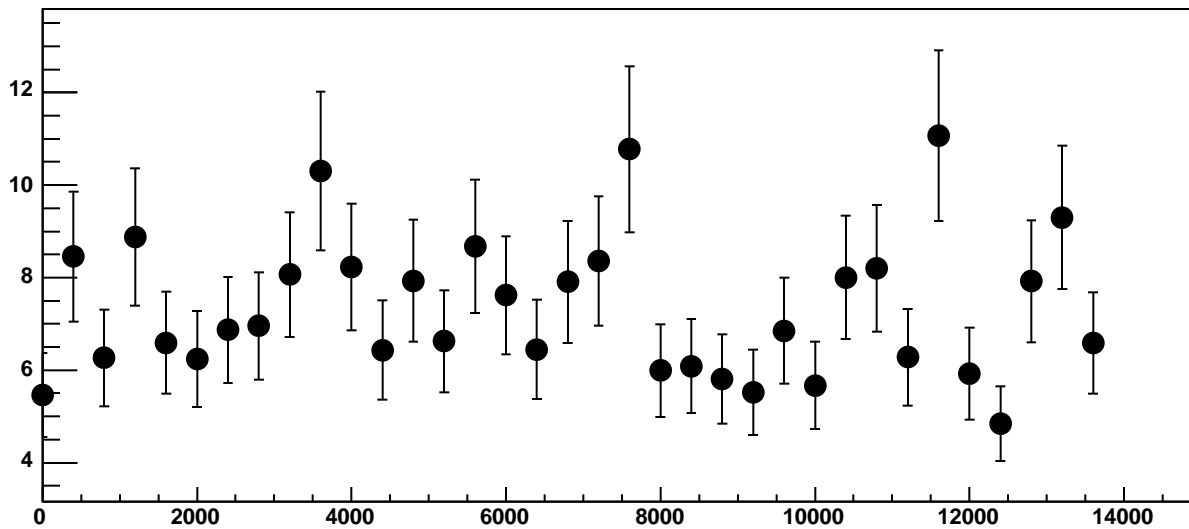




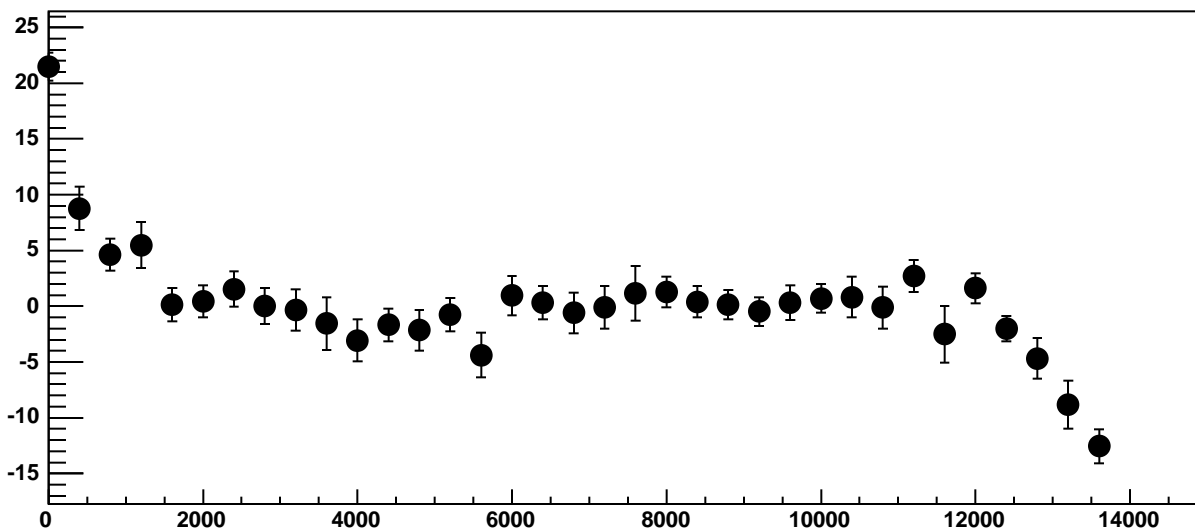
Chip 9, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



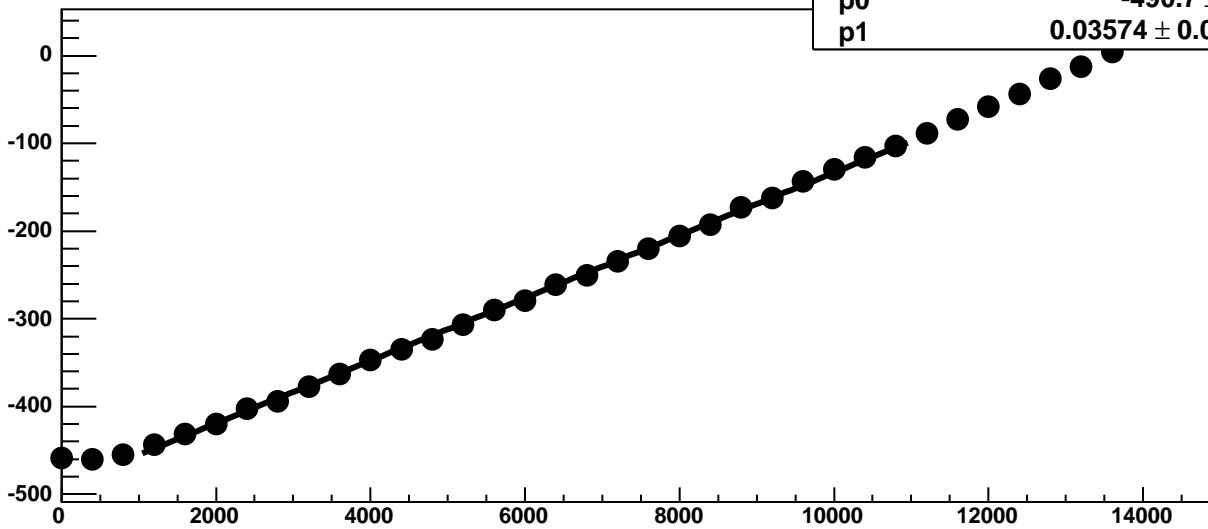
Chip 9, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

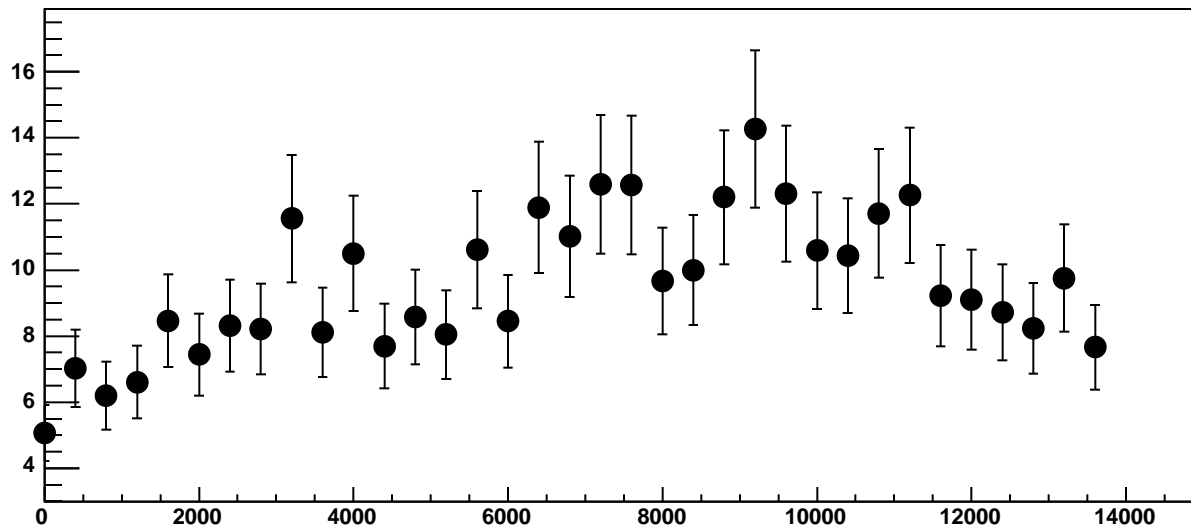


Chip 9, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

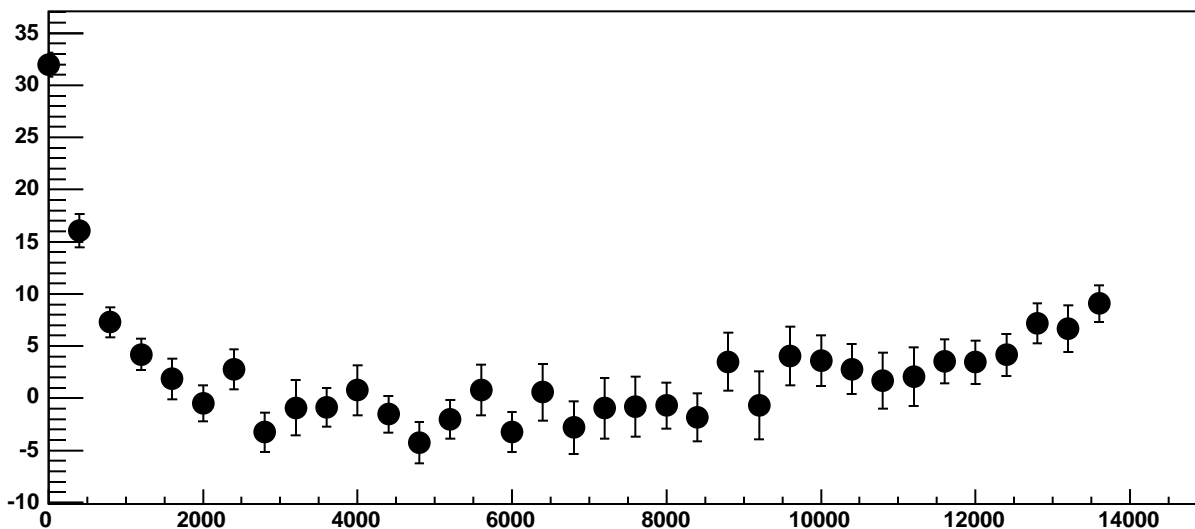


$\chi^2 / \text{ndf}$  33.53 / 23  
p0  $-490.7 \pm 0.8941$   
p1  $0.03574 \pm 0.0001516$

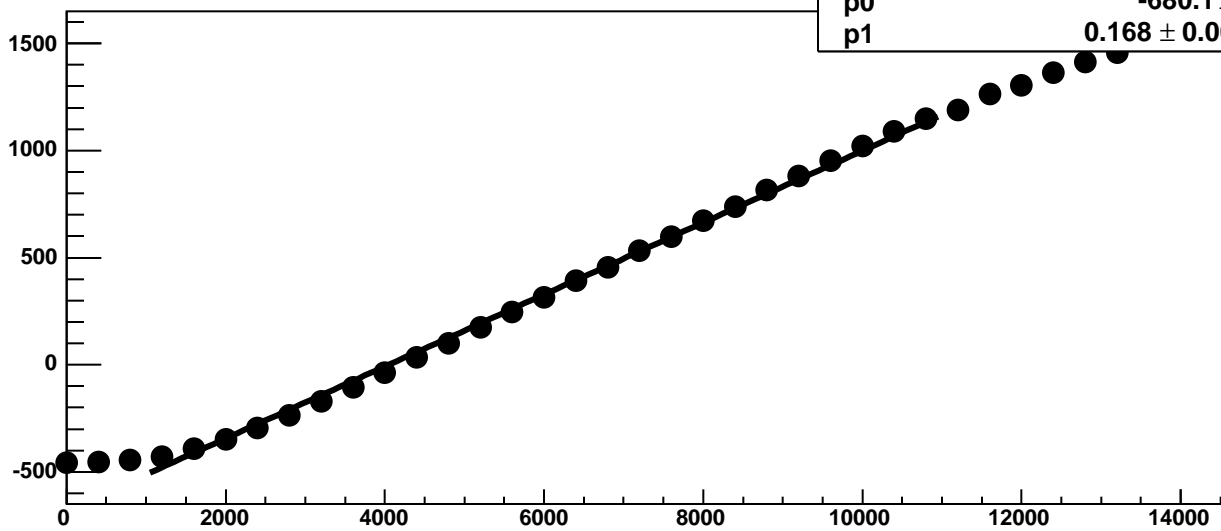
Chip 9, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



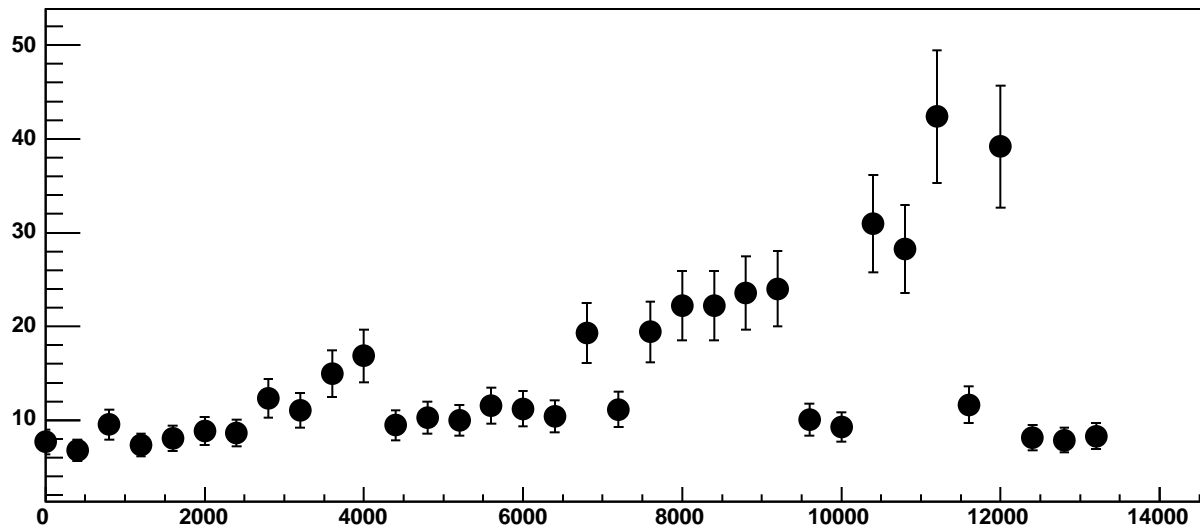
Chip 9, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



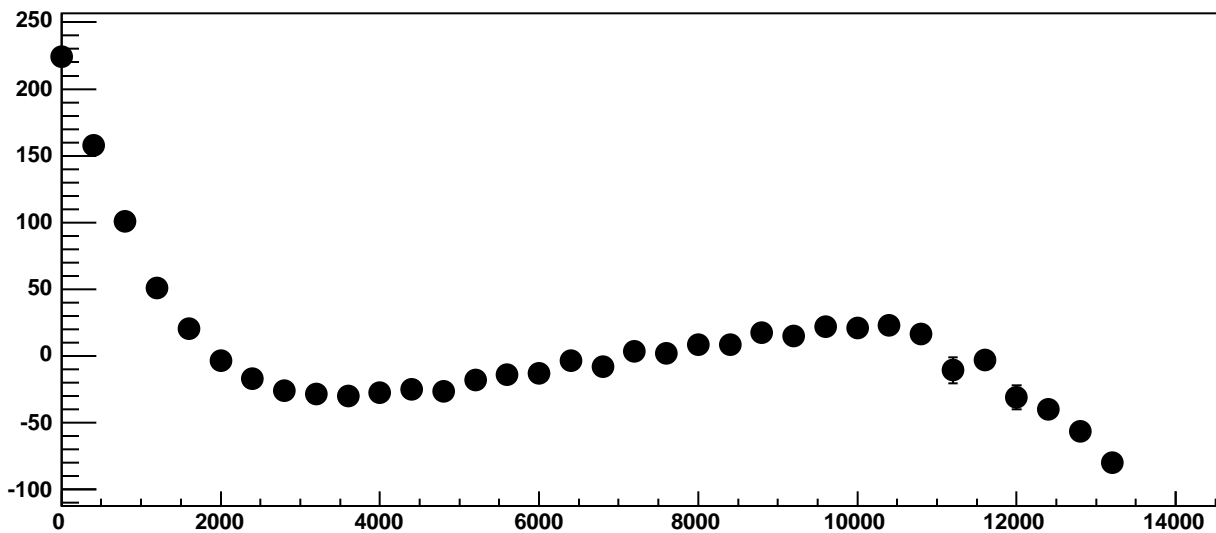
Chip 9, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



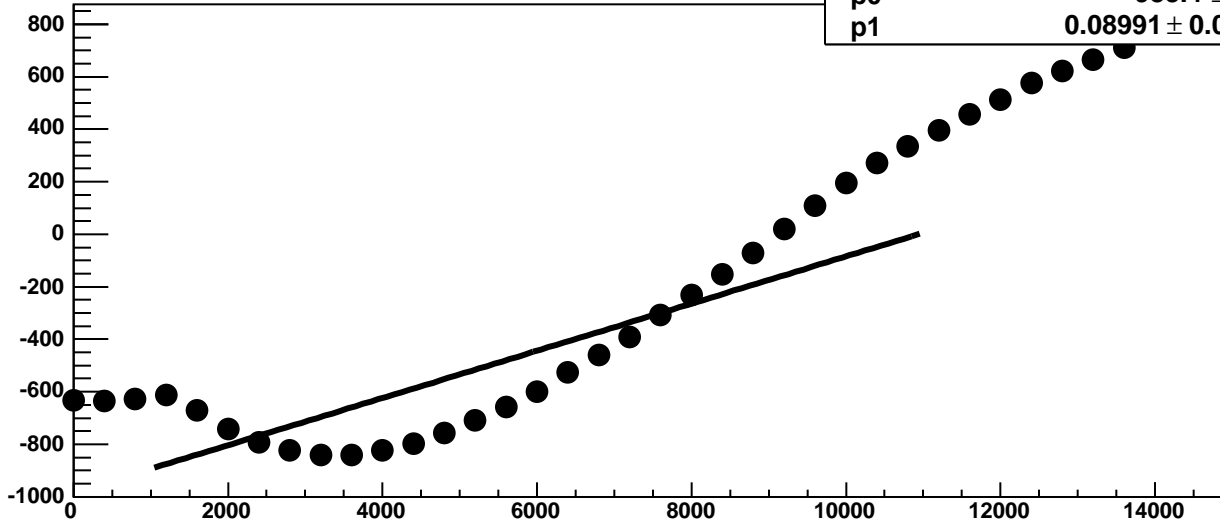
Chip 9, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



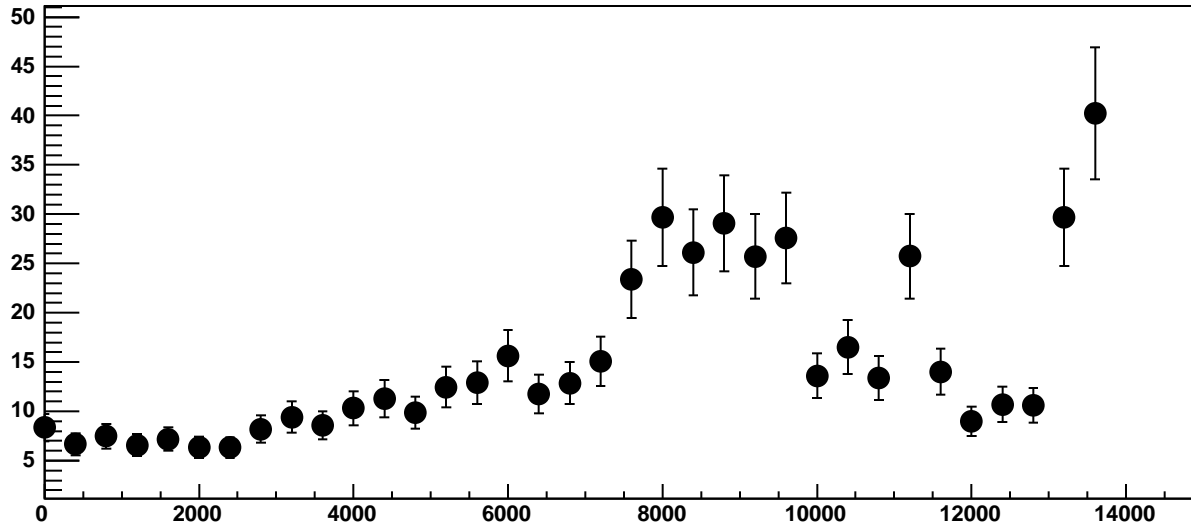
Chip 9, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



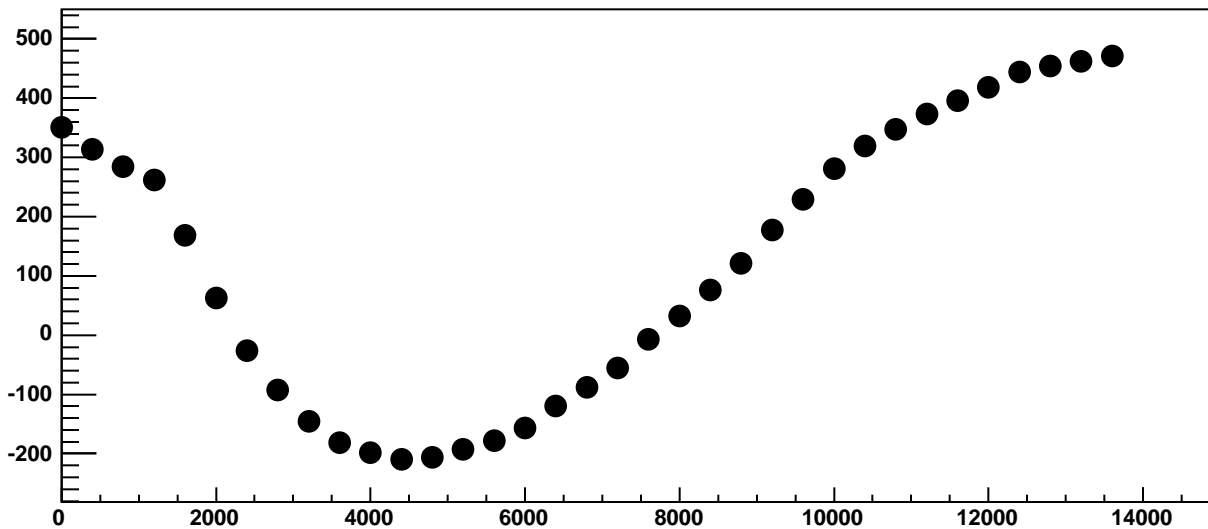
Chip 9, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



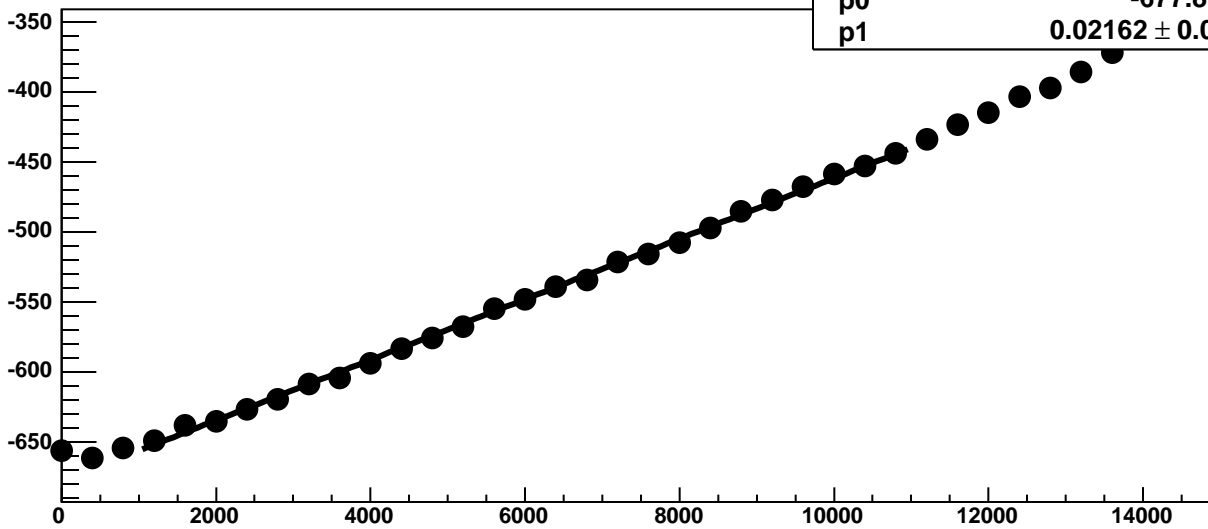
Chip 9, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

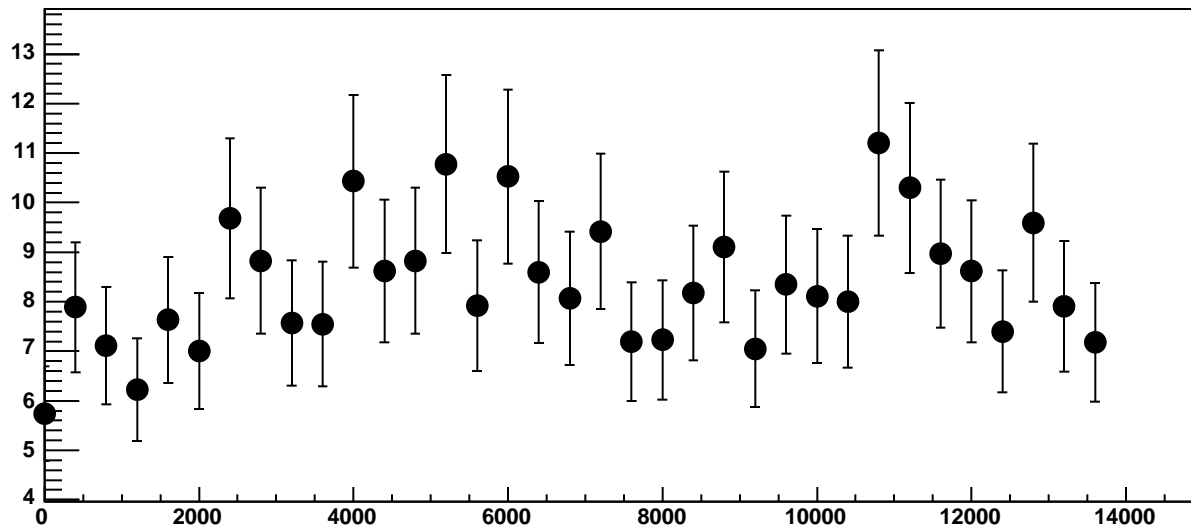


Chip 9, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

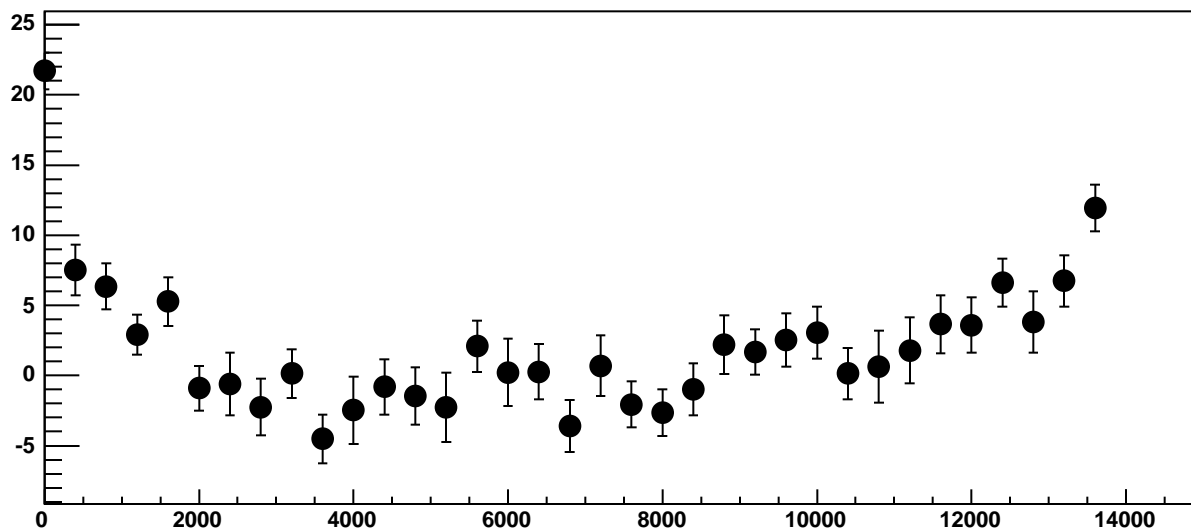


$\chi^2 / \text{ndf}$  40.52 / 23  
p0  $-677.8 \pm 0.826$   
p1  $0.02162 \pm 0.0001262$

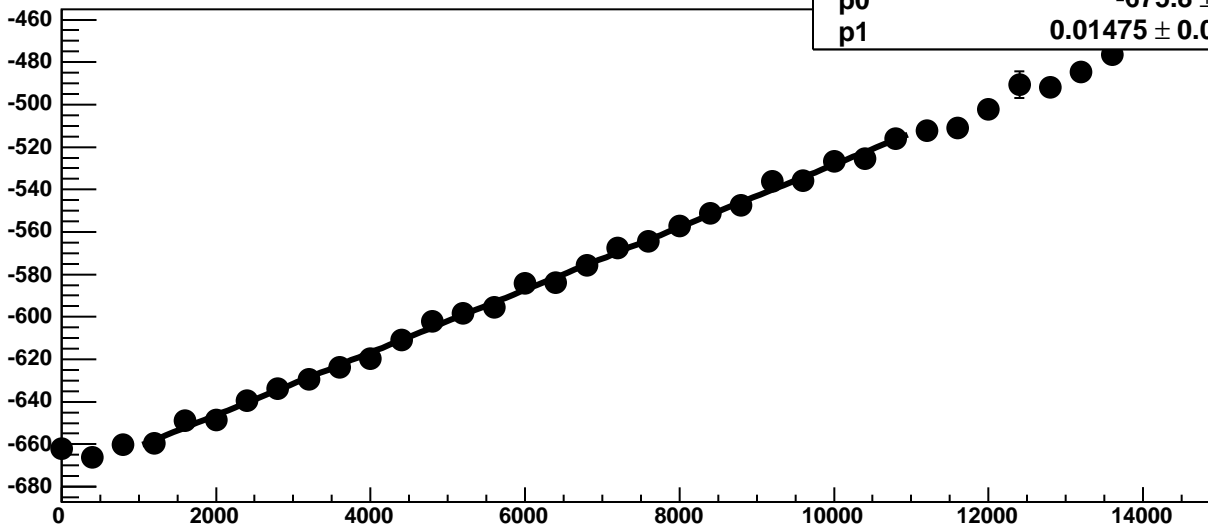
Chip 9, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



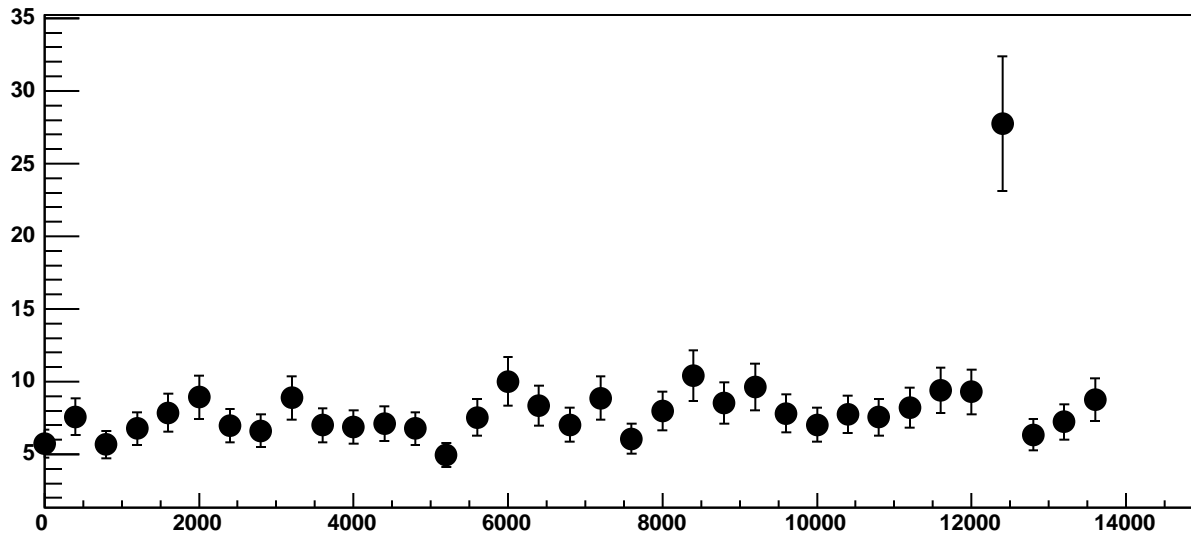
Chip 9, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



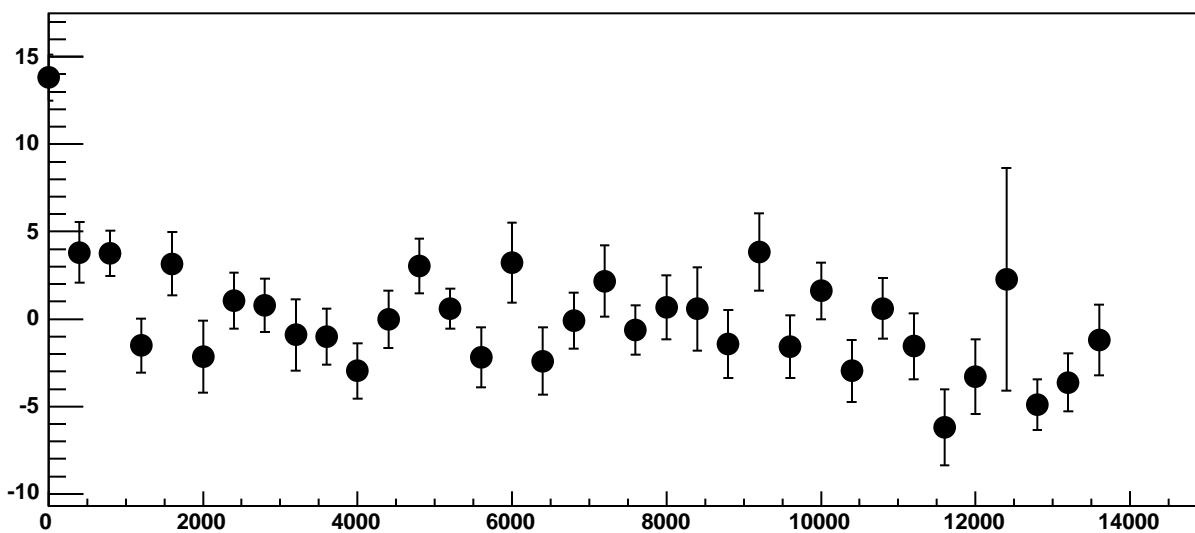
Chip 9, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



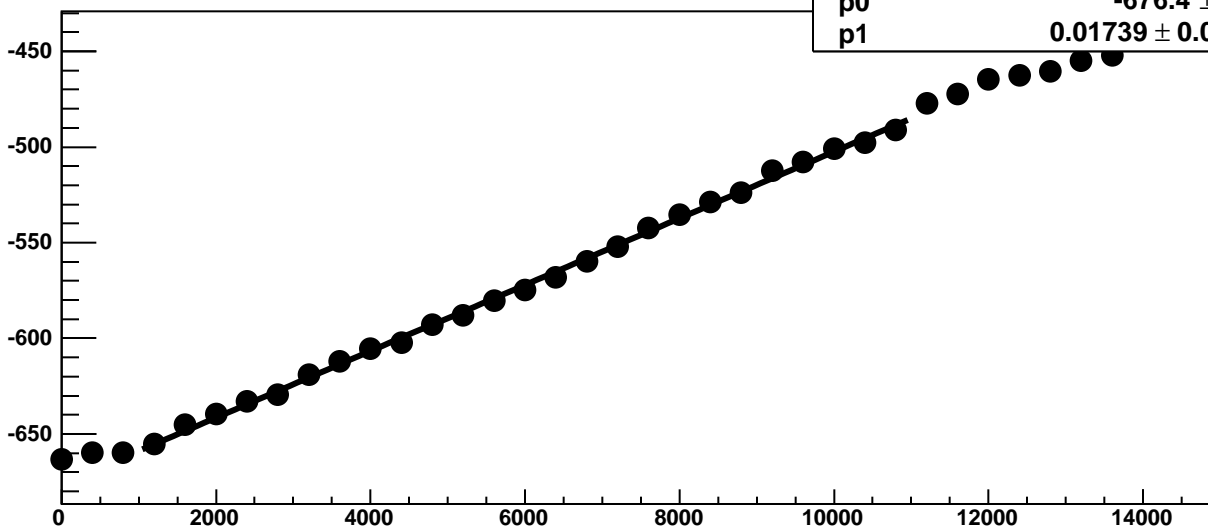
Chip 9, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



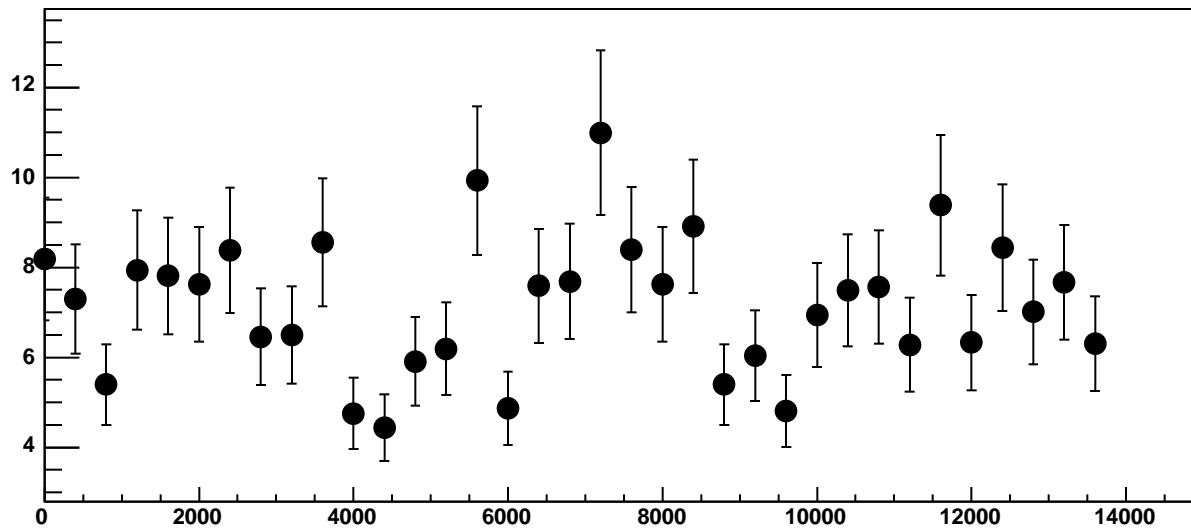
Chip 9, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



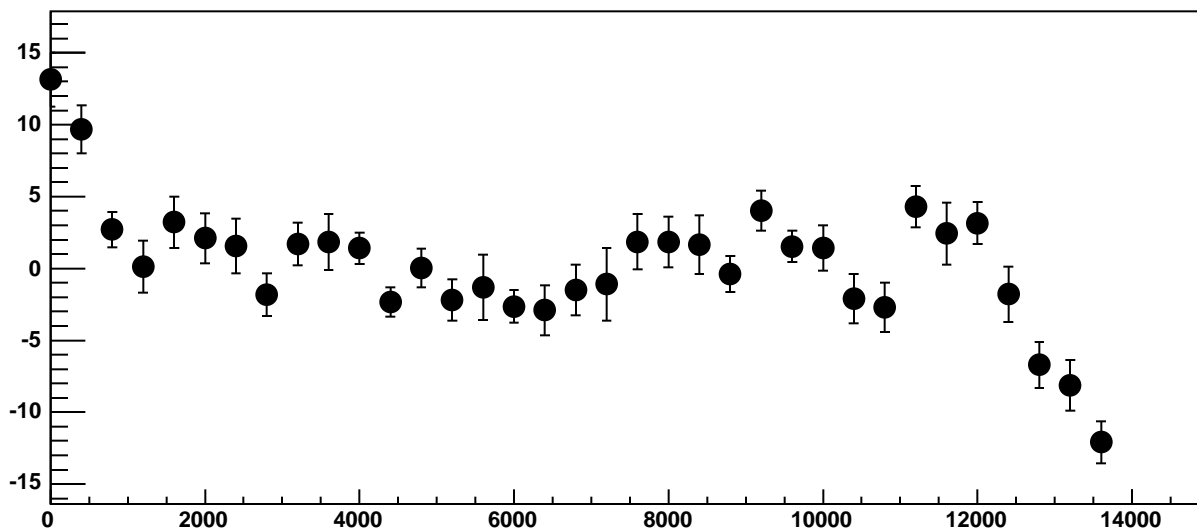
Chip 9, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



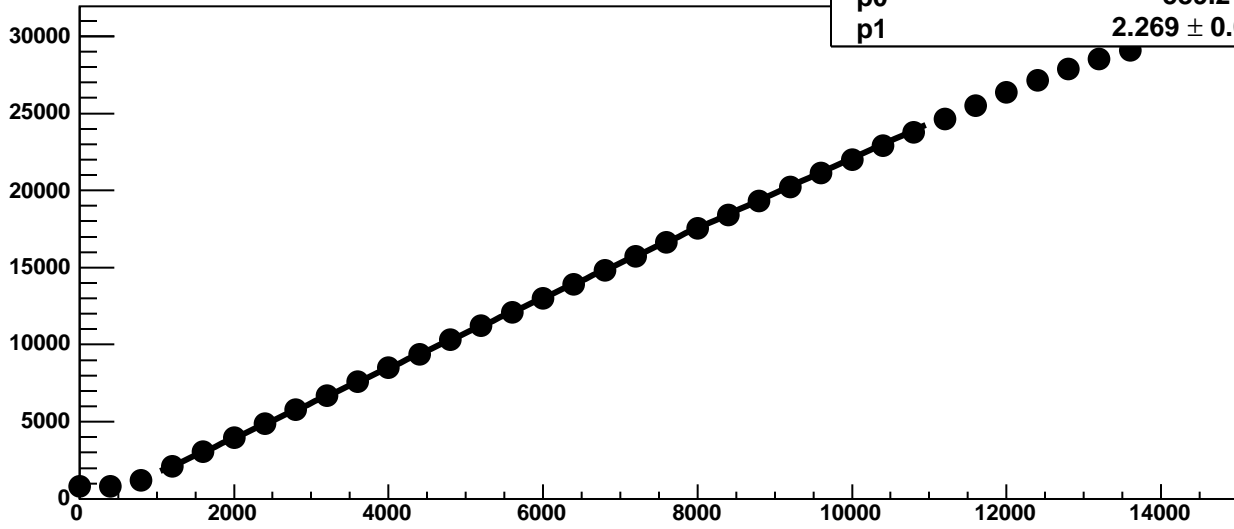
Chip 9, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



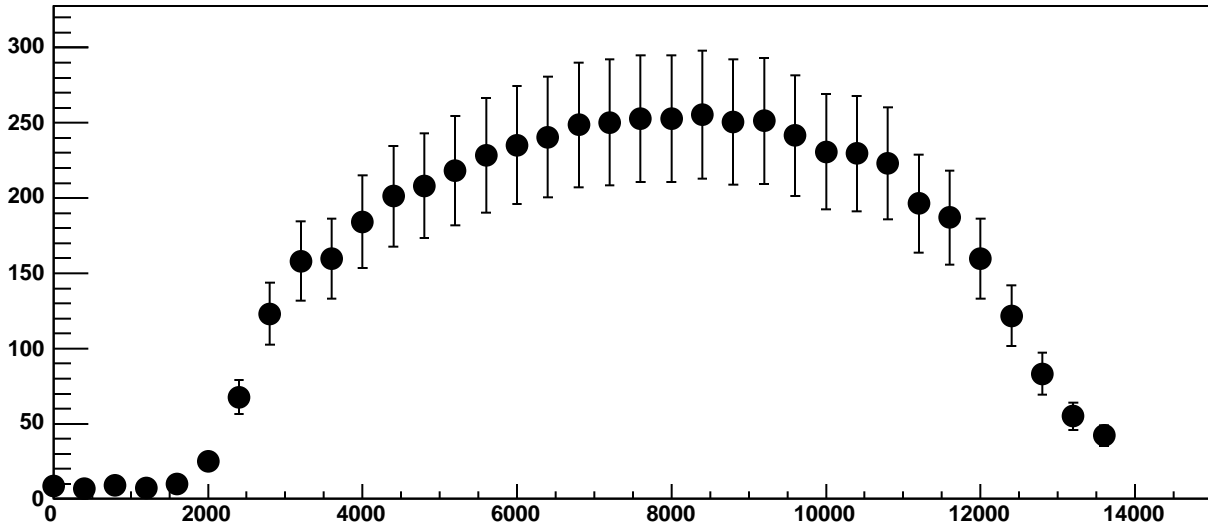
Chip 9, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



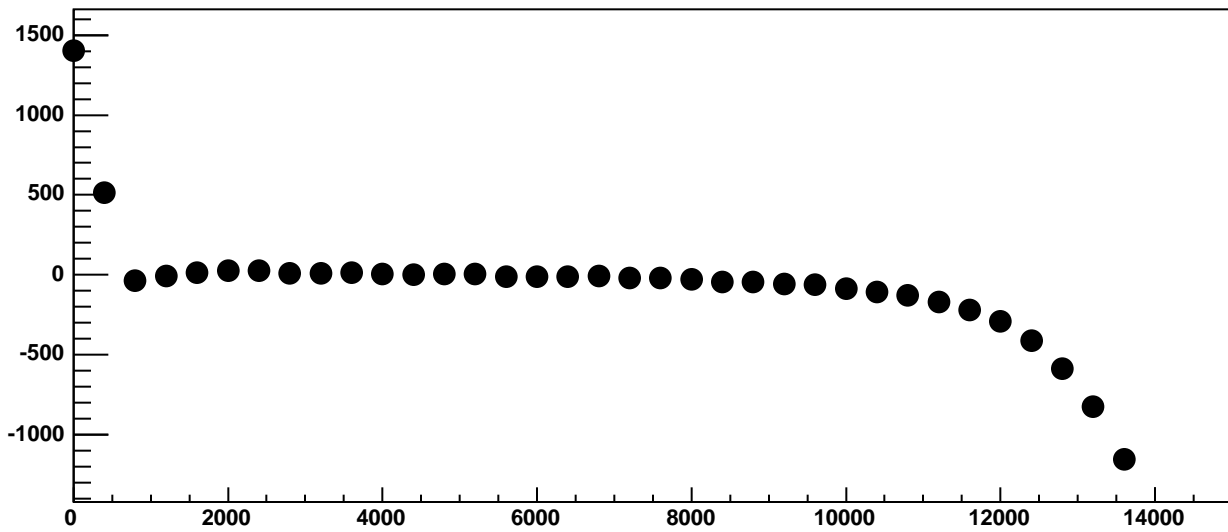
Chip 9, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 9, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

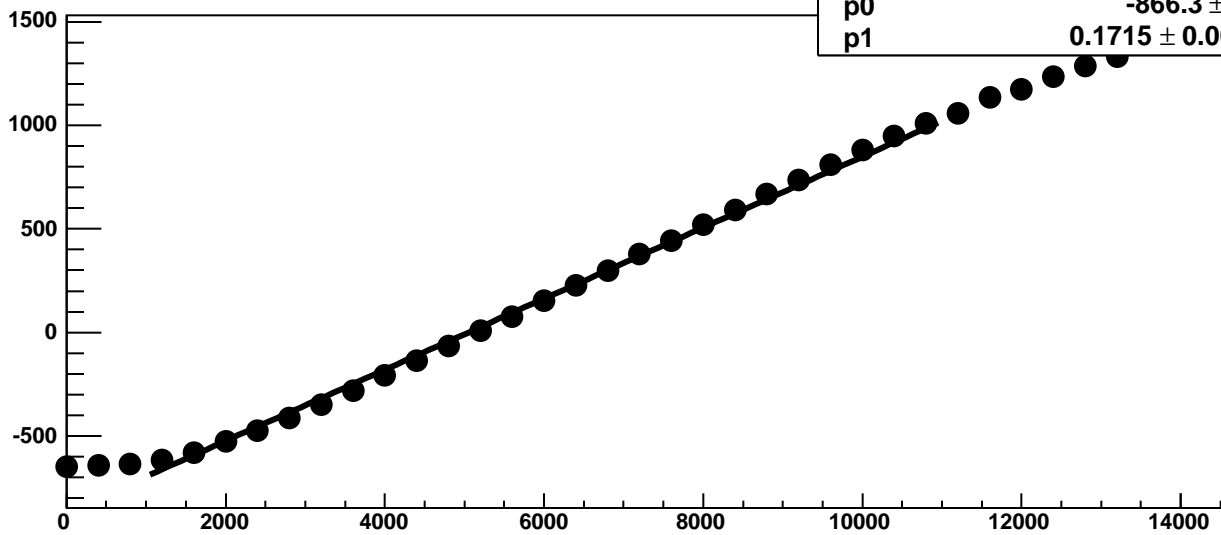


Chip 9, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC



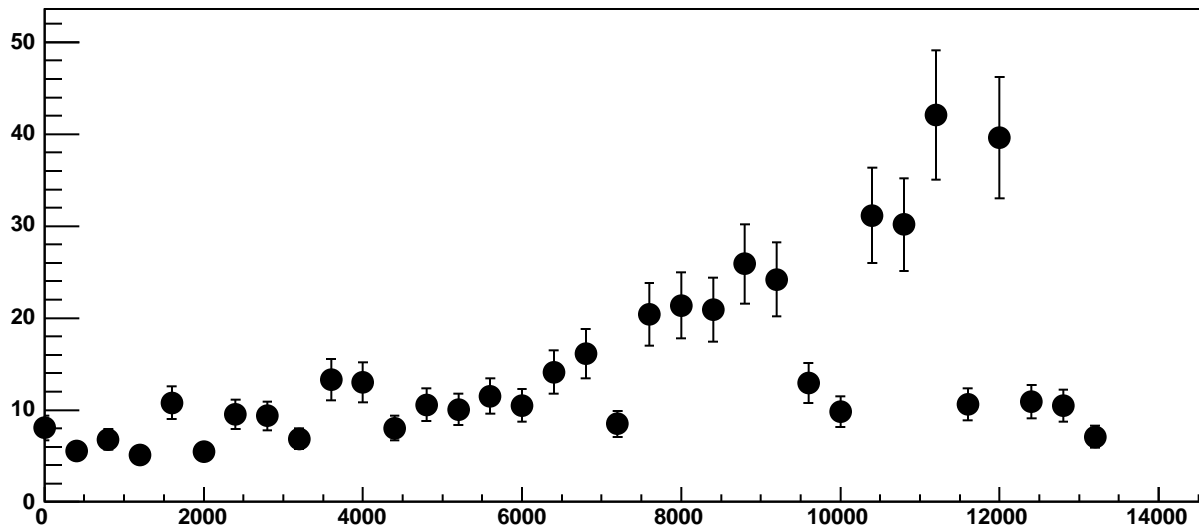


Chip 9, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

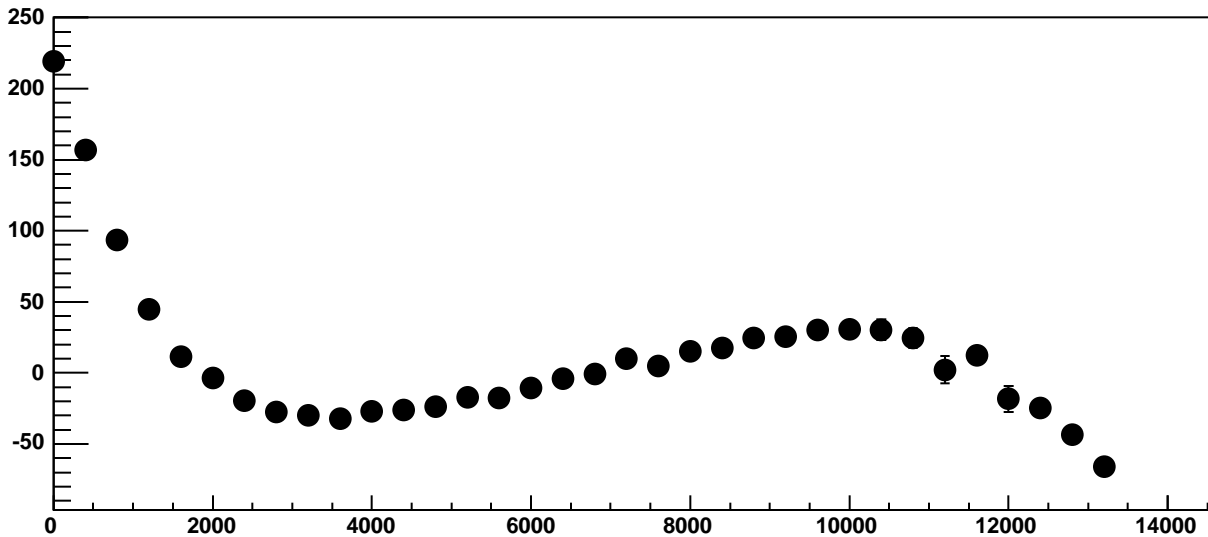


$\chi^2 / \text{ndf}$  3097 / 23  
p0 -866.3 ± 0.8741  
p1 0.1715 ± 0.0001782

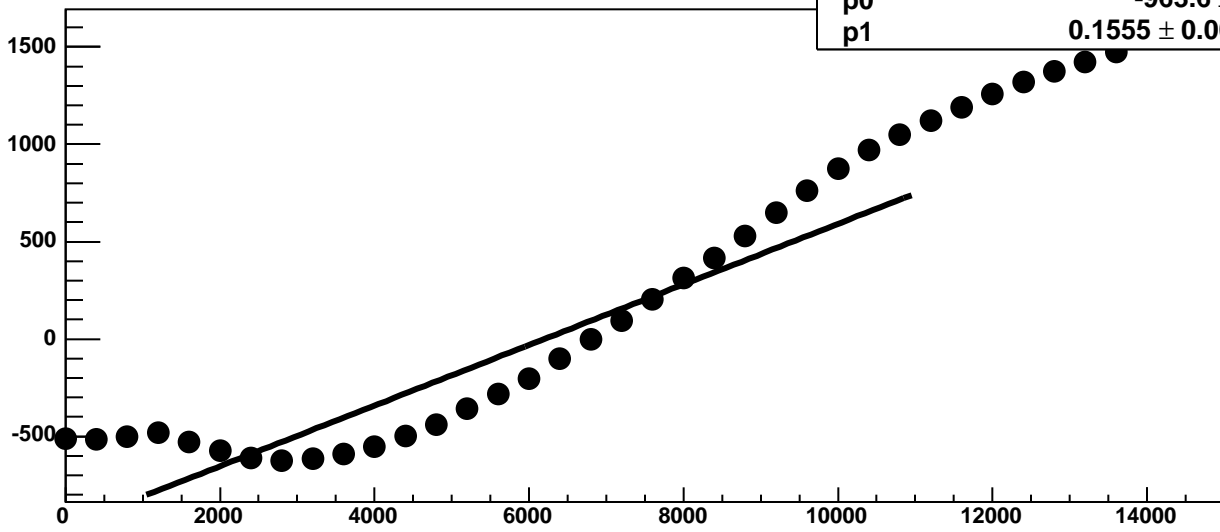
Chip 9, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

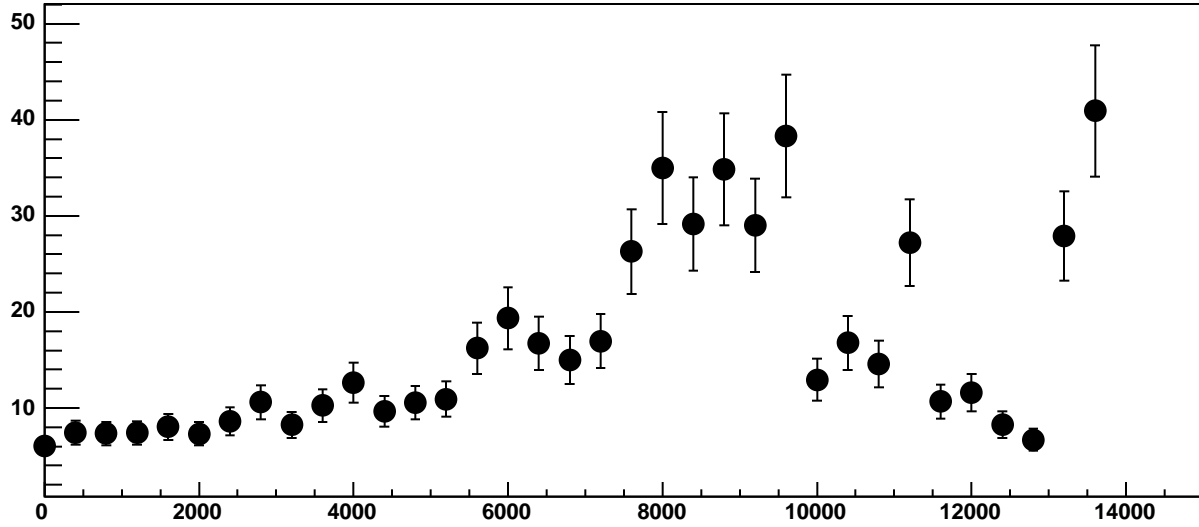


Chip 9, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

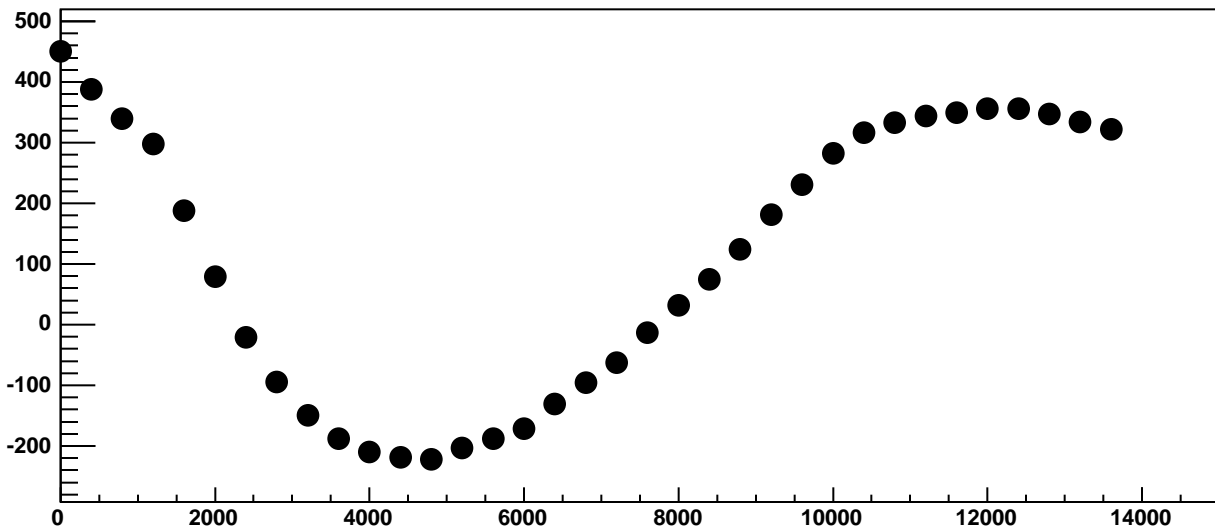


$\chi^2 / \text{ndf}$  1.214e+05 / 23  
p0 -963.6 ± 1.014  
p1 0.1555 ± 0.0002094

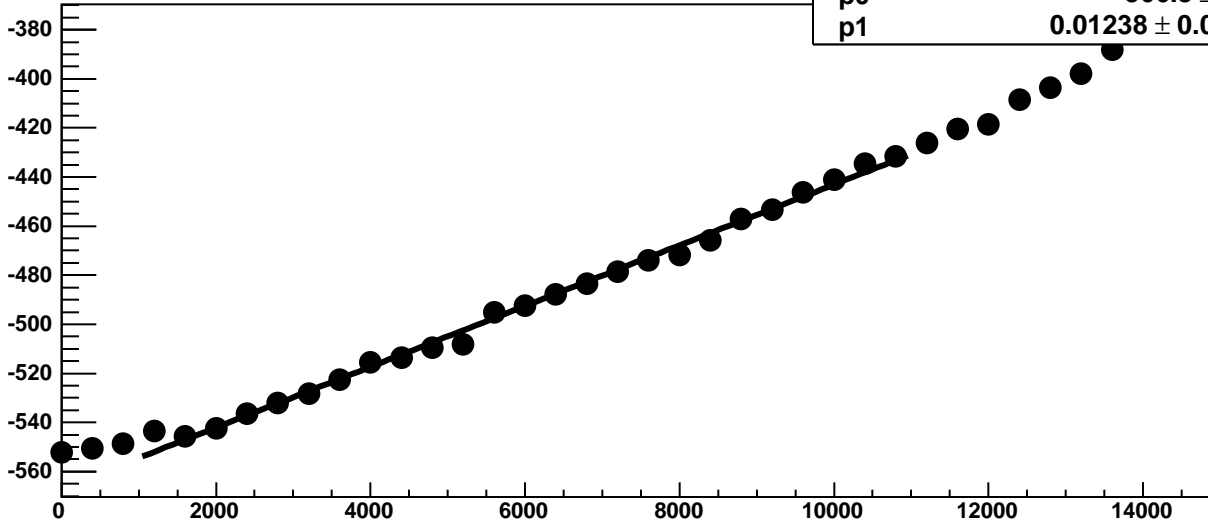
Chip 9, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



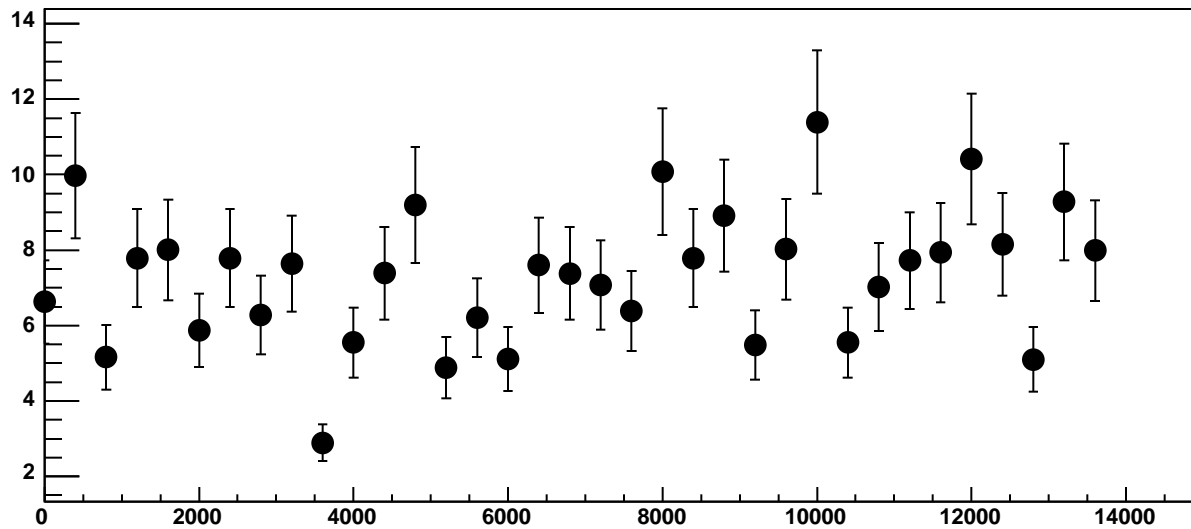
Chip 9, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



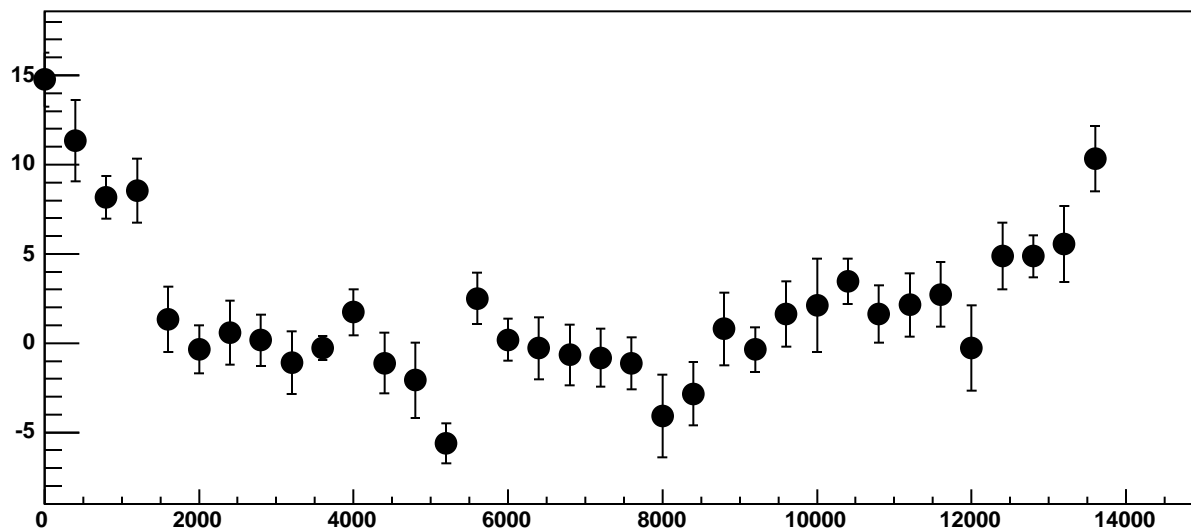
Chip 9, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



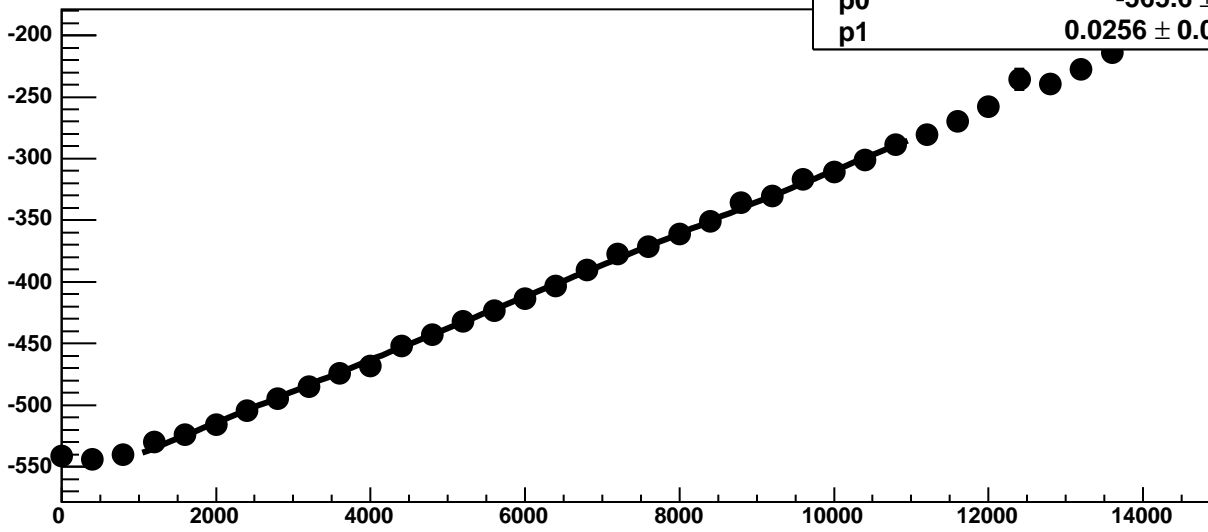
Chip 9, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

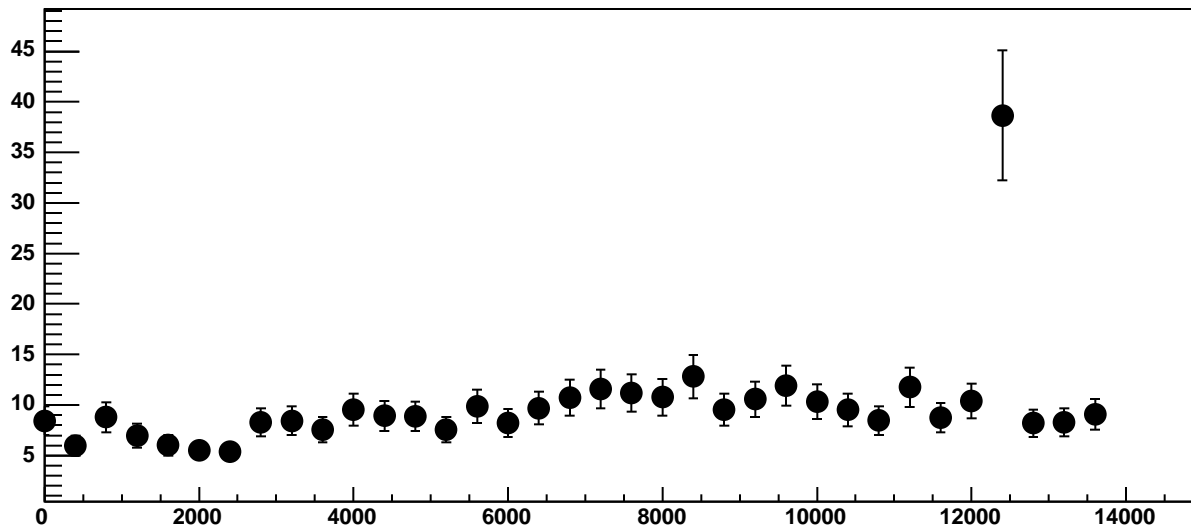


Chip 9, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

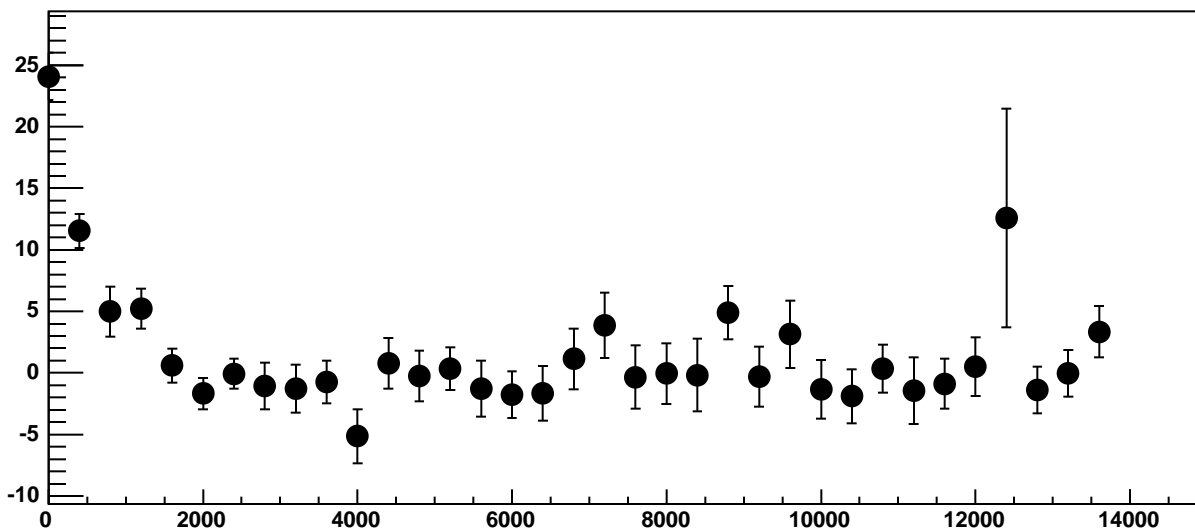


$\chi^2 / \text{ndf}$  30.8 / 23  
p0  $-565.6 \pm 0.7493$   
p1  $0.0256 \pm 0.0001302$

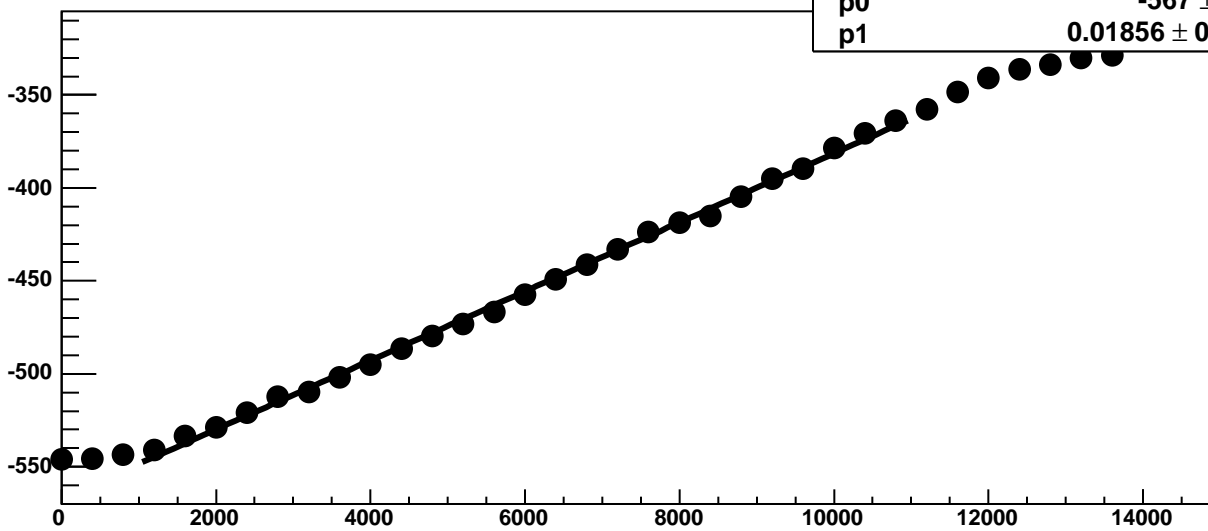
Chip 9, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



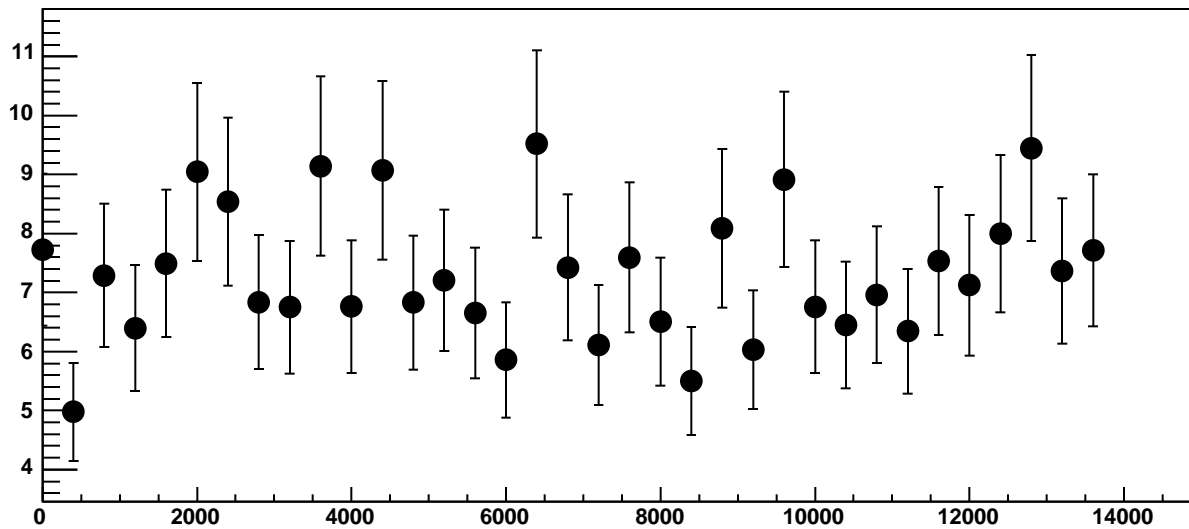
Chip 9, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



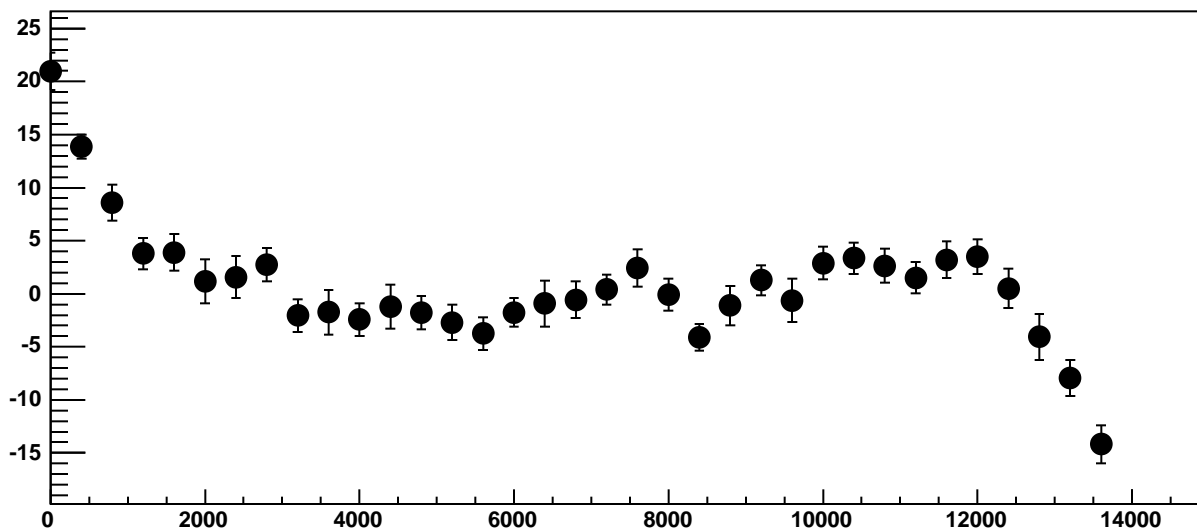
Chip 9, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



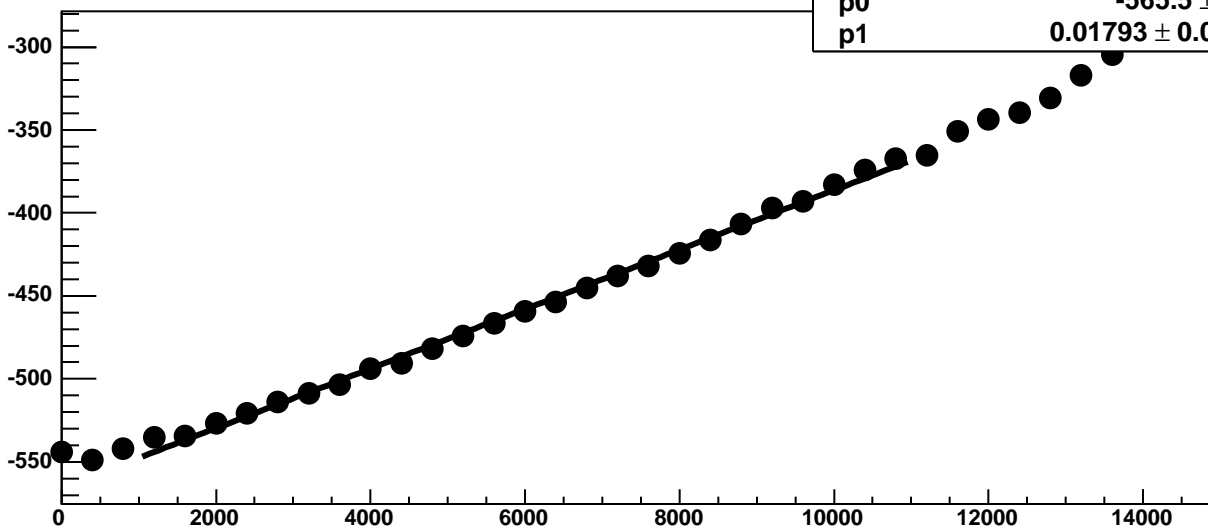
Chip 9, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



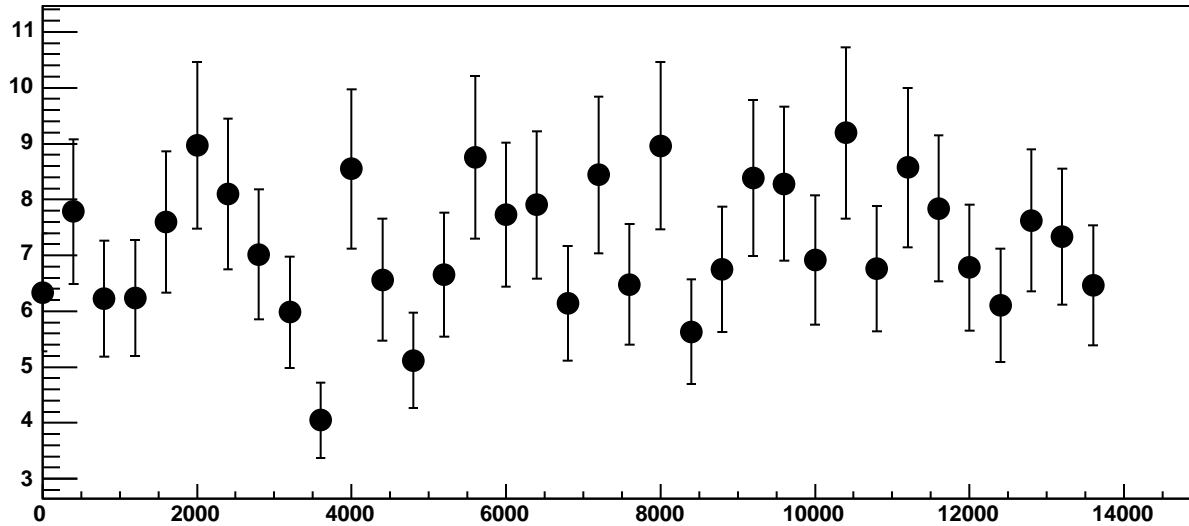
Chip 9, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



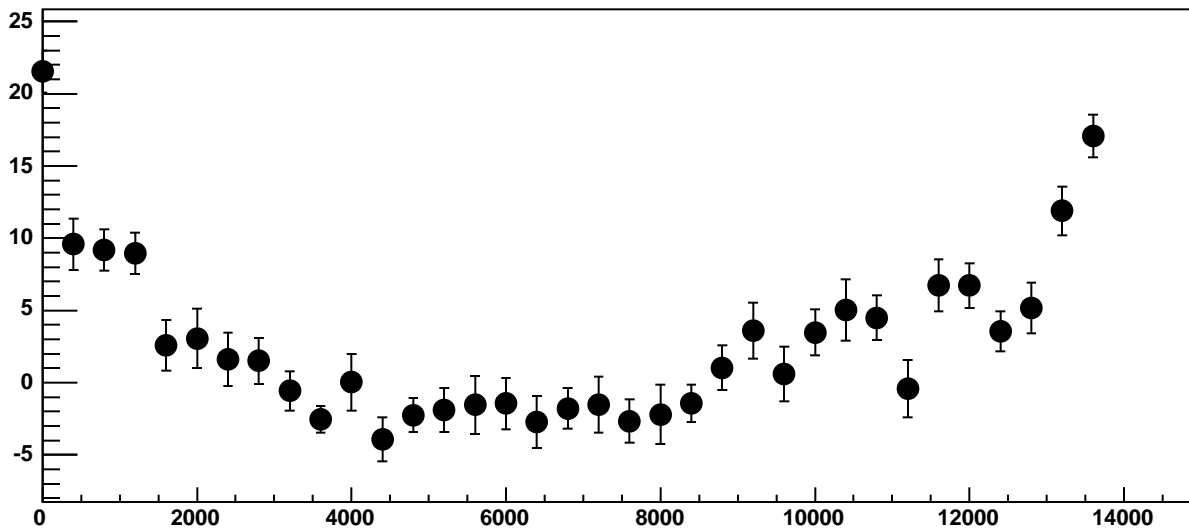
Chip 9, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



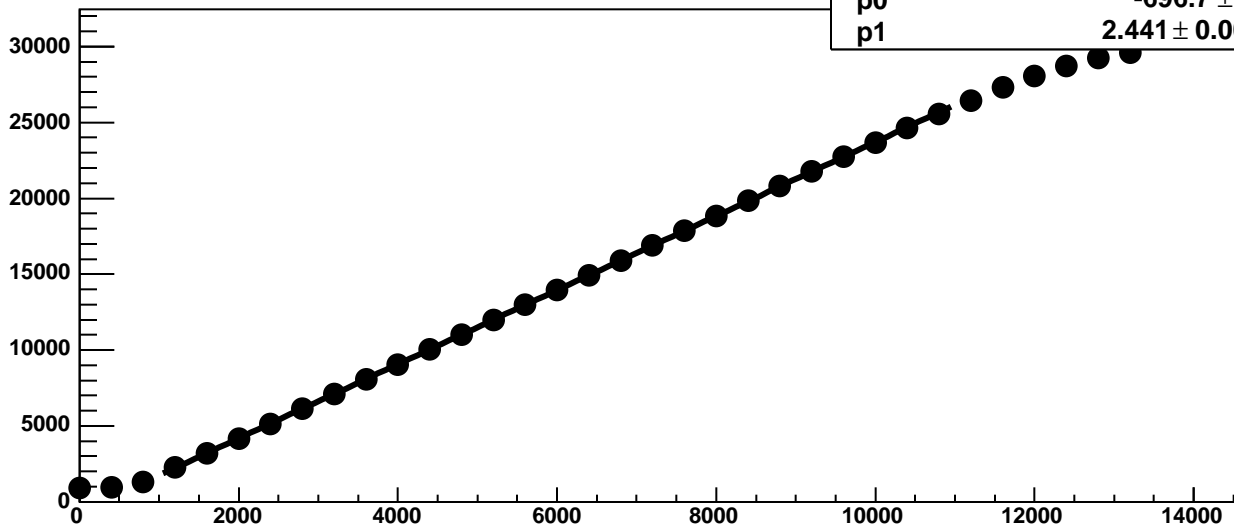
Chip 9, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



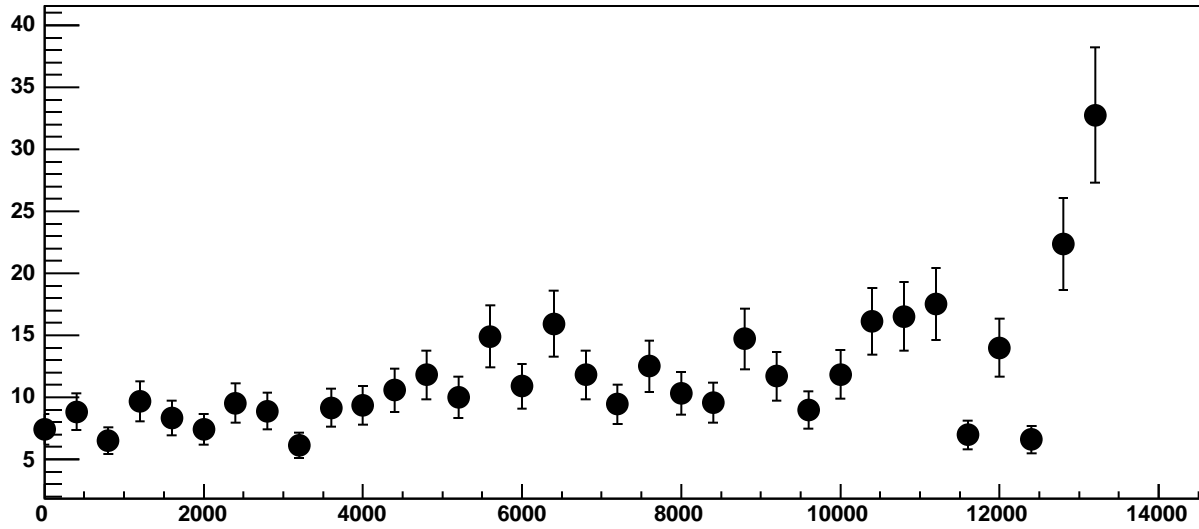
Chip 9, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



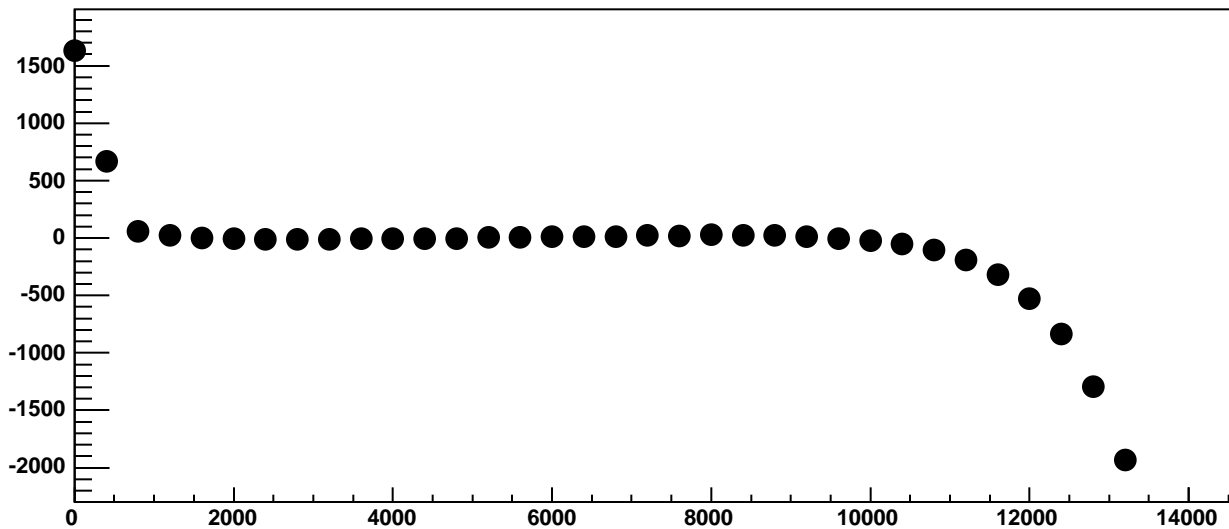
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC



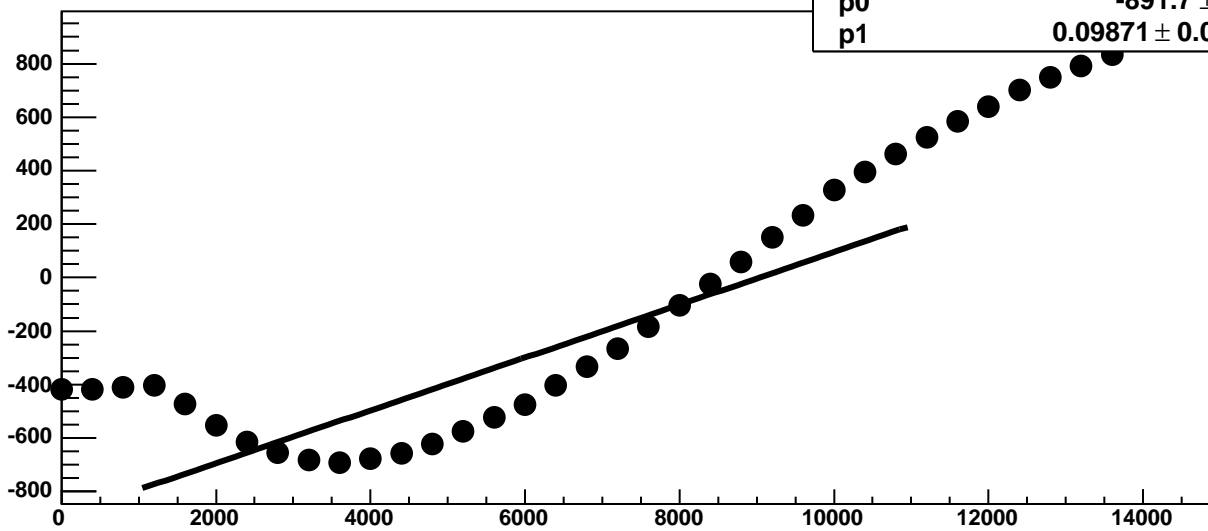
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC



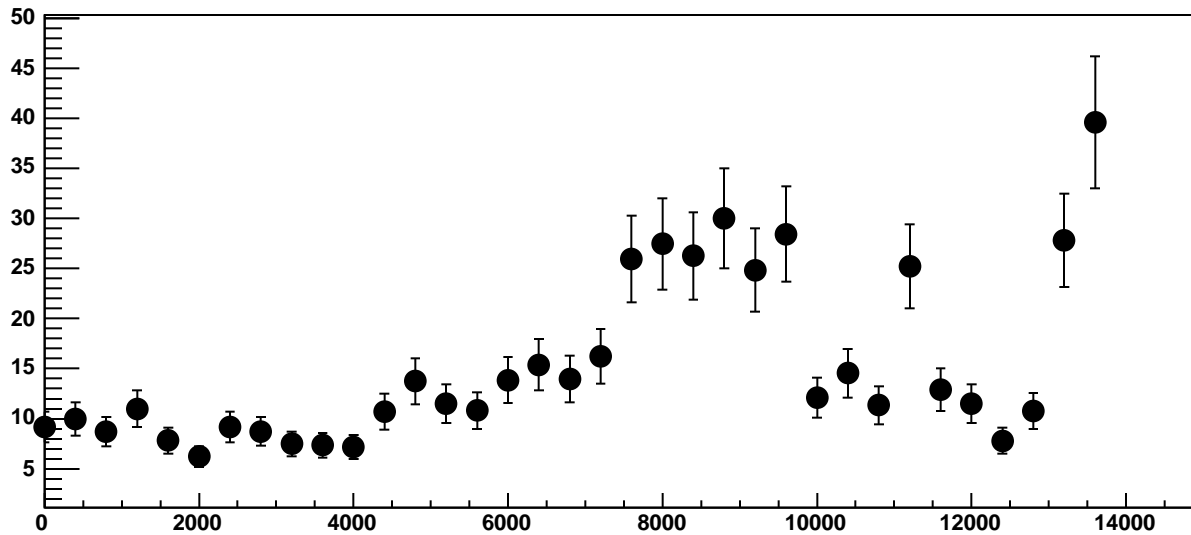
Chip 9, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC



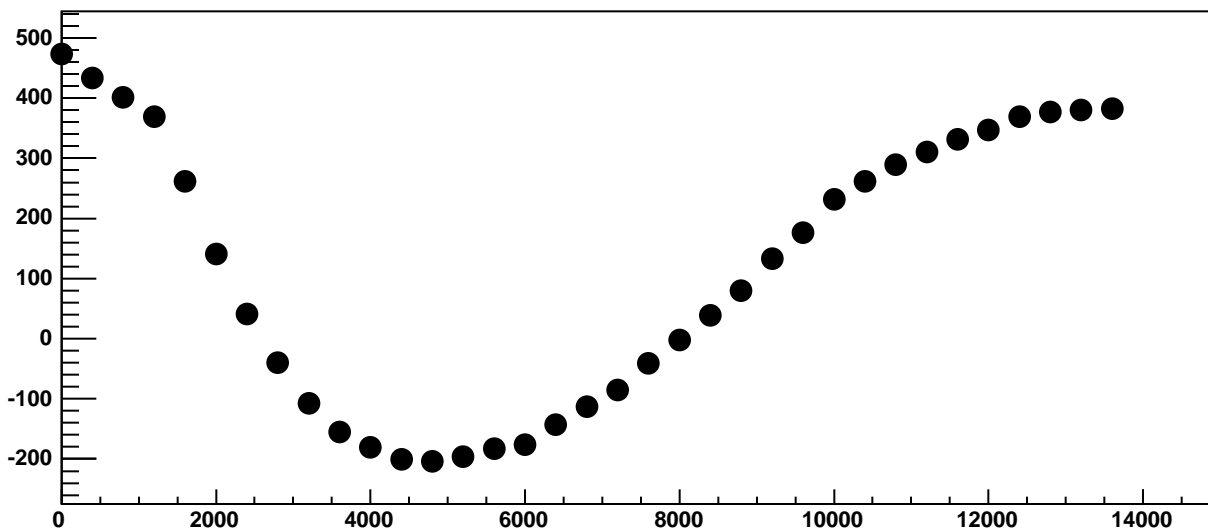
Chip 9, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



Chip 9, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

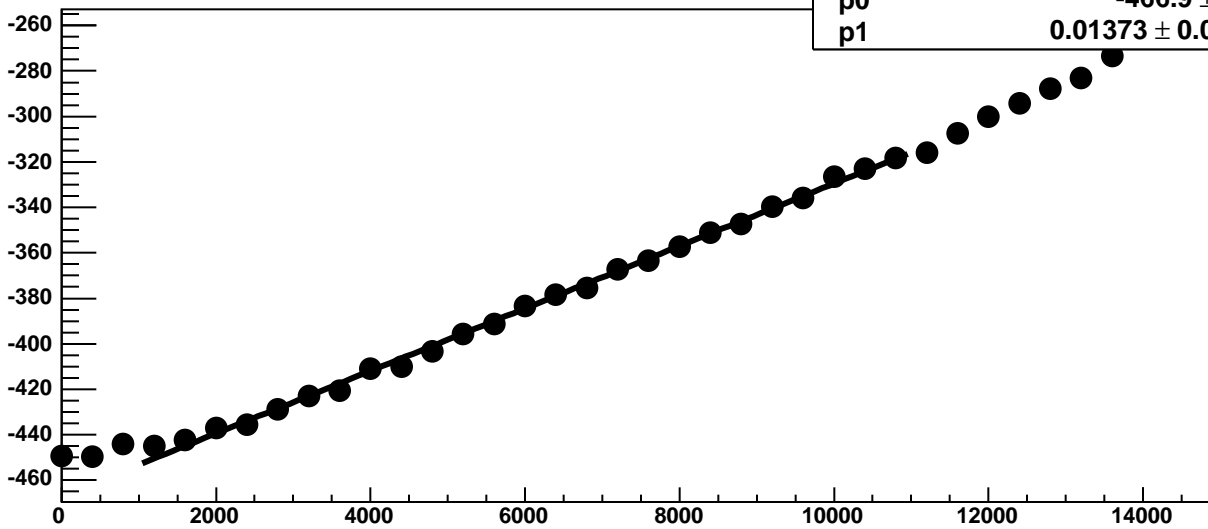


Chip 9, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



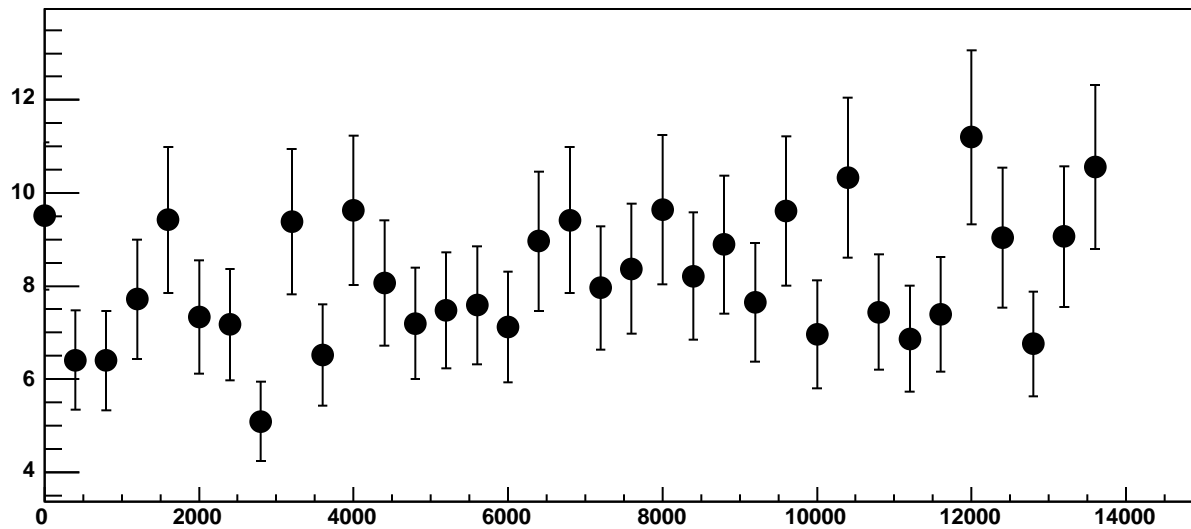


Chip 9, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

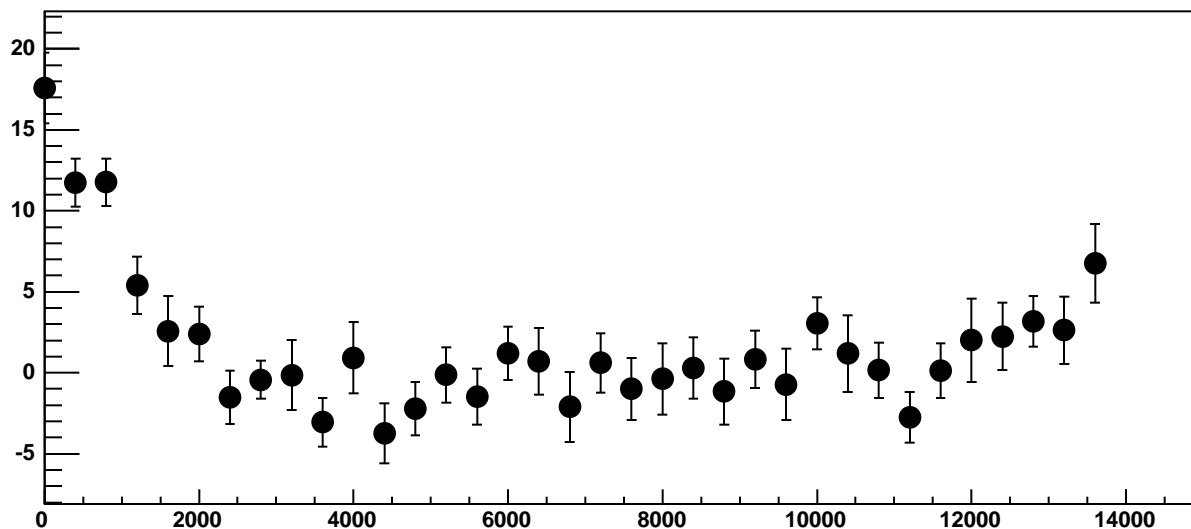


$\chi^2 / \text{ndf}$  31.18 / 23  
p0  $-466.9 \pm 0.7945$   
p1  $0.01373 \pm 0.0001244$

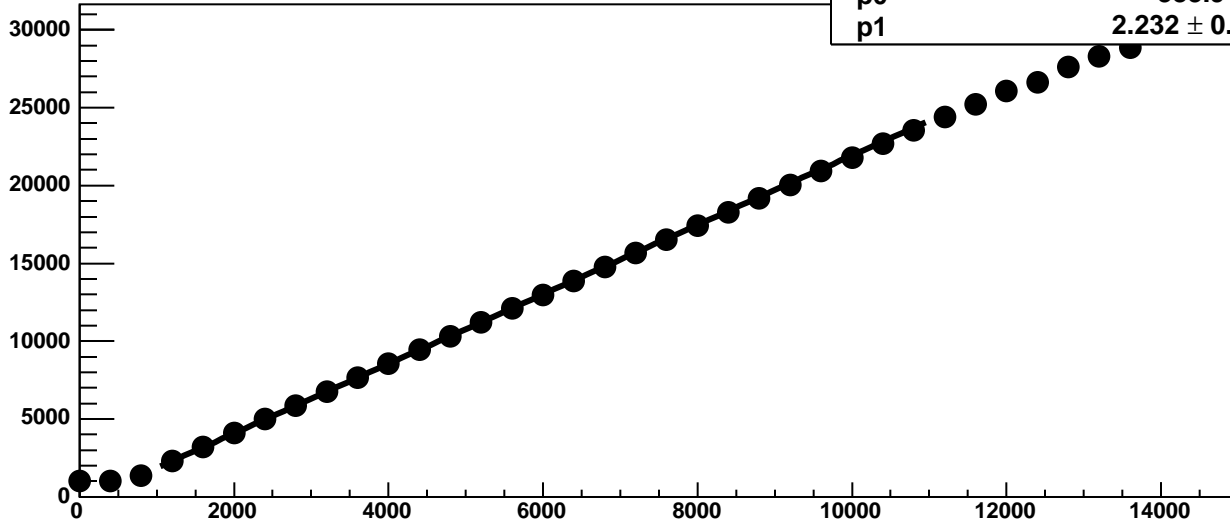
Chip 9, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



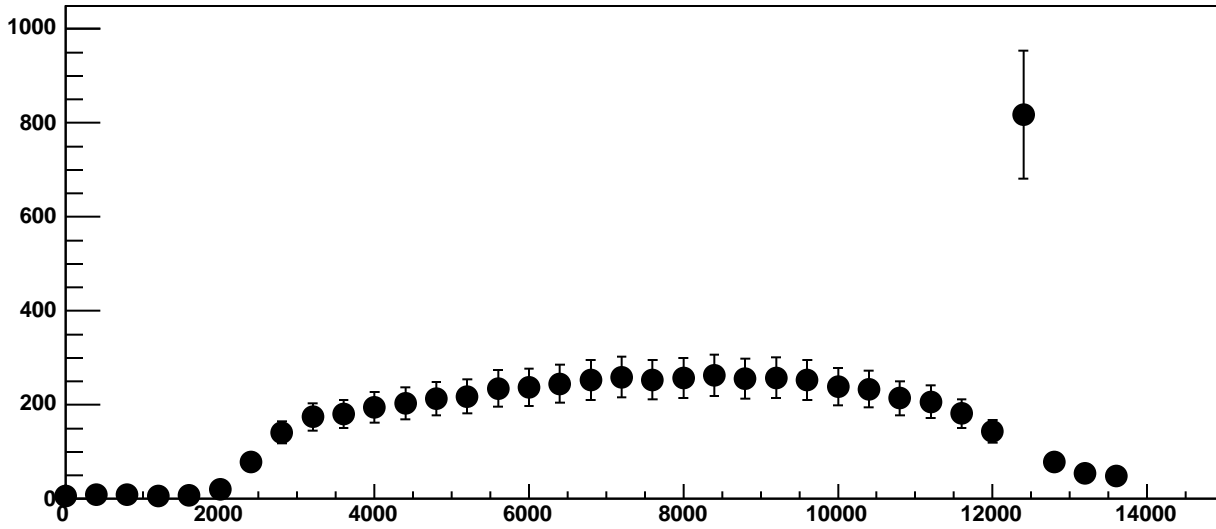
Chip 9, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



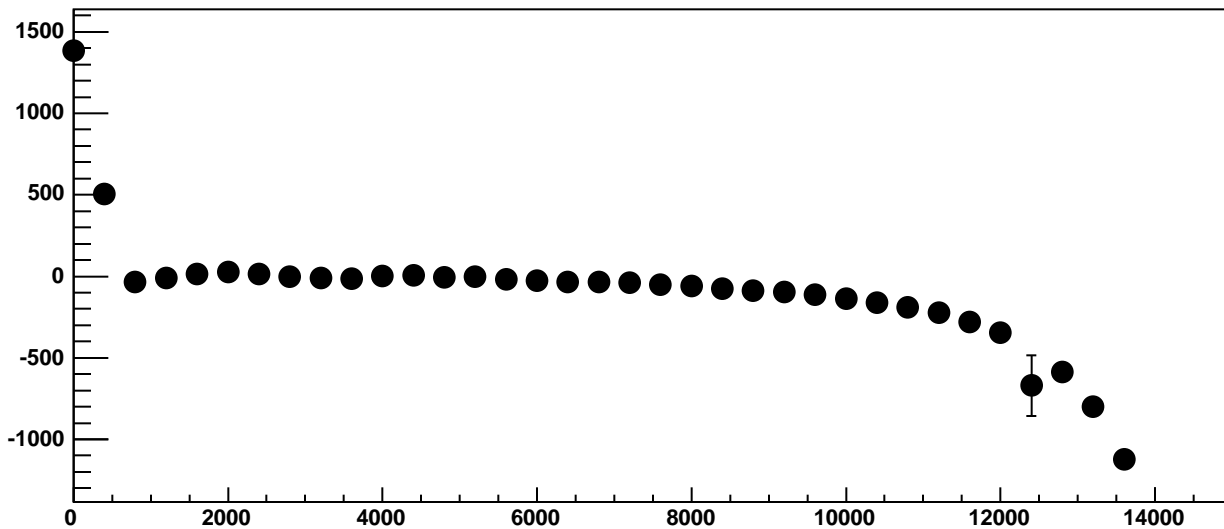
Chip 9, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



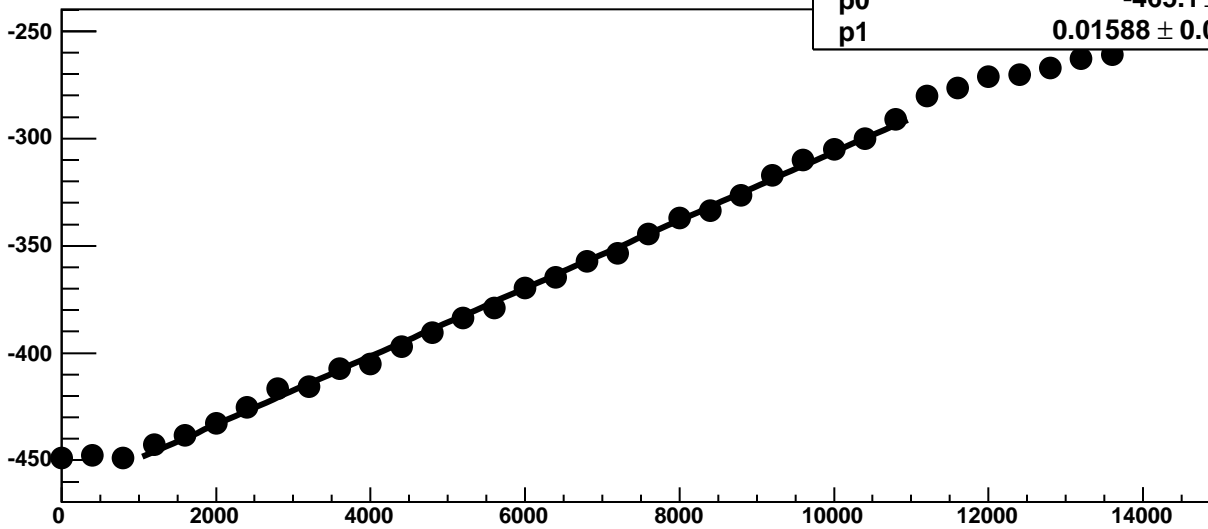
Chip 9, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC

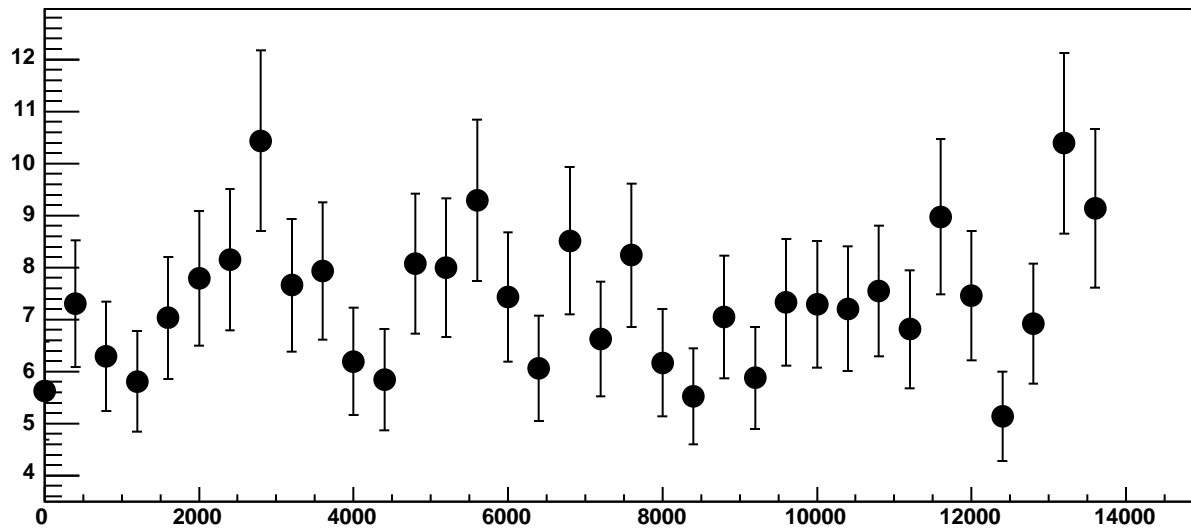


Chip 9, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

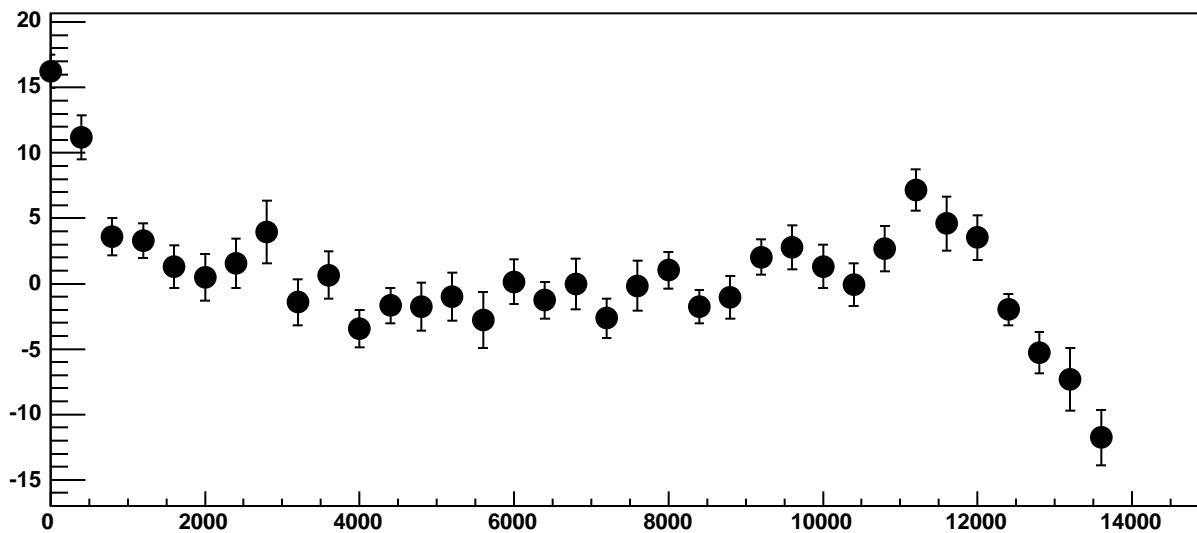


$\chi^2 / \text{ndf}$  35.96 / 23  
p0  $-465.1 \pm 0.7581$   
p1  $0.01588 \pm 0.0001118$

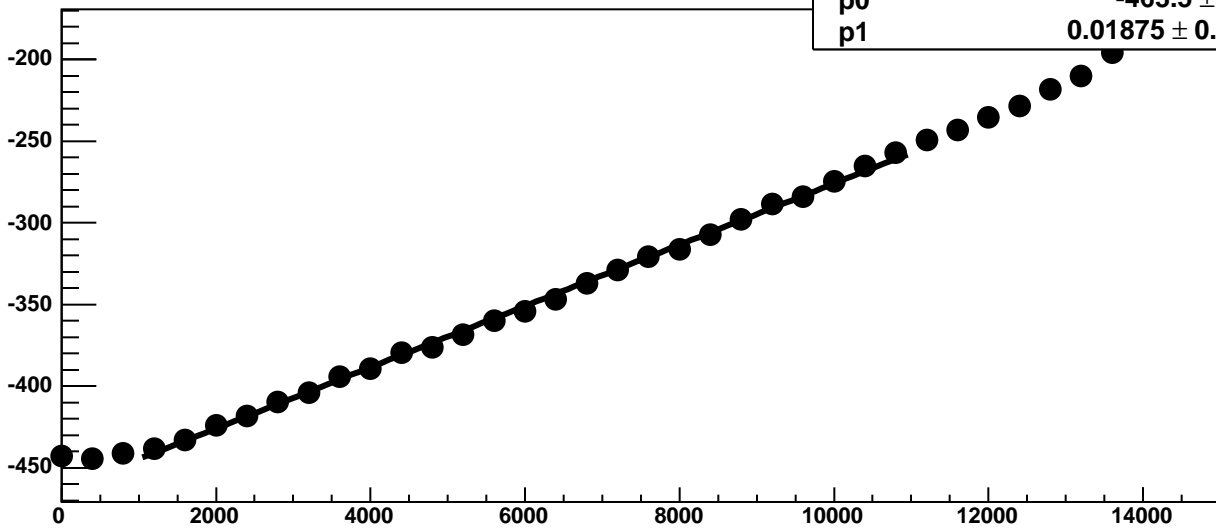
Chip 9, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

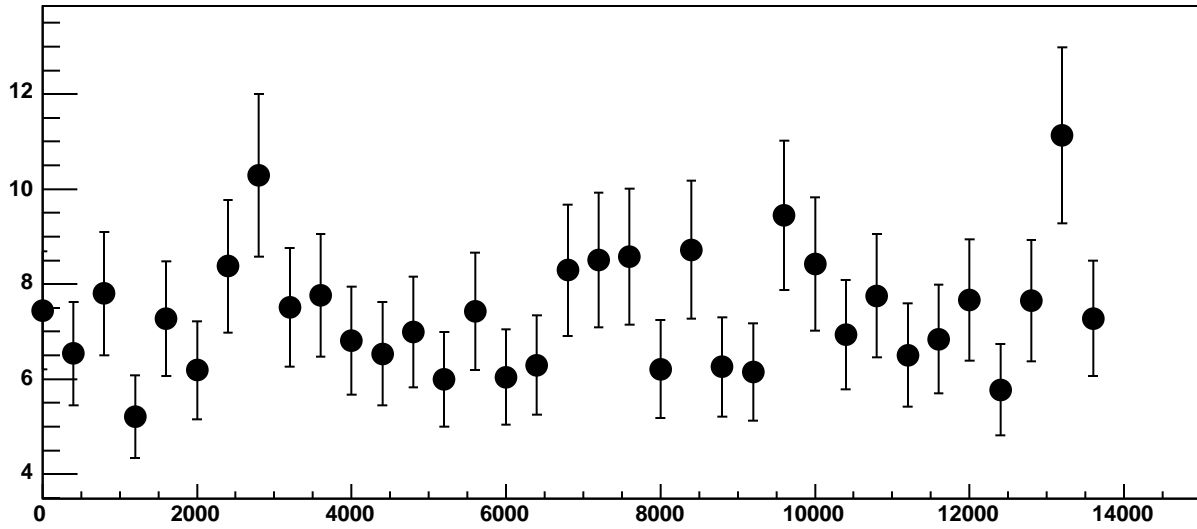


Chip 9, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

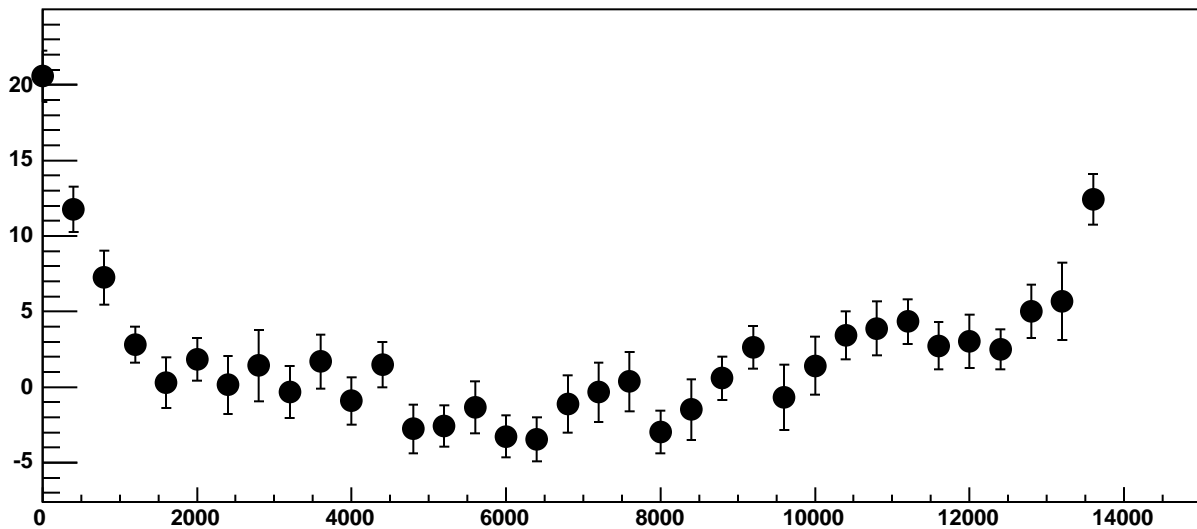


$\chi^2 / \text{ndf}$  47.18 / 23  
p0  $-463.5 \pm 0.7255$   
p1  $0.01875 \pm 0.000112$

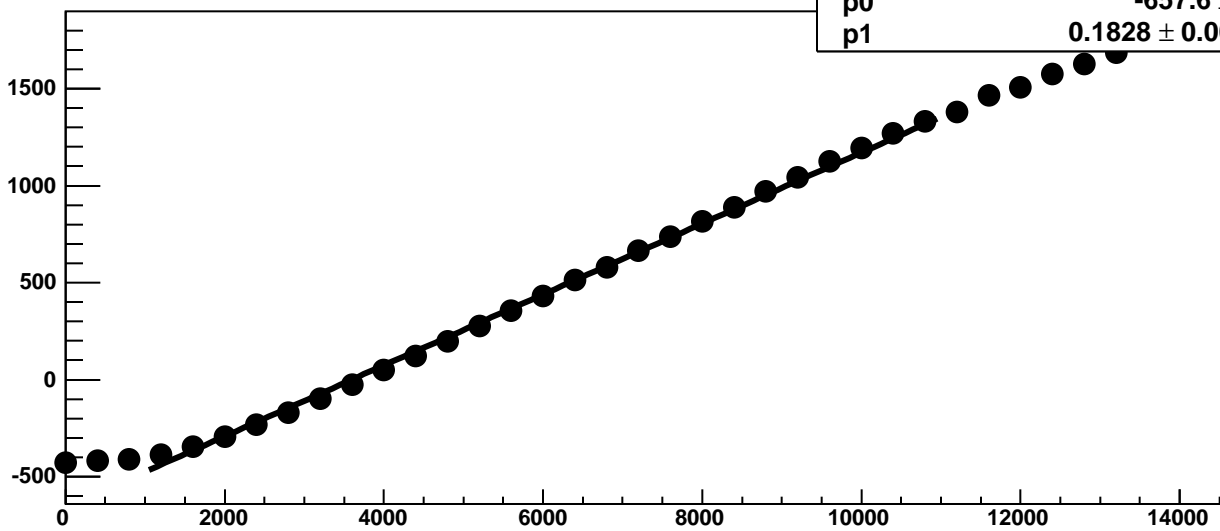
Chip 9, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



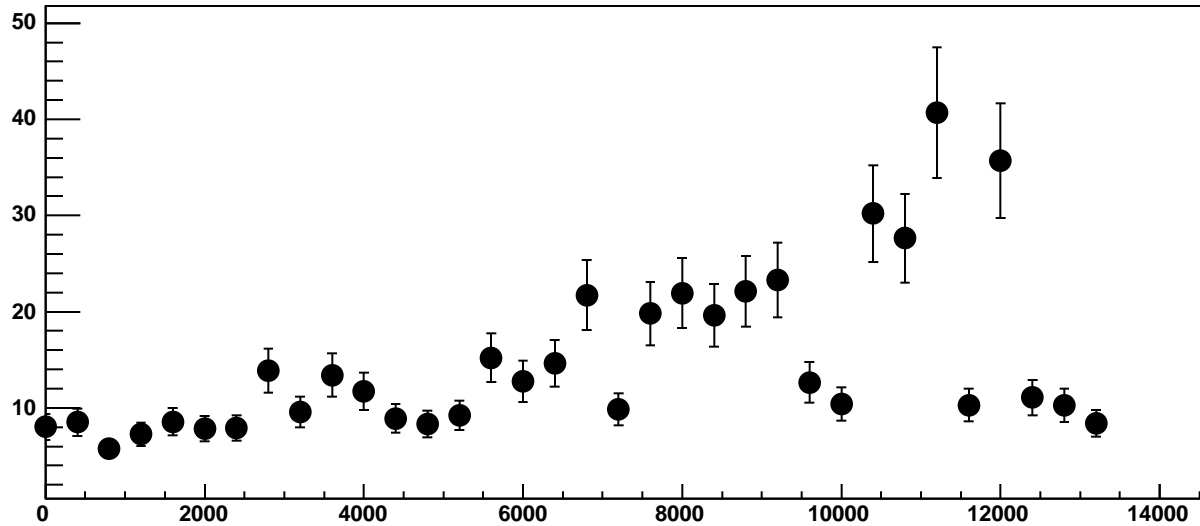
Chip 9, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



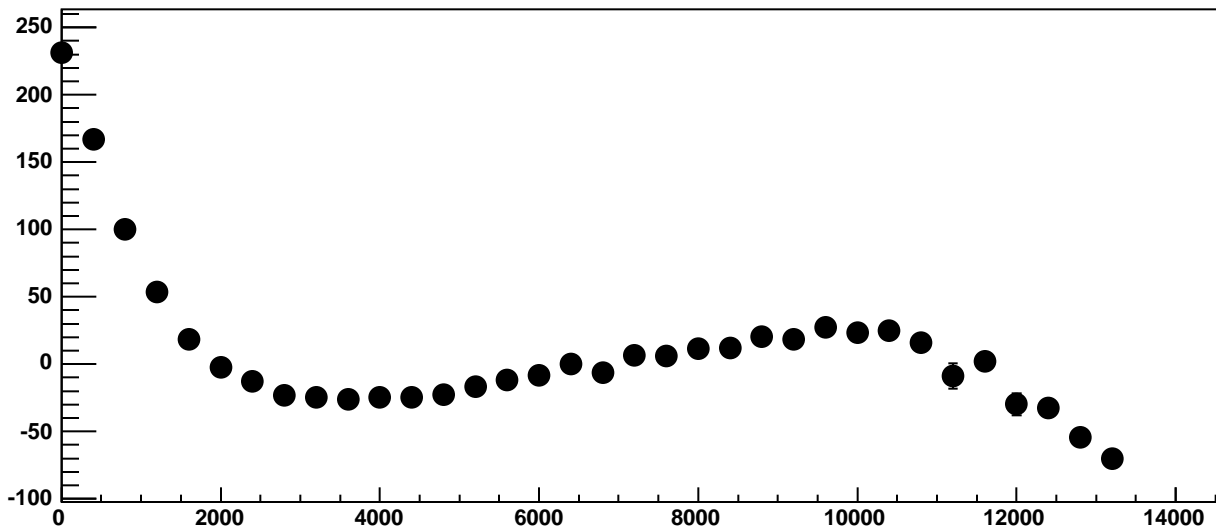
Chip 9, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



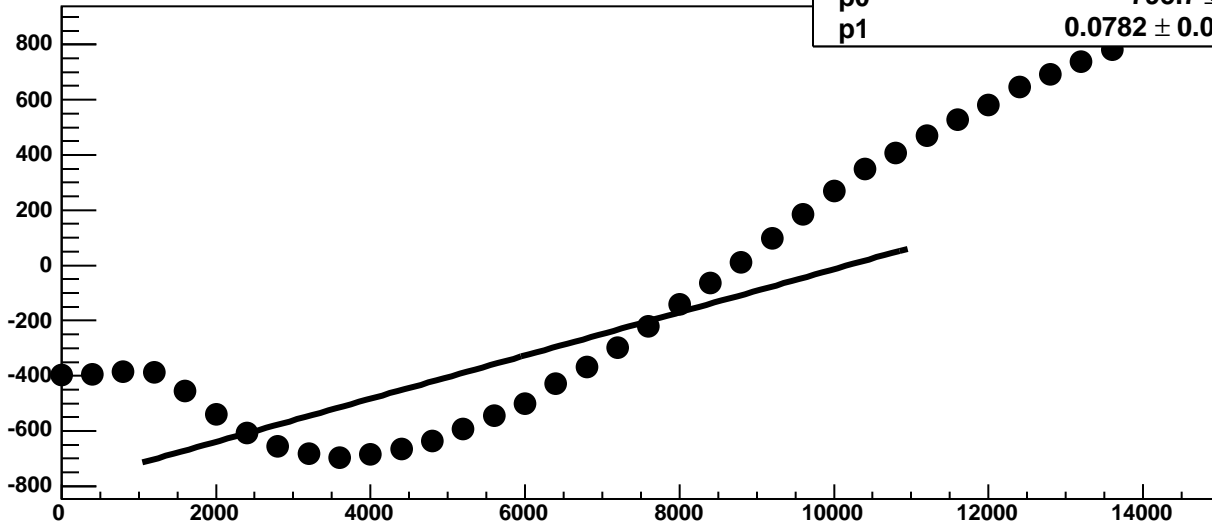
Chip 9, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

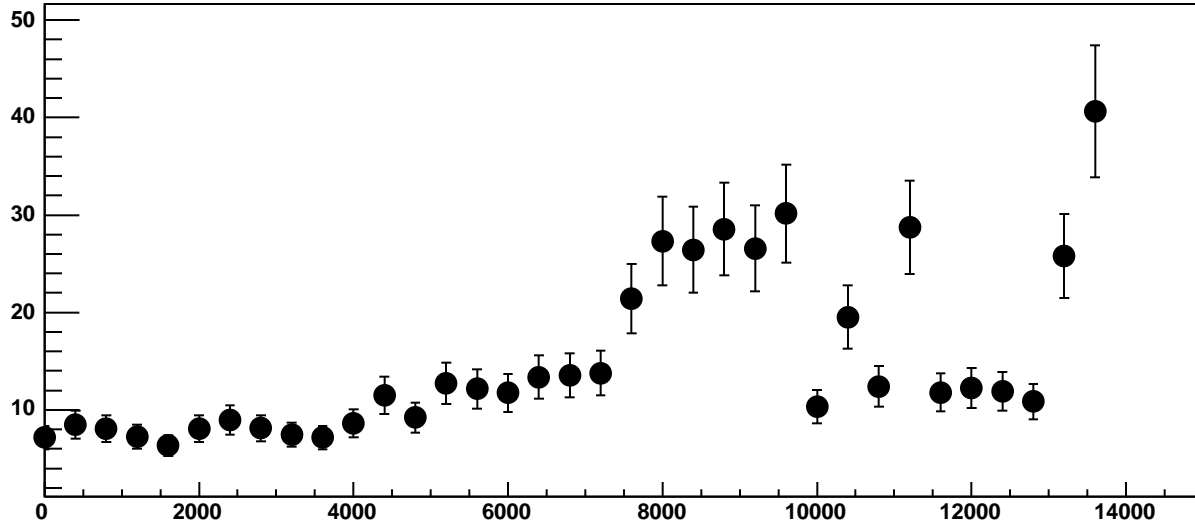


Chip 9, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

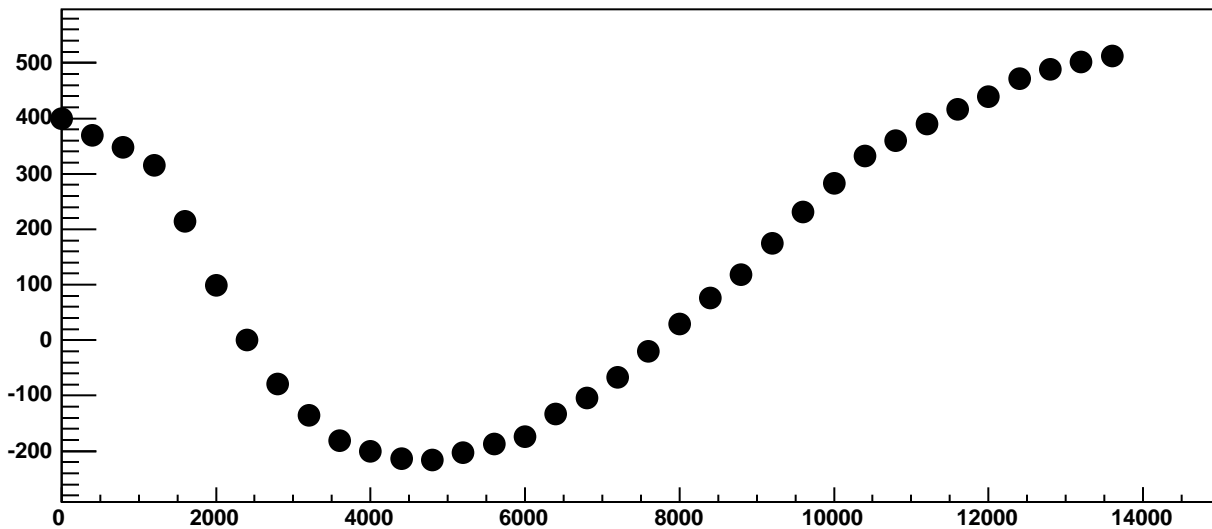


$\chi^2 / \text{ndf}$  1.633e+05 / 23  
p0 -795.7 ± 0.9181  
p1 0.0782 ± 0.0001855

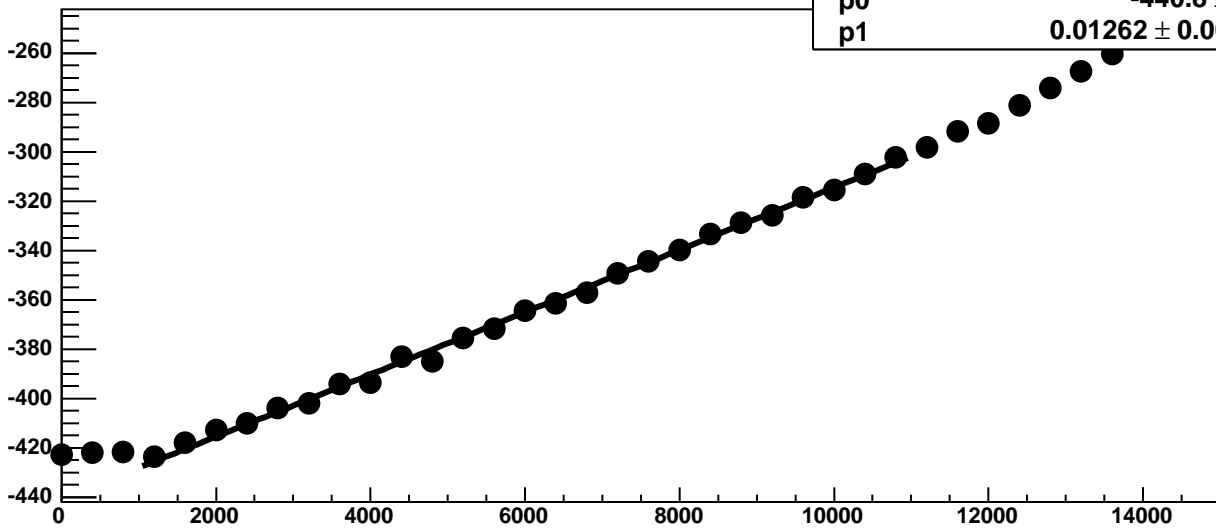
Chip 9, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

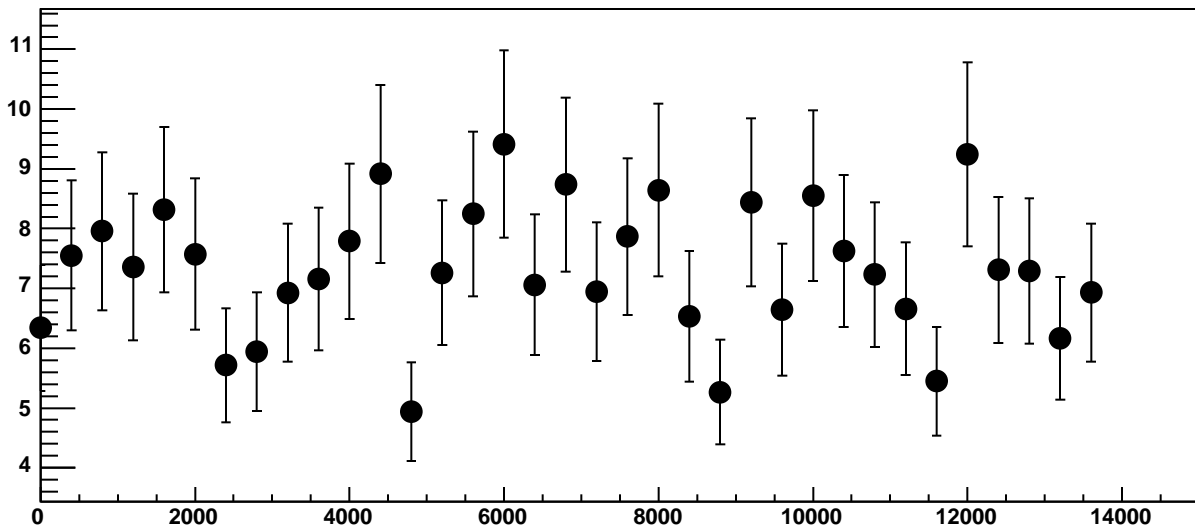


Chip 9, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

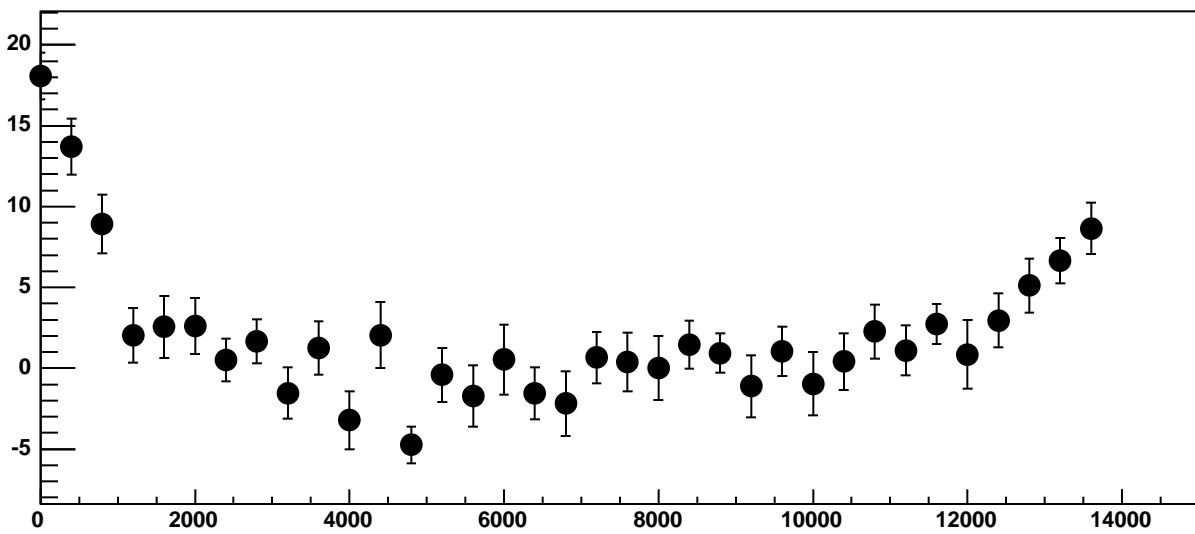


$\chi^2 / \text{ndf}$  38.16 / 23  
p0  $-440.8 \pm 0.744$   
p1  $0.01262 \pm 0.0001133$

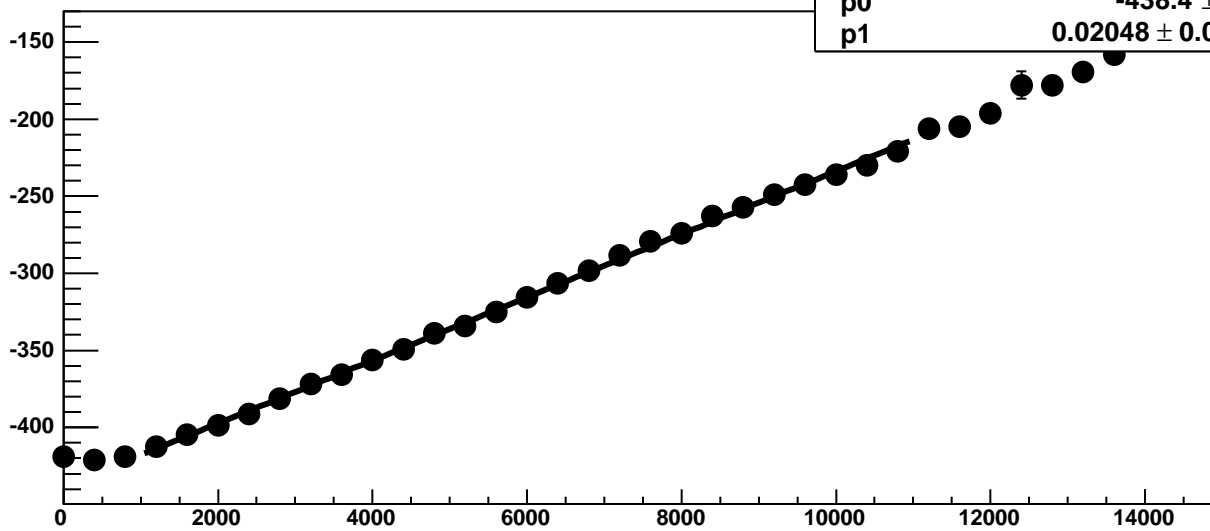
Chip 9, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

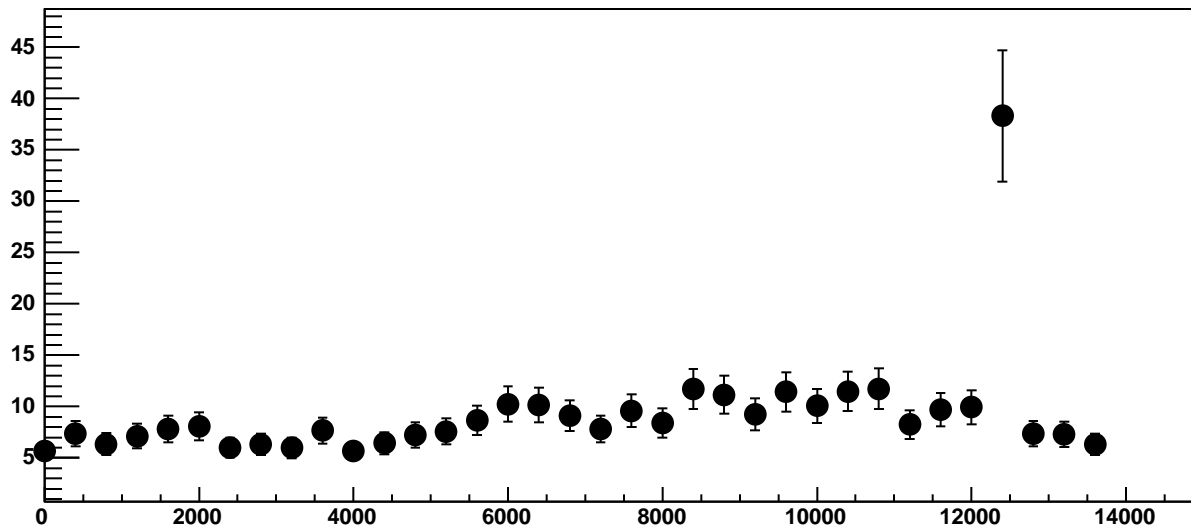


Chip 9, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

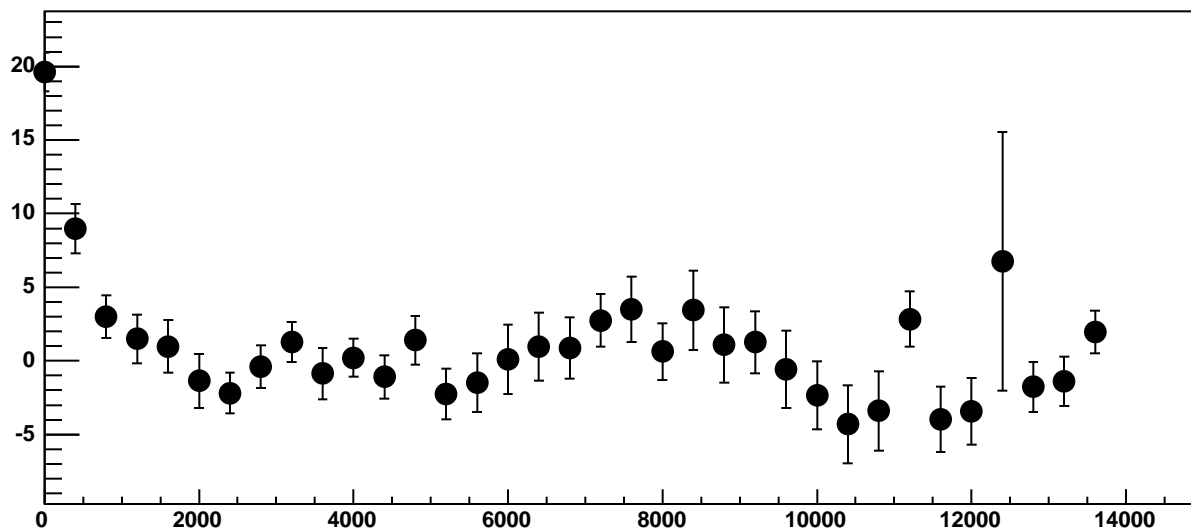


$\chi^2 / \text{ndf}$  21.68 / 23  
p0 -438.4  $\pm$  0.7904  
p1 0.02048  $\pm$  0.0001392

Chip 9, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

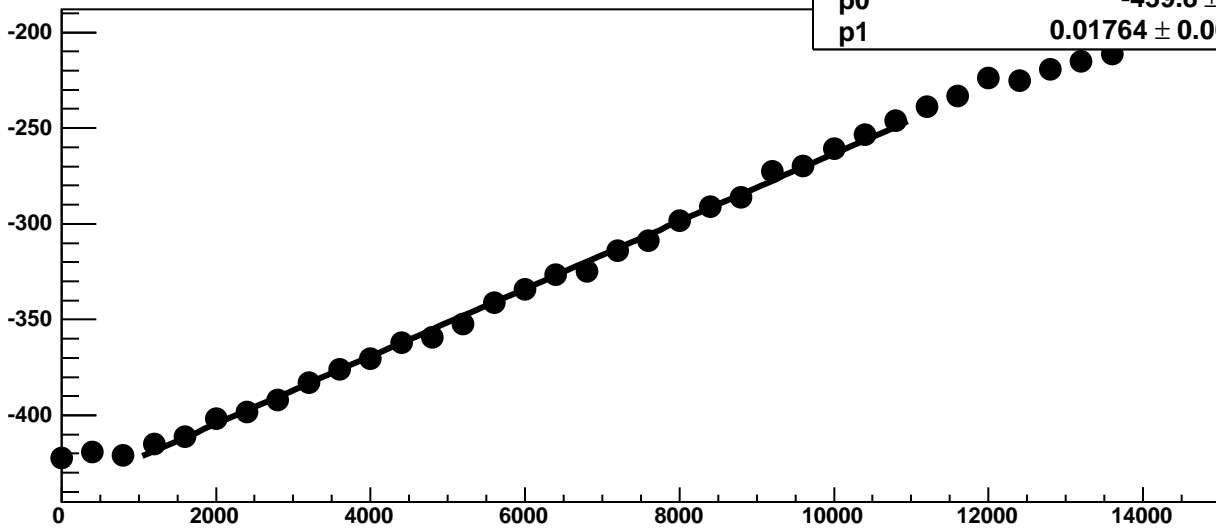


Chip 9, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



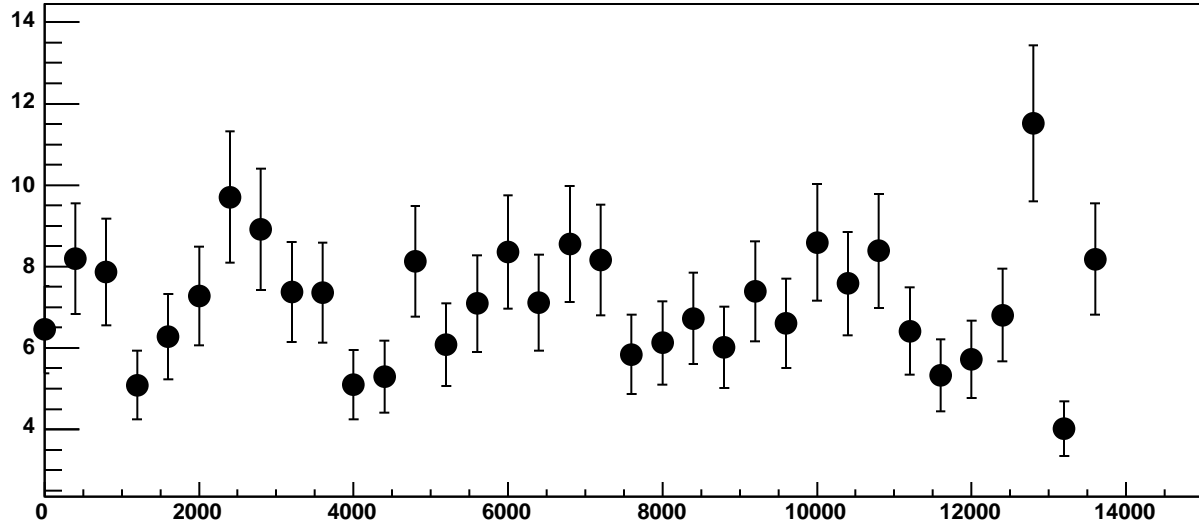


Chip 9, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

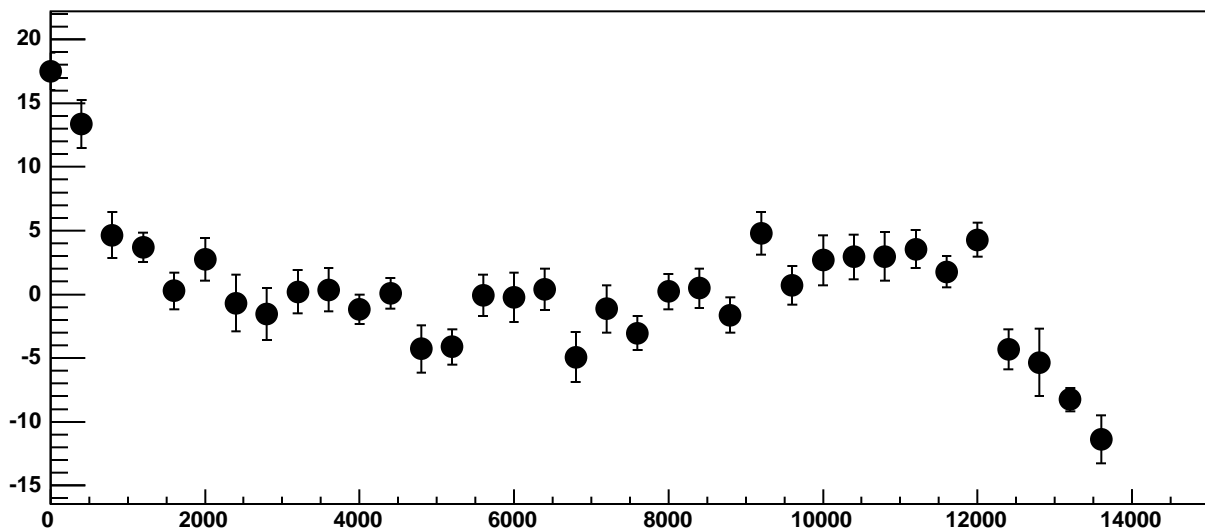


$\chi^2 / \text{ndf}$  57.25 / 23  
p0  $-439.8 \pm 0.7061$   
p1  $0.01764 \pm 0.0001096$

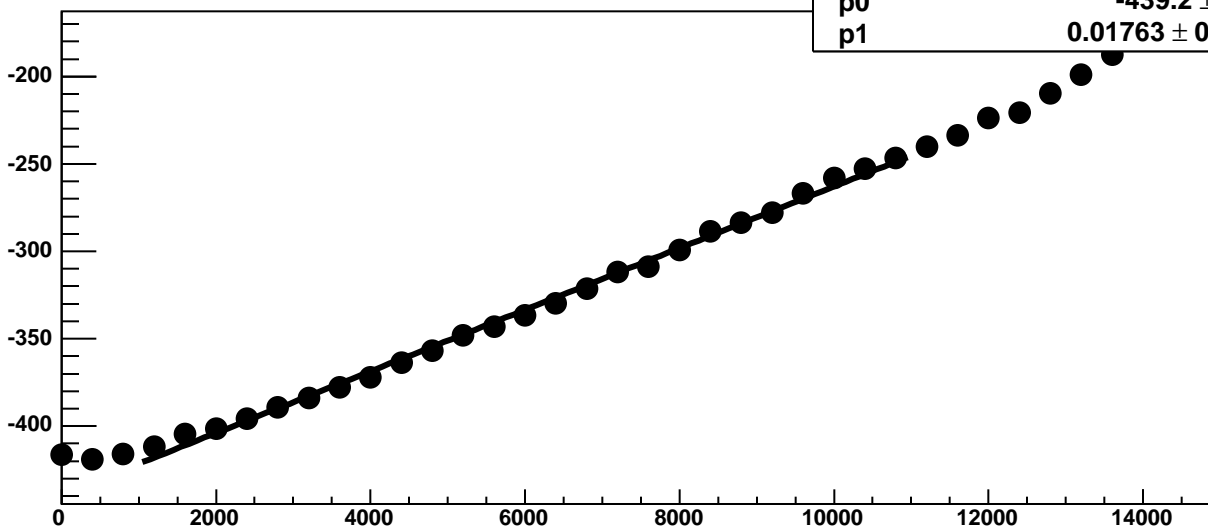
Chip 9, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



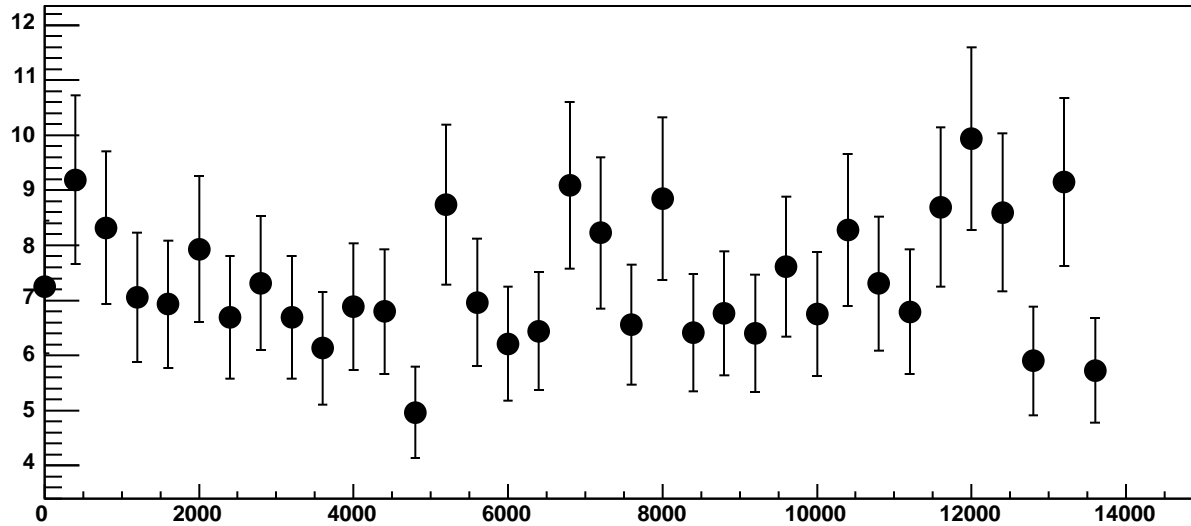
Chip 9, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



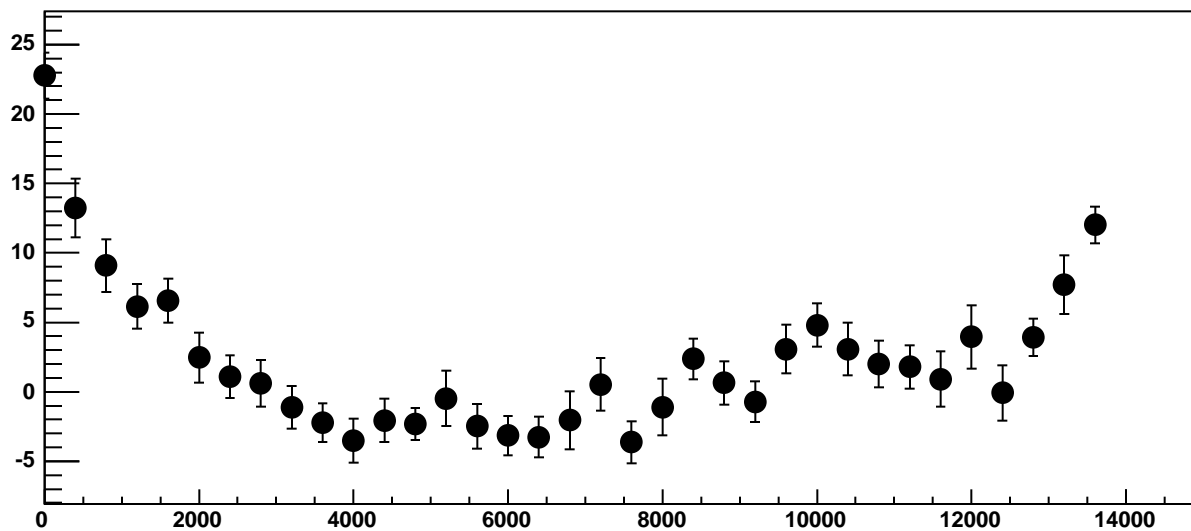
Chip 9, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



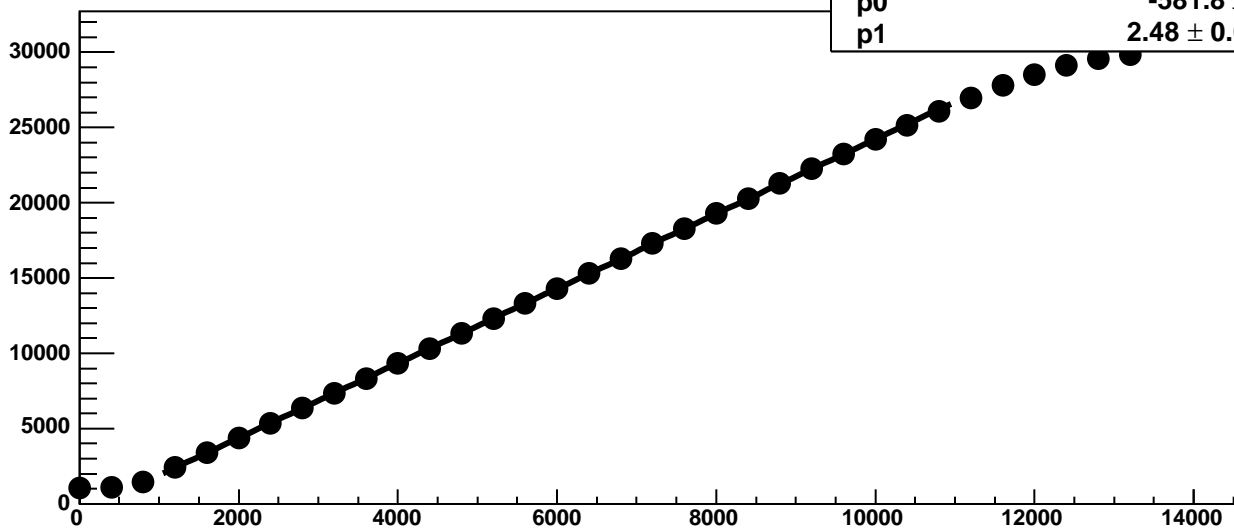
Chip 9, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



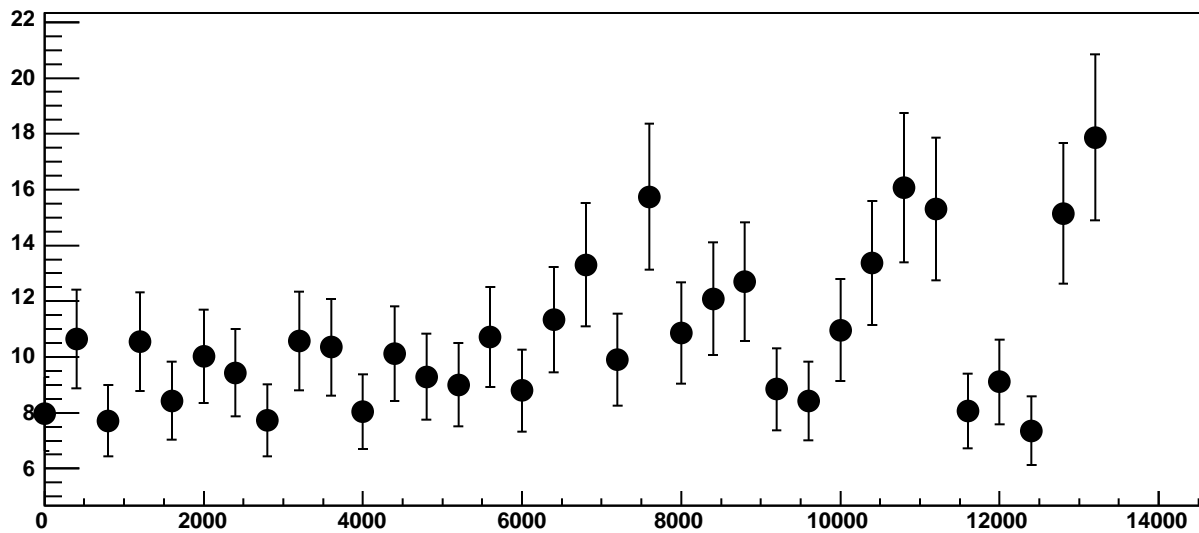
Chip 9, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



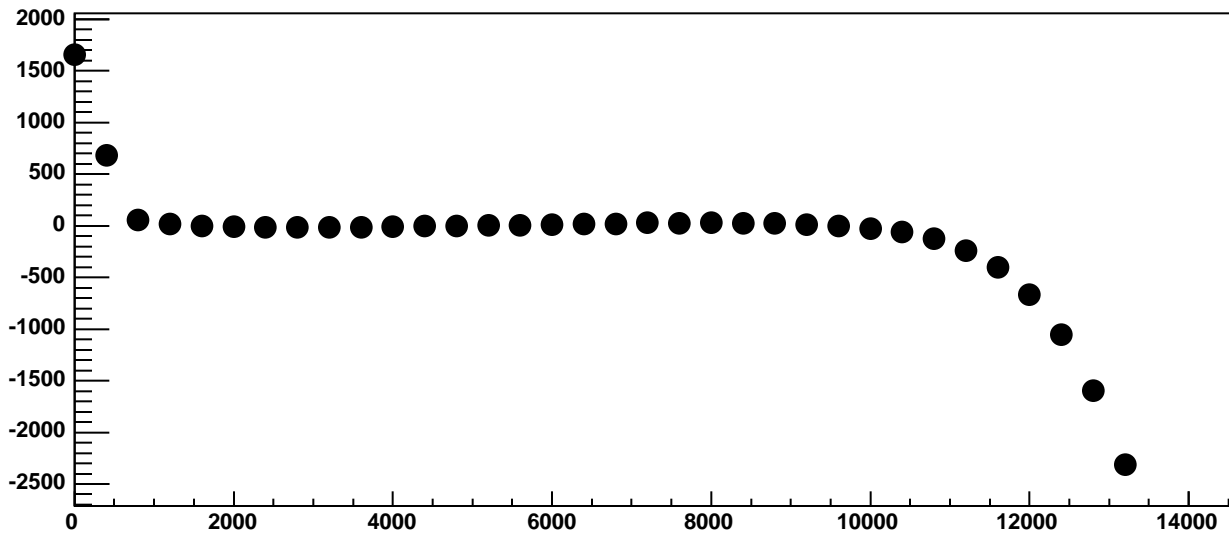
Chip 9, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC



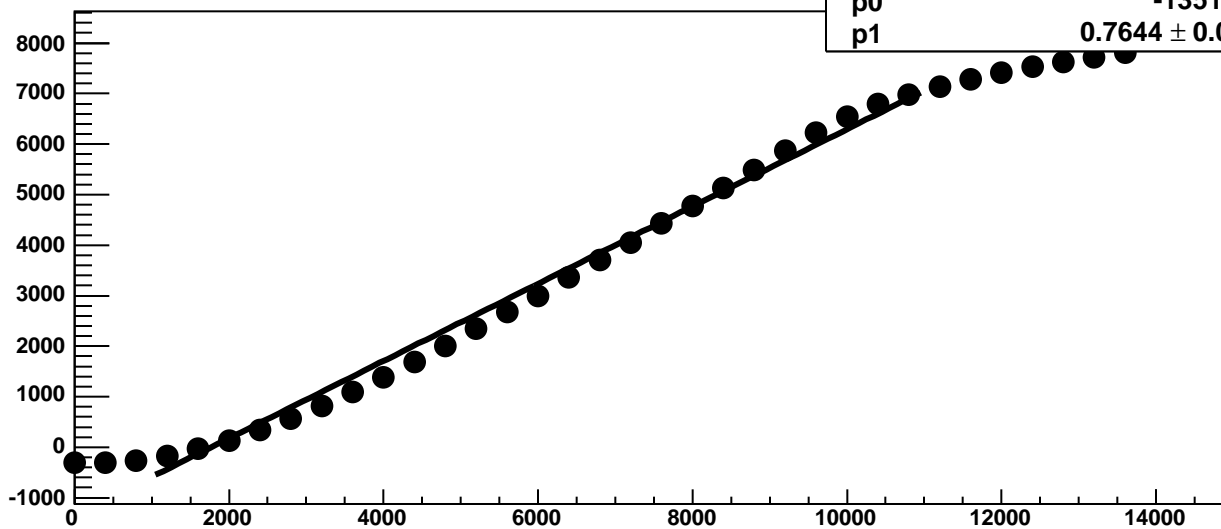
Chip 9, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

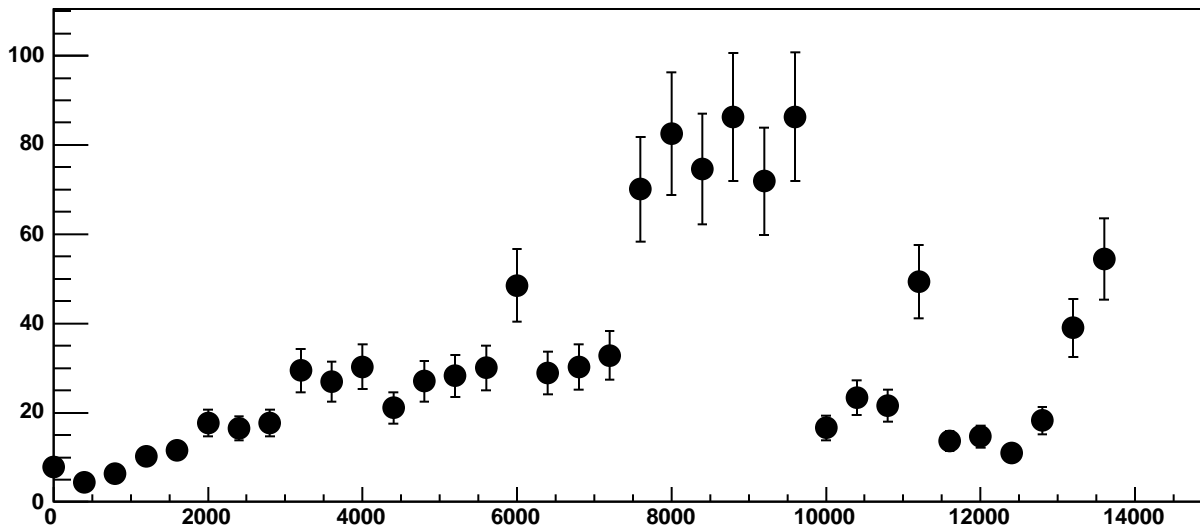


Chip 9, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

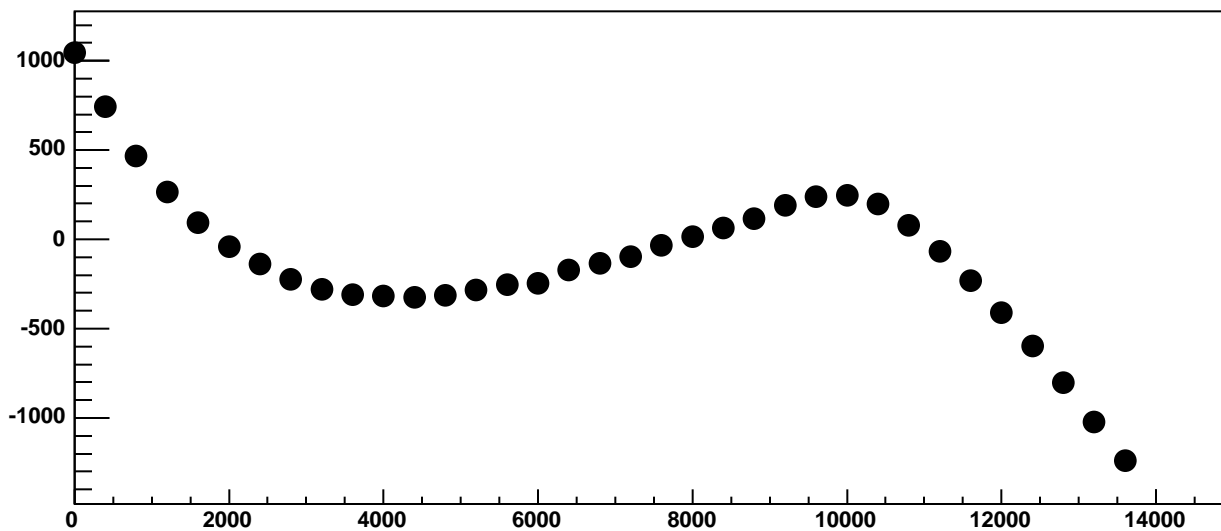


$\chi^2 / \text{ndf}$  4.315e+04 / 23  
p0 -1351 ± 1.685  
p1 0.7644 ± 0.0003231

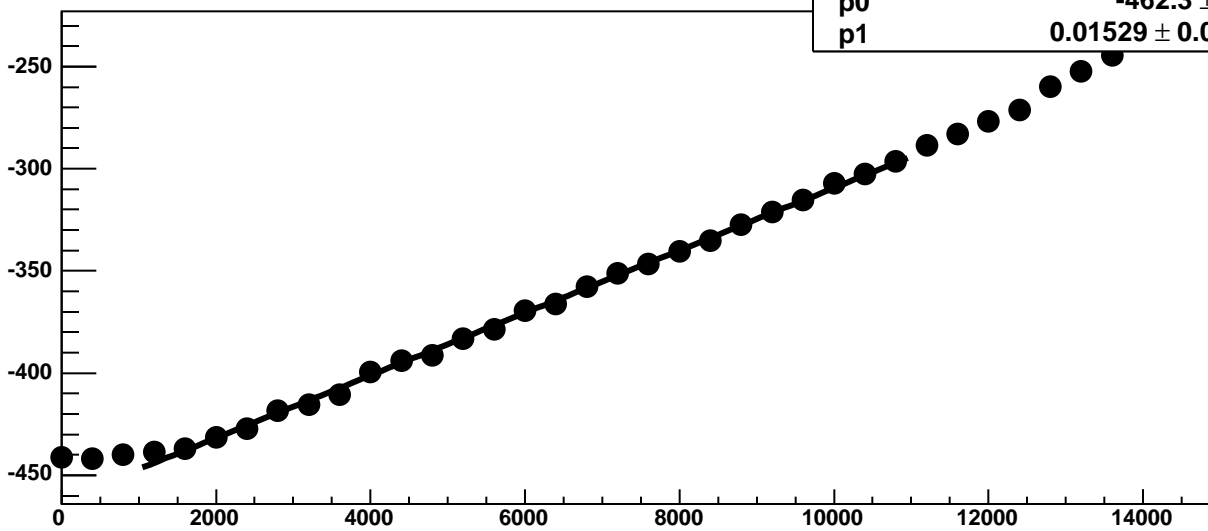
Chip 9, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

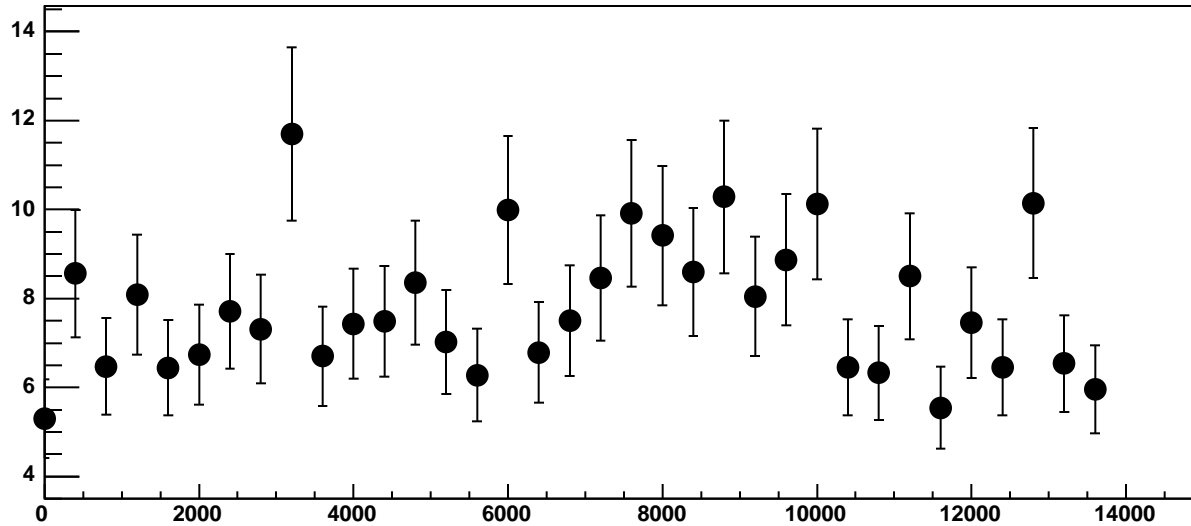


Chip 9, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

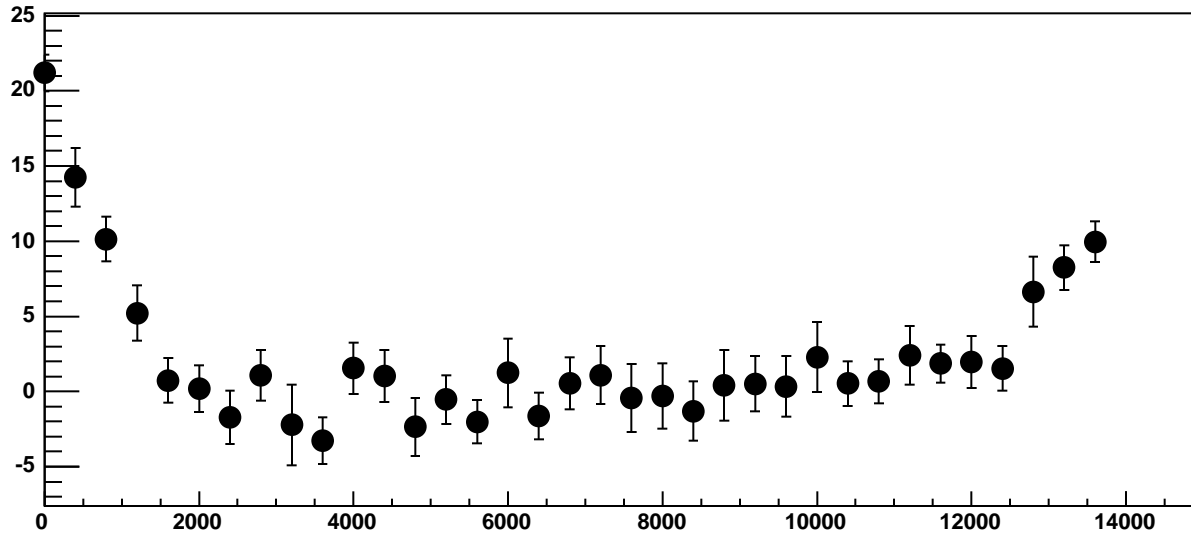


$\chi^2 / \text{ndf}$  23.2 / 23  
p0  $-462.3 \pm 0.7817$   
p1  $0.01529 \pm 0.0001195$

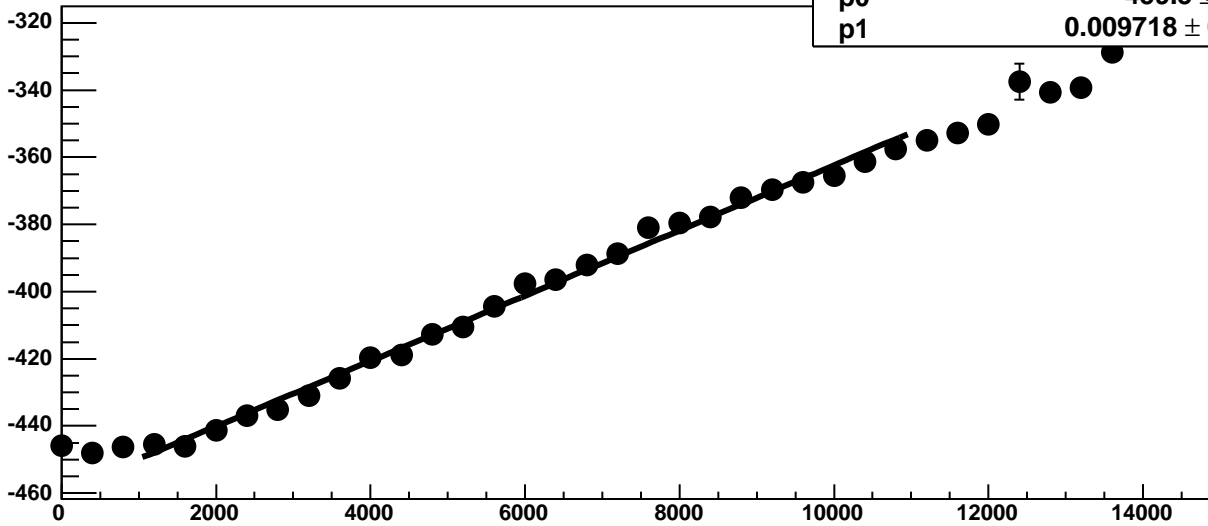
Chip 9, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



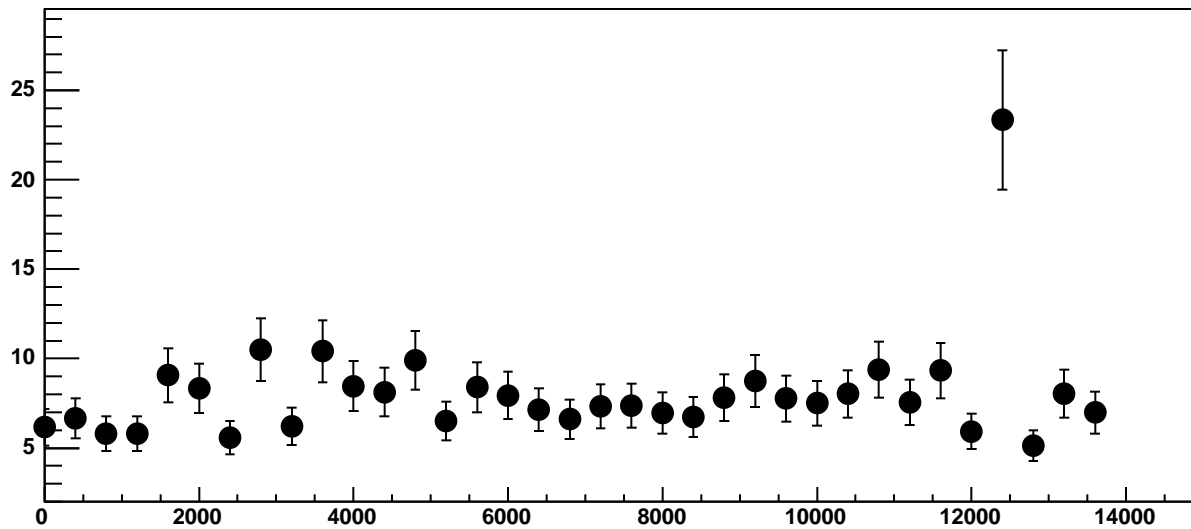
Chip 9, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



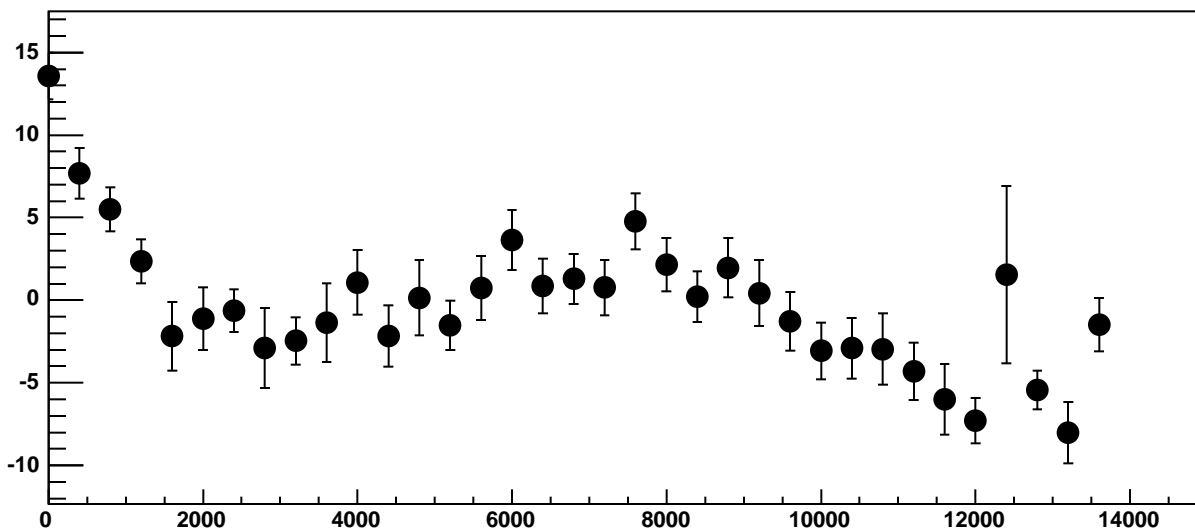
Chip 9, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



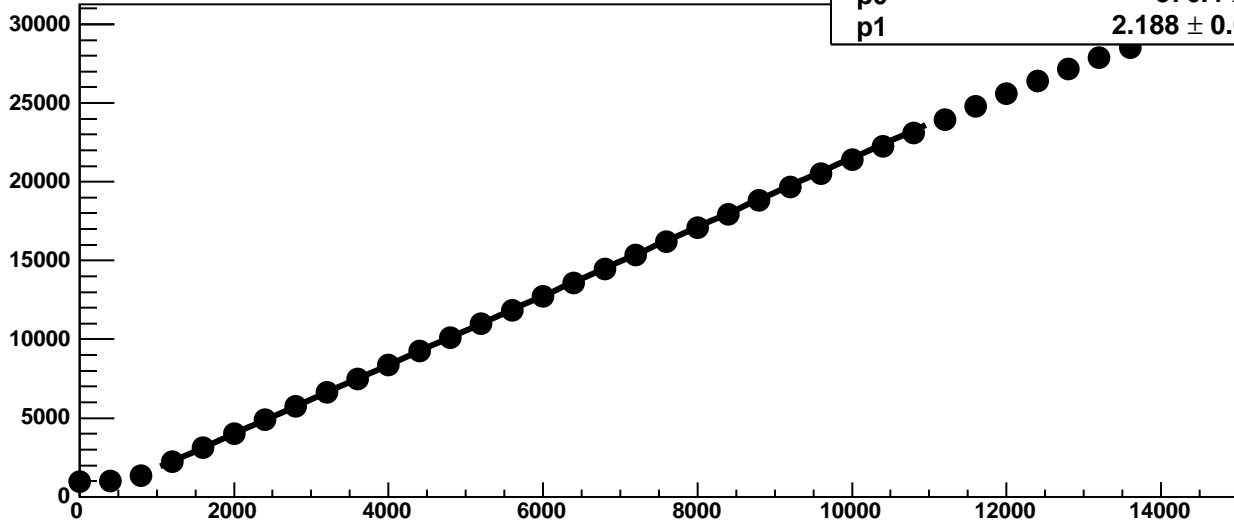
Chip 9, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



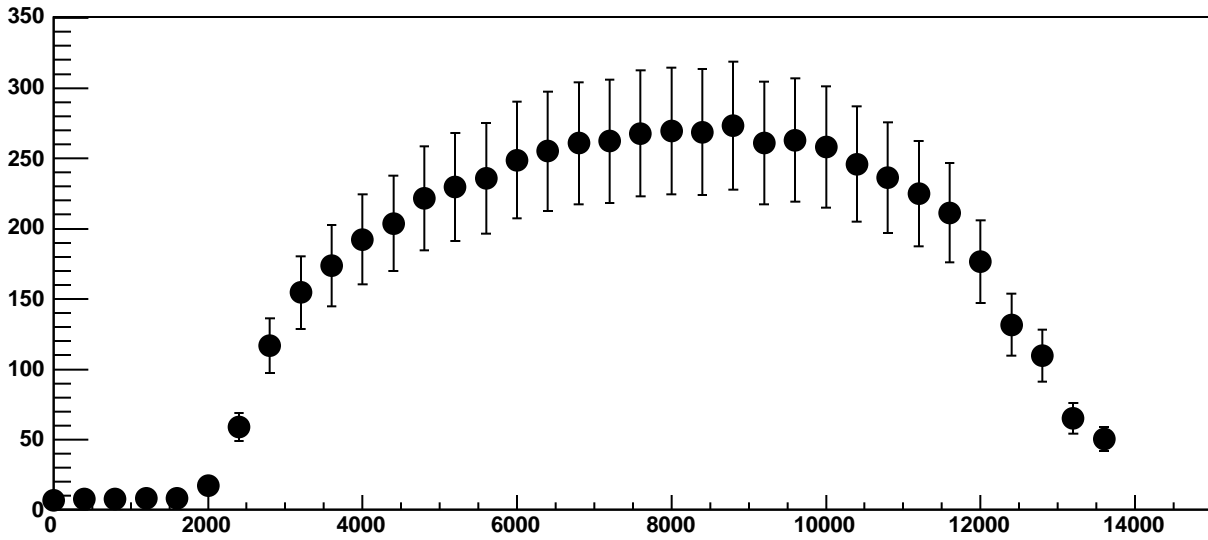
Chip 9, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC



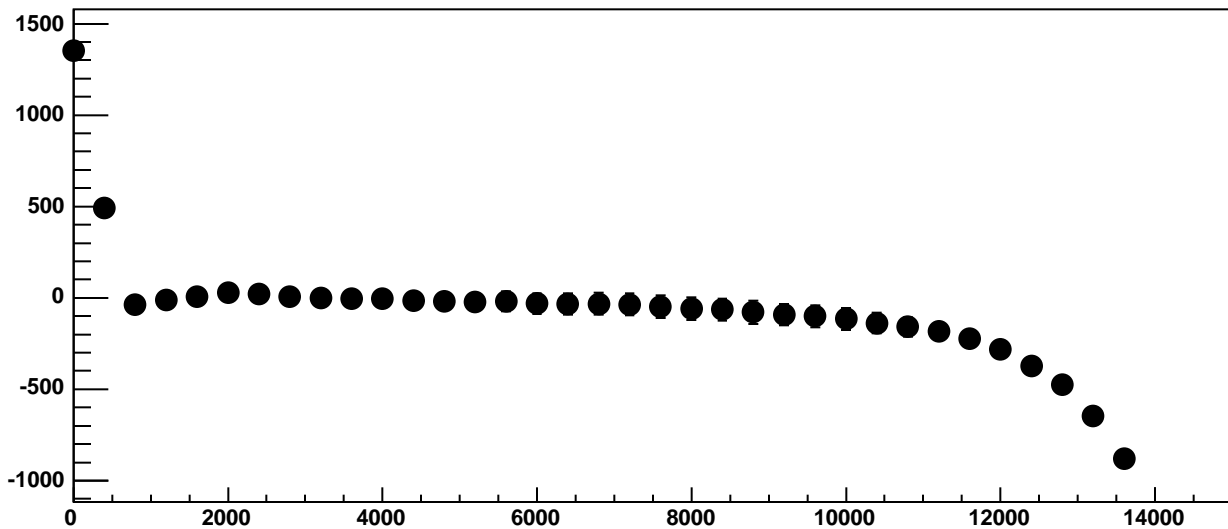
Chip 9, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC



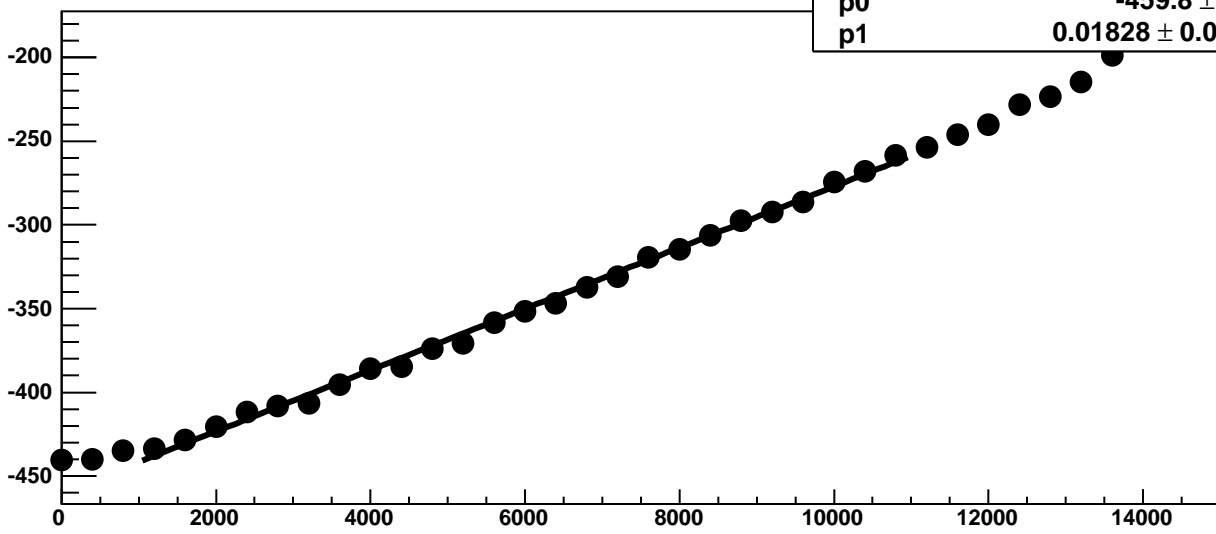
Chip 9, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC

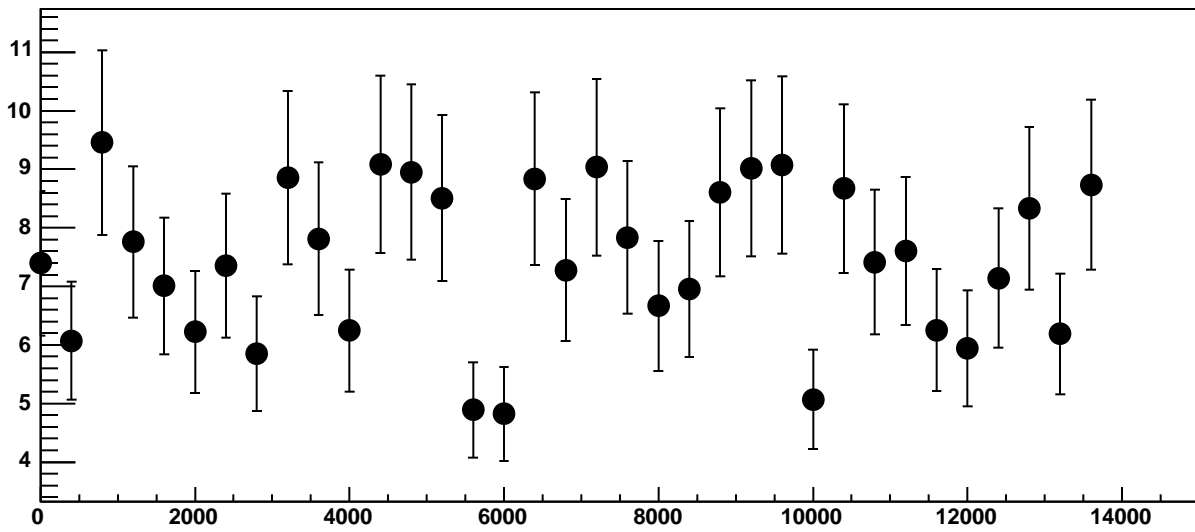


Chip 9, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

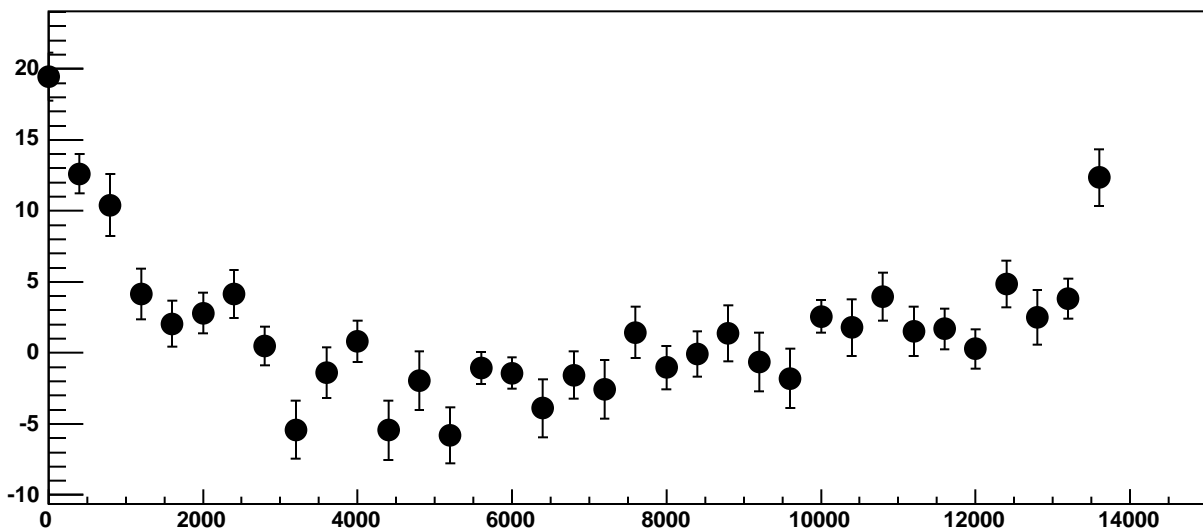


$\chi^2 / \text{ndf}$  63.88 / 23  
p0  $-459.8 \pm 0.7476$   
p1  $0.01828 \pm 0.0001139$

Chip 9, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

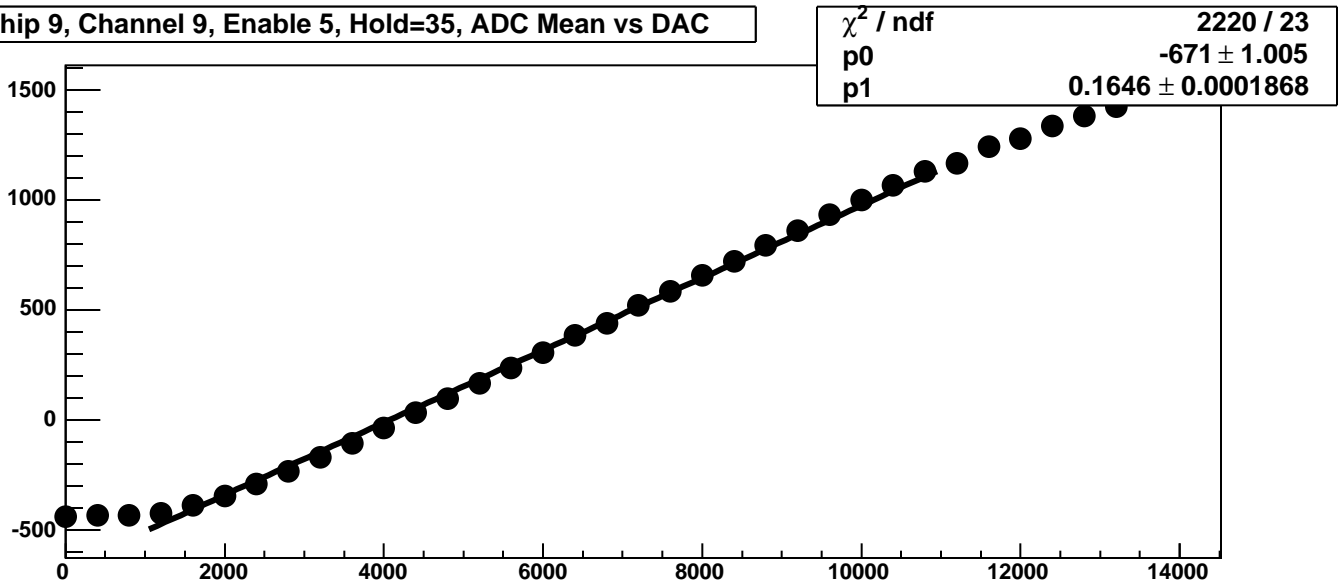


Chip 9, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

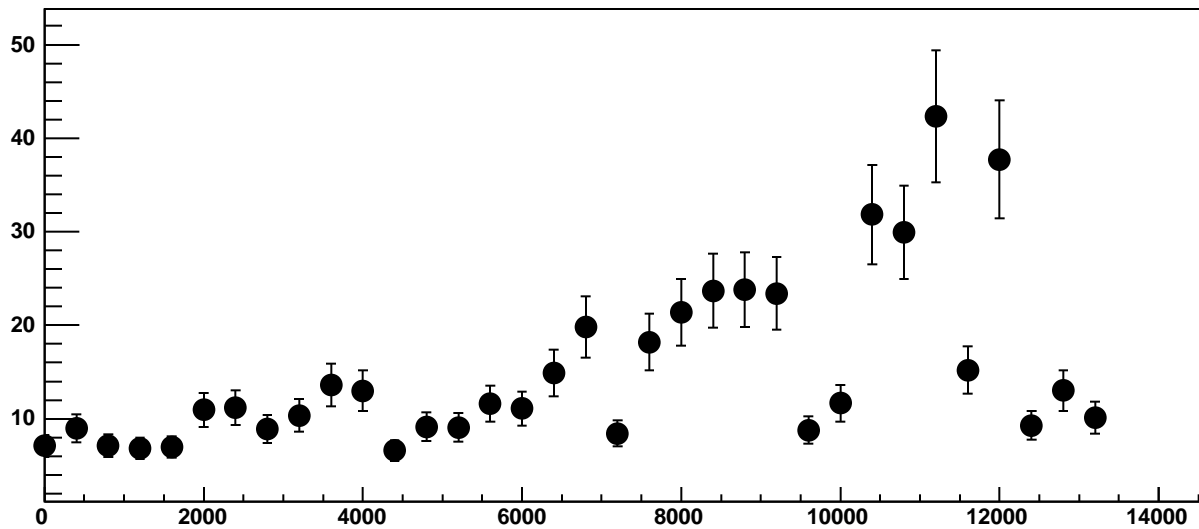




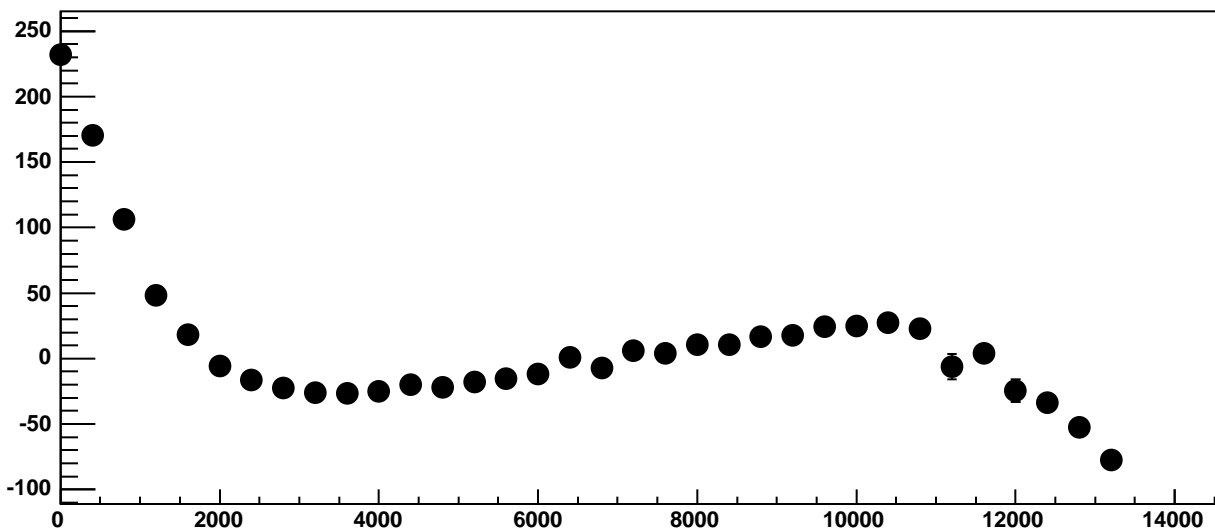
Chip 9, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

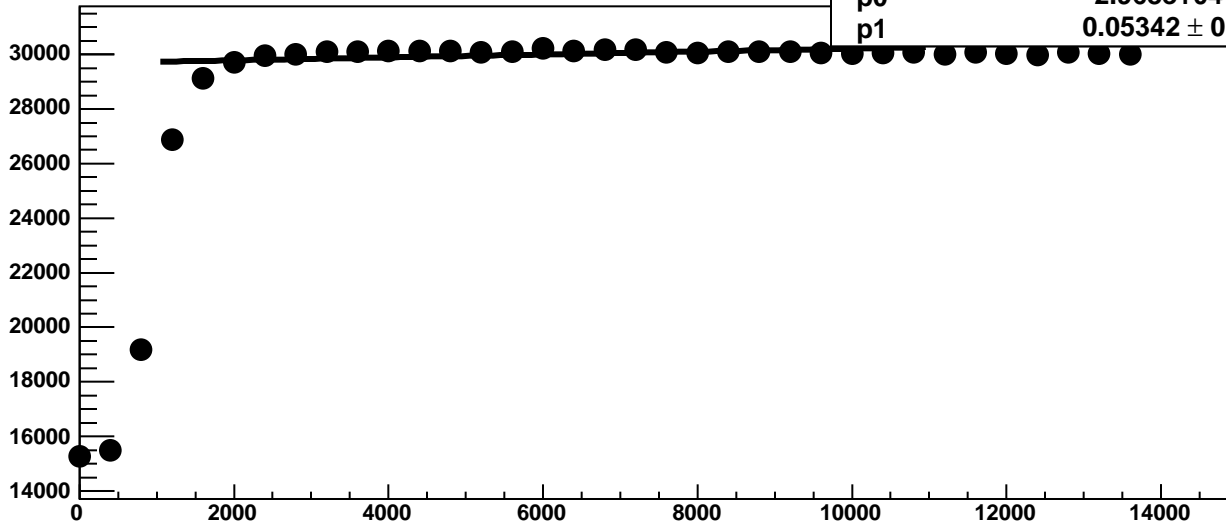
1931 / 23

p0

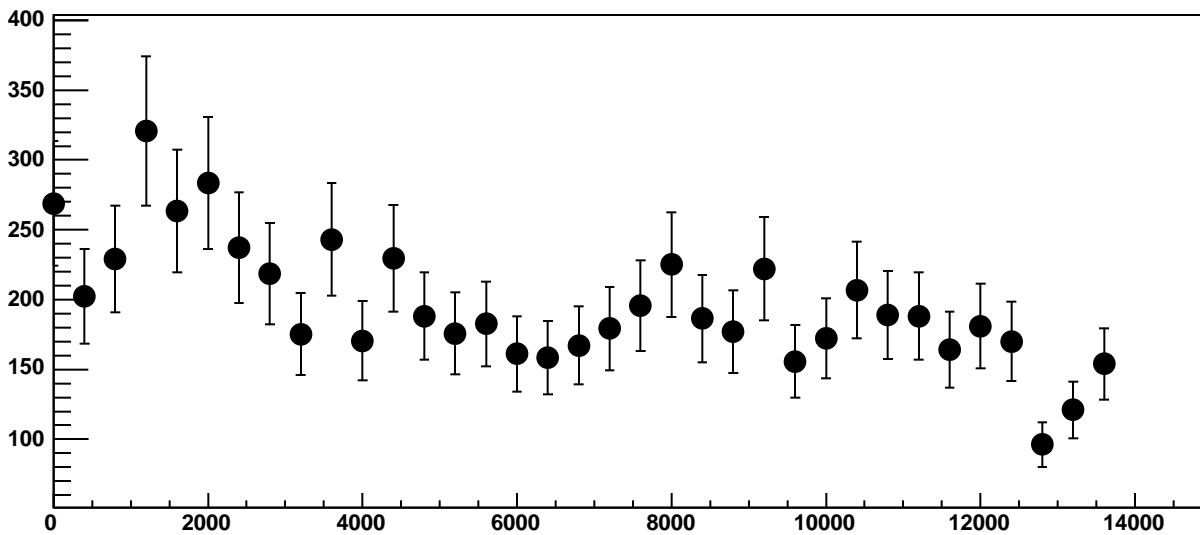
$2.968e+04 \pm 23.58$

p1

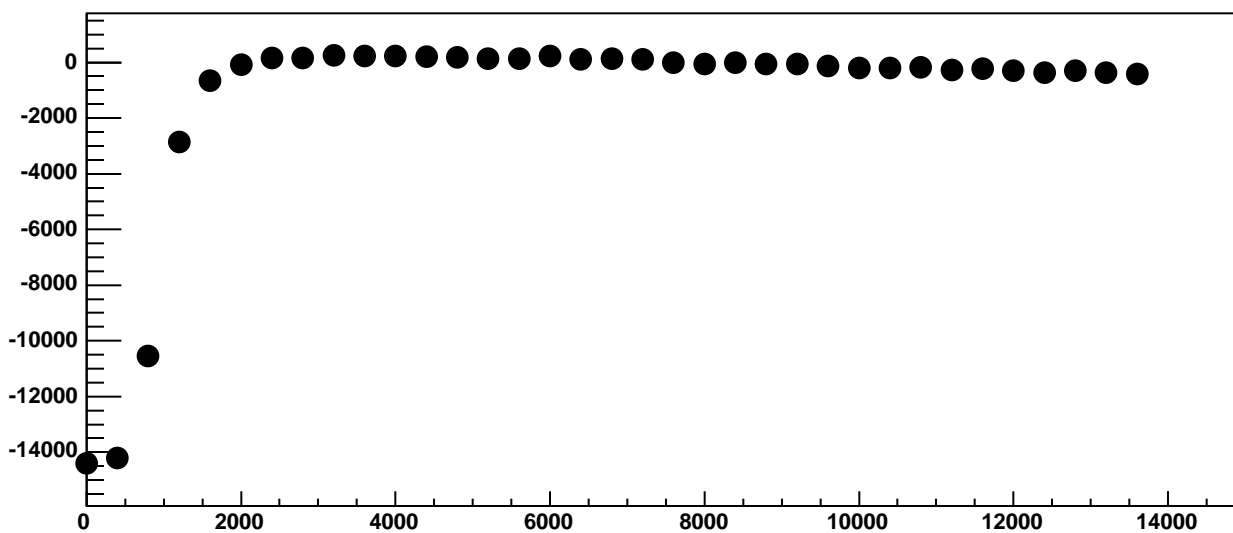
$0.05342 \pm 0.003376$



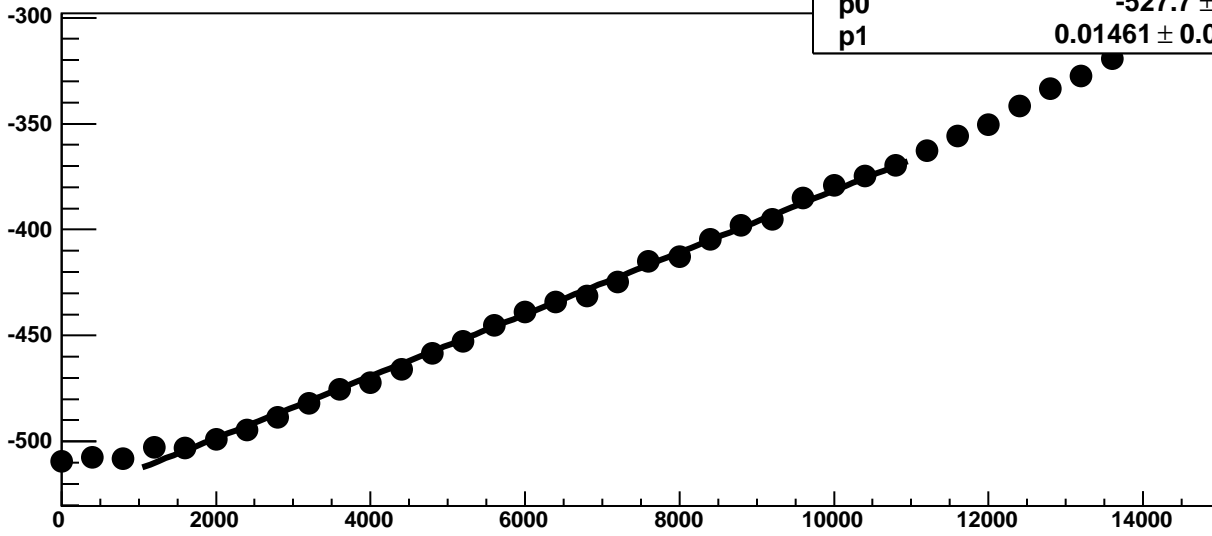
Chip 9, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

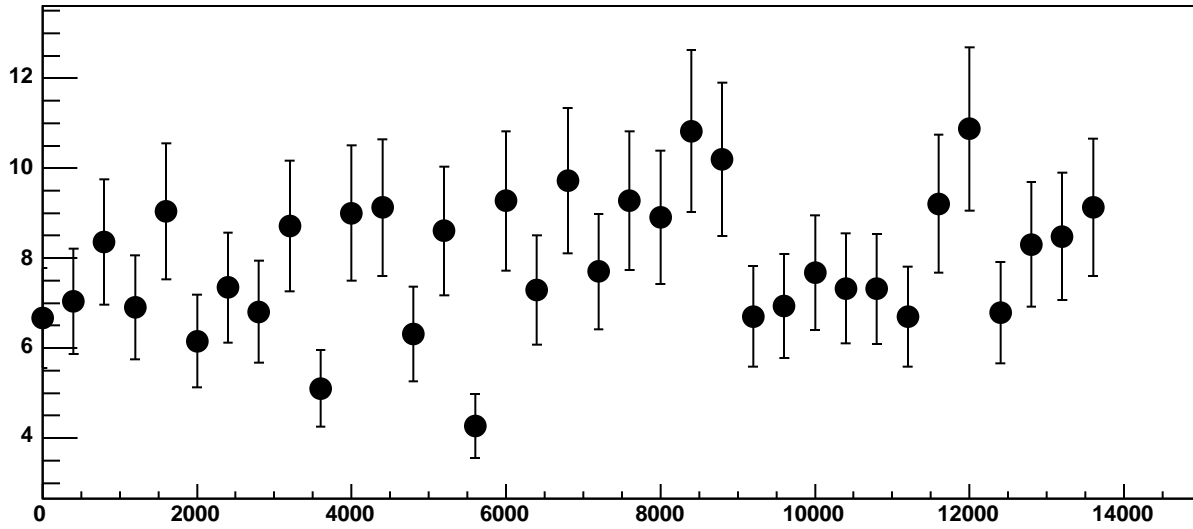


Chip 9, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

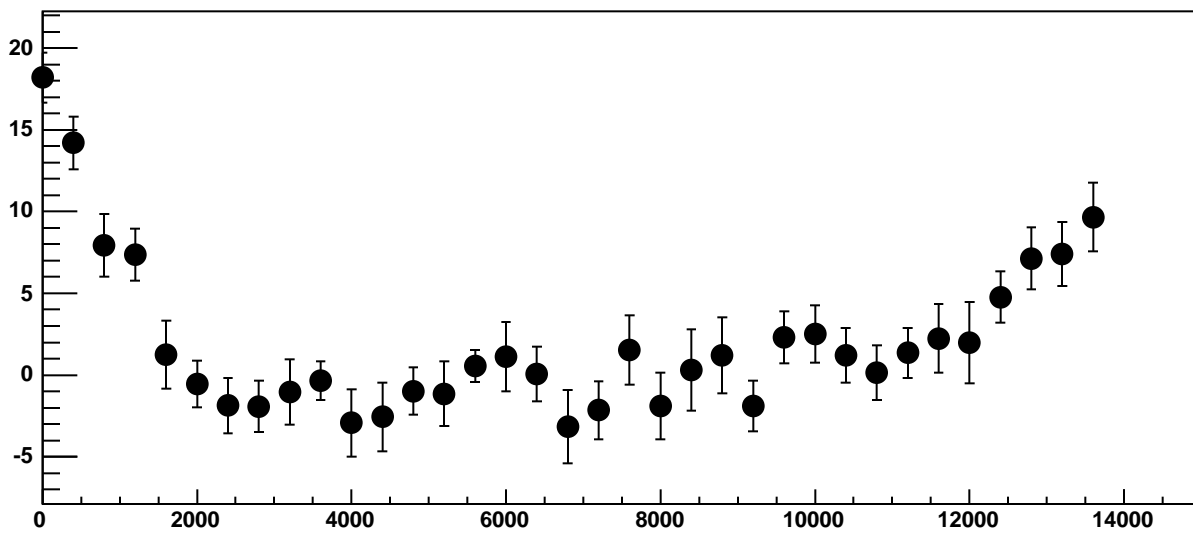


$\chi^2 / \text{ndf}$  41.32 / 23  
p0  $-527.7 \pm 0.7527$   
p1  $0.01461 \pm 0.0001172$

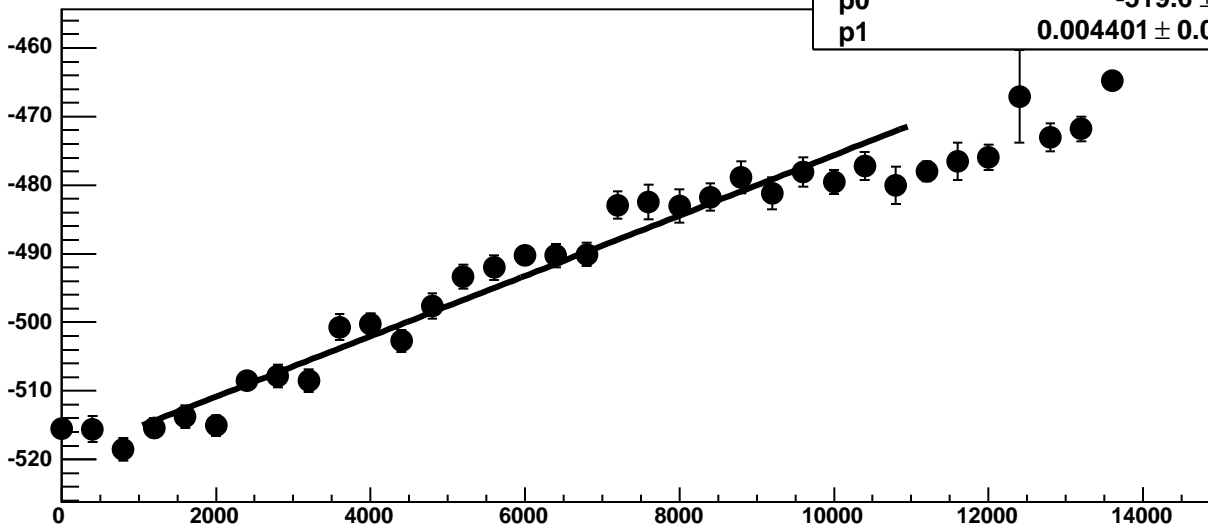
Chip 9, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



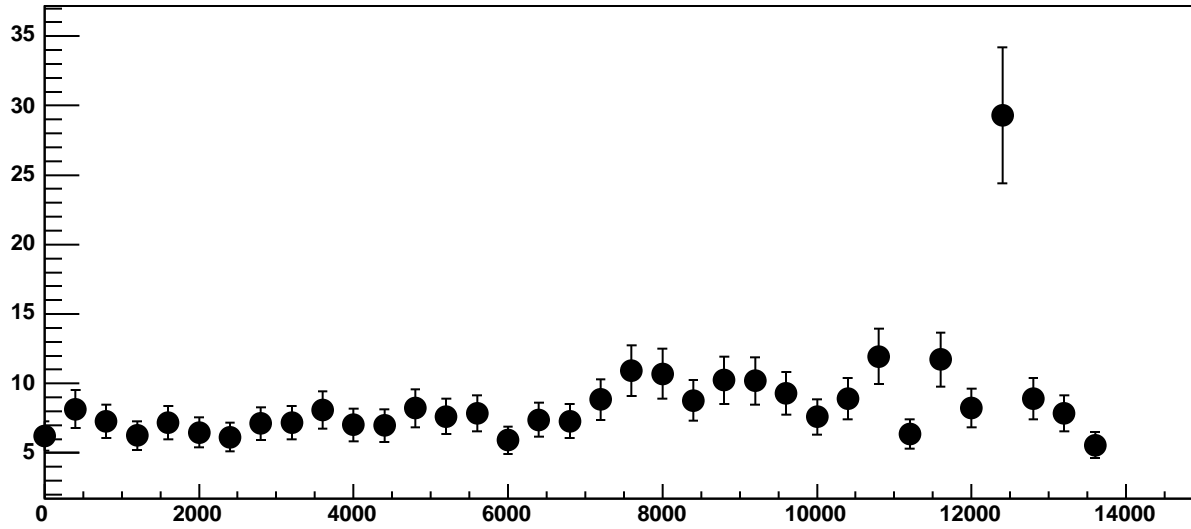
Chip 9, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



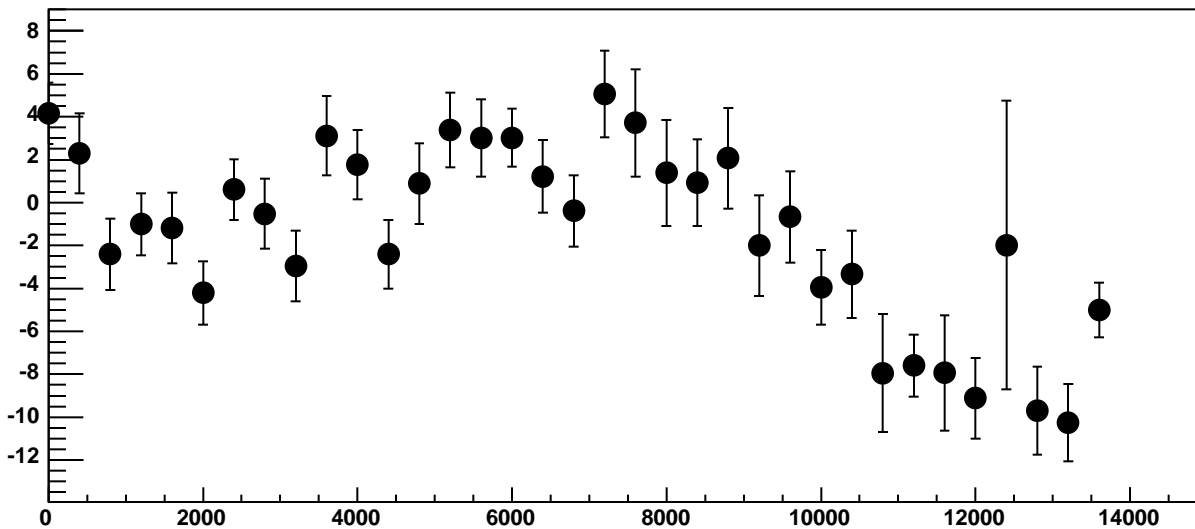
Chip 9, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



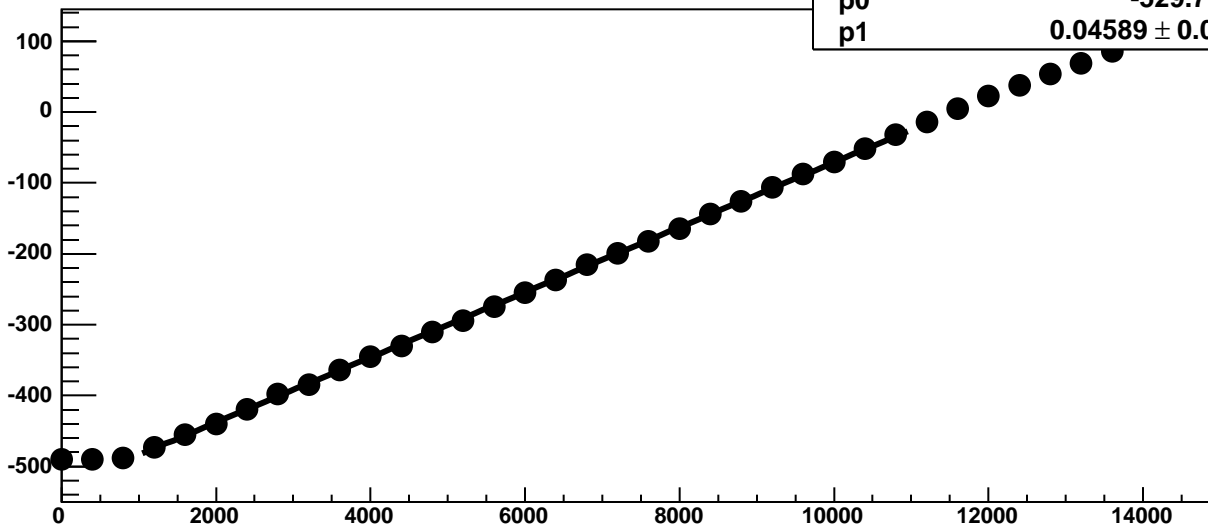
Chip 9, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



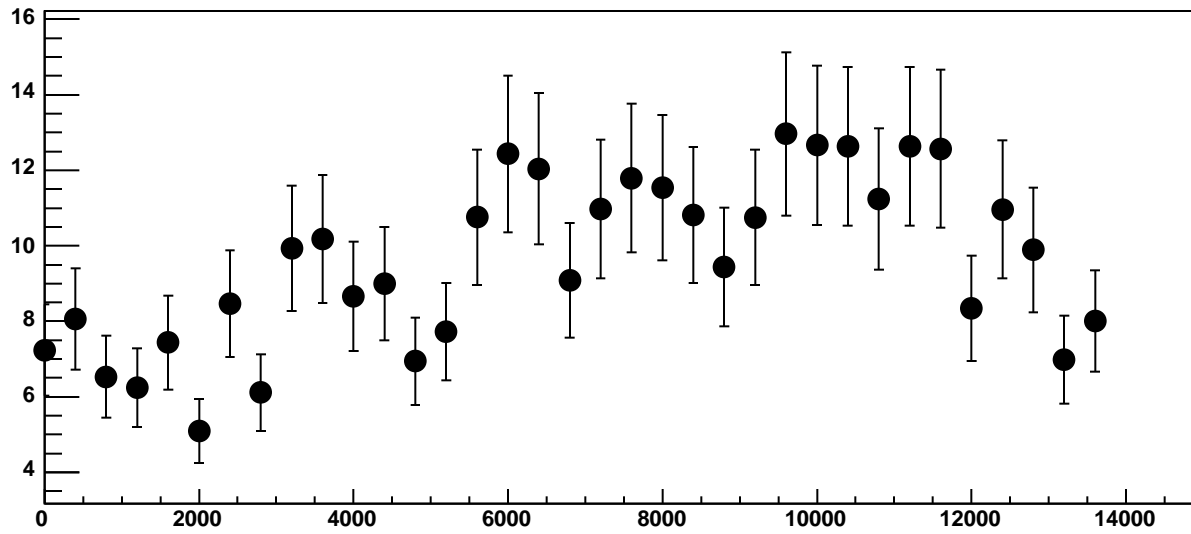
Chip 9, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



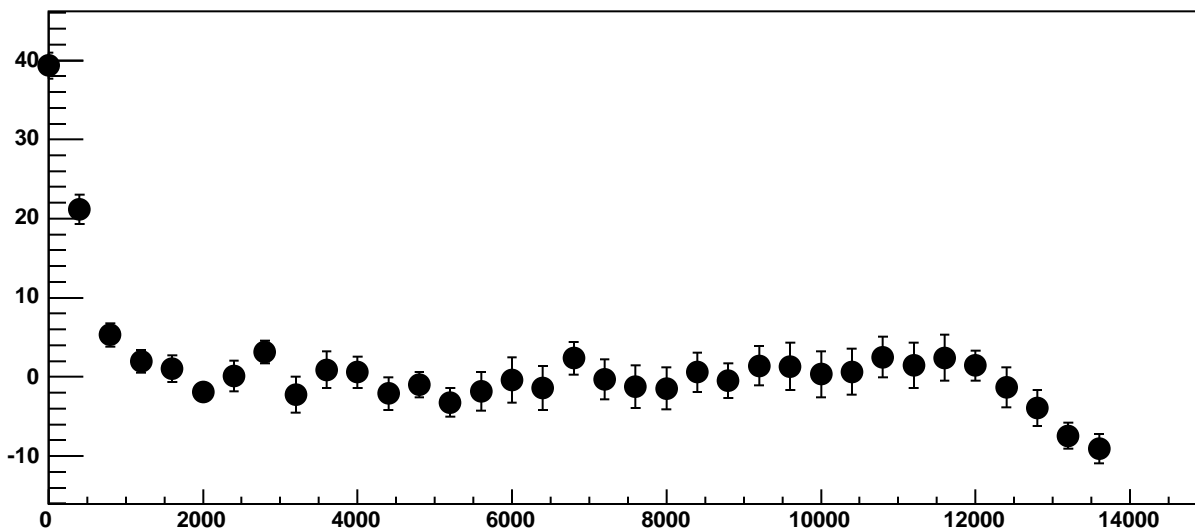
Chip 9, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



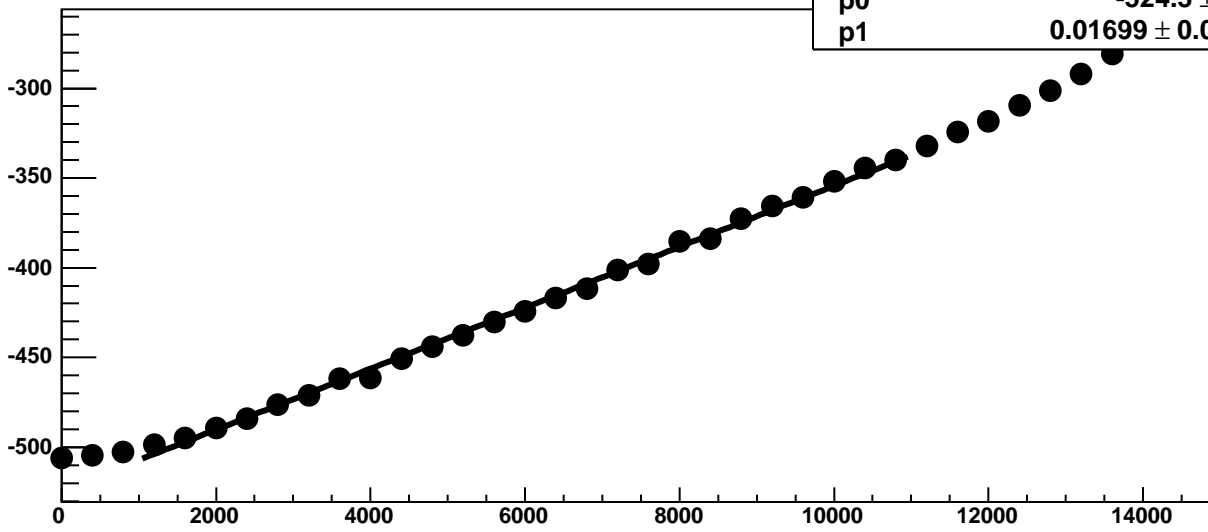
Chip 9, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

52.08 / 23

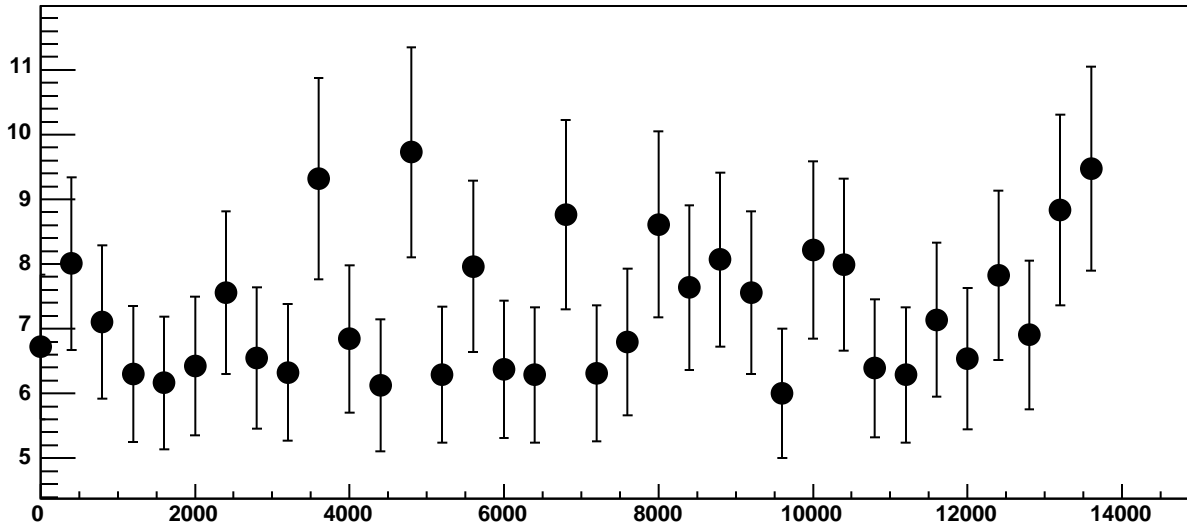
p0

$-524.3 \pm 0.7127$

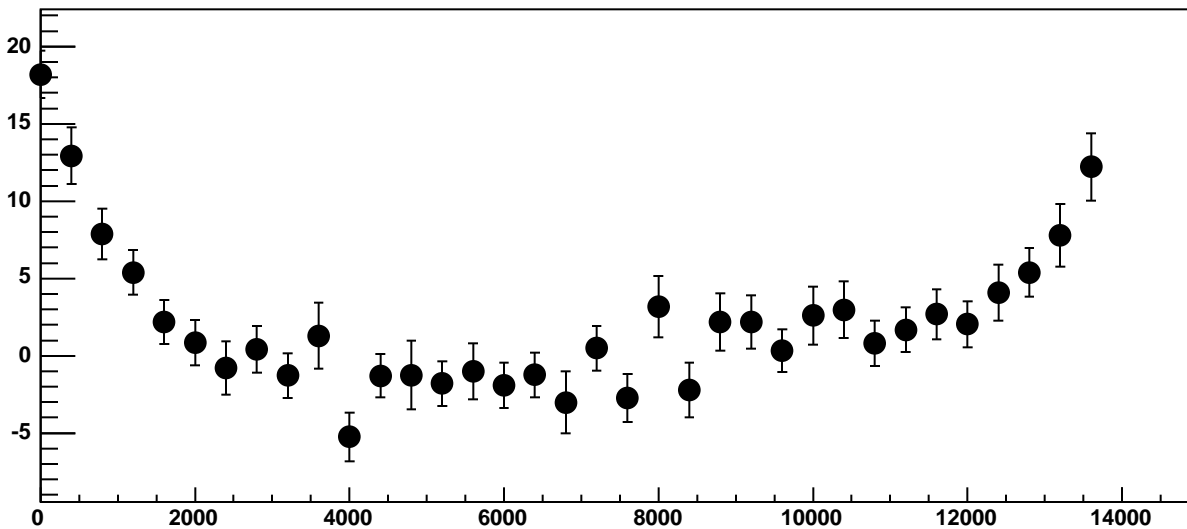
p1

$0.01699 \pm 0.0001094$

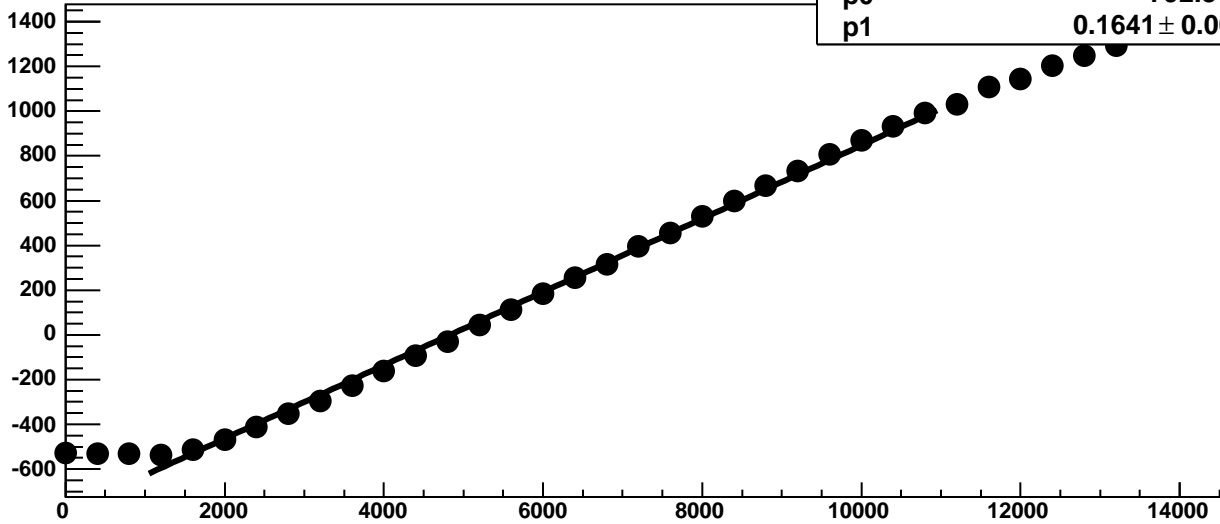
Chip 9, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



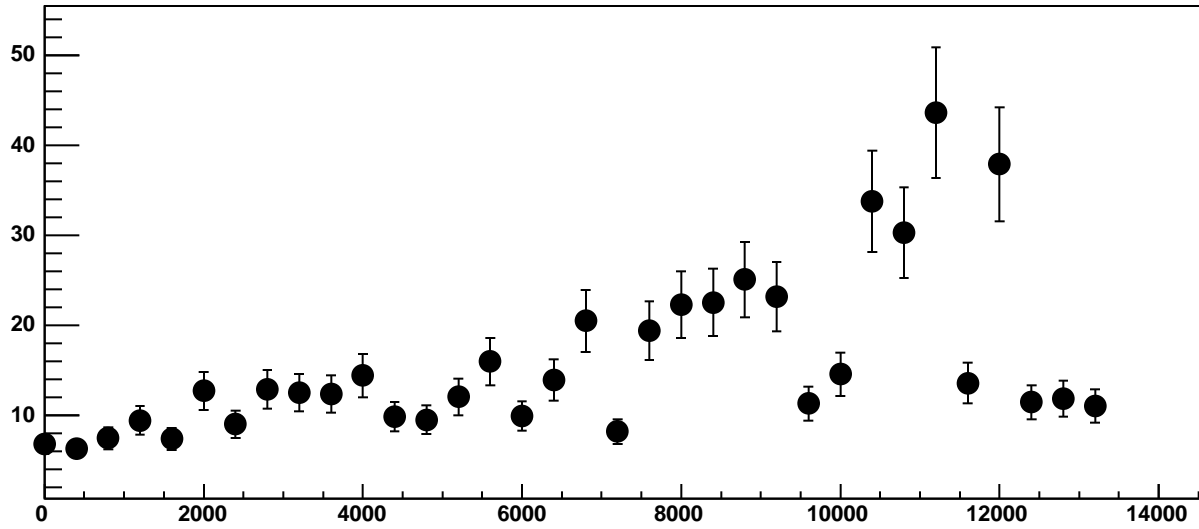
Chip 9, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



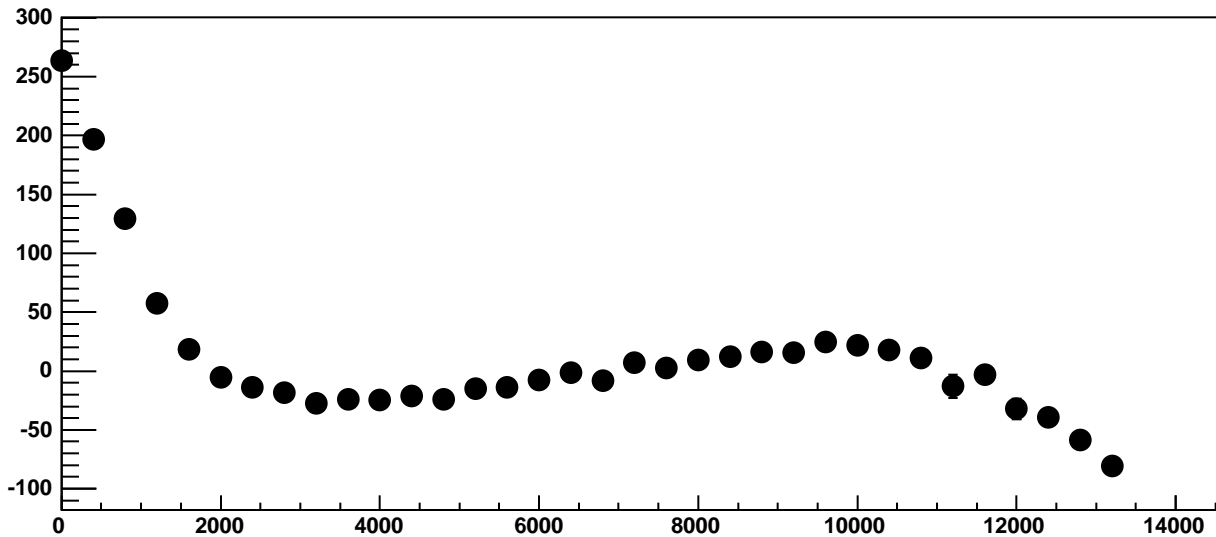
Chip 9, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



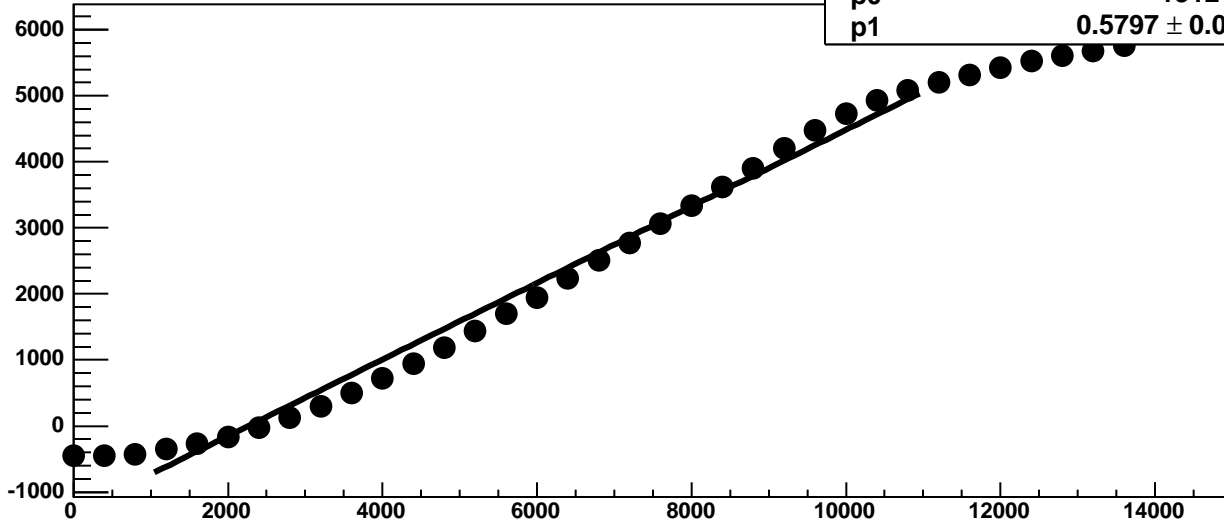
Chip 9, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



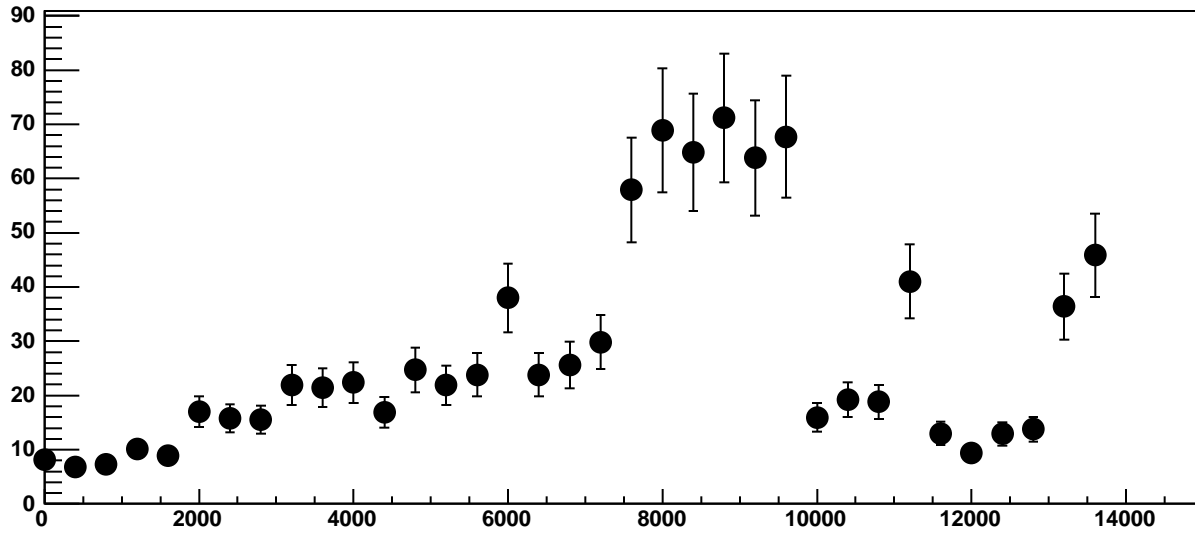
Chip 9, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



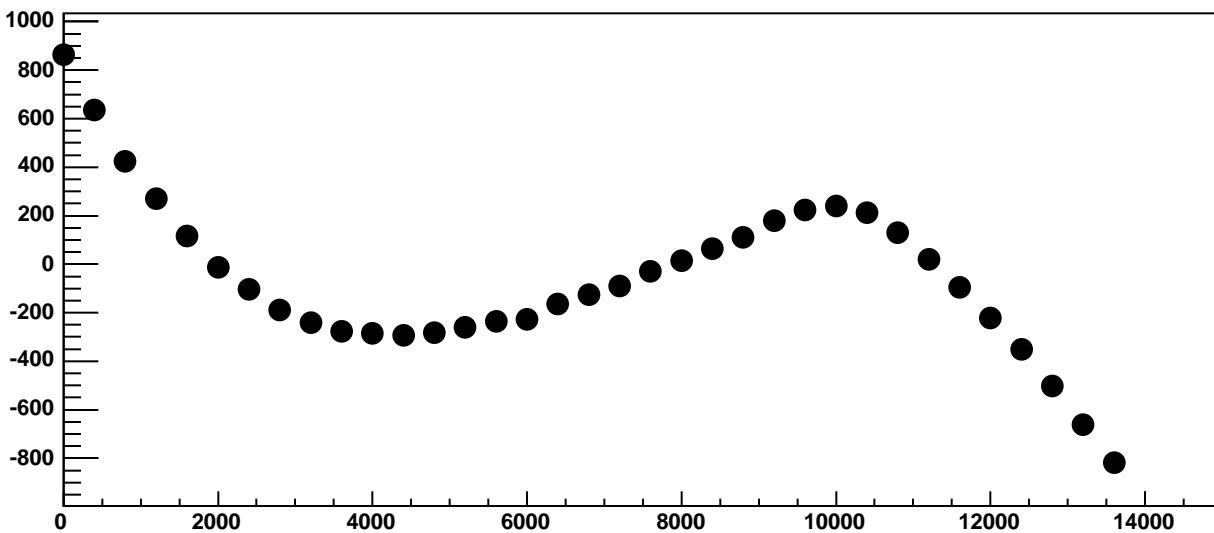
Chip 9, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC



Chip 9, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

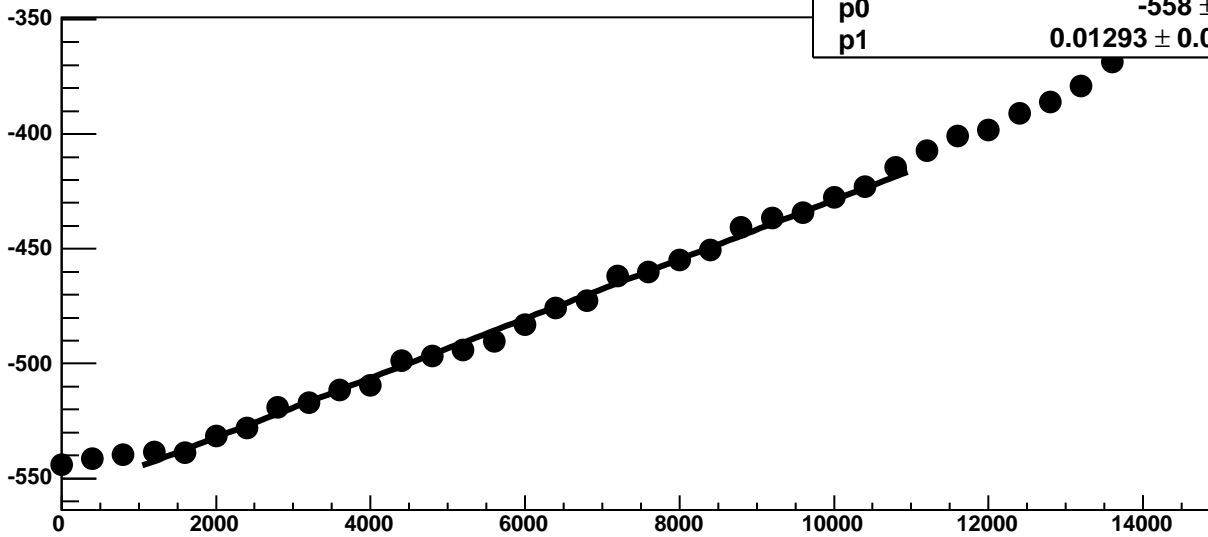


Chip 9, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

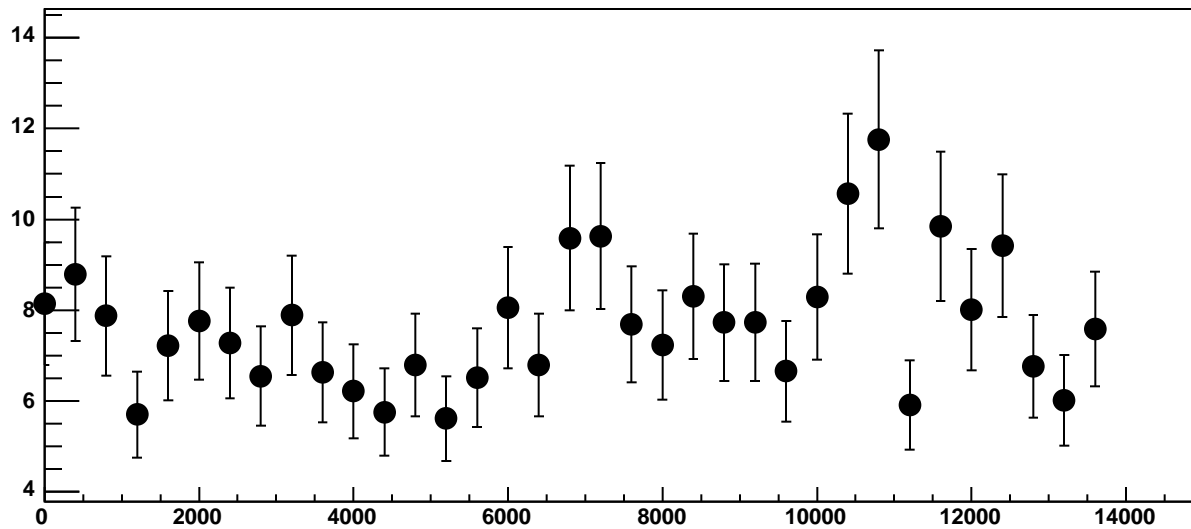




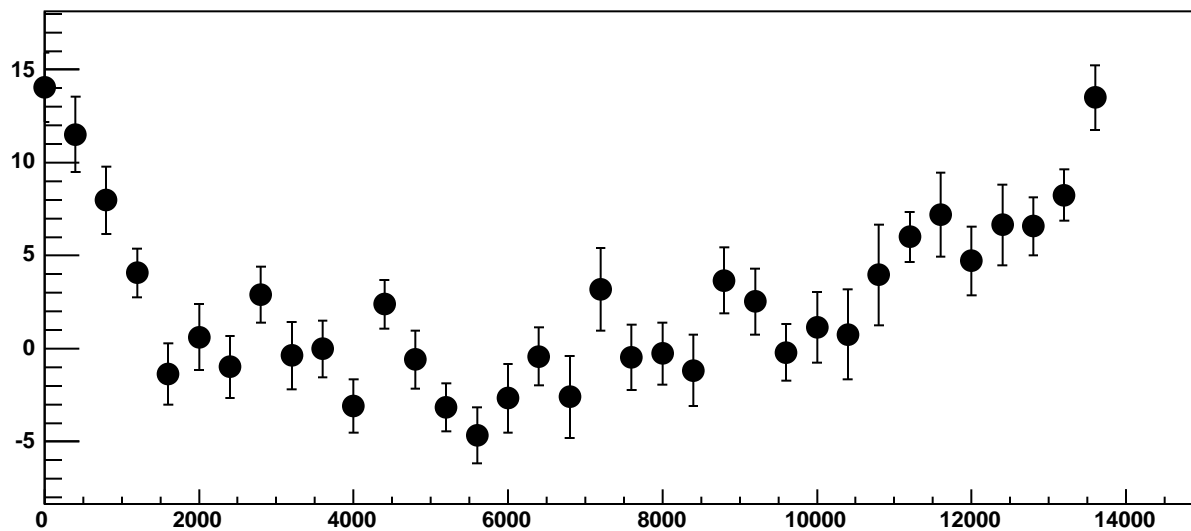
Chip 9, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



Chip 9, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

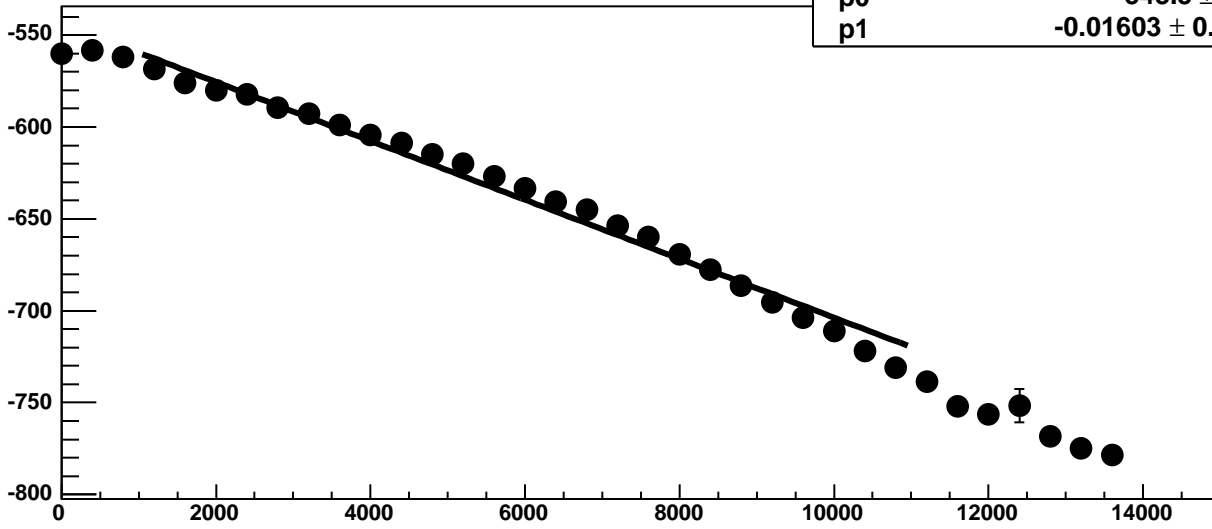


Chip 9, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

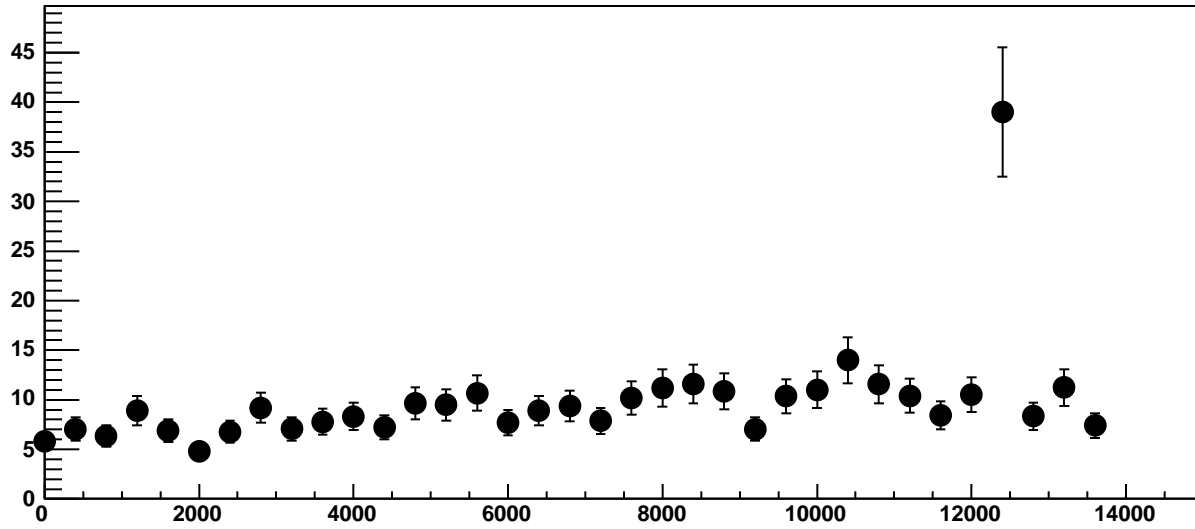


Chip 9, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

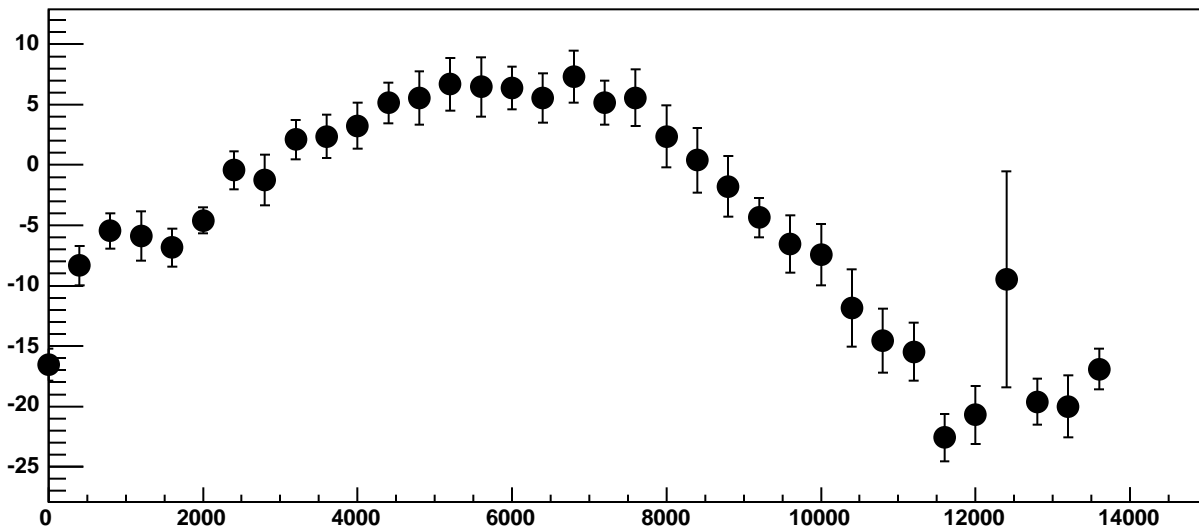
$\chi^2 / \text{ndf}$  197.9 / 23  
p0  $-543.5 \pm 0.7952$   
p1  $-0.01603 \pm 0.000137$



Chip 9, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

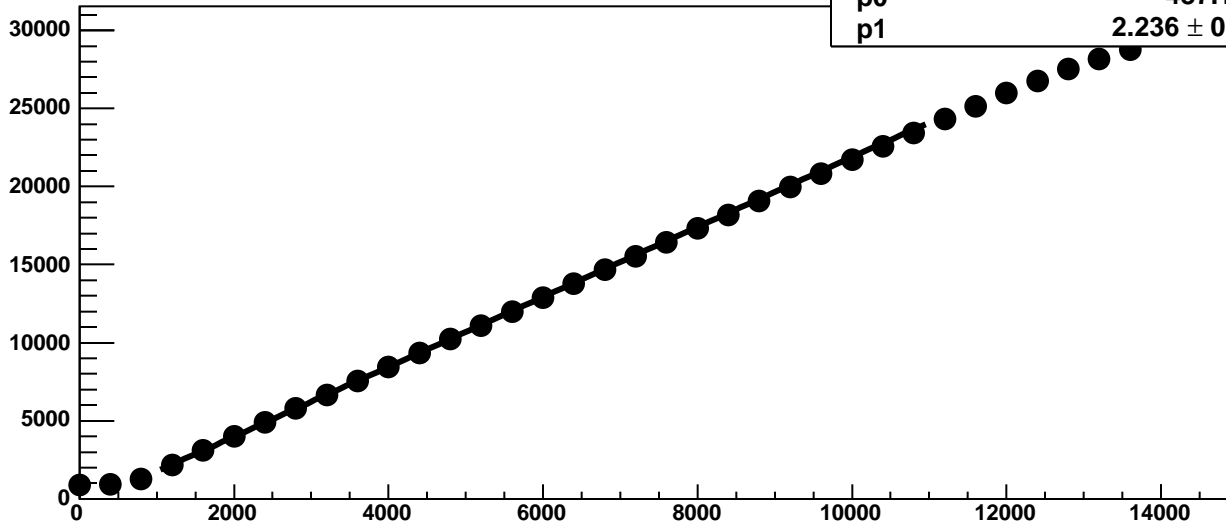


Chip 9, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

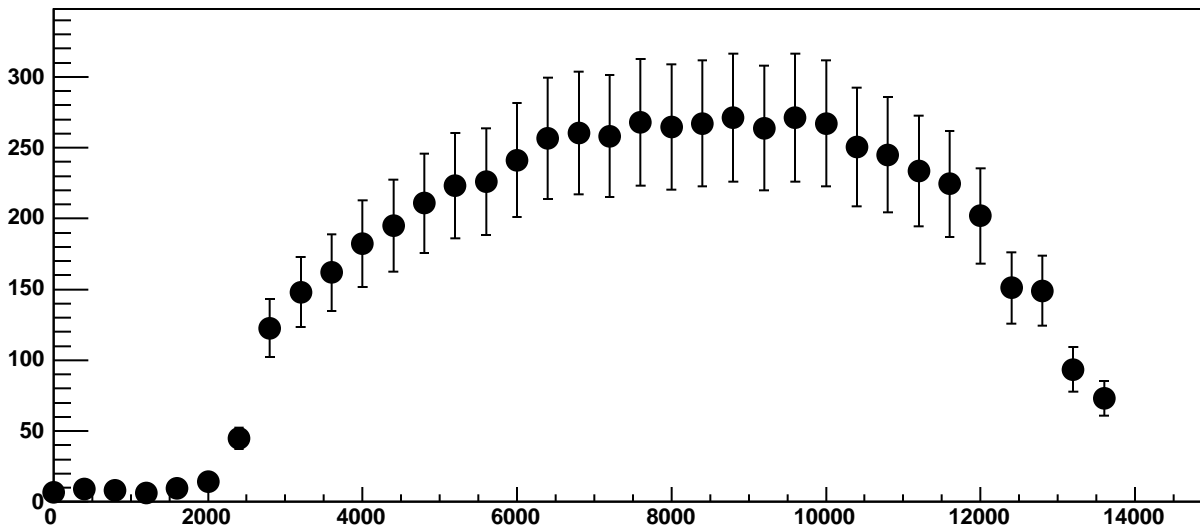


Chip 9, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC

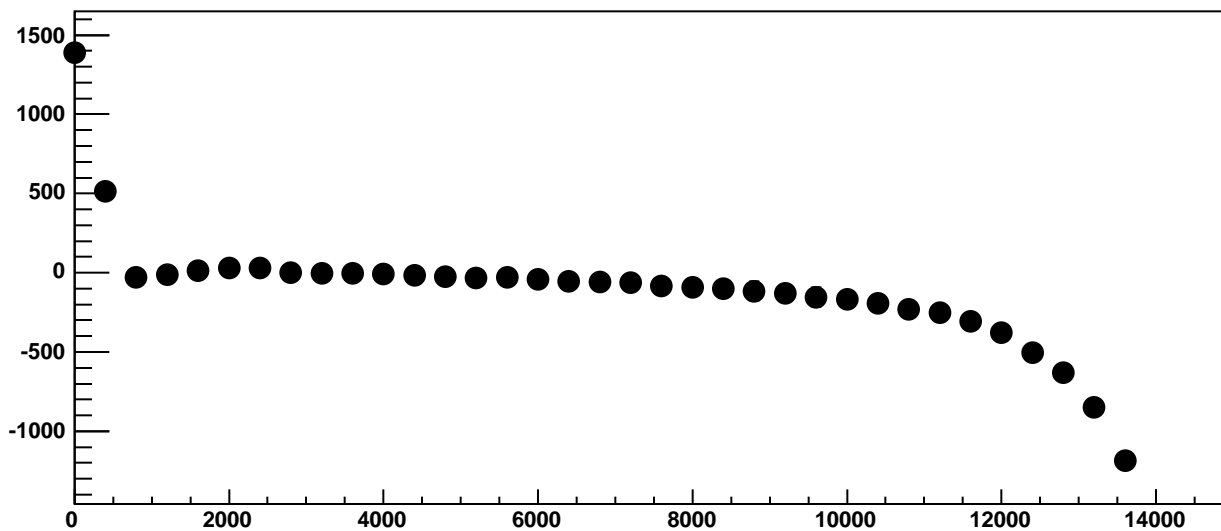
$\chi^2 / \text{ndf}$  234.3 / 23  
p0  $-487.1 \pm 2.971$   
p1  $2.236 \pm 0.001865$



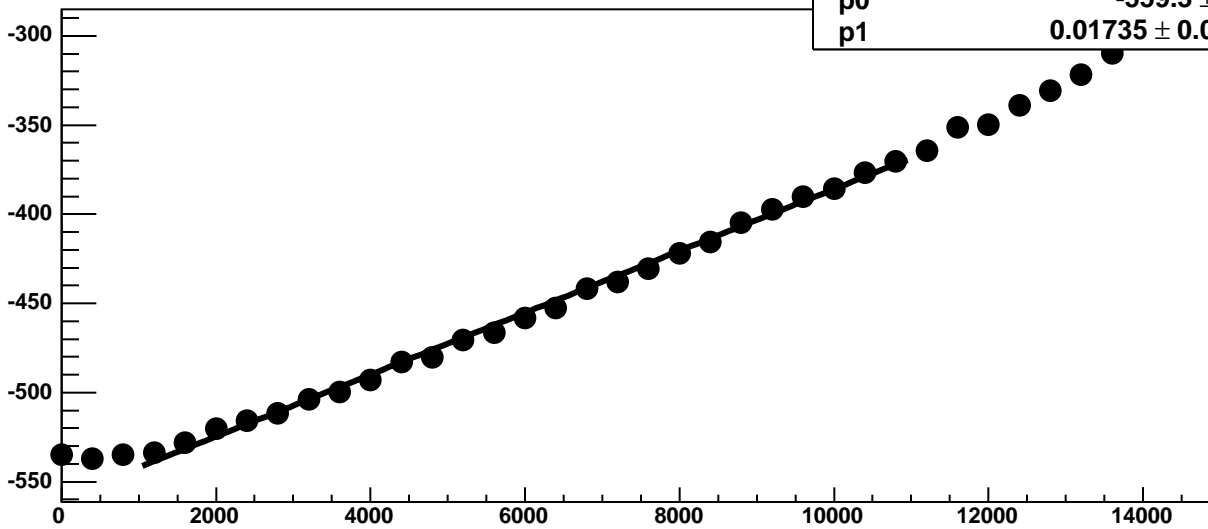
Chip 9, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

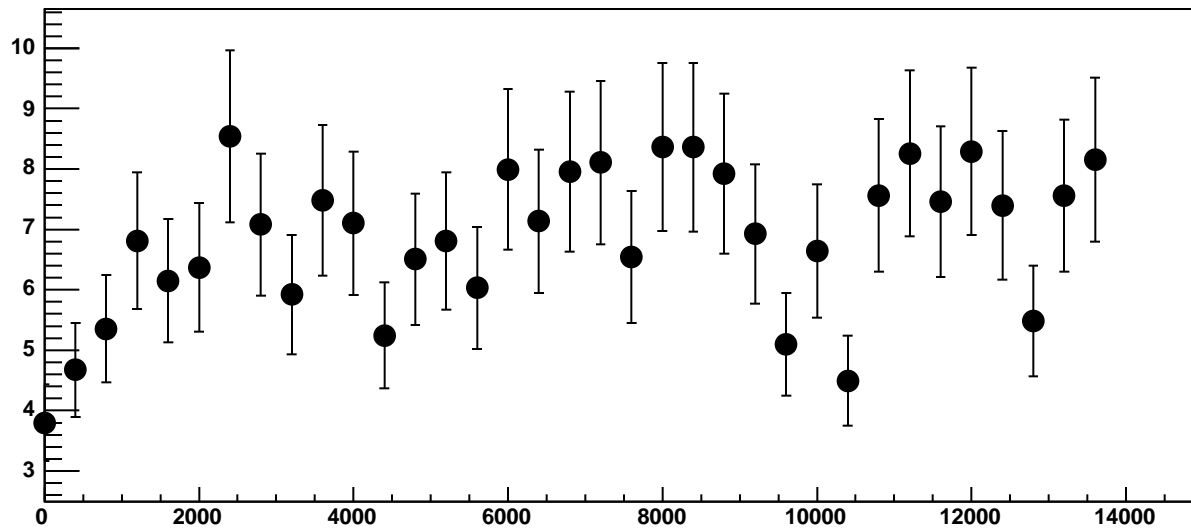


Chip 9, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

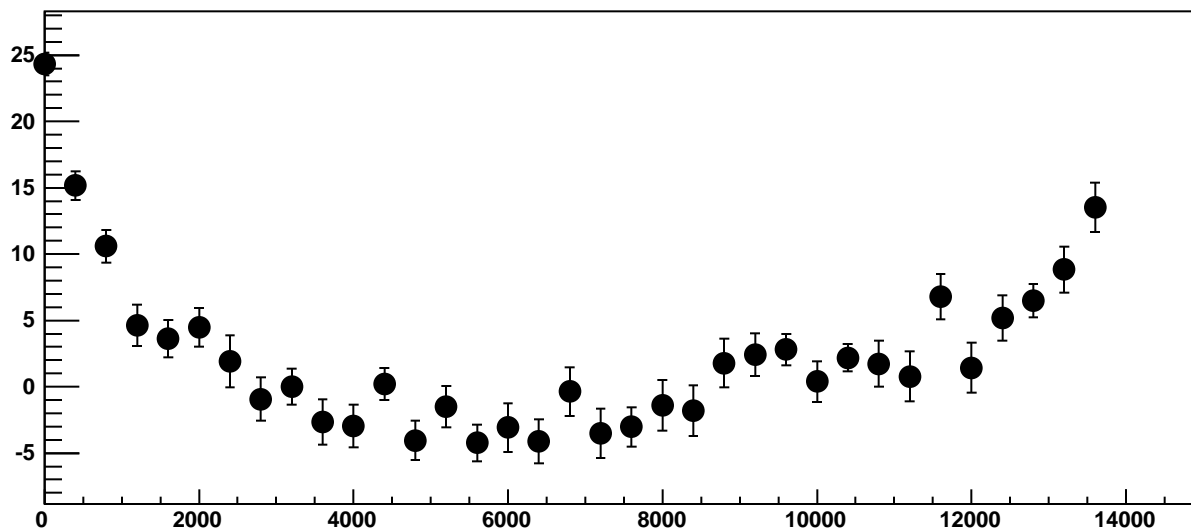


$\chi^2 / \text{ndf}$  82.08 / 23  
p0  $-559.3 \pm 0.6923$   
p1  $0.01735 \pm 0.0001014$

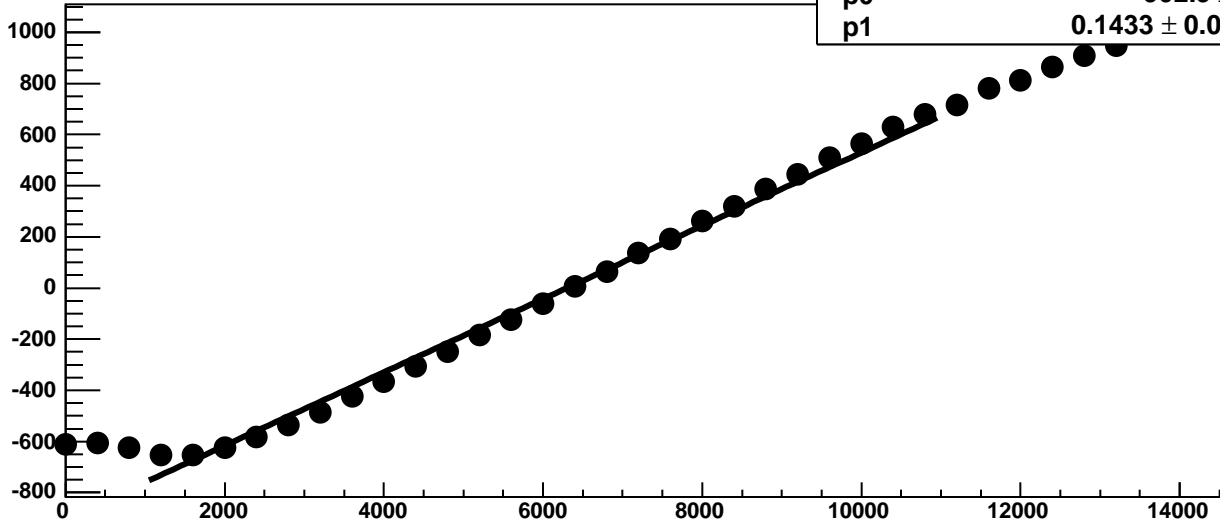
Chip 9, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



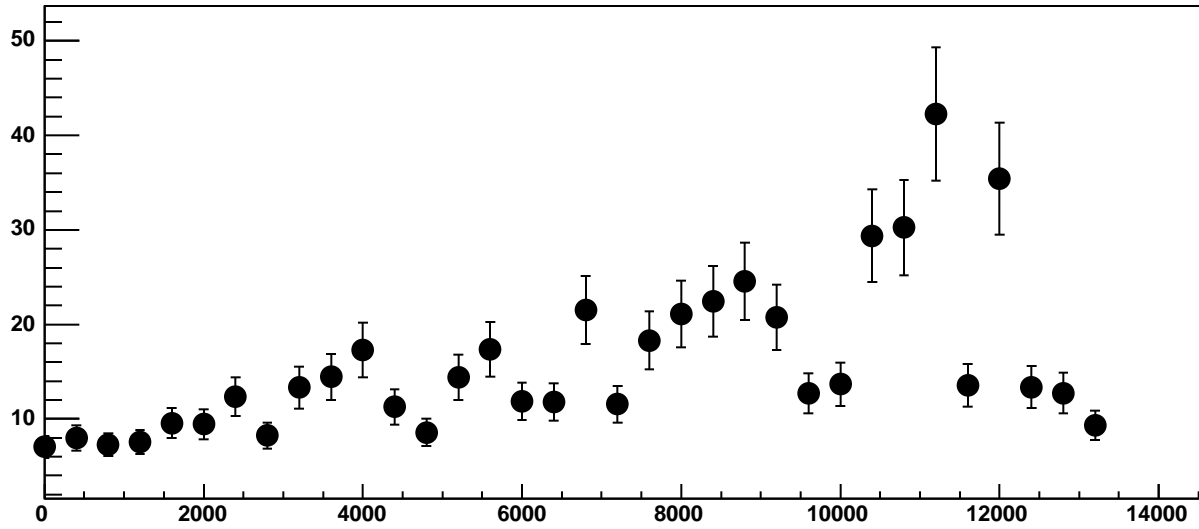
Chip 9, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



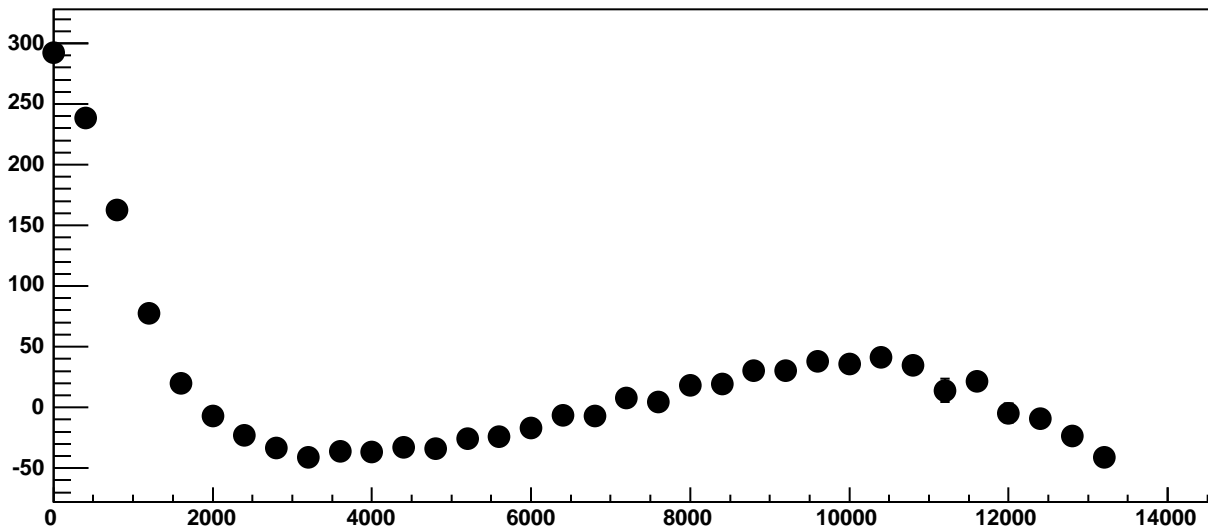
Chip 9, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



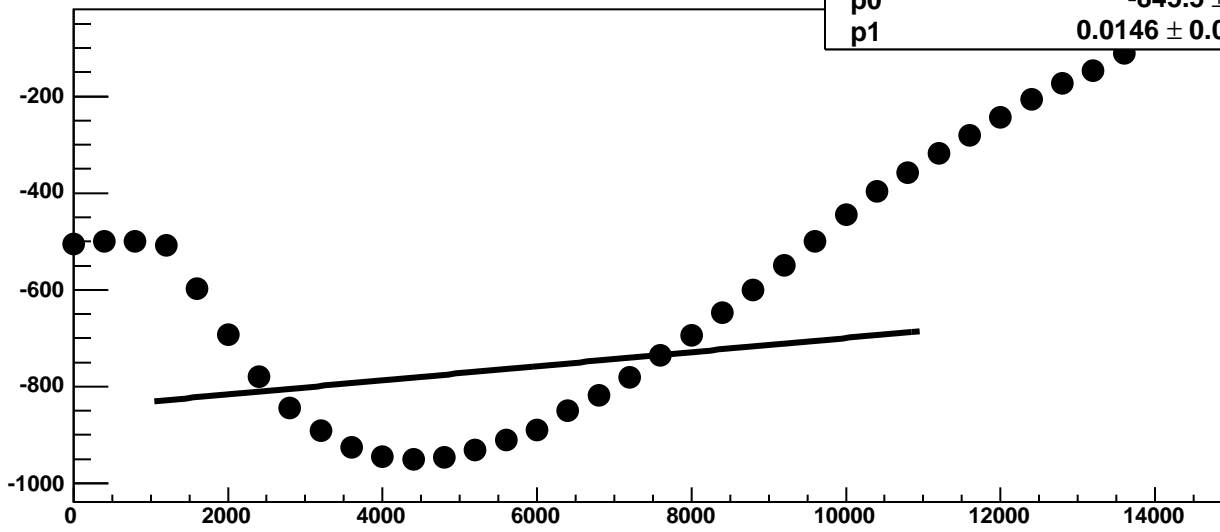
Chip 9, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

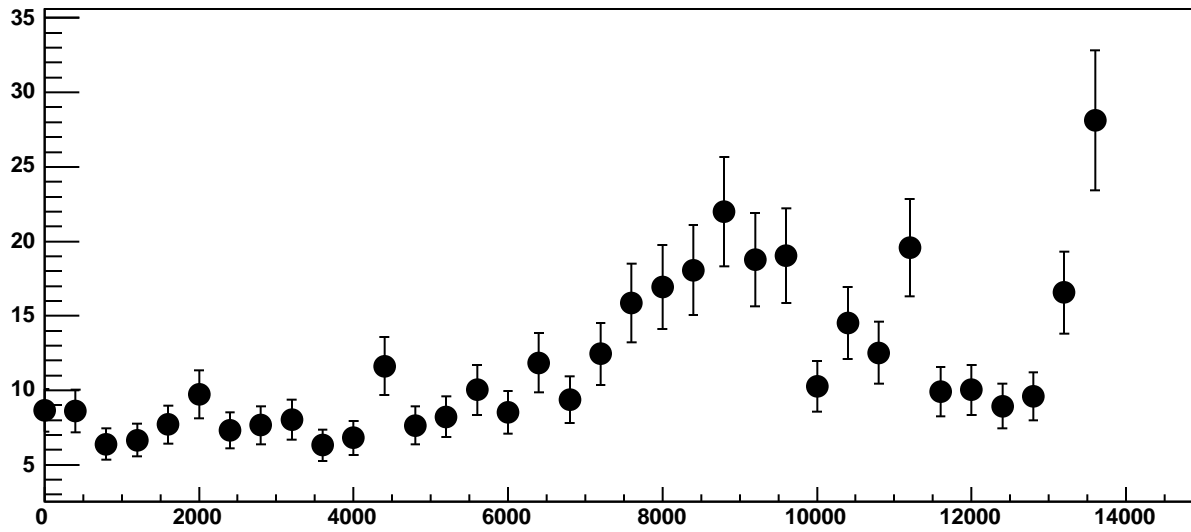


Chip 9, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

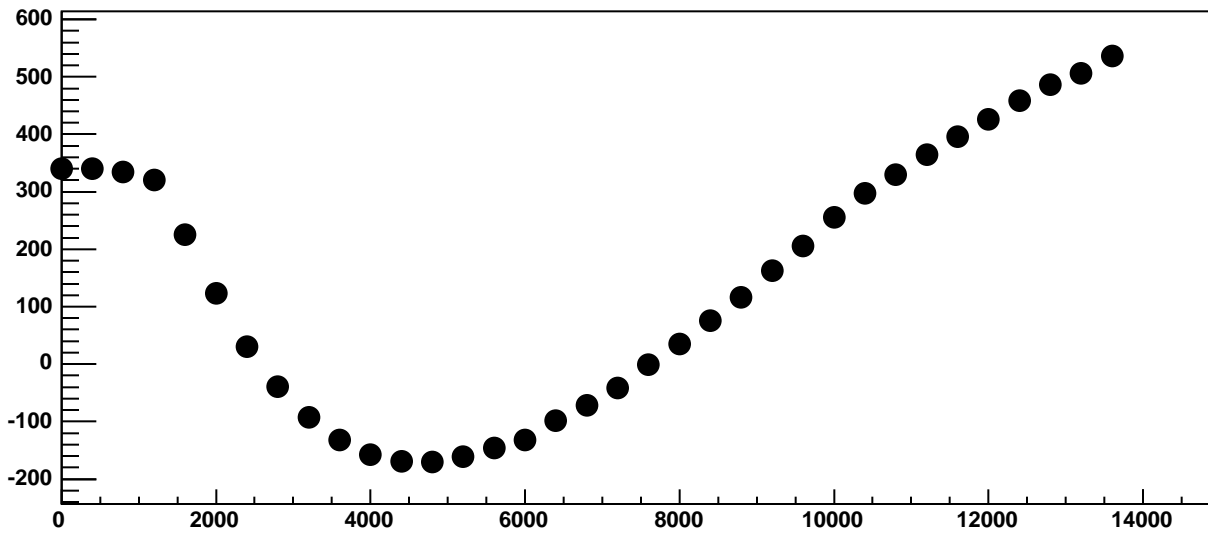


$\chi^2 / \text{ndf}$  1.543e+05 / 23  
p0 -845.5 ± 0.8947  
p1 0.0146 ± 0.0001699

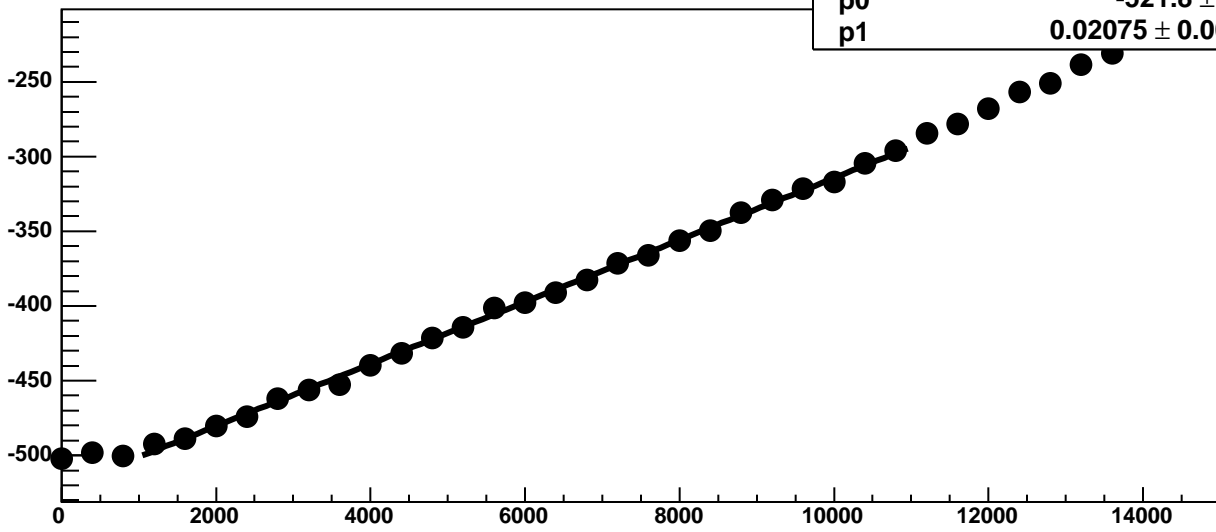
Chip 9, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

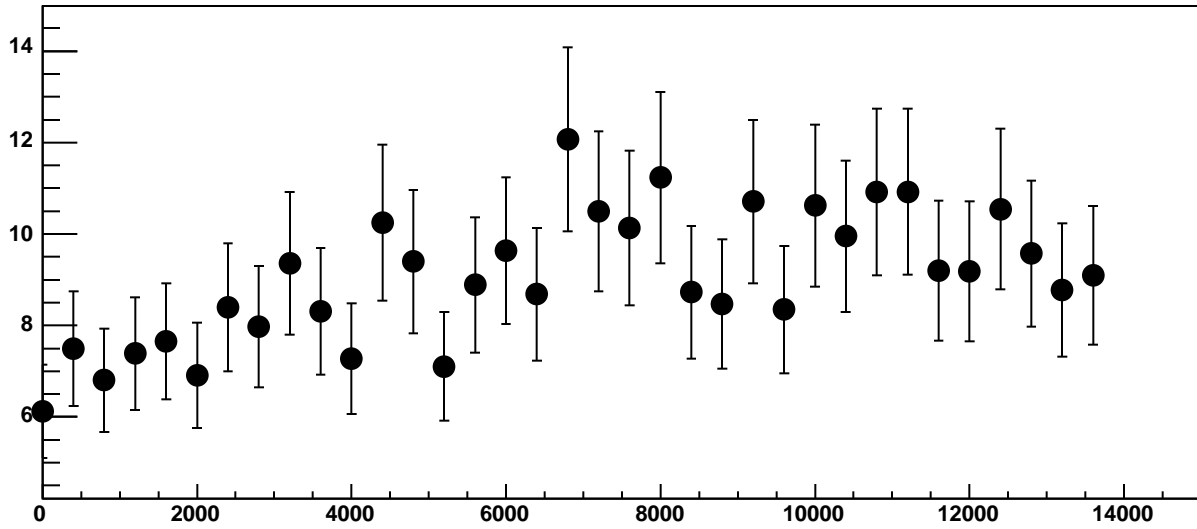


Chip 9, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

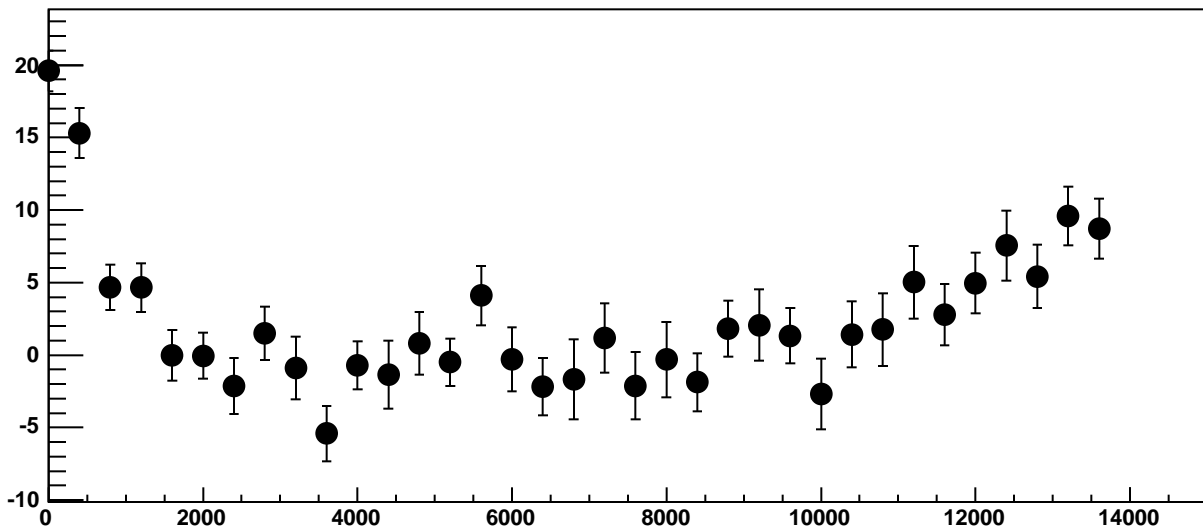


$\chi^2 / \text{ndf}$  30.17 / 23  
p0  $-521.8 \pm 0.8606$   
p1  $0.02075 \pm 0.0001395$

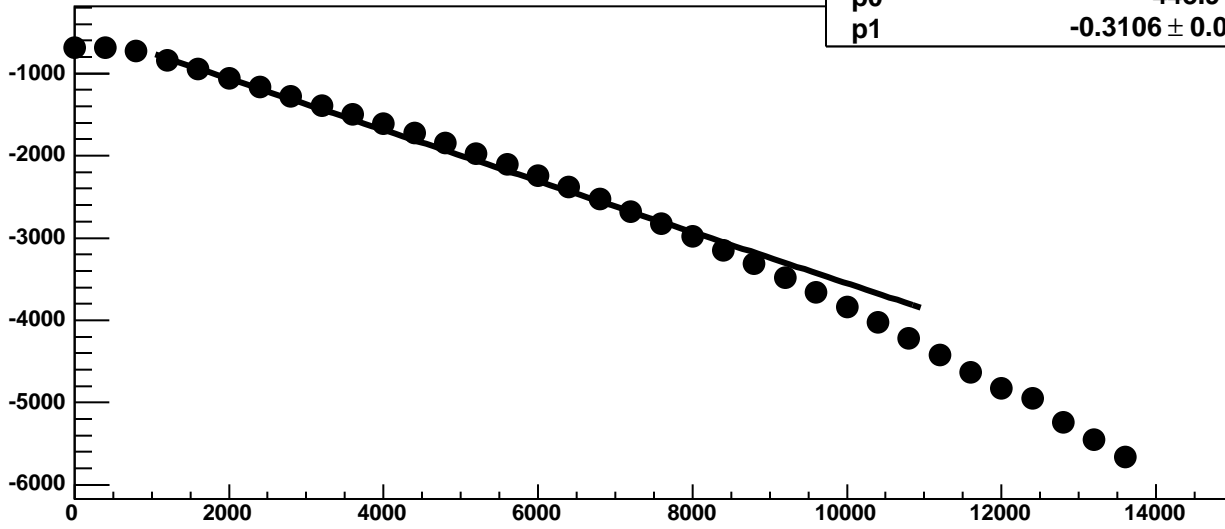
Chip 9, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4424 / 23

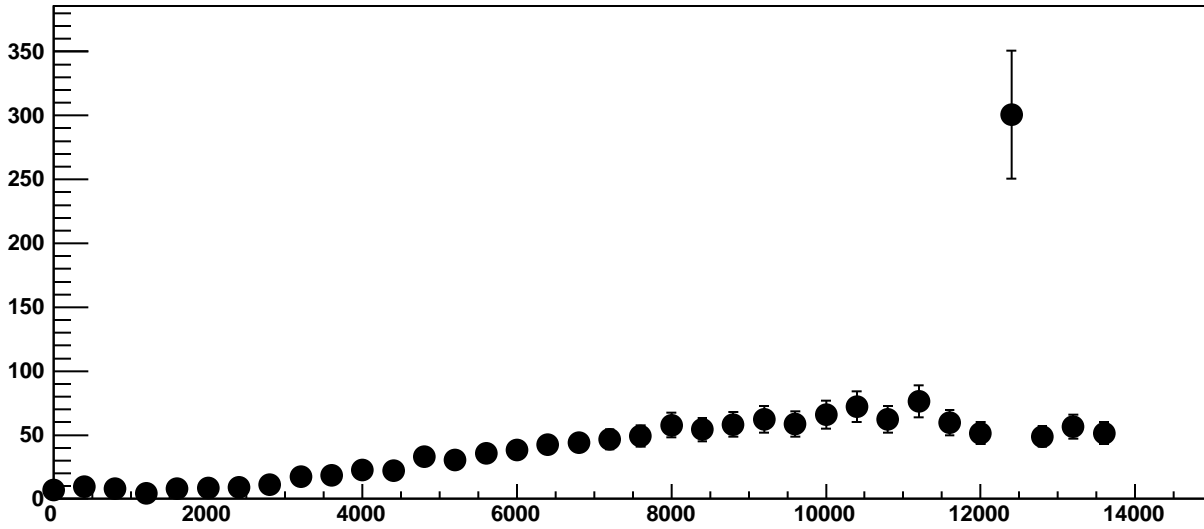
p0

$-443.9 \pm 1.186$

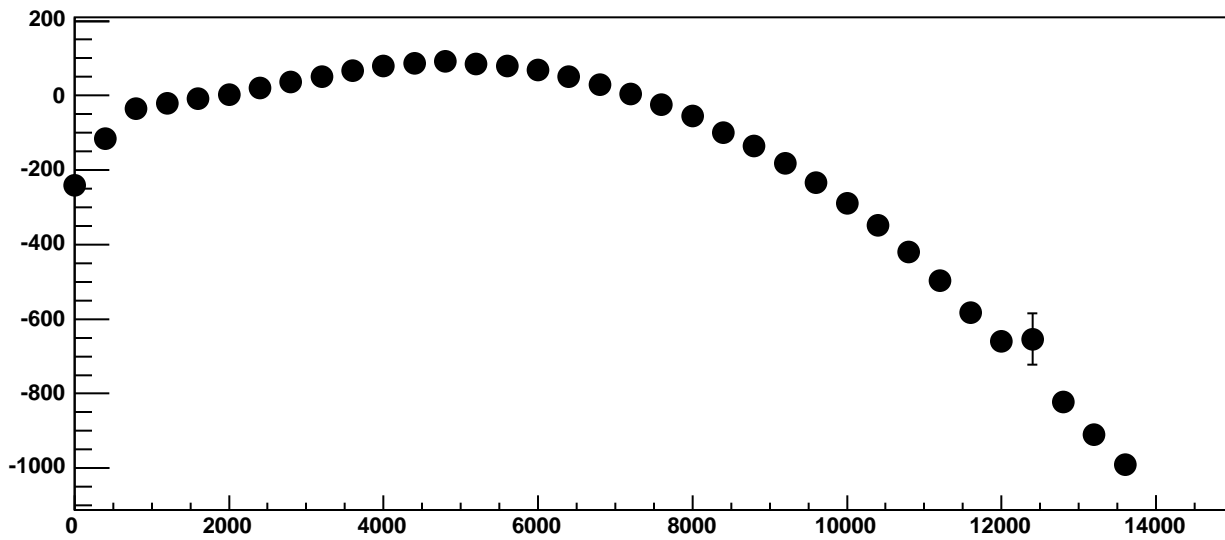
p1

$-0.3106 \pm 0.0004419$

Chip 9, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

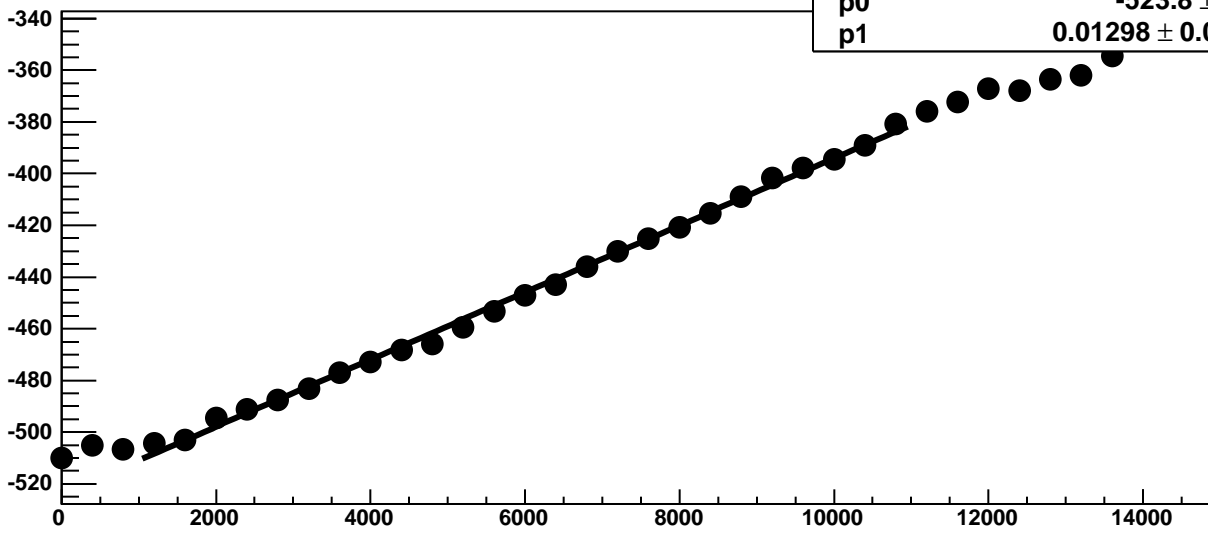


Chip 9, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

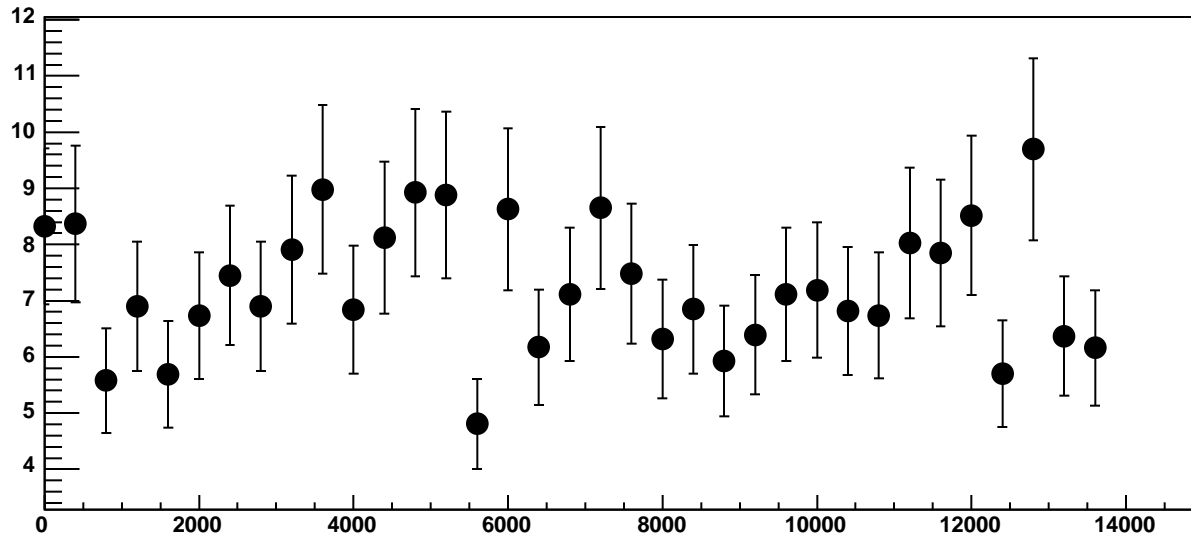




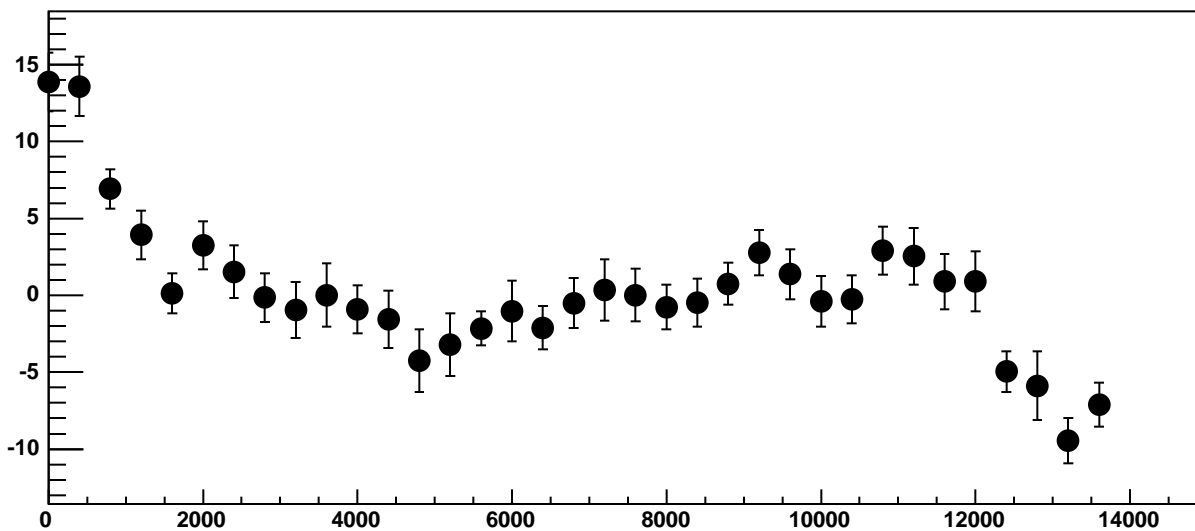
Chip 9, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



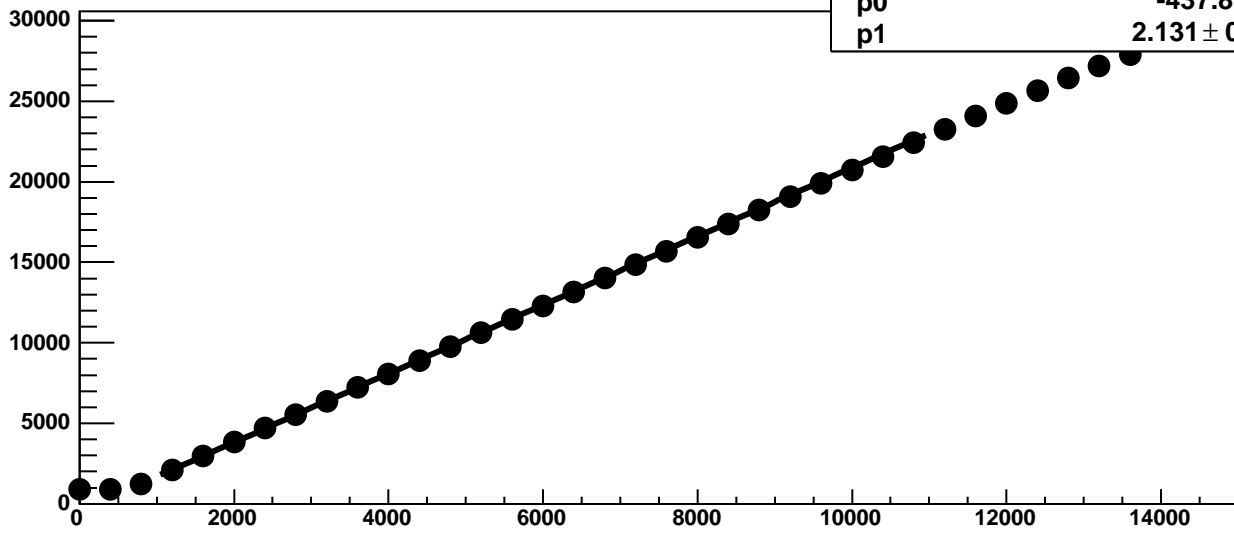
Chip 9, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



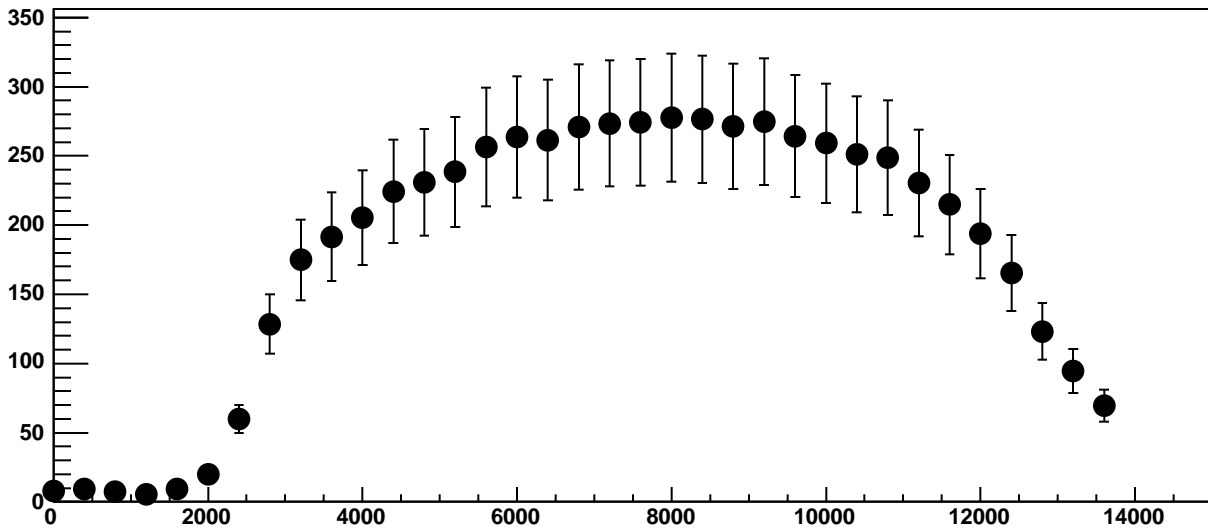
Chip 9, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



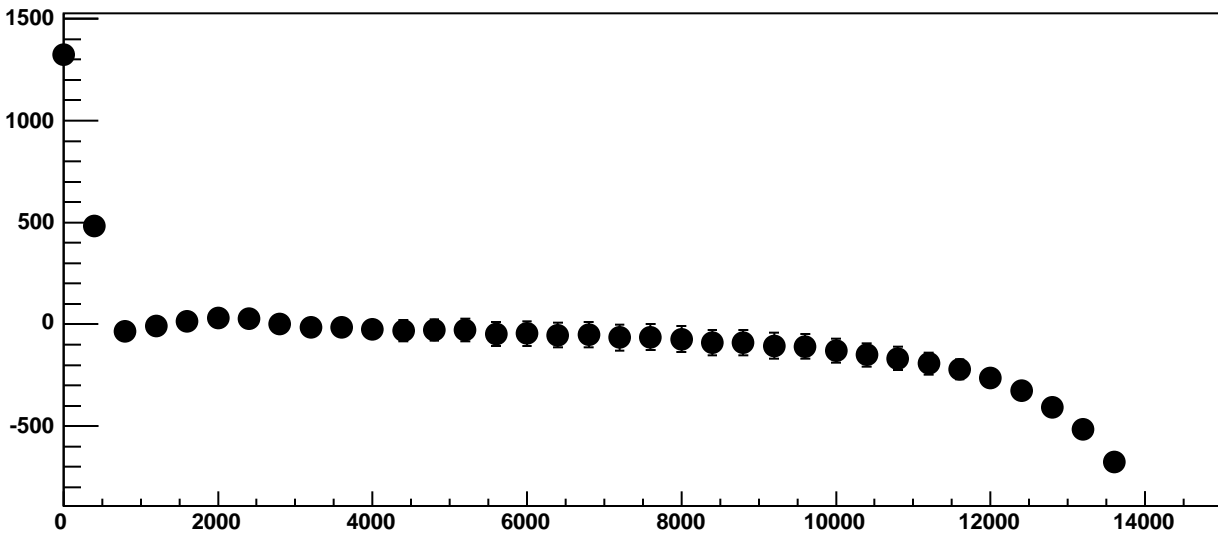
Chip 9, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 9, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

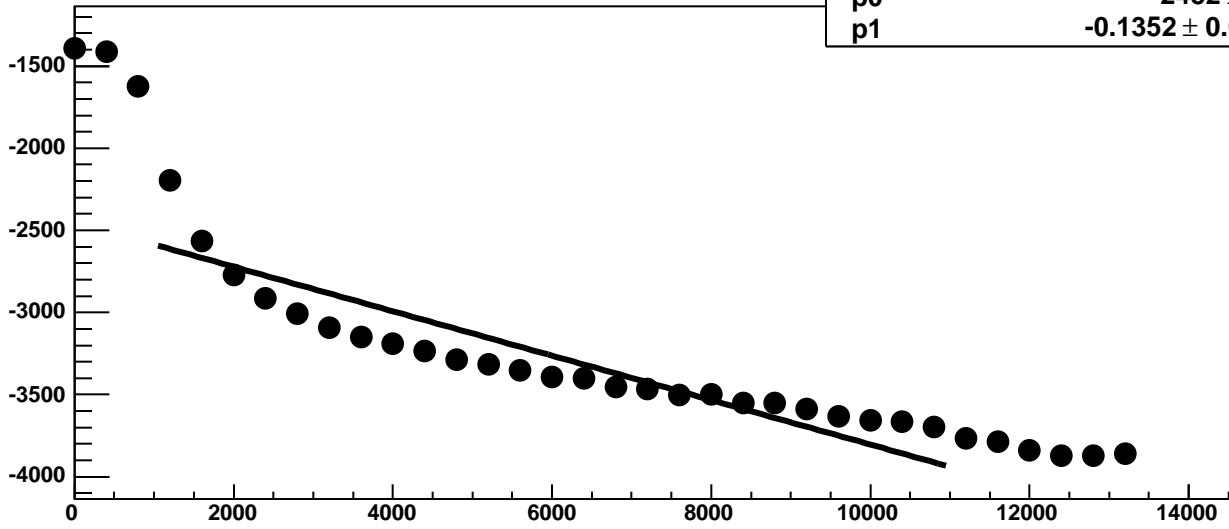


Chip 9, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

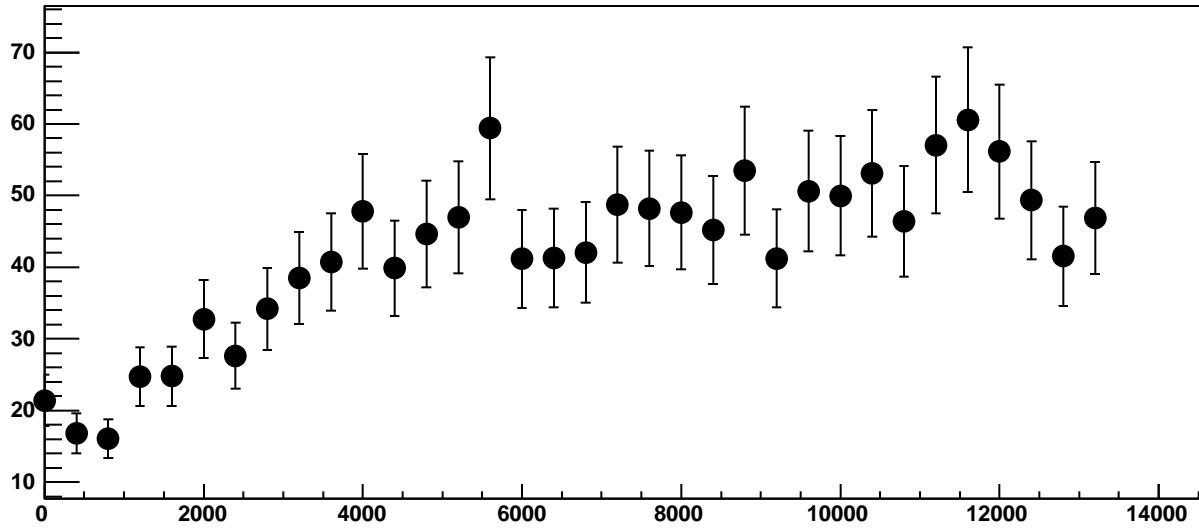


Chip 9, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

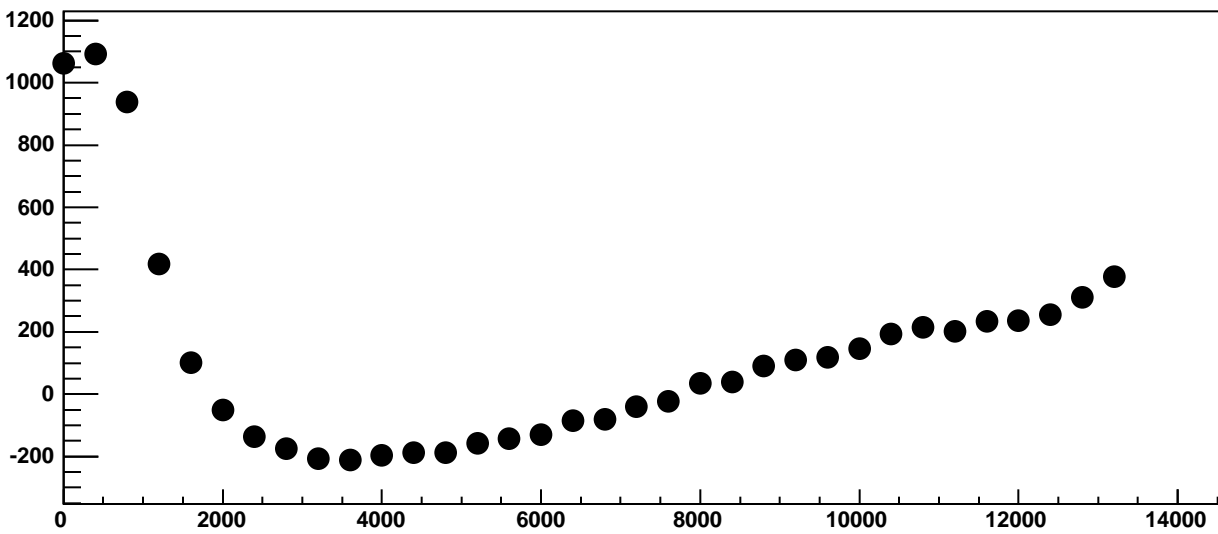
$\chi^2 / \text{ndf}$  1.073e+04 / 23  
p0 -2452 ± 3.422  
p1 -0.1352 ± 0.000602



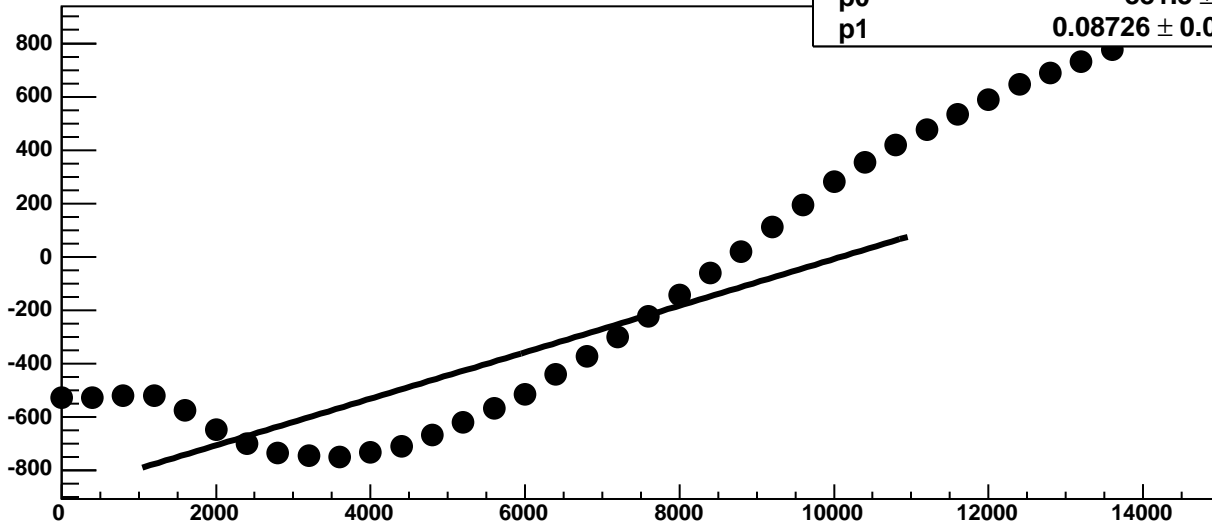
Chip 9, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

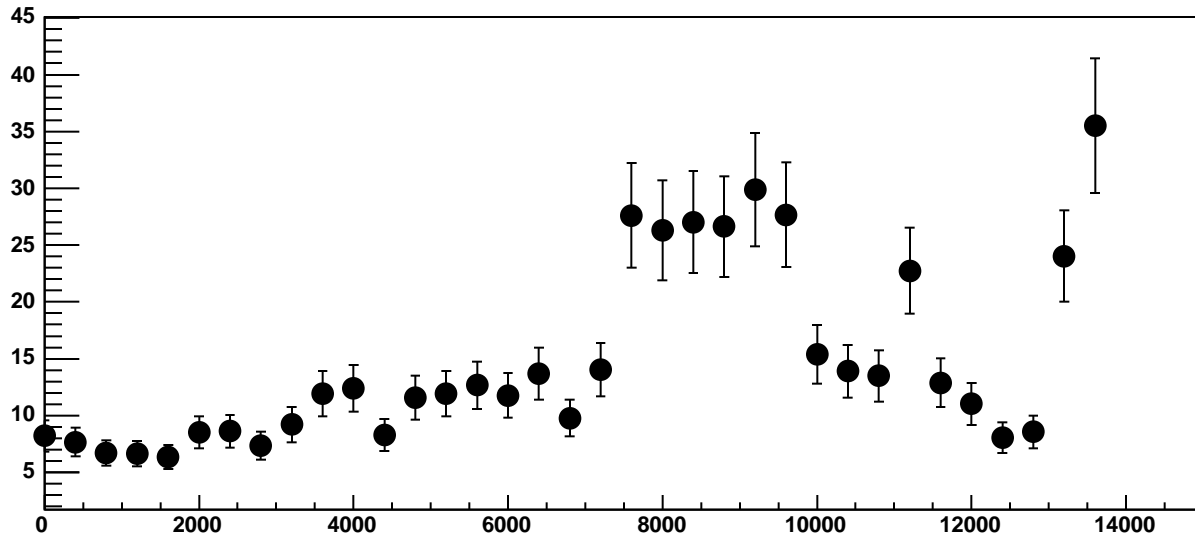


Chip 9, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

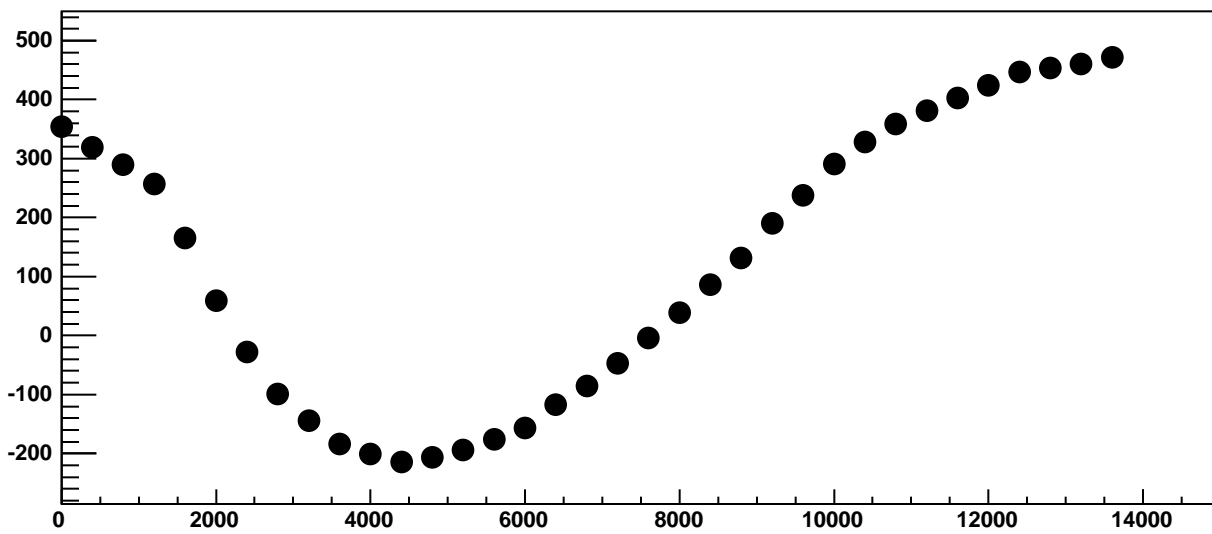


$\chi^2 / \text{ndf}$  1.277e+05 / 23  
p0 -881.3 ± 0.9363  
p1 0.08726 ± 0.0001901

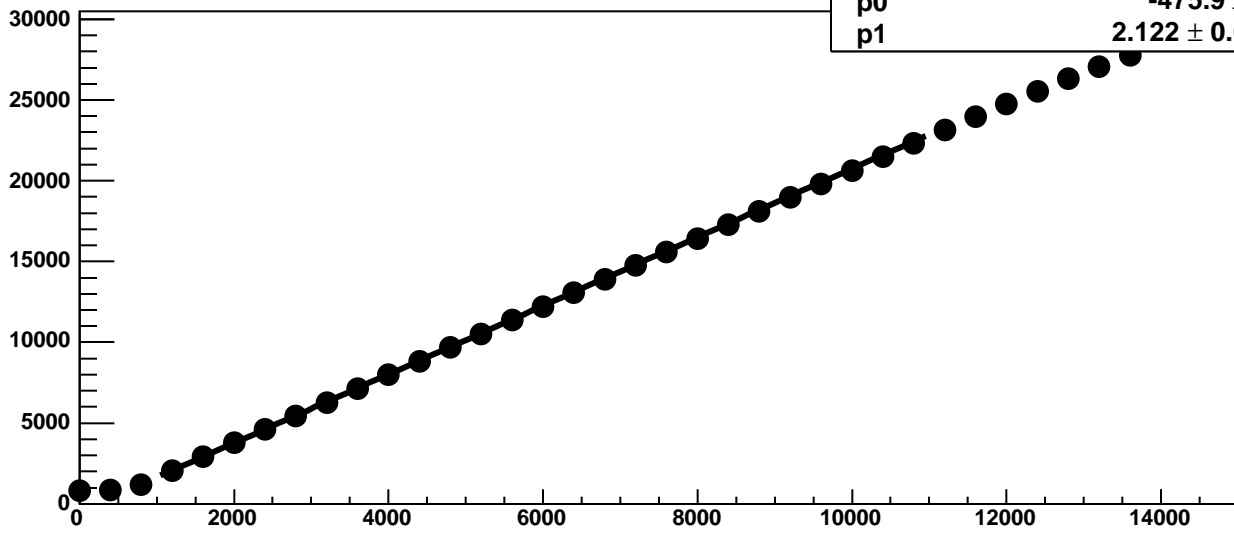
Chip 9, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



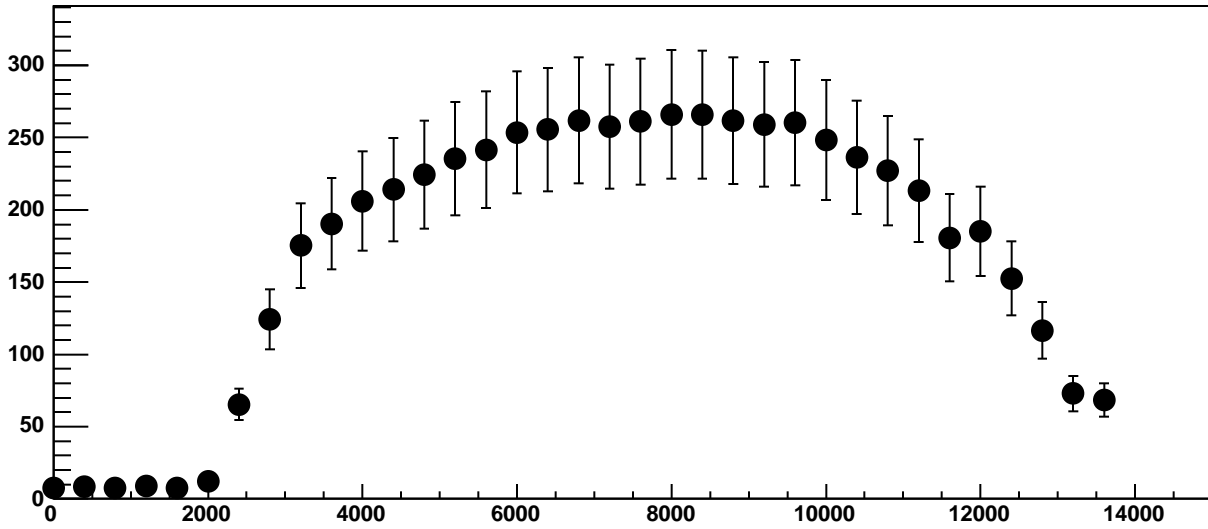
Chip 9, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



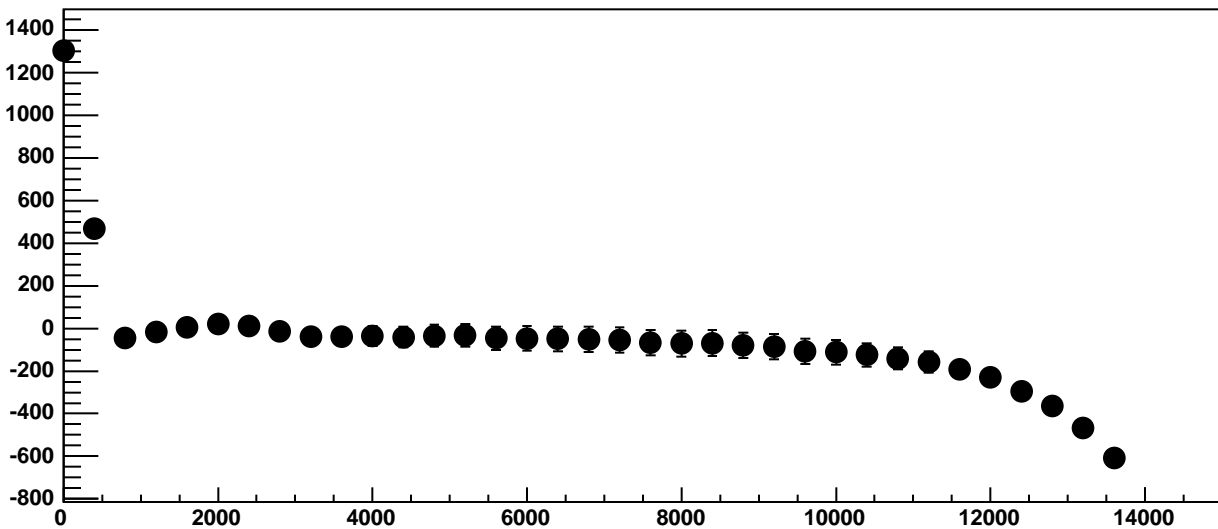
Chip 9, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 9, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

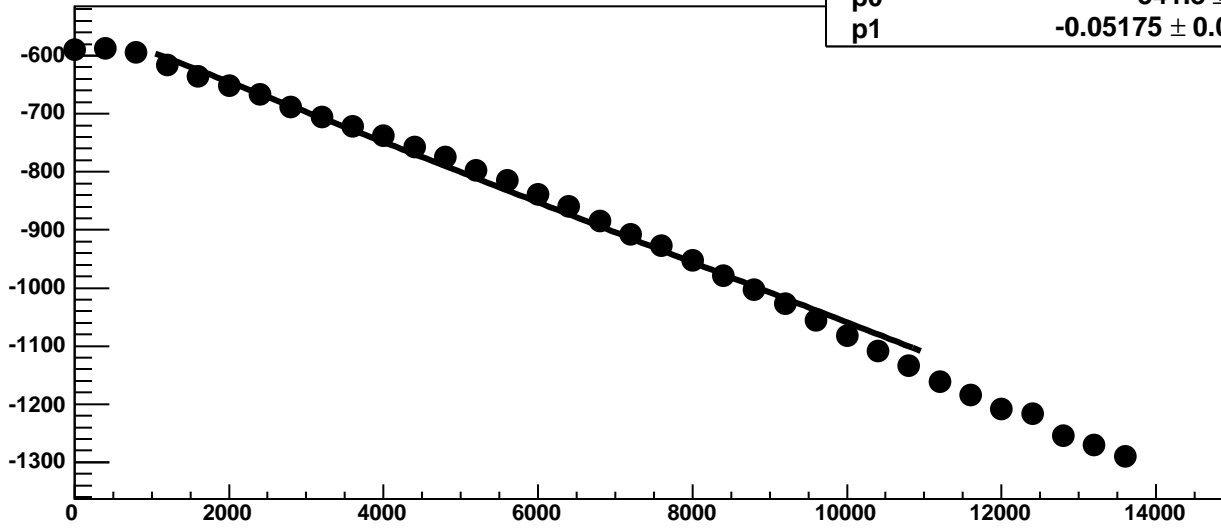
690 / 23

p0

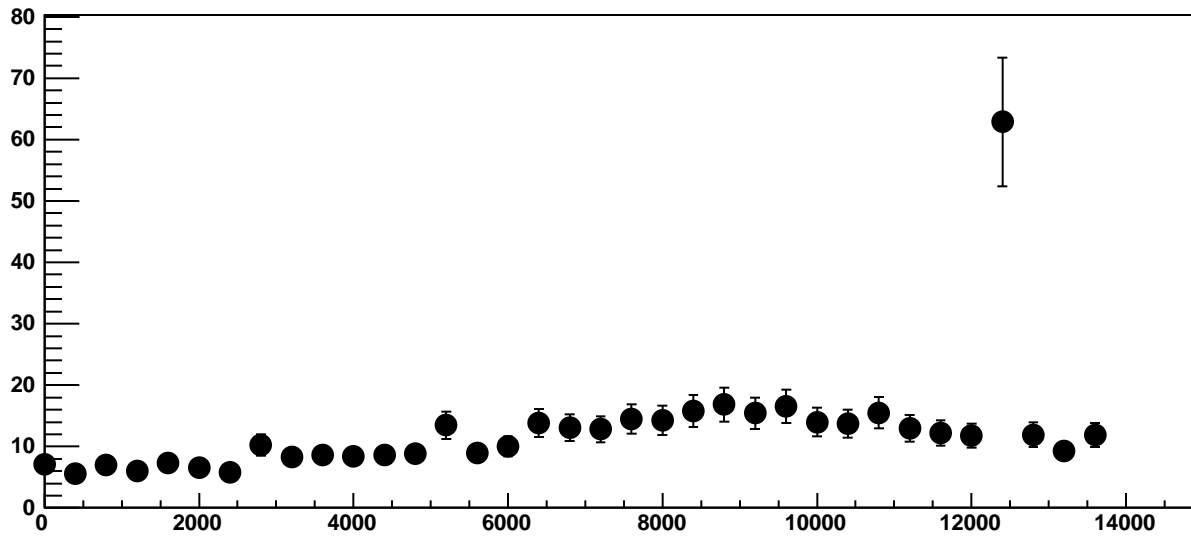
$-541.8 \pm 0.8523$

p1

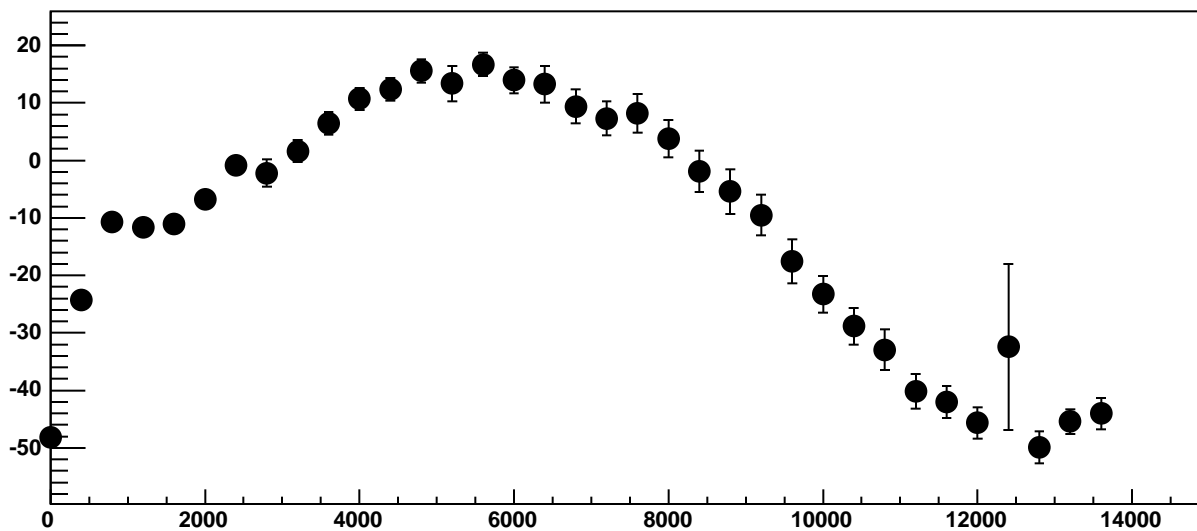
$-0.05175 \pm 0.0001691$



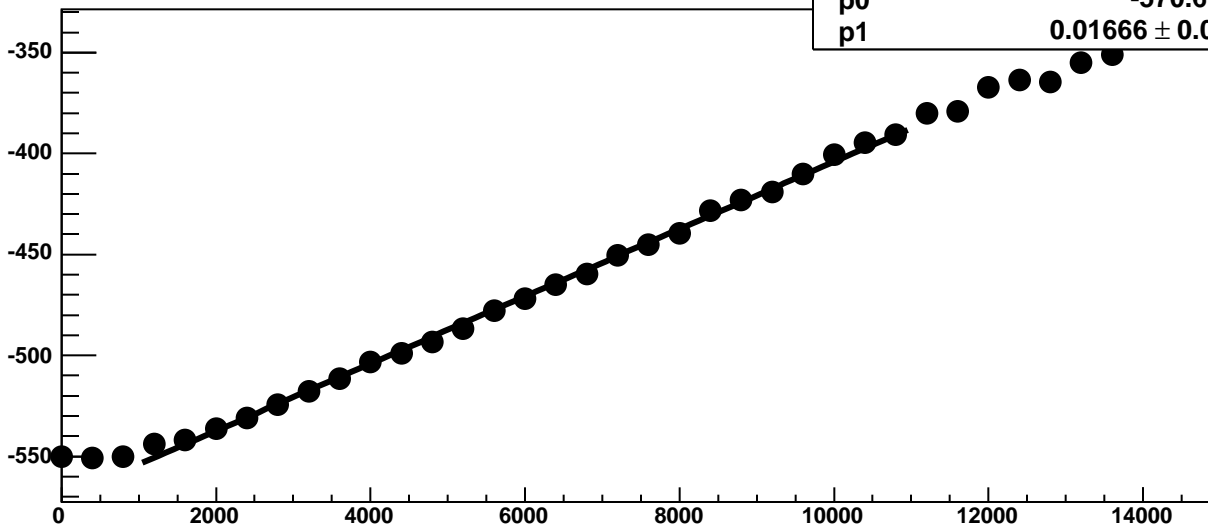
Chip 9, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

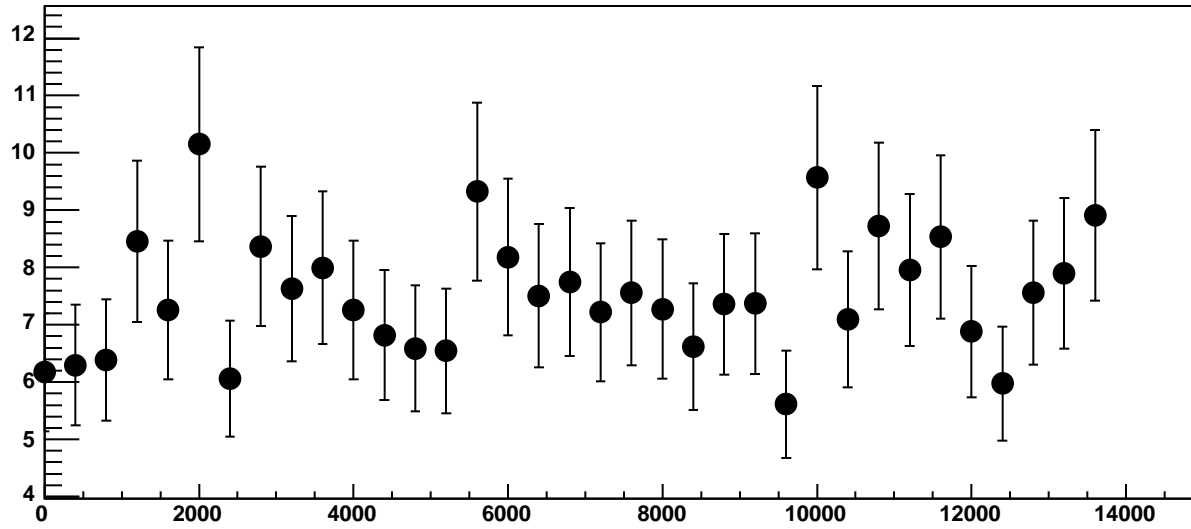


Chip 9, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

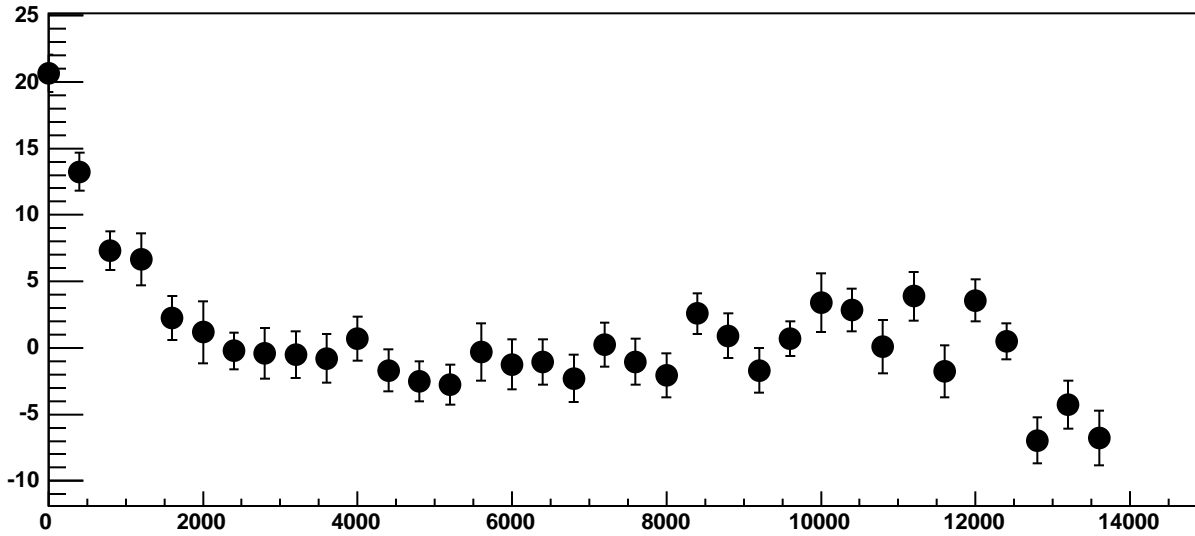


$\chi^2 / \text{ndf}$  36.05 / 23  
p0  $-570.6 \pm 0.807$   
p1  $0.01666 \pm 0.0001202$

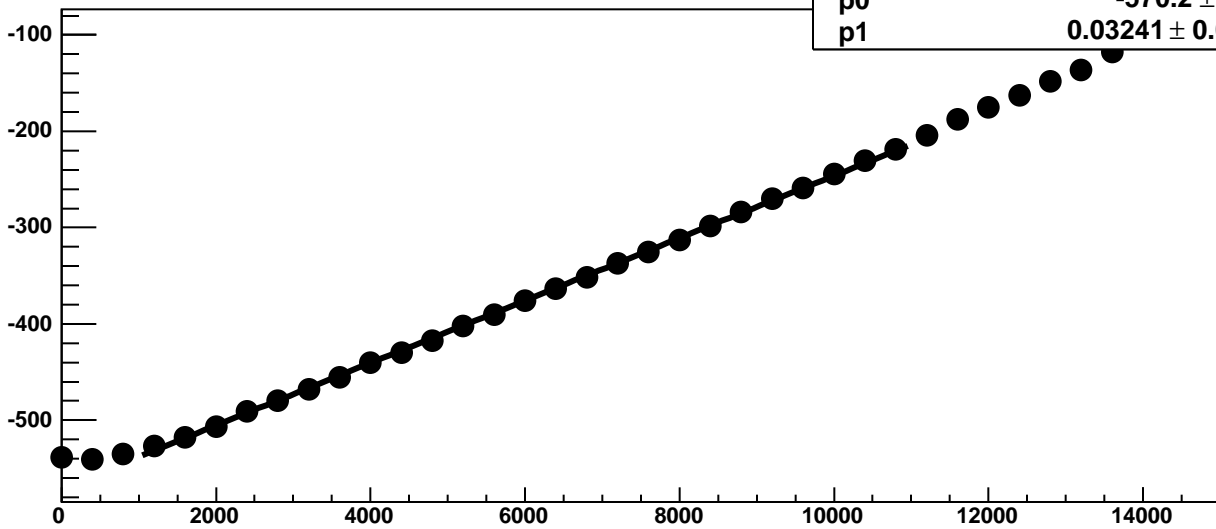
Chip 9, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

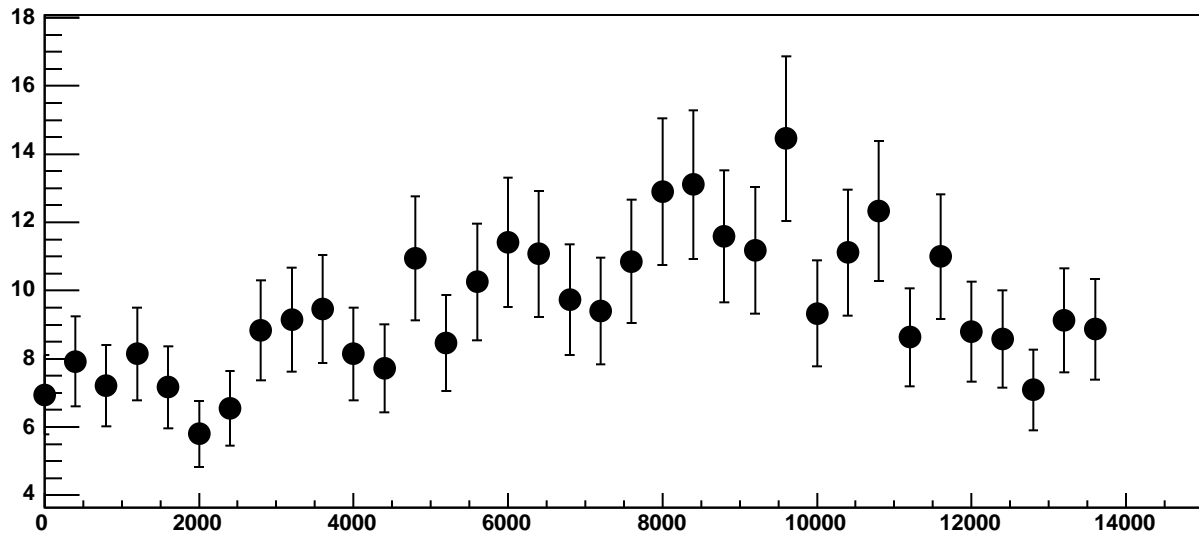


Chip 9, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

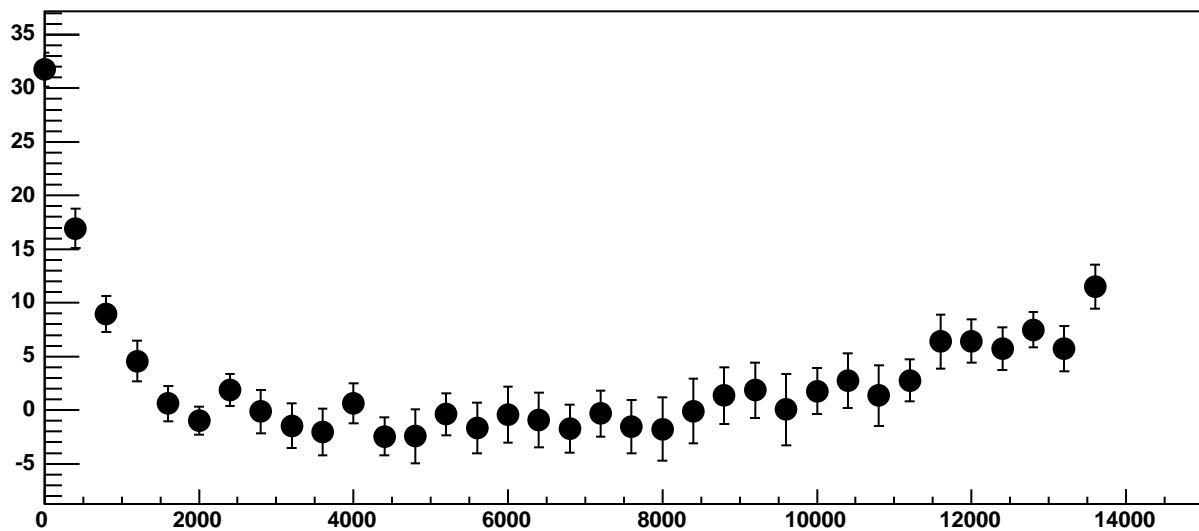


$\chi^2 / \text{ndf}$  17.55 / 23  
p0  $-570.2 \pm 0.8485$   
p1  $0.03241 \pm 0.000148$

Chip 9, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

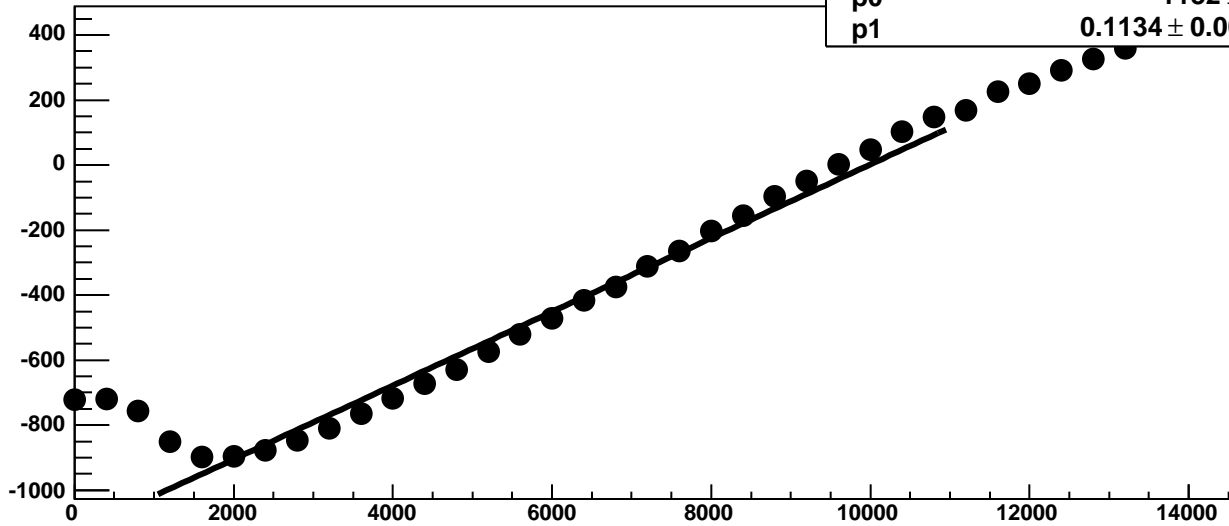


Chip 9, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

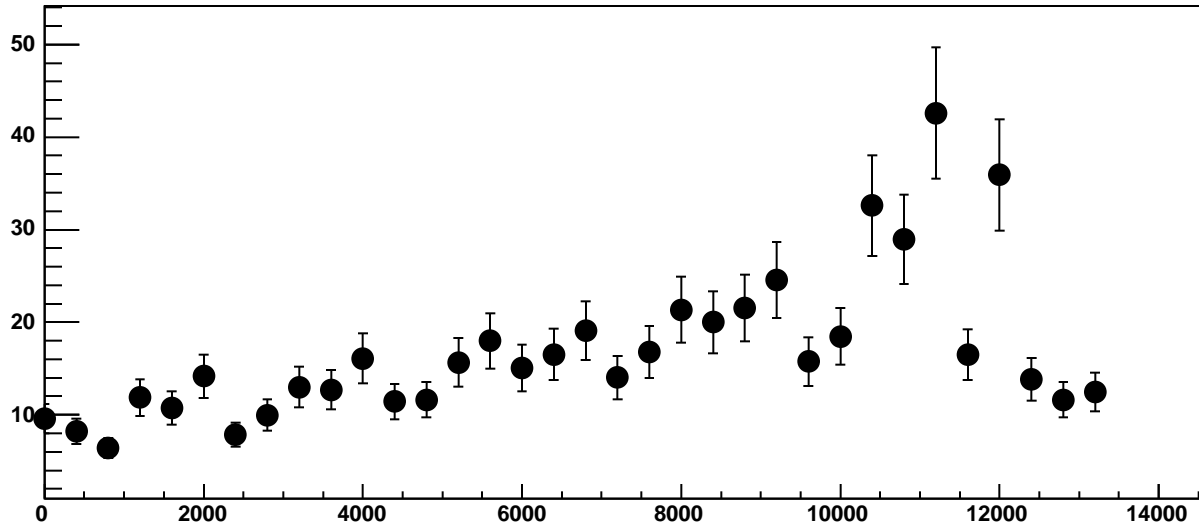




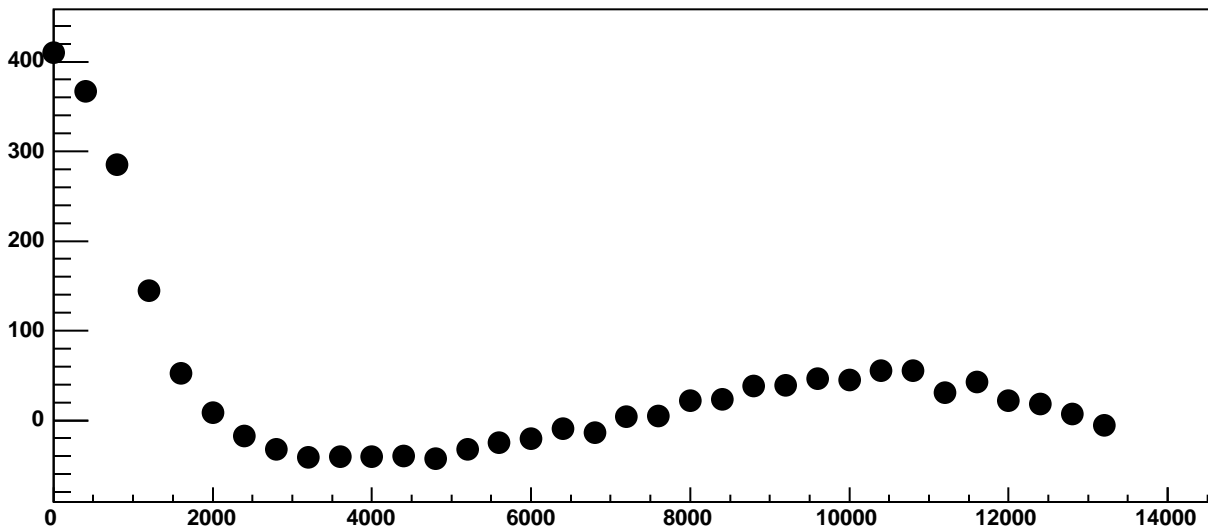
Chip 9, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



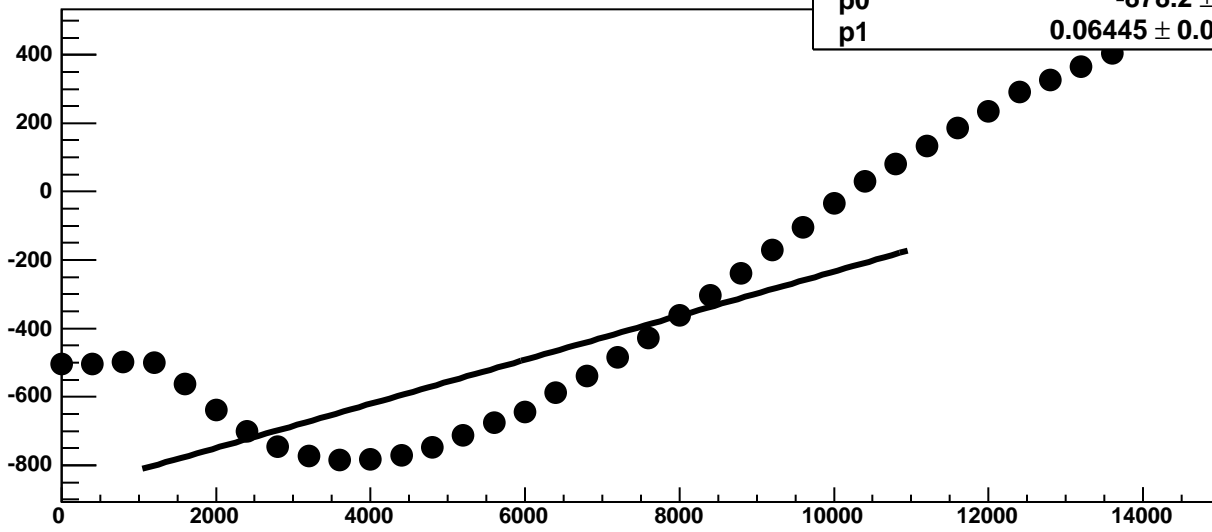
Chip 9, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



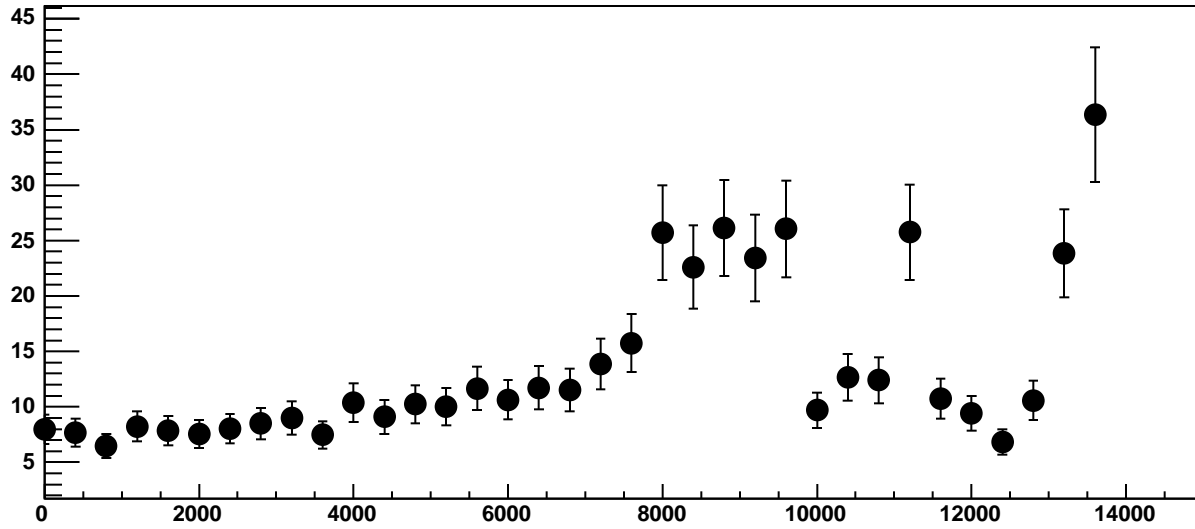
Chip 9, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



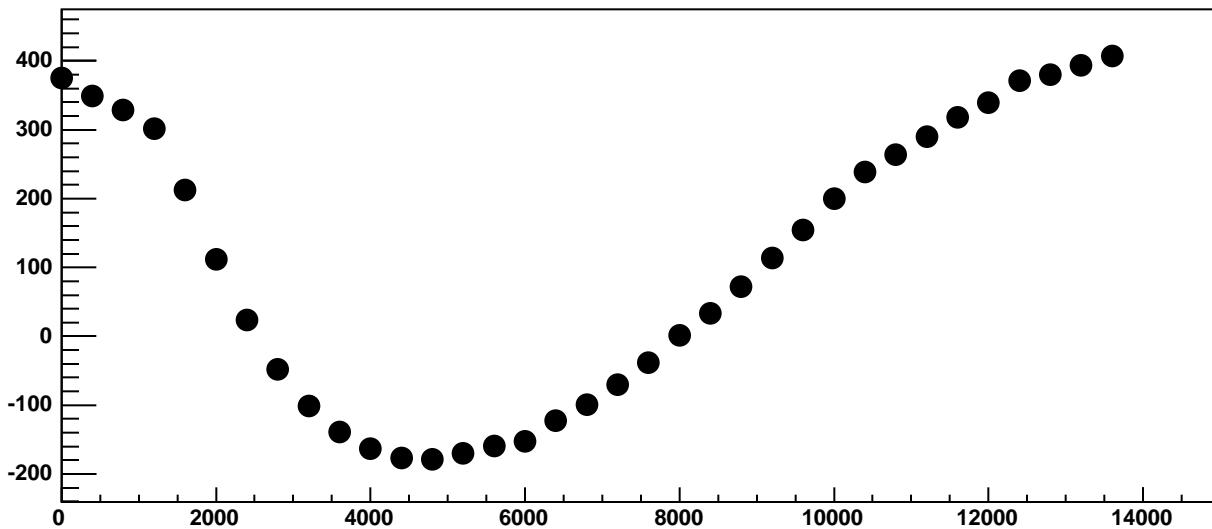
Chip 9, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC



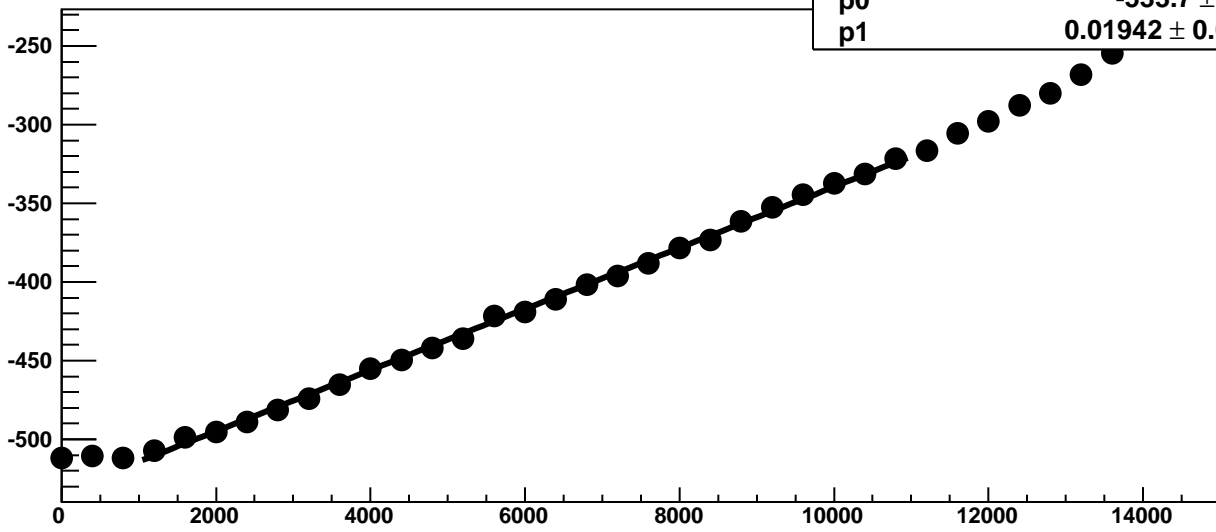
Chip 9, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

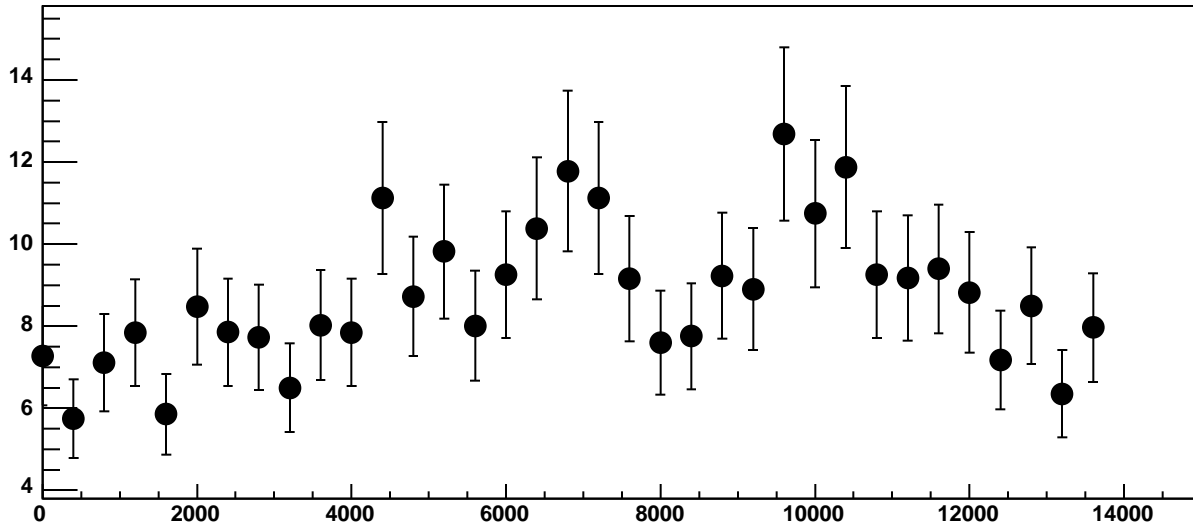


Chip 9, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

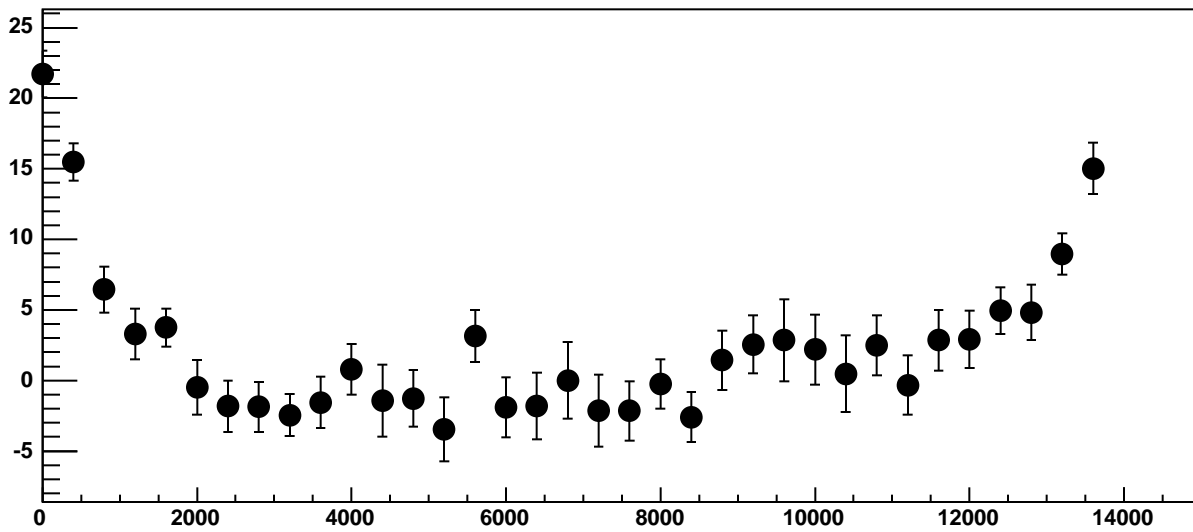


$\chi^2 / \text{ndf}$  33.51 / 23  
p0  $-533.7 \pm 0.8268$   
p1  $0.01942 \pm 0.000136$

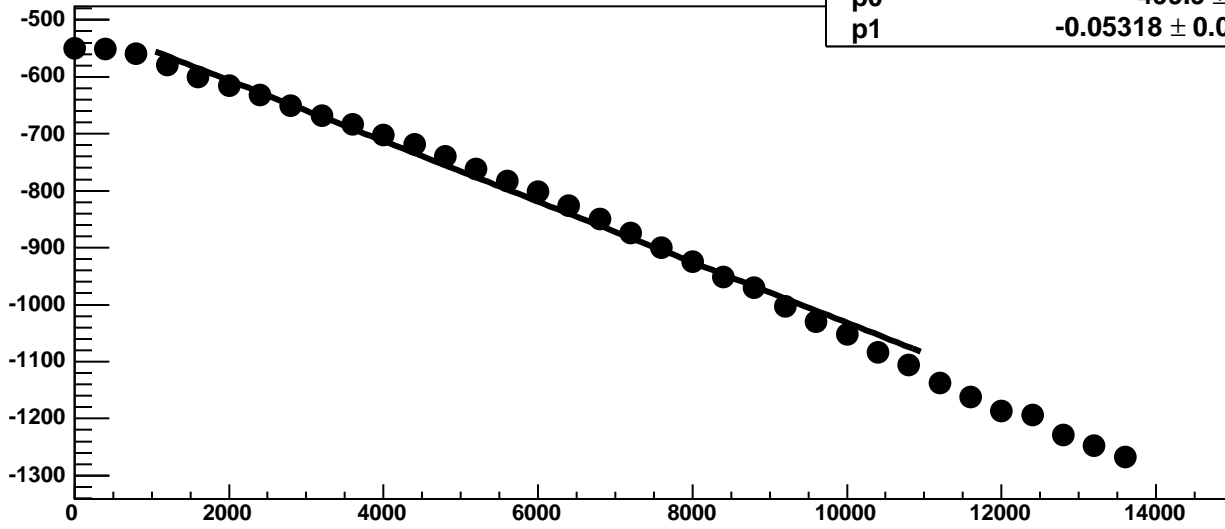
Chip 9, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

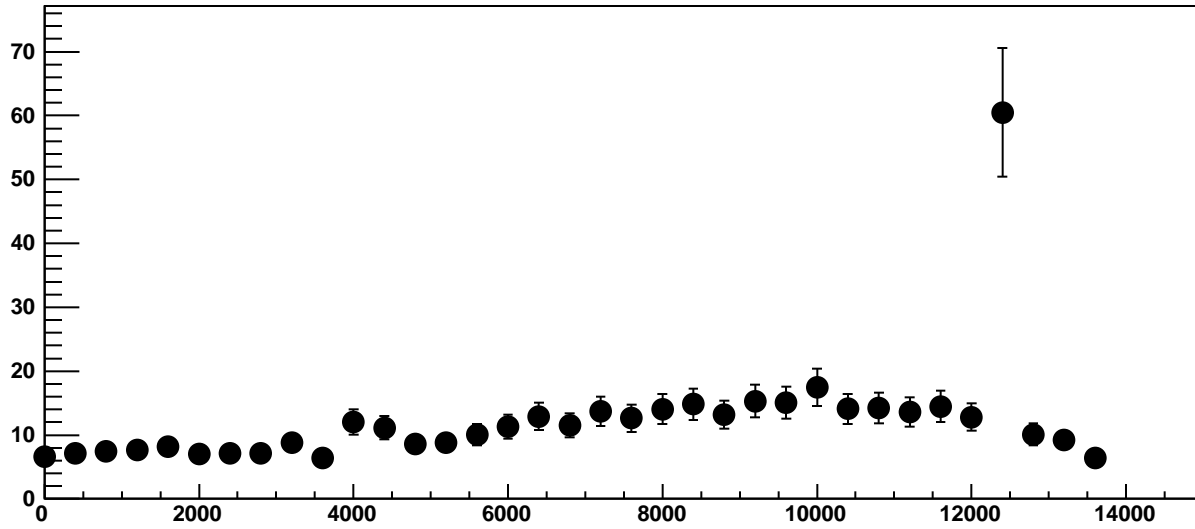


Chip 9, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

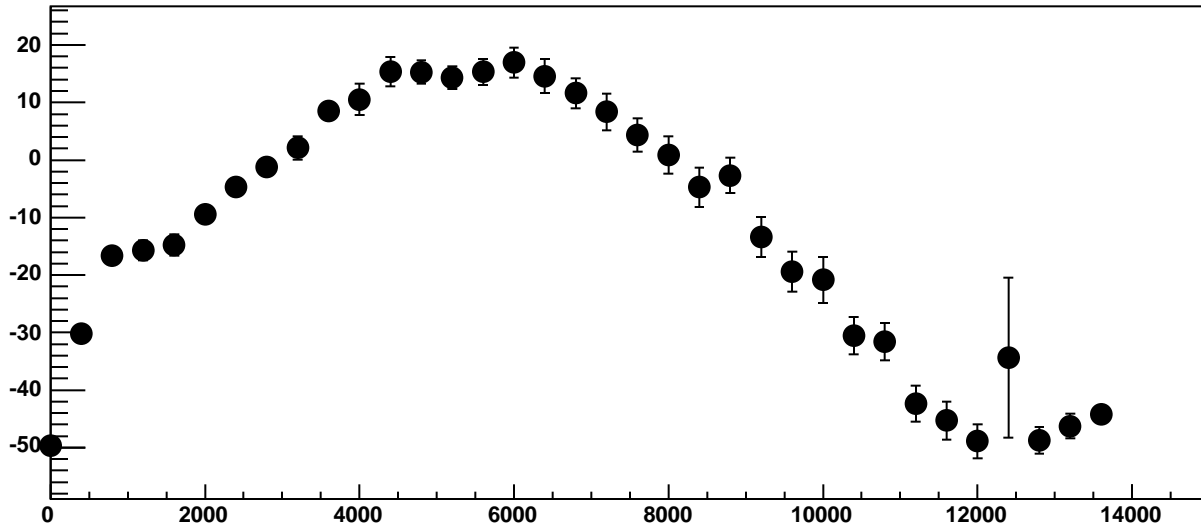


$\chi^2 / \text{ndf}$	778.5 / 23
p0	-499.9 ± 0.9152
p1	-0.05318 ± 0.0001741

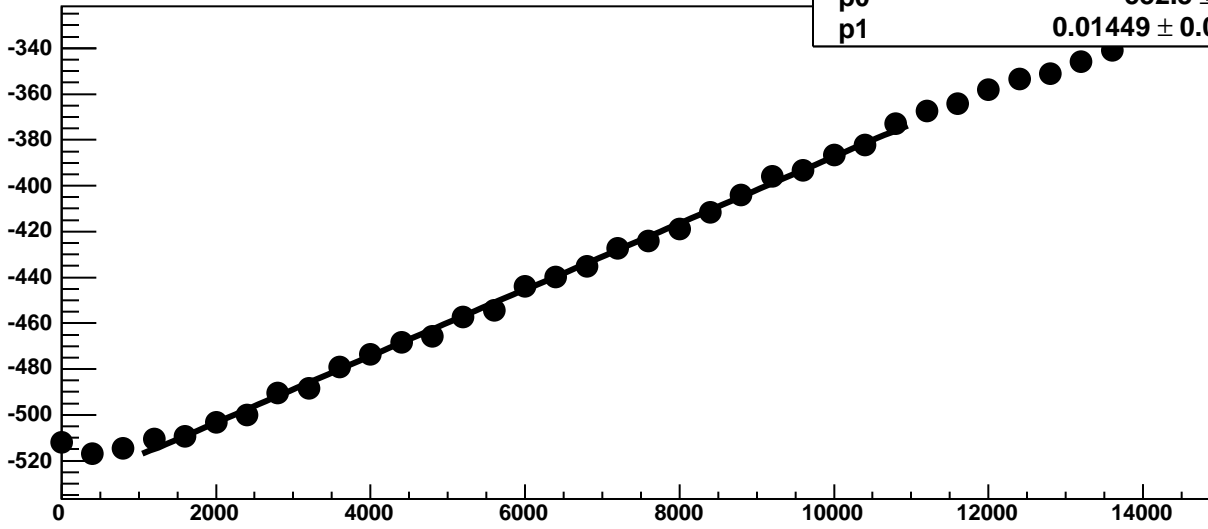
Chip 9, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



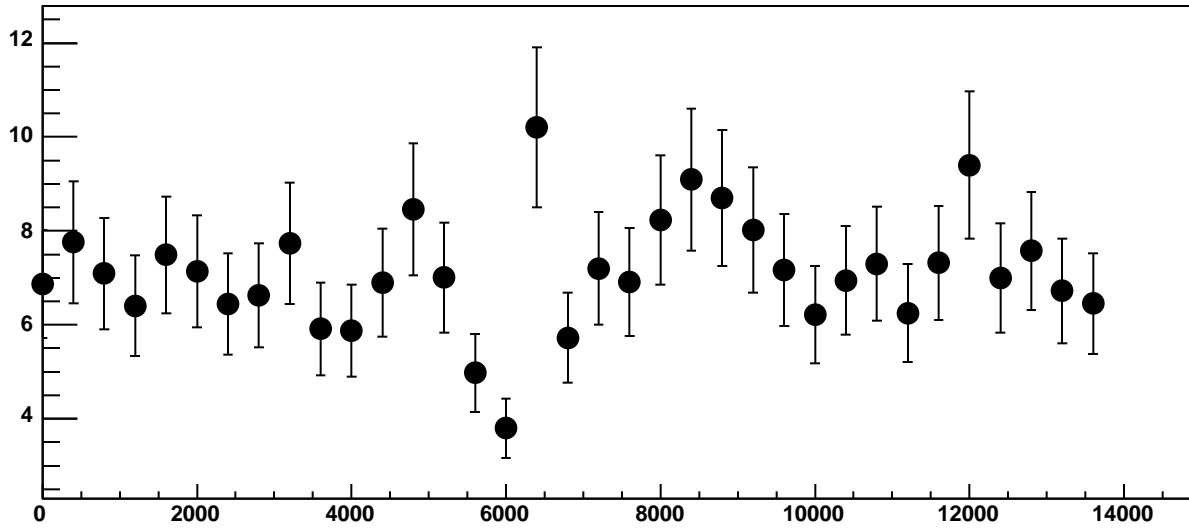
Chip 9, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



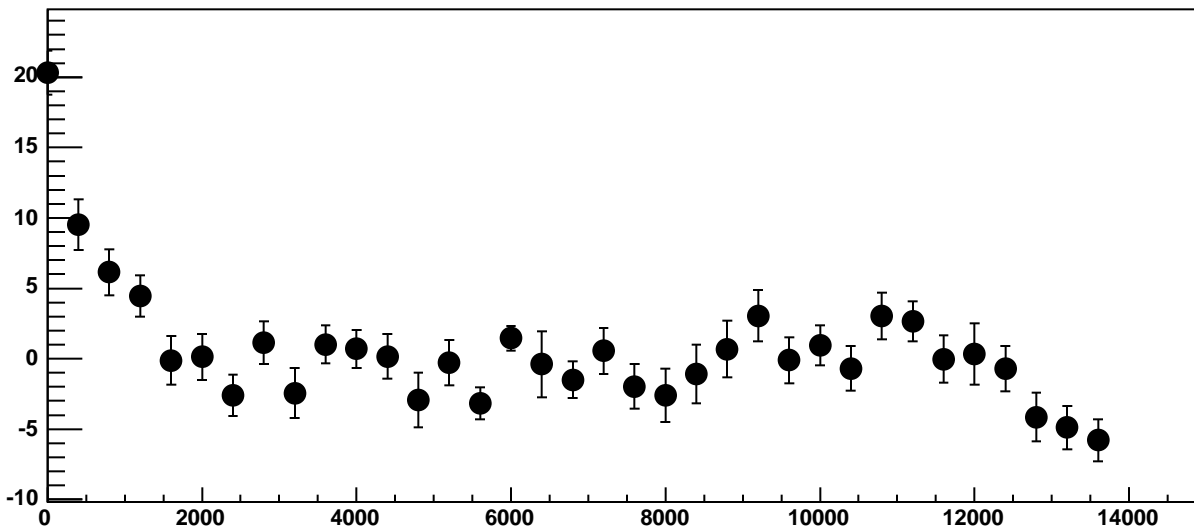
Chip 9, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



Chip 9, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

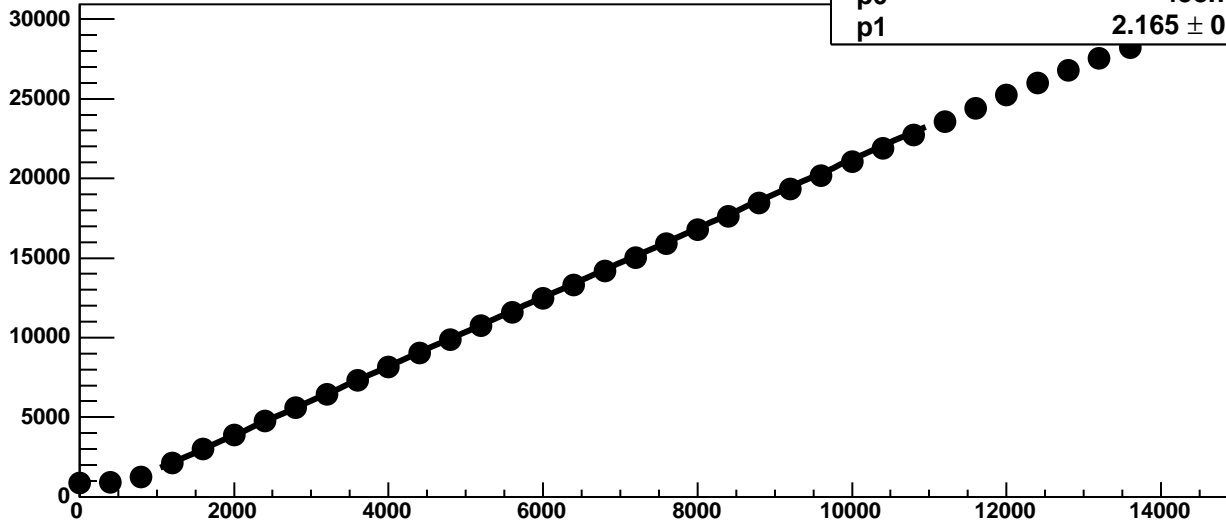


Chip 9, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

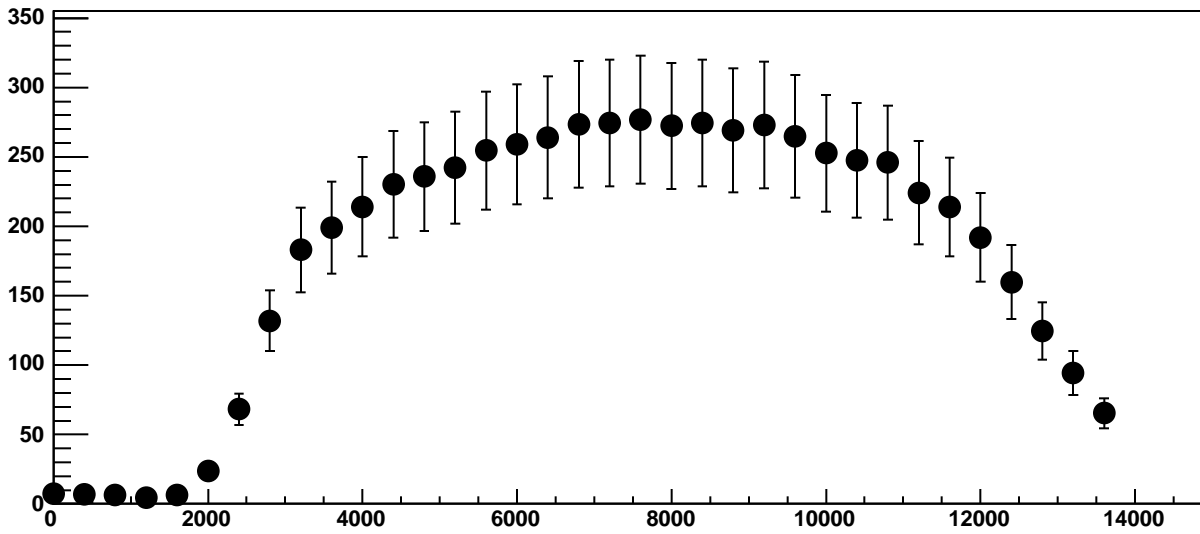


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

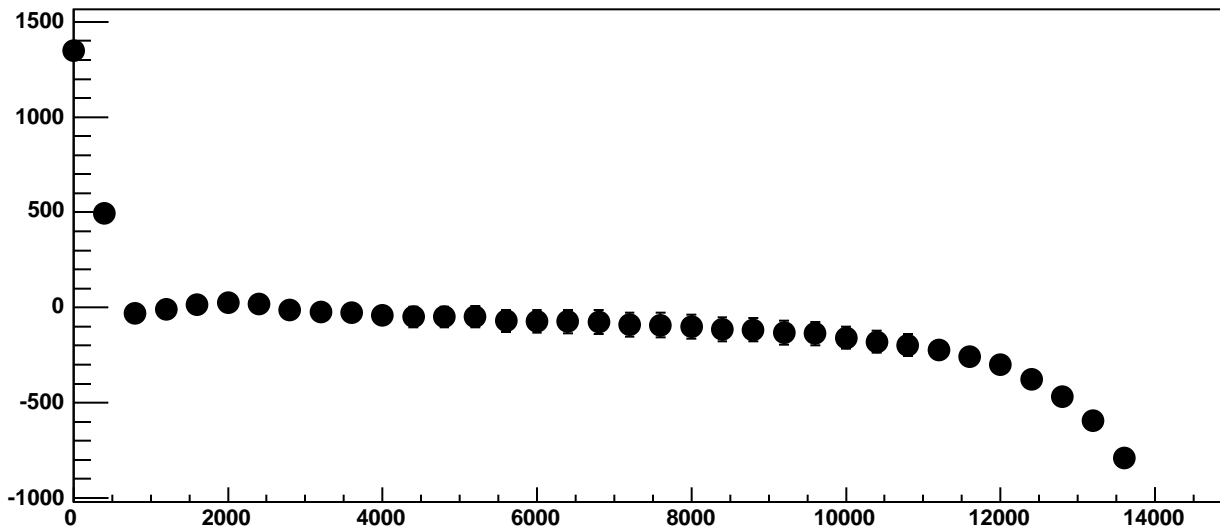
$\chi^2 / \text{ndf}$  272.8 / 23  
p0  $-458.7 \pm 2.741$   
p1  $2.165 \pm 0.001889$



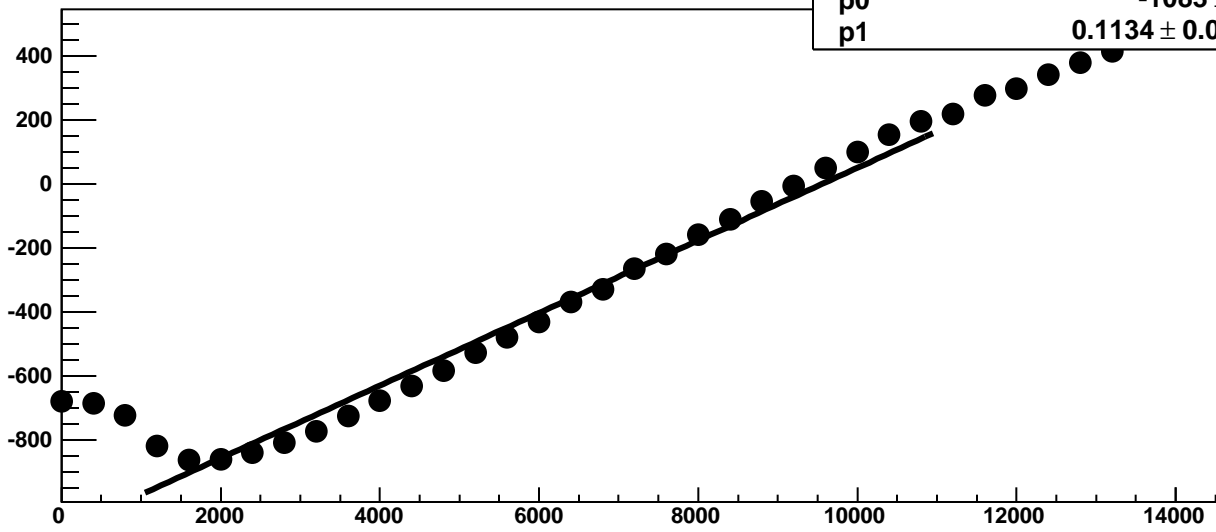
Chip 9, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



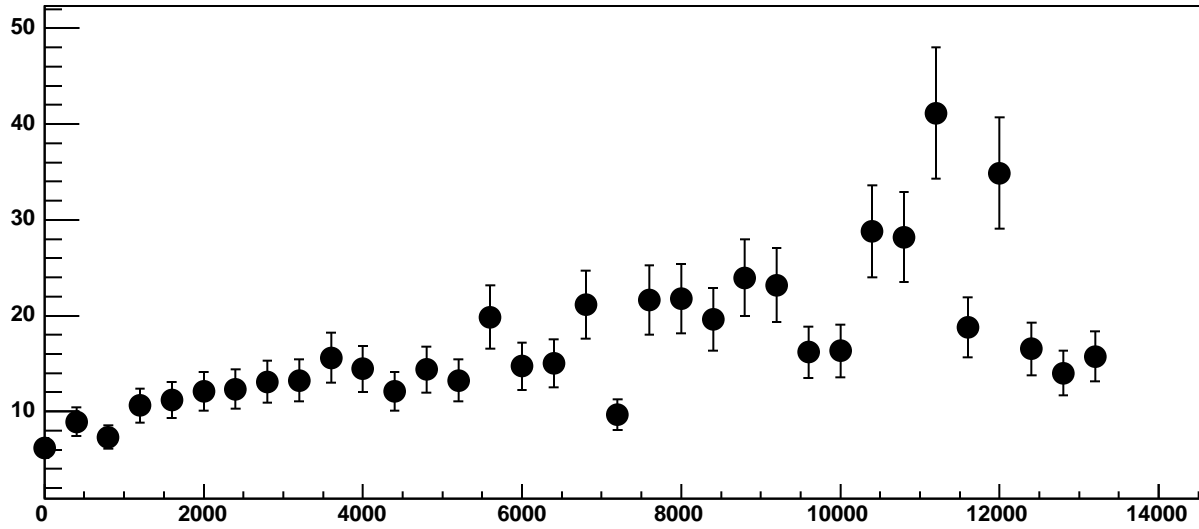
Chip 9, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



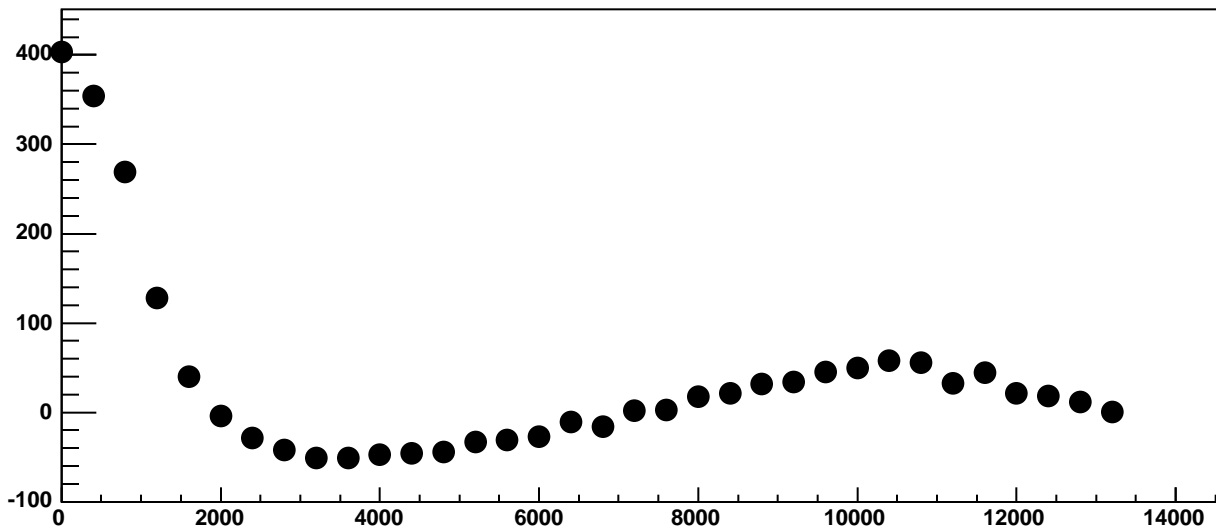
Chip 9, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



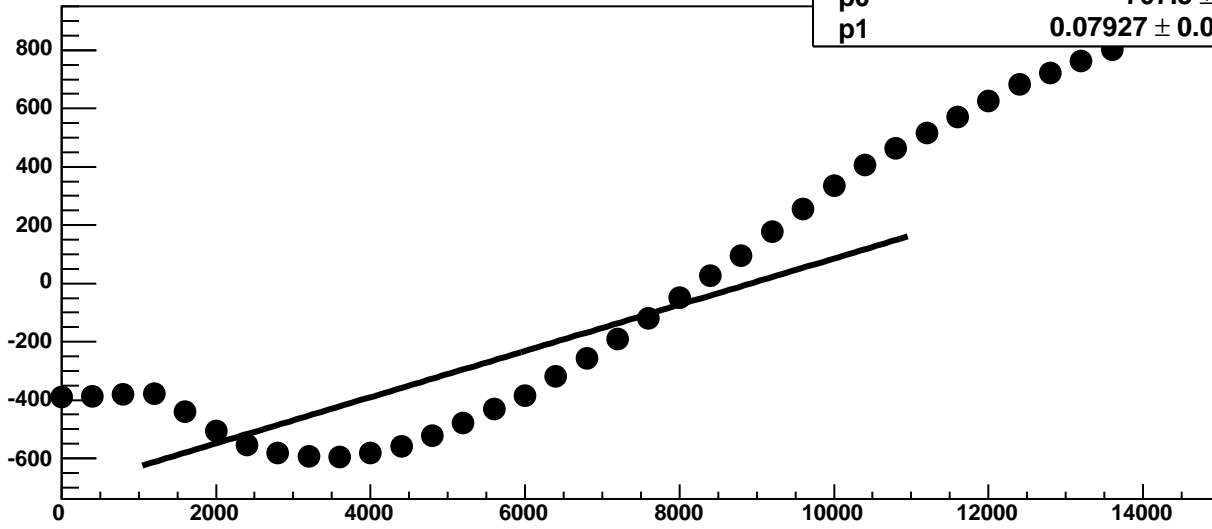
Chip 9, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



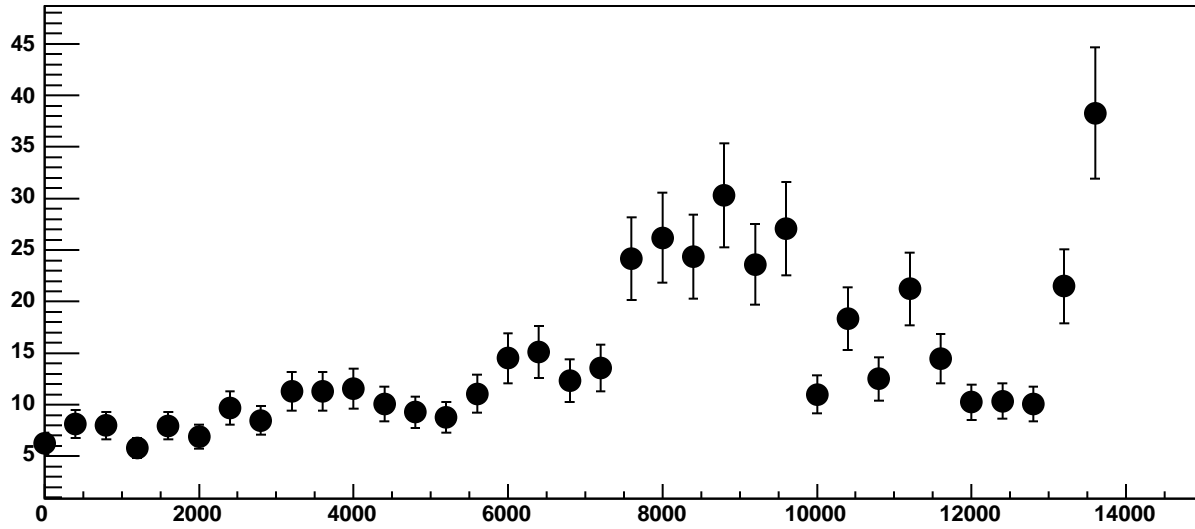
Chip 9, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



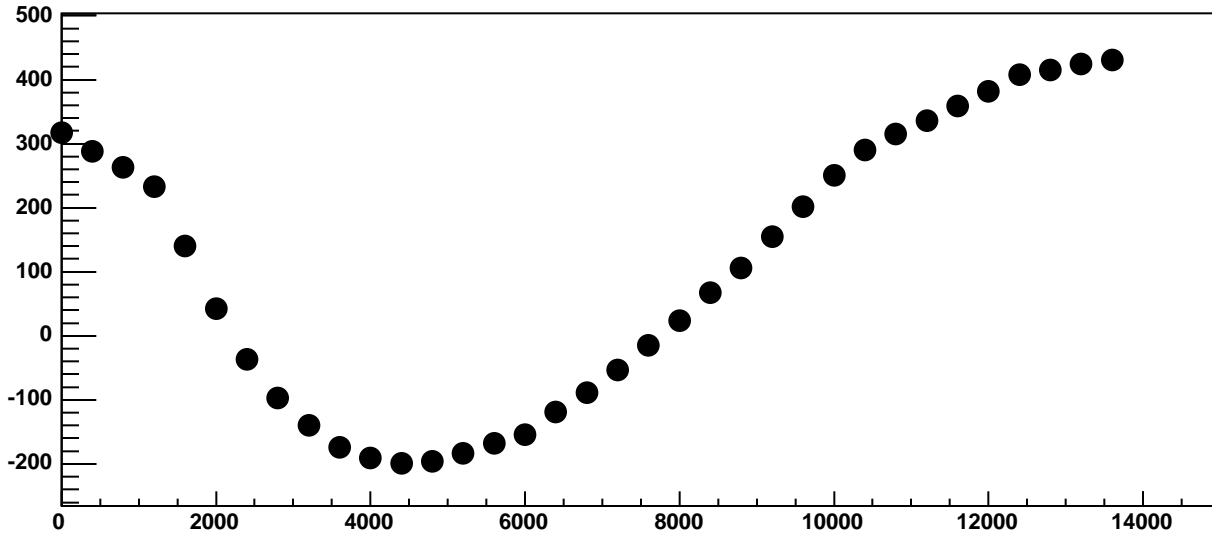
Chip 9, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



Chip 9, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

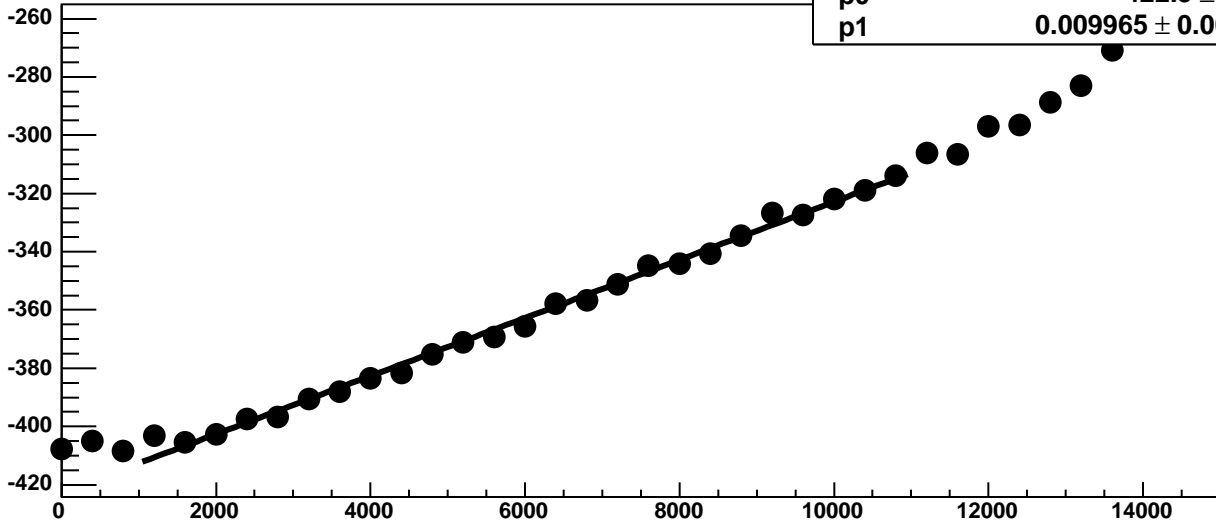


Chip 9, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



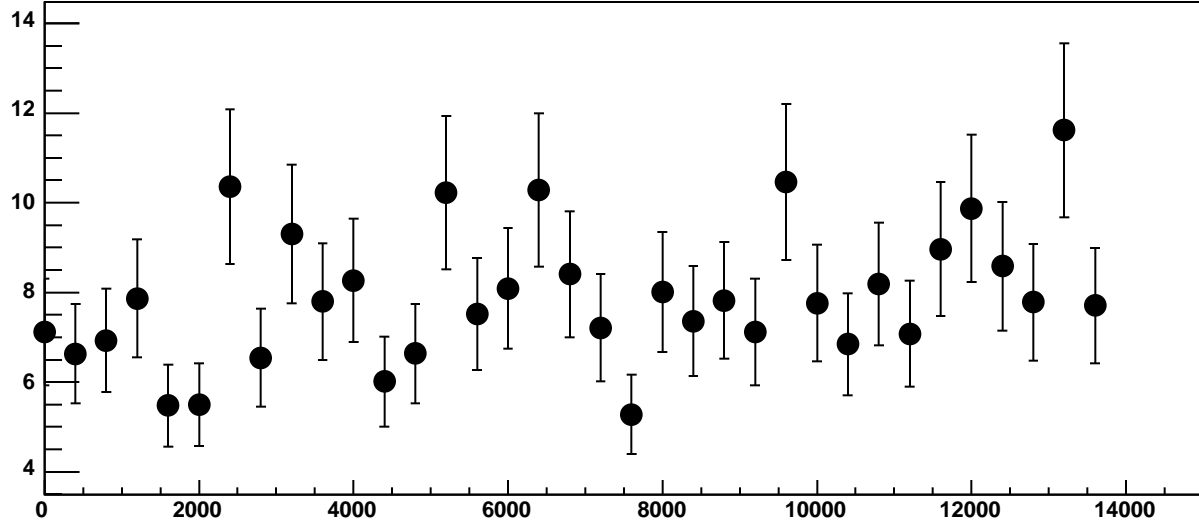


Chip 9, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

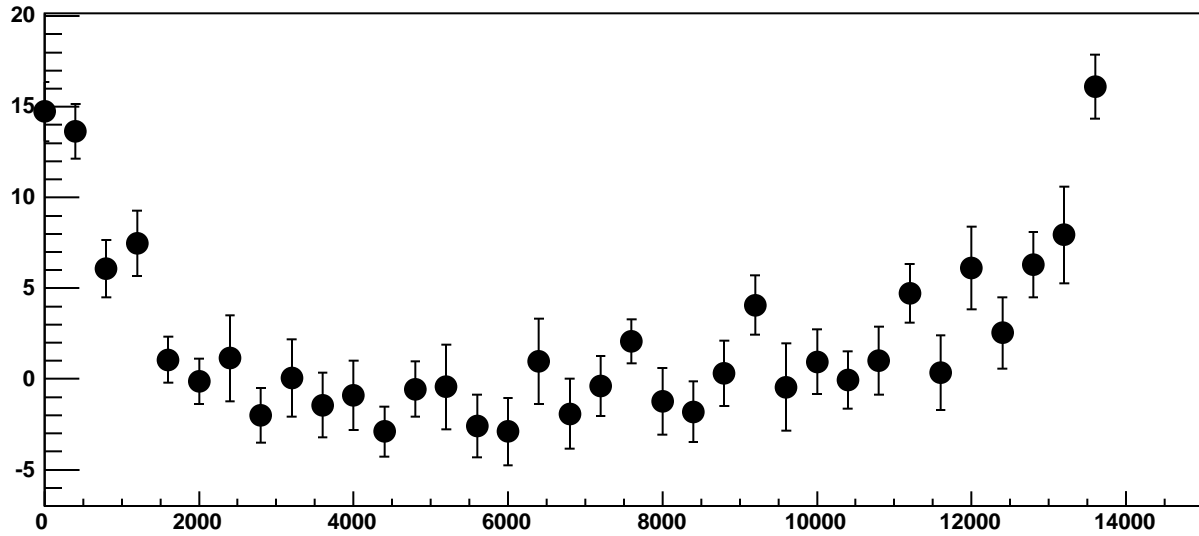


$\chi^2 / \text{ndf}$  42.51 / 23  
p0  $-422.5 \pm 0.7422$   
p1  $0.009965 \pm 0.0001146$

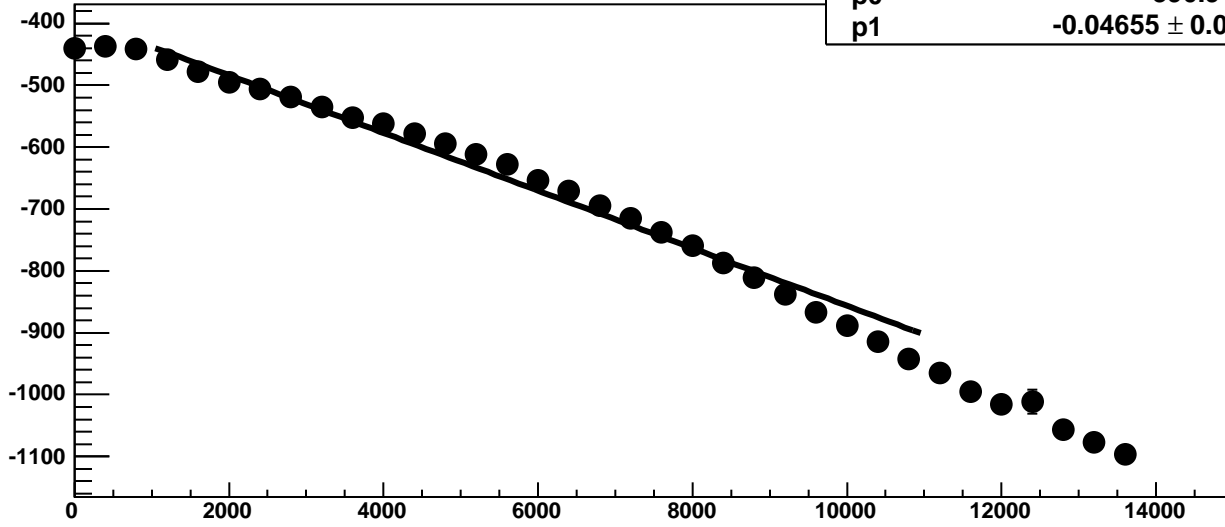
Chip 9, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

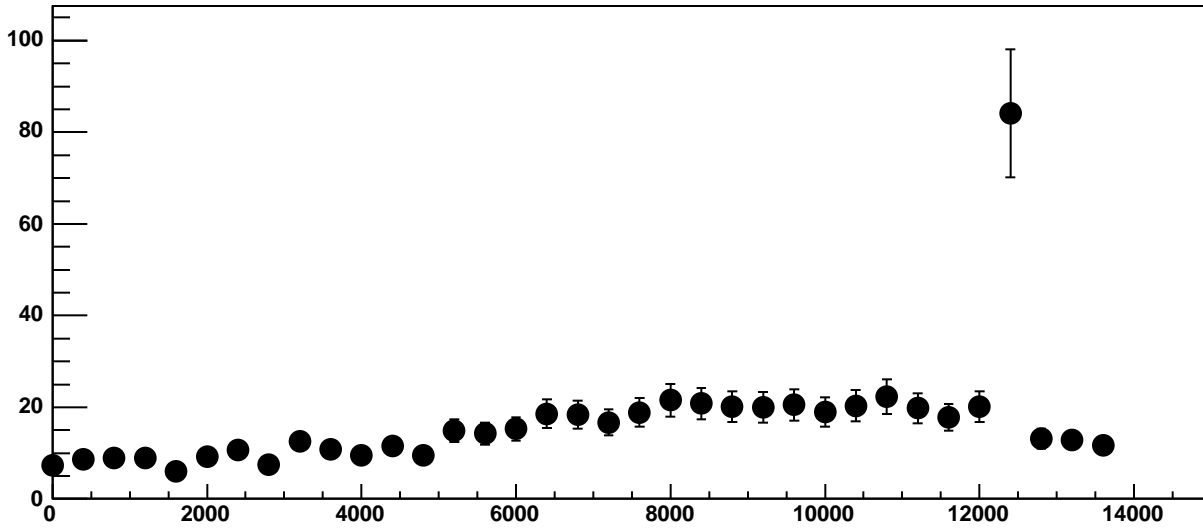


Chip 9, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

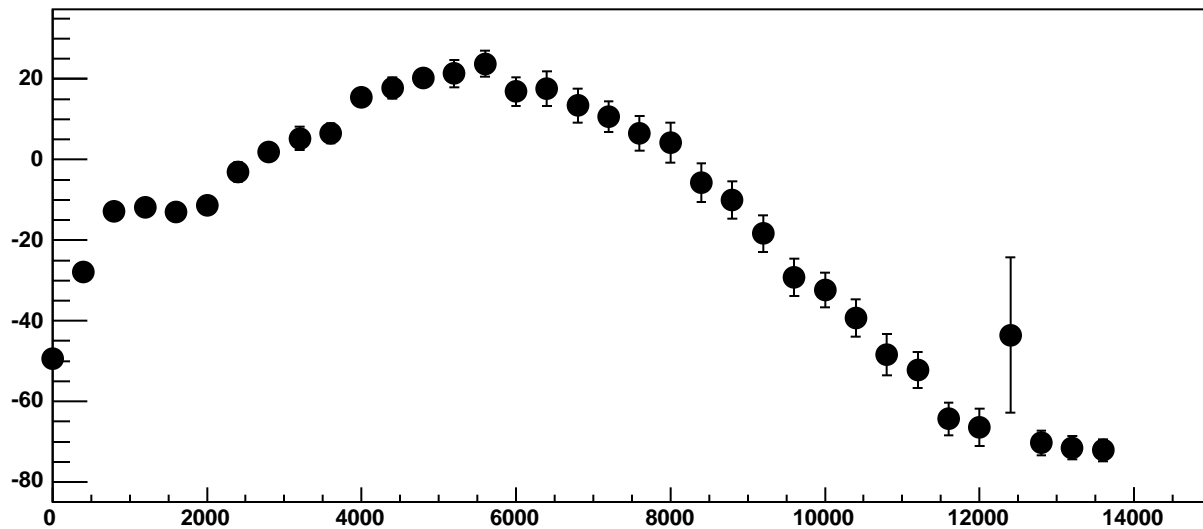


$\chi^2 / \text{ndf}$  772.3 / 23  
p0  $-390.9 \pm 1.053$   
p1  $-0.04655 \pm 0.0002207$

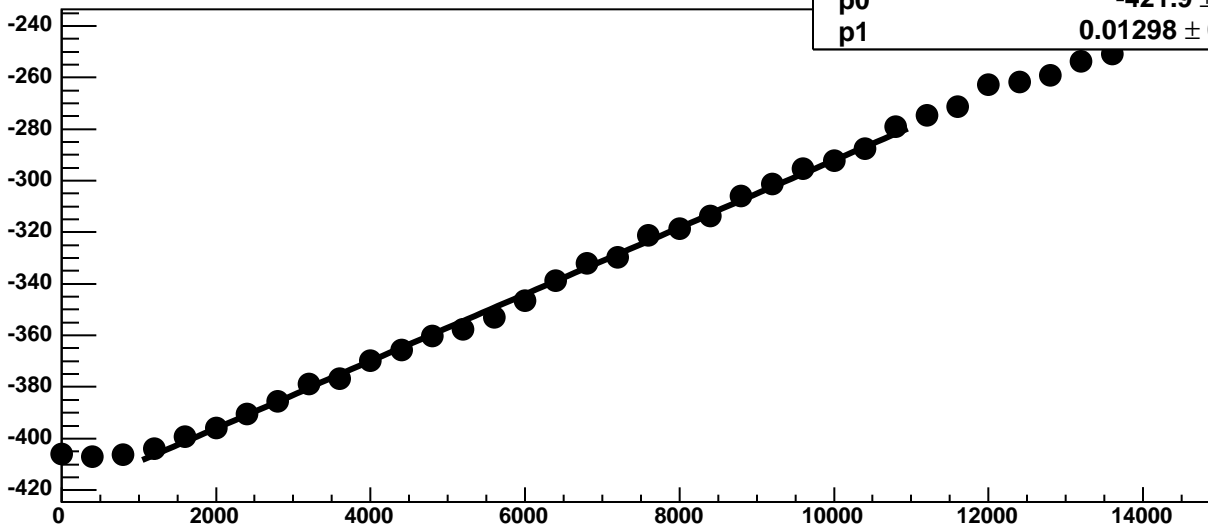
Chip 9, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC

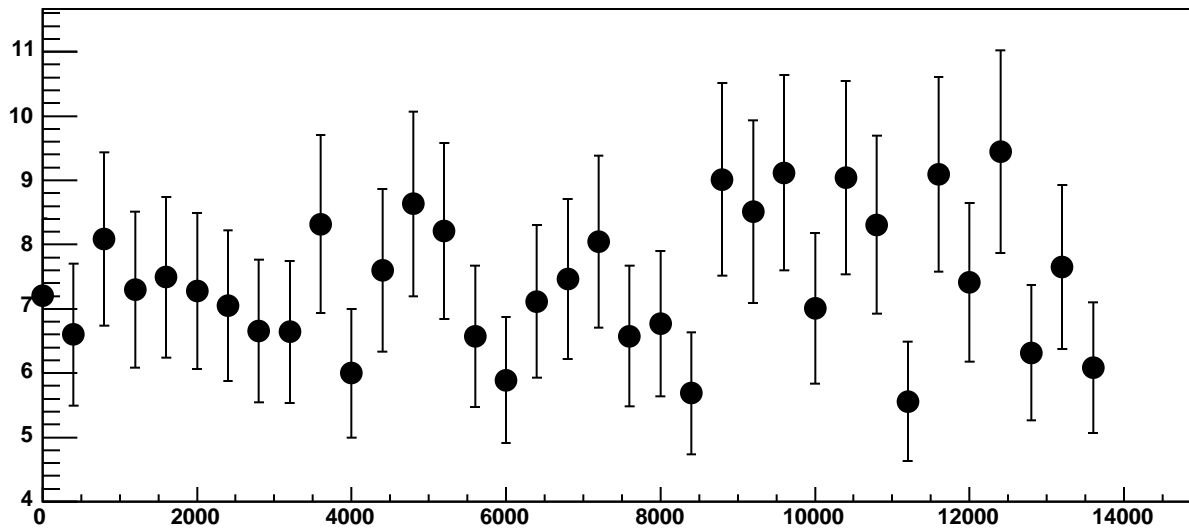


Chip 9, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

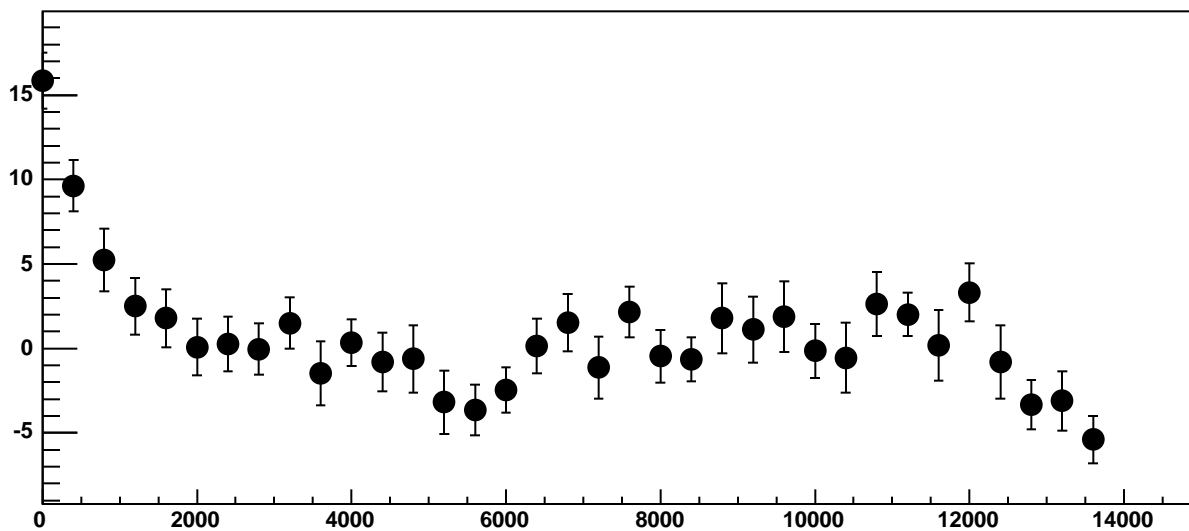


$\chi^2 / \text{ndf}$  24.7 / 23  
p0  $-421.9 \pm 0.7737$   
p1  $0.01298 \pm 0.00012$

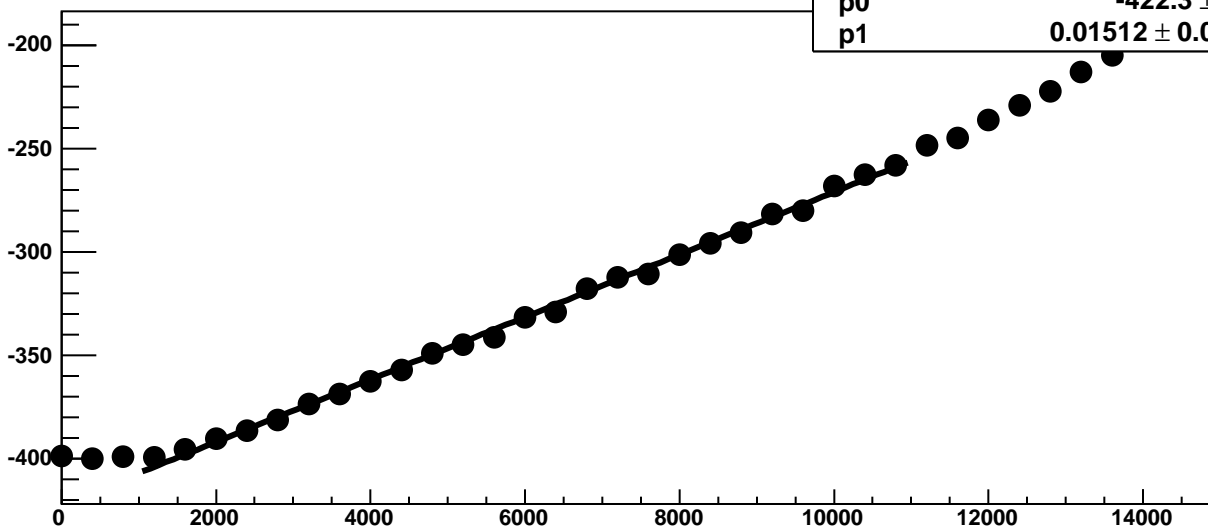
Chip 9, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

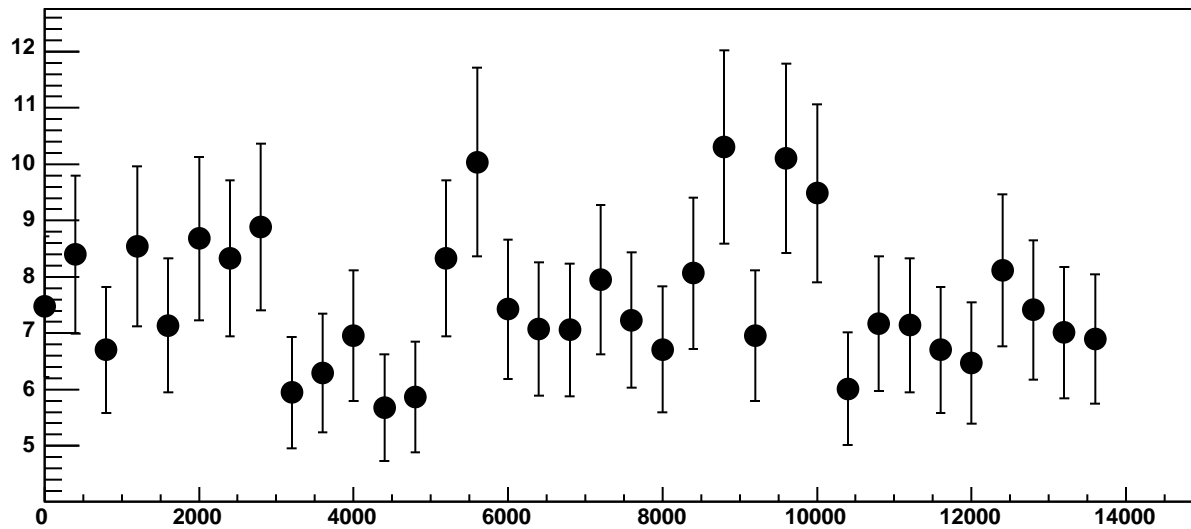


Chip 9, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

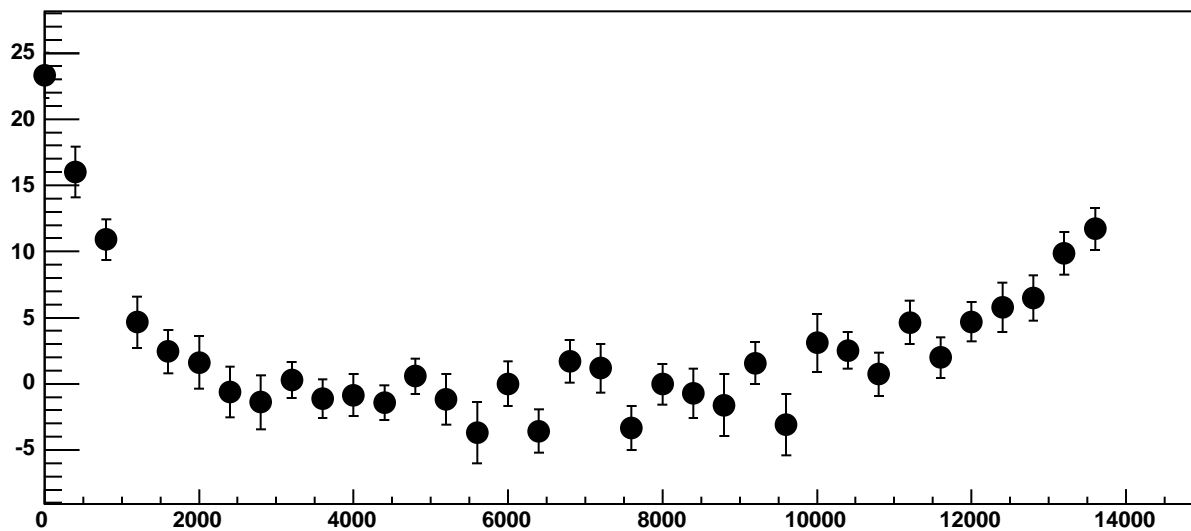


$\chi^2 / \text{ndf}$  33.59 / 23  
p0  $-422.3 \pm 0.7878$   
p1  $0.01512 \pm 0.0001202$

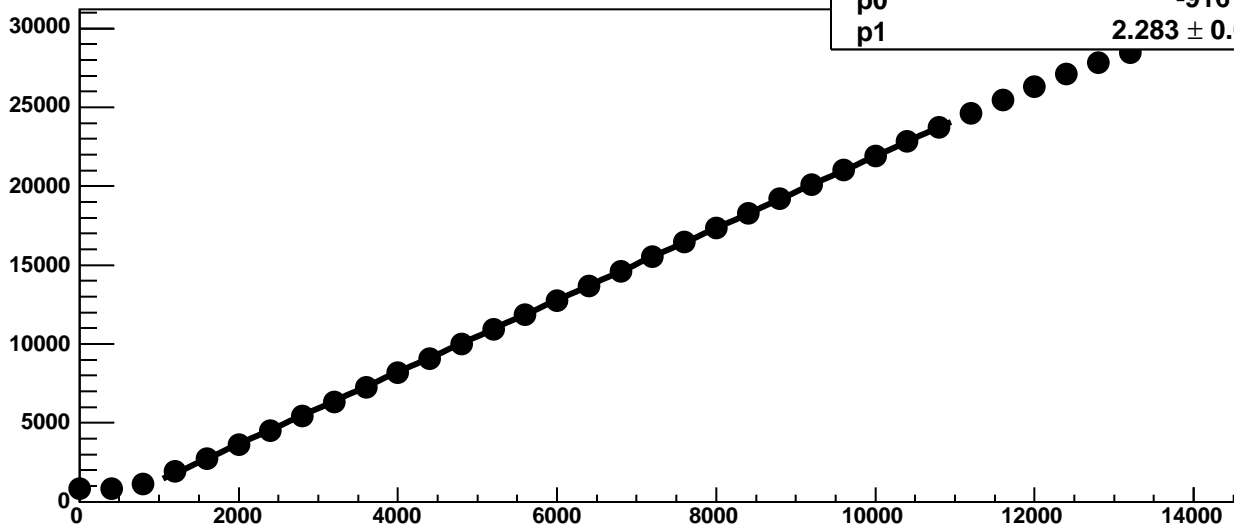
Chip 9, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



Chip 9, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

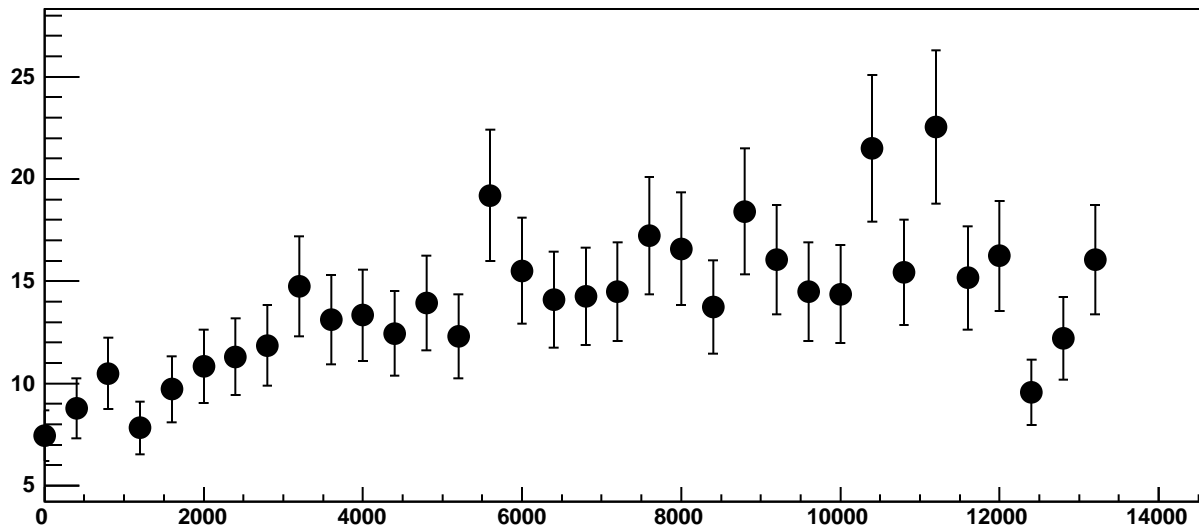


Chip 9, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC

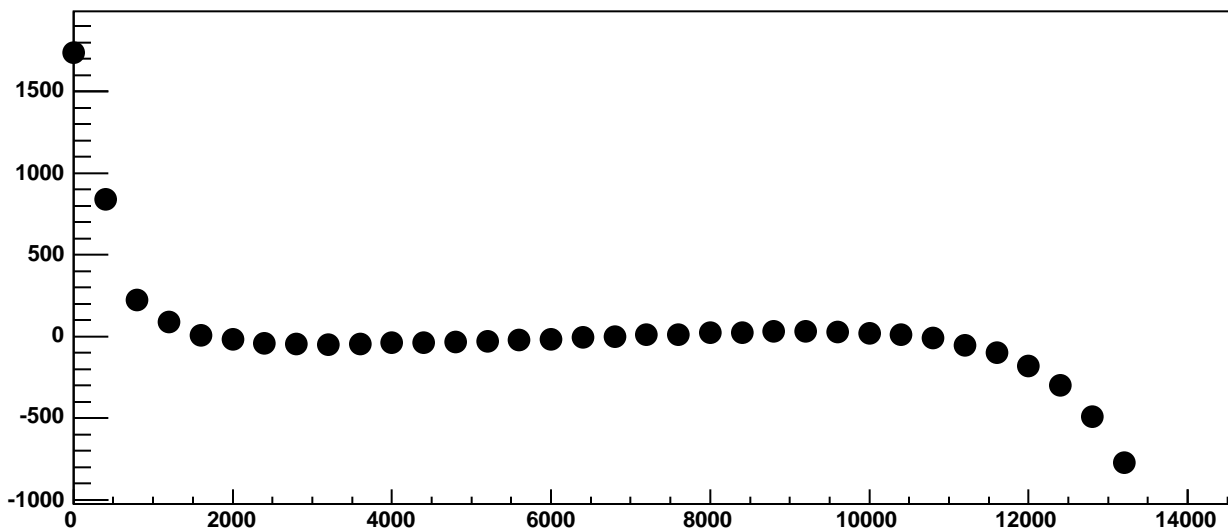


$\chi^2 / \text{ndf}$  4546 / 23  
p0 -916 ± 1.181  
p1 2.283 ± 0.000204

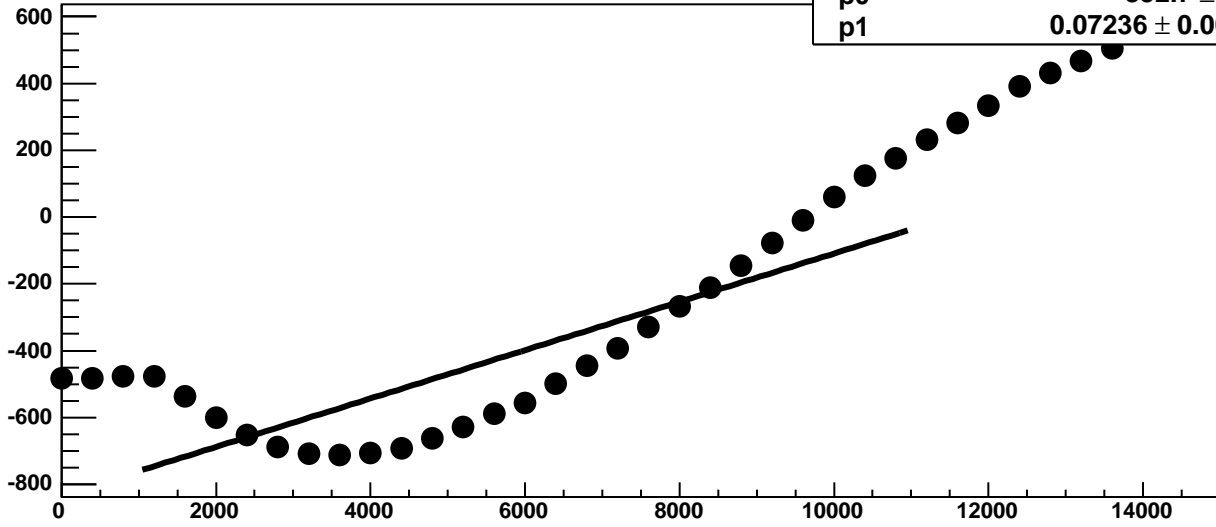
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC



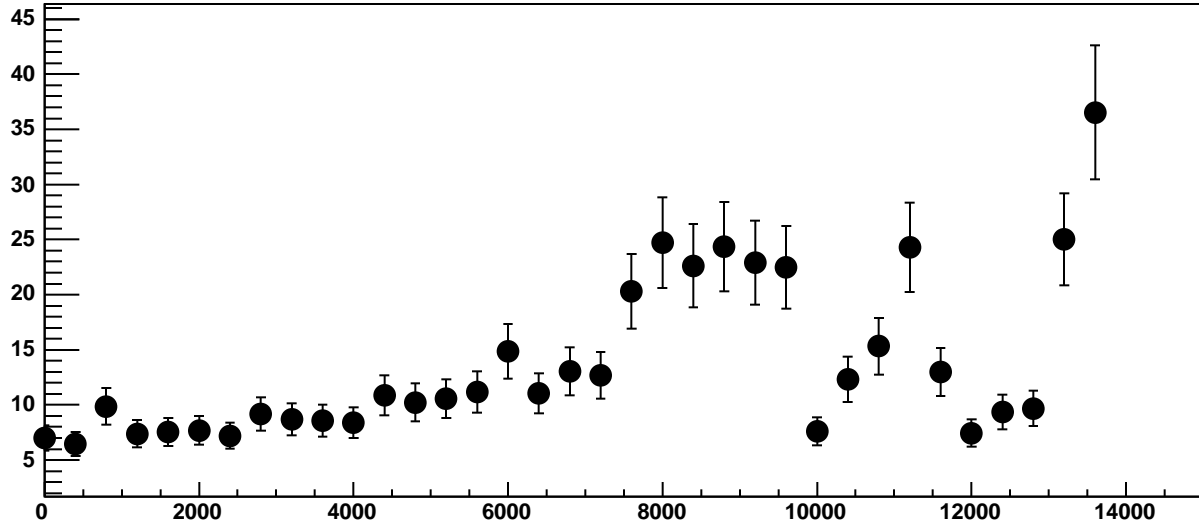
Chip 9, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC



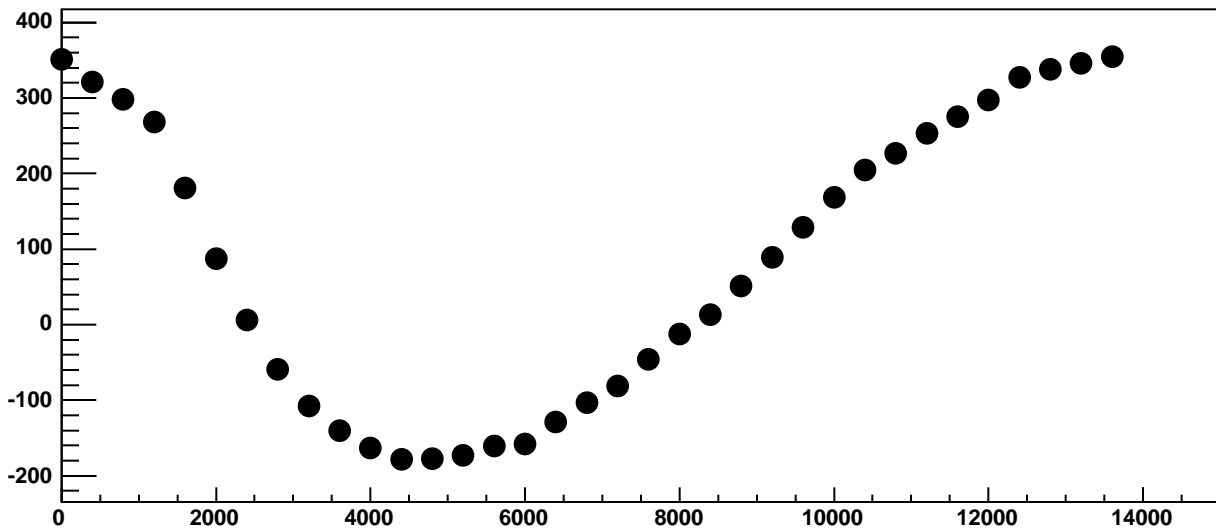
Chip 9, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



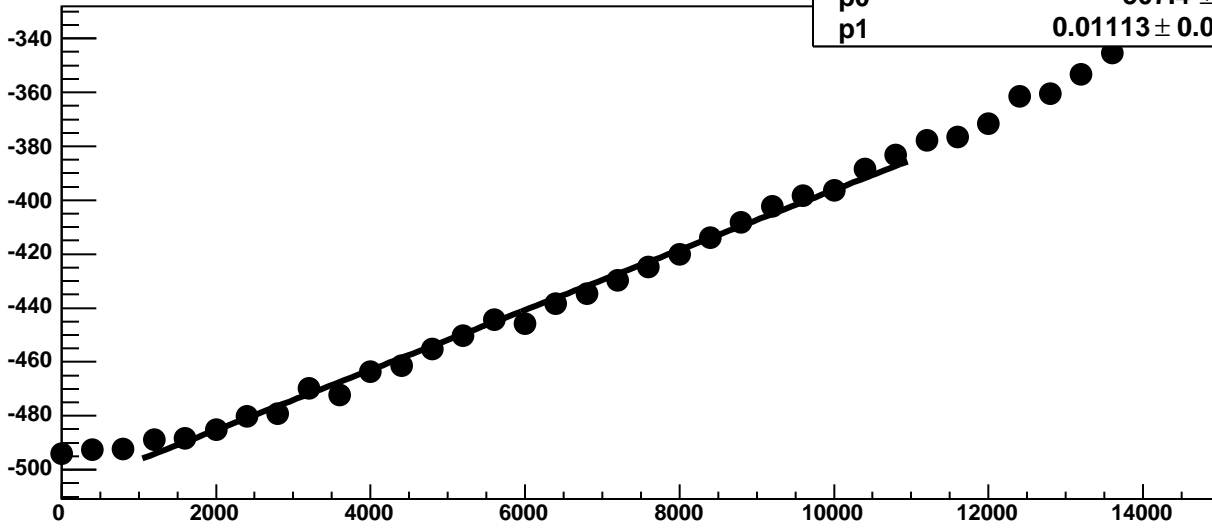
Chip 9, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

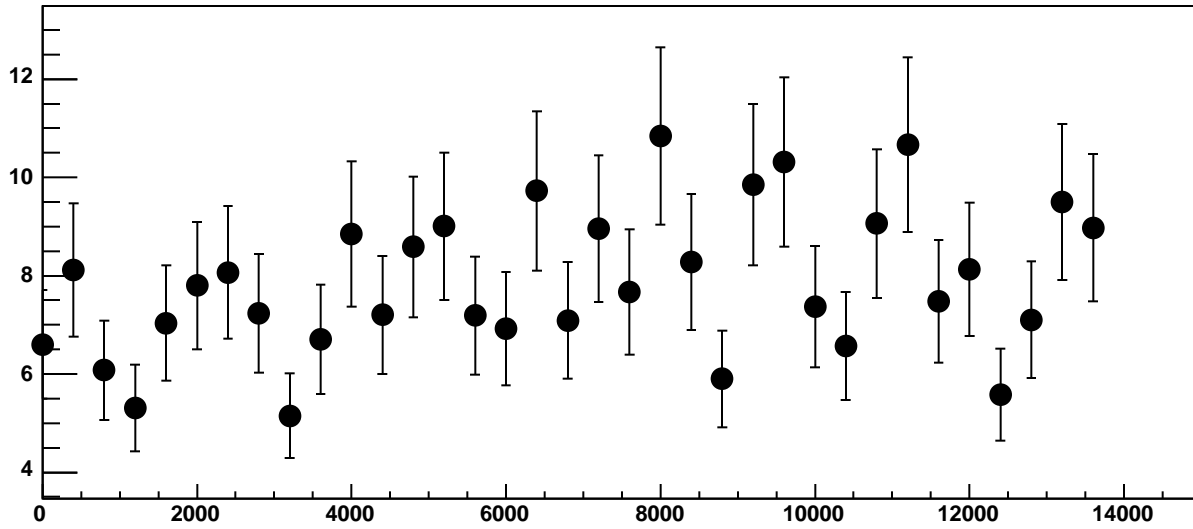


Chip 9, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

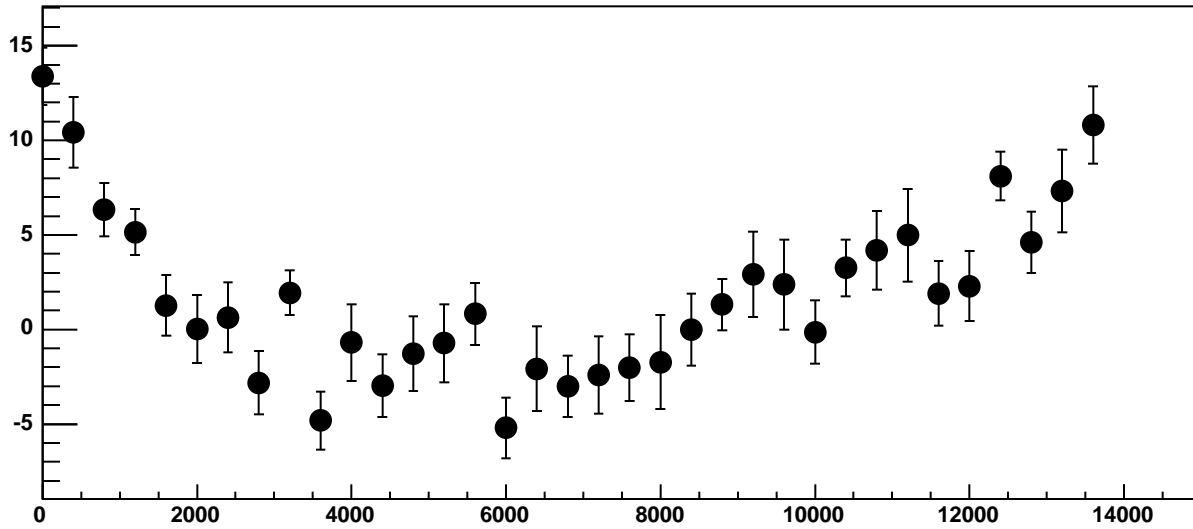


$\chi^2 / \text{ndf}$  68.69 / 23  
p0  $-507.4 \pm 0.7238$   
p1  $0.01113 \pm 0.0001146$

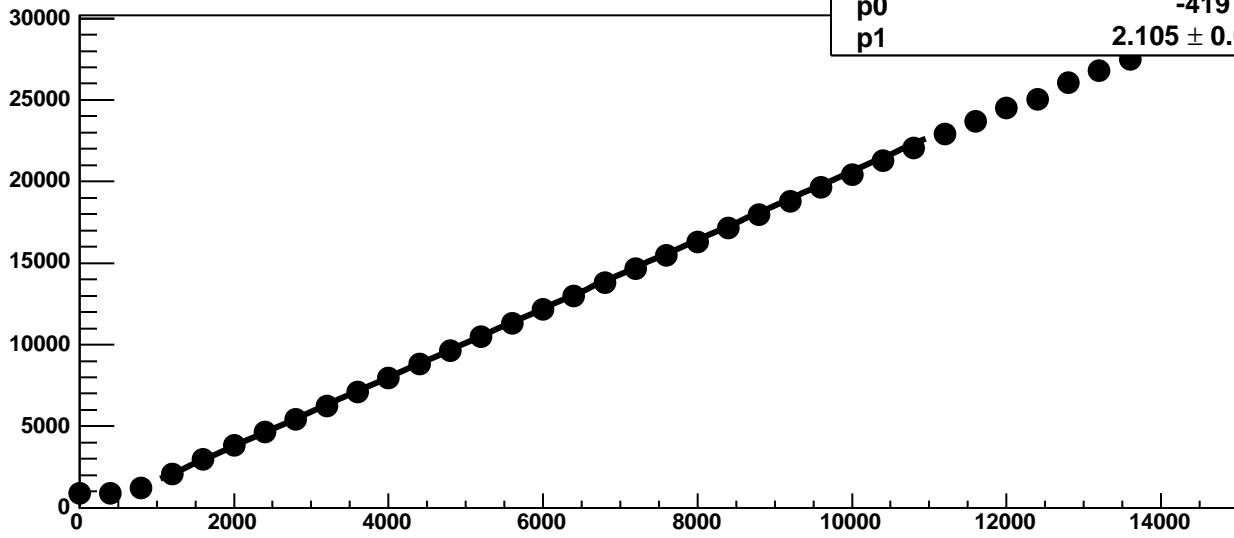
Chip 9, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 9, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

288 / 23

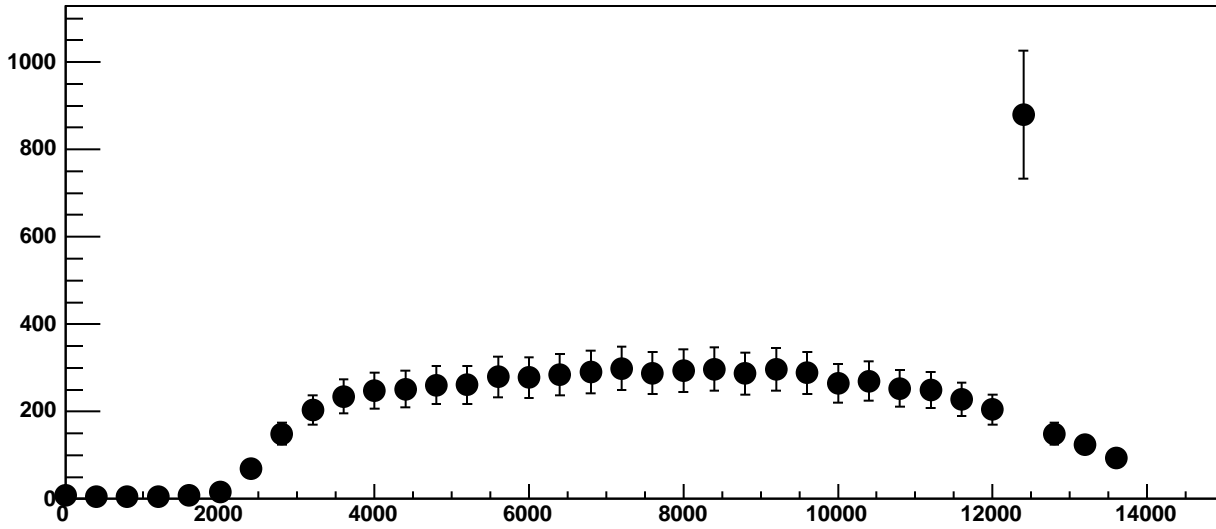
p0

$-419 \pm 3.081$

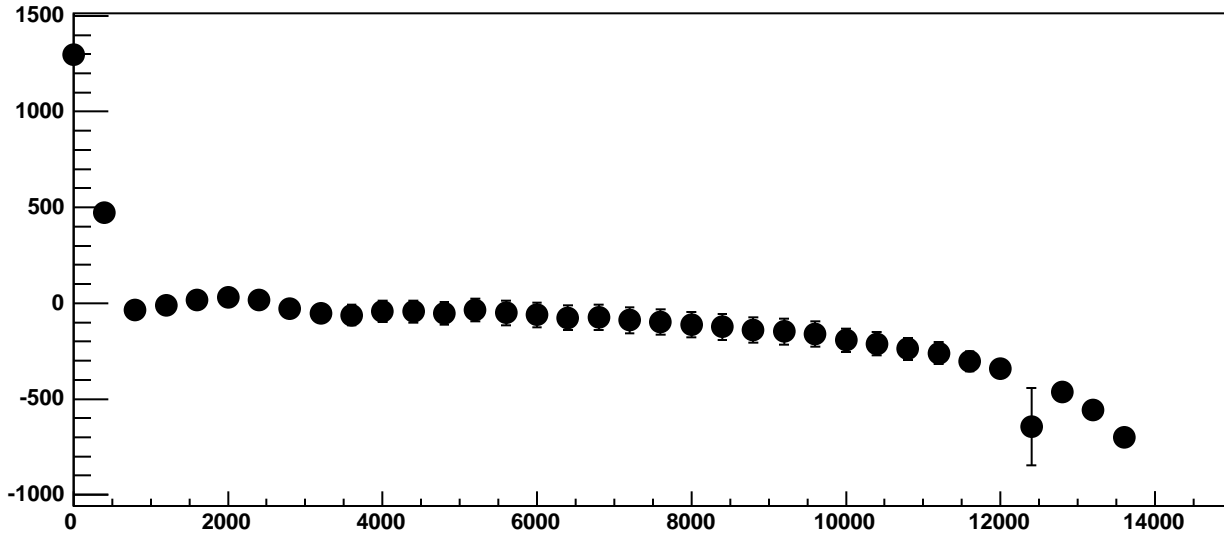
p1

$2.105 \pm 0.002015$

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

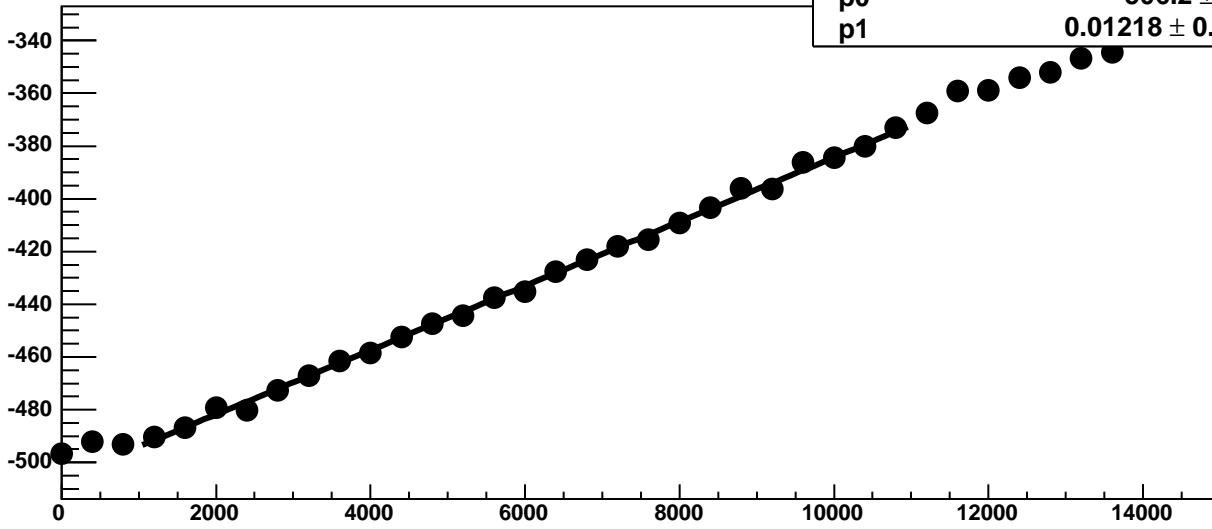


Chip 9, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



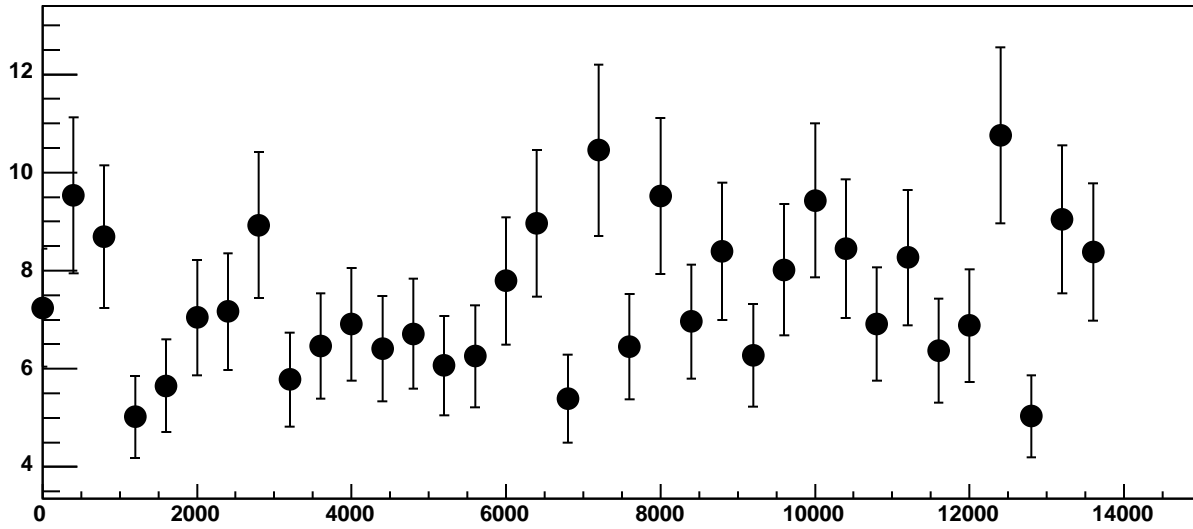


Chip 9, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

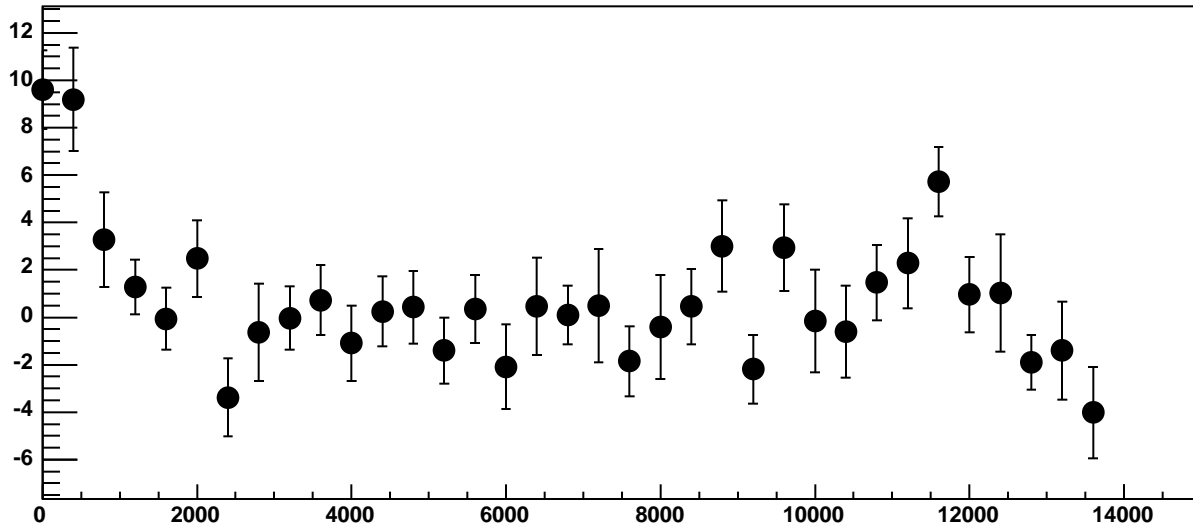


$\chi^2 / \text{ndf}$  21.25 / 23  
p0  $-506.2 \pm 0.6779$   
p1  $0.01218 \pm 0.000109$

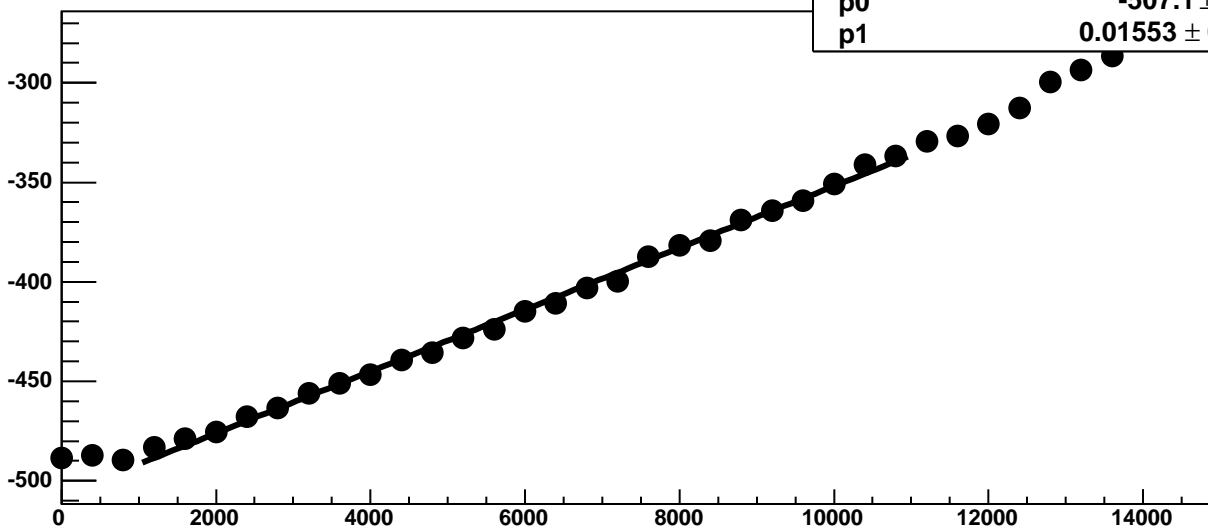
Chip 9, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

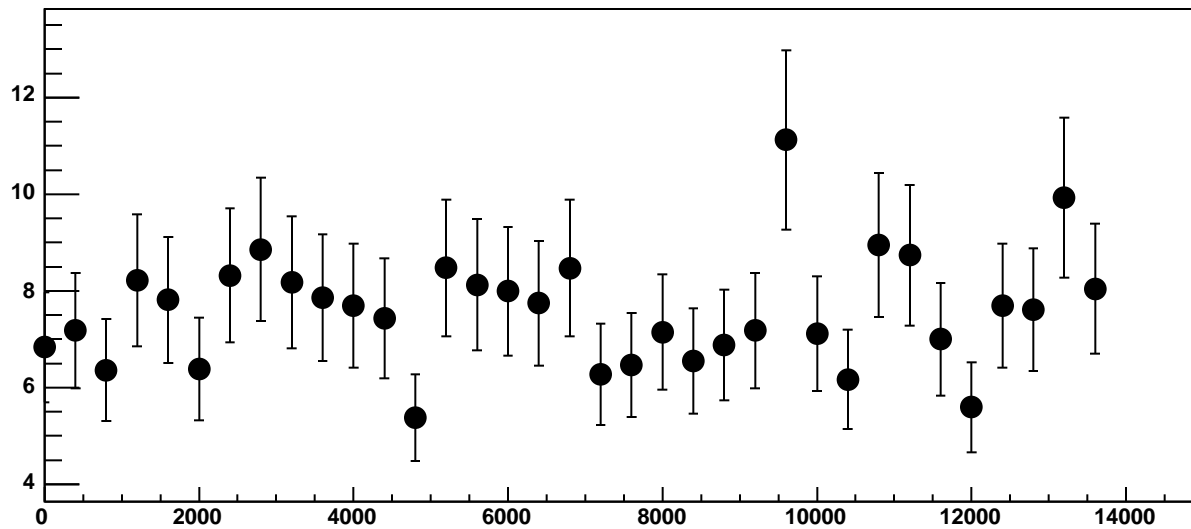


Chip 9, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

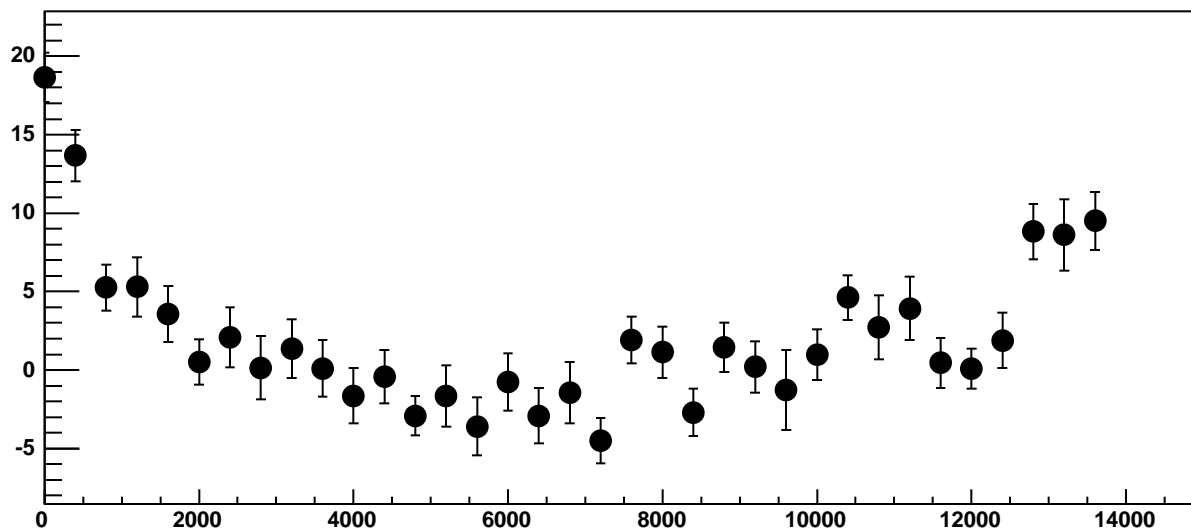


$\chi^2 / \text{ndf}$  57.05 / 23  
p0  $-507.1 \pm 0.8078$   
p1  $0.01553 \pm 0.00012$

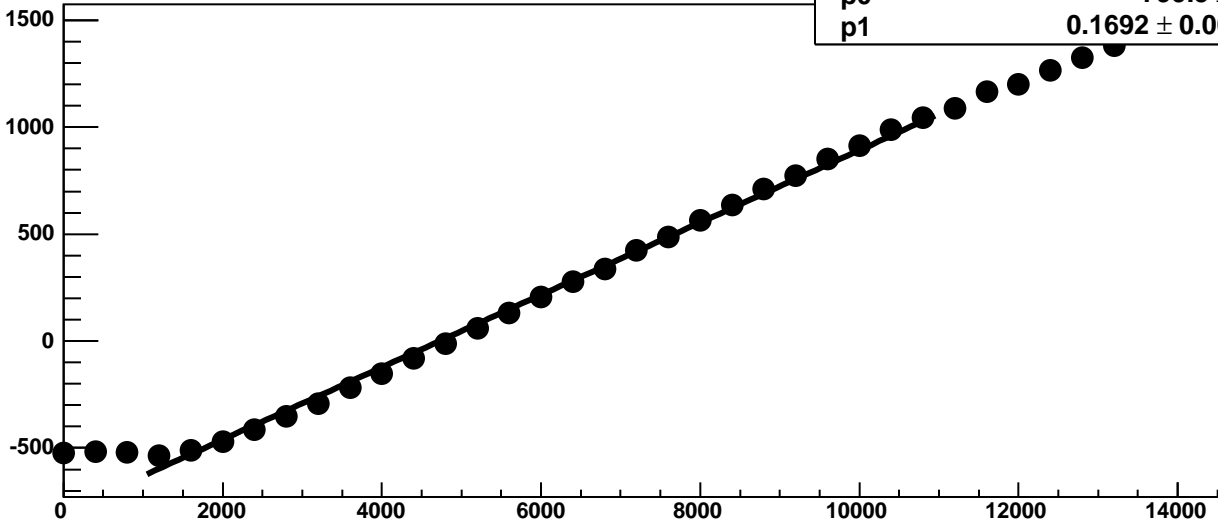
Chip 9, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



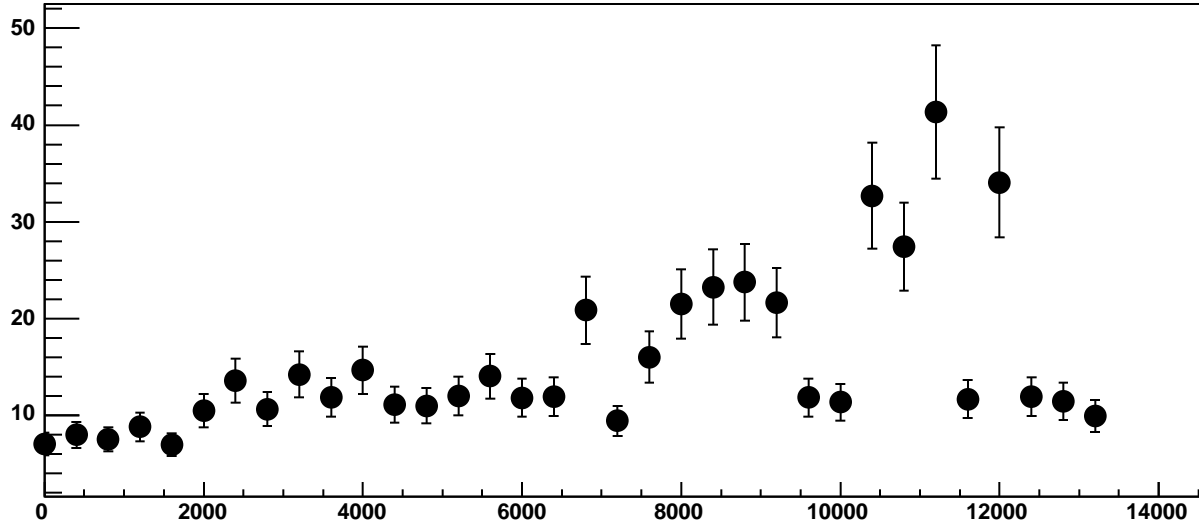
Chip 9, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



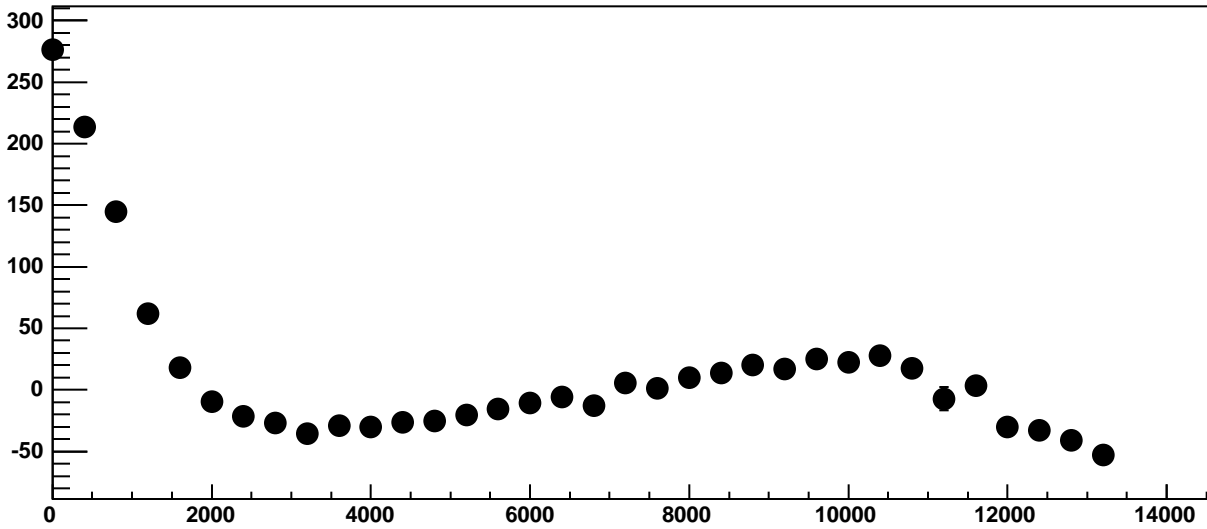
Chip 9, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



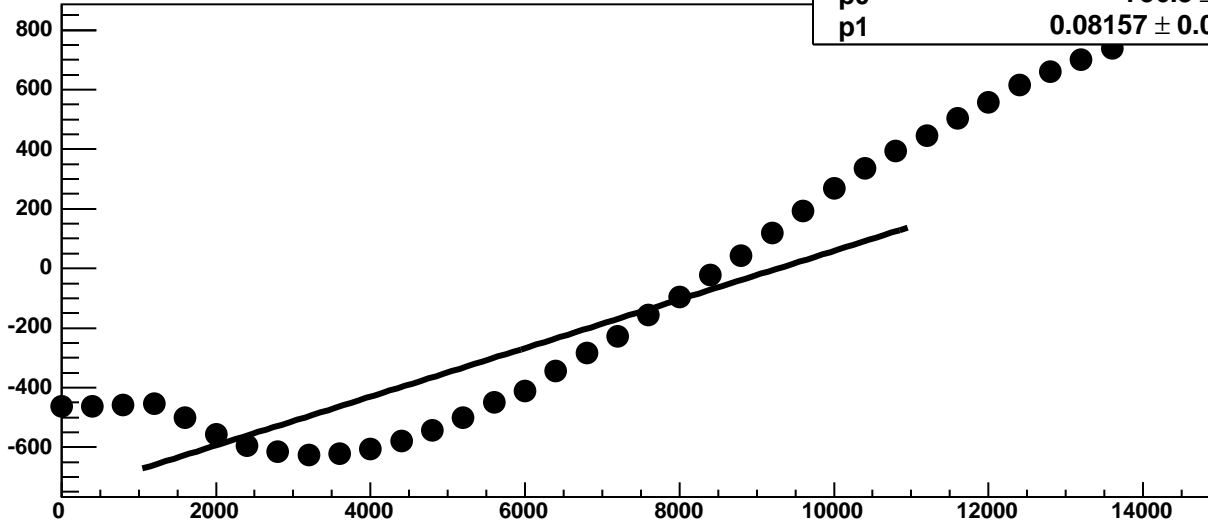
Chip 9, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 9, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

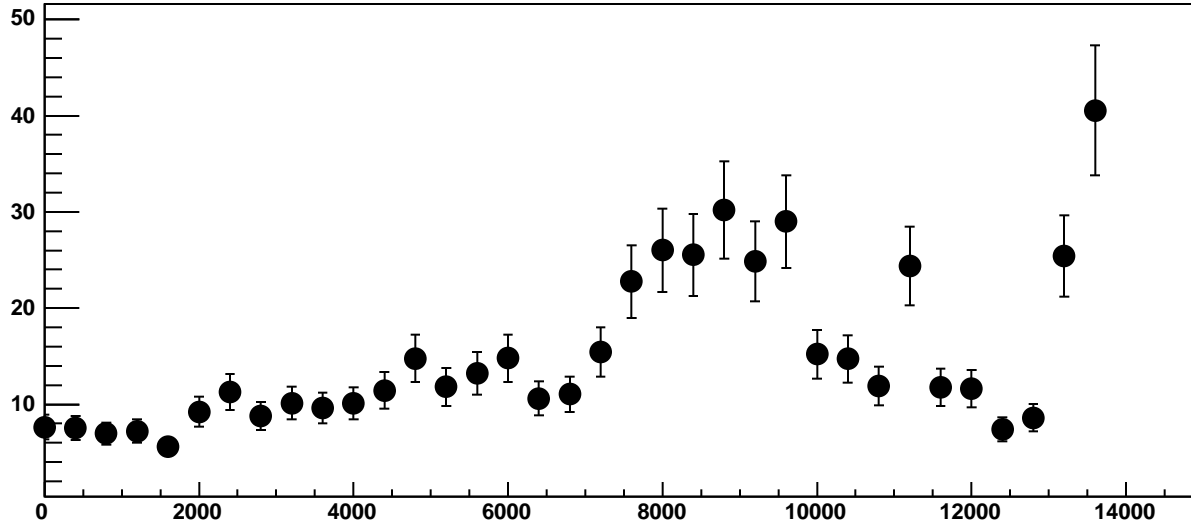


Chip 9, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

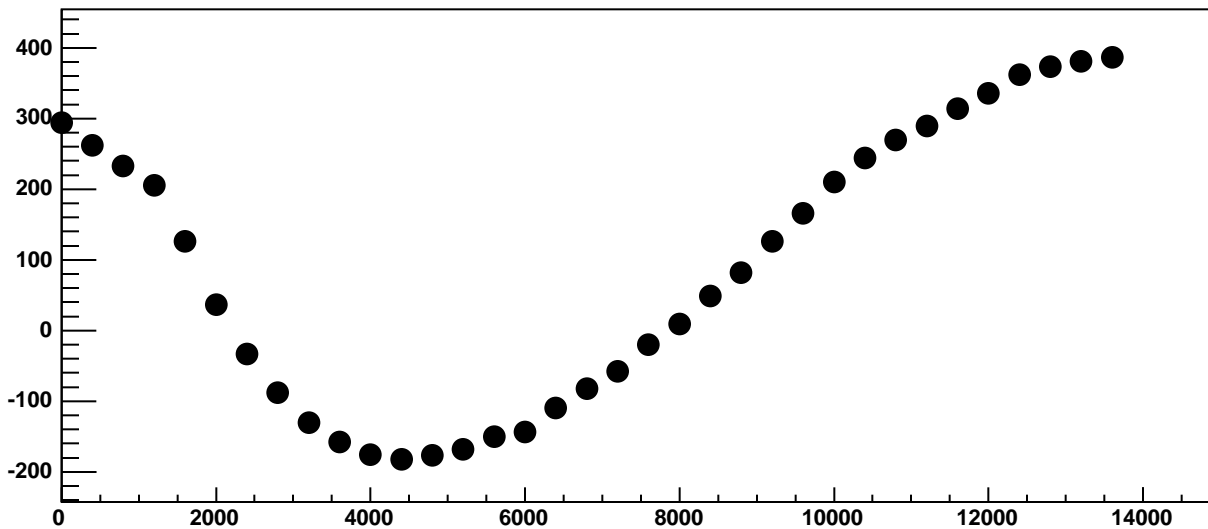


$\chi^2 / \text{ndf}$  8.028e+04 / 23  
p0 -756.8 ± 0.9522  
p1 0.08157 ± 0.0001885

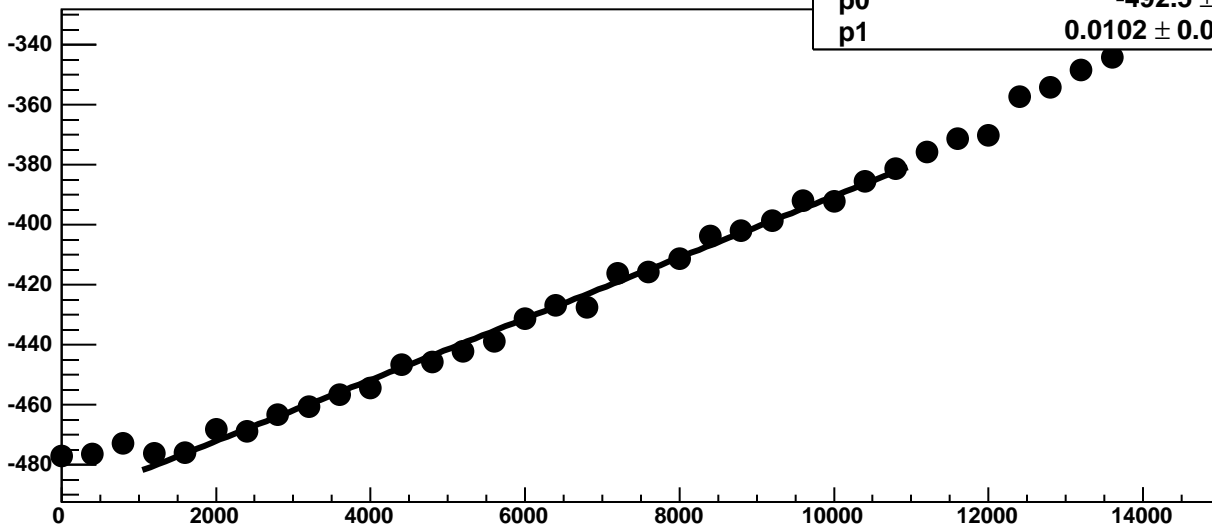
Chip 9, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 9, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

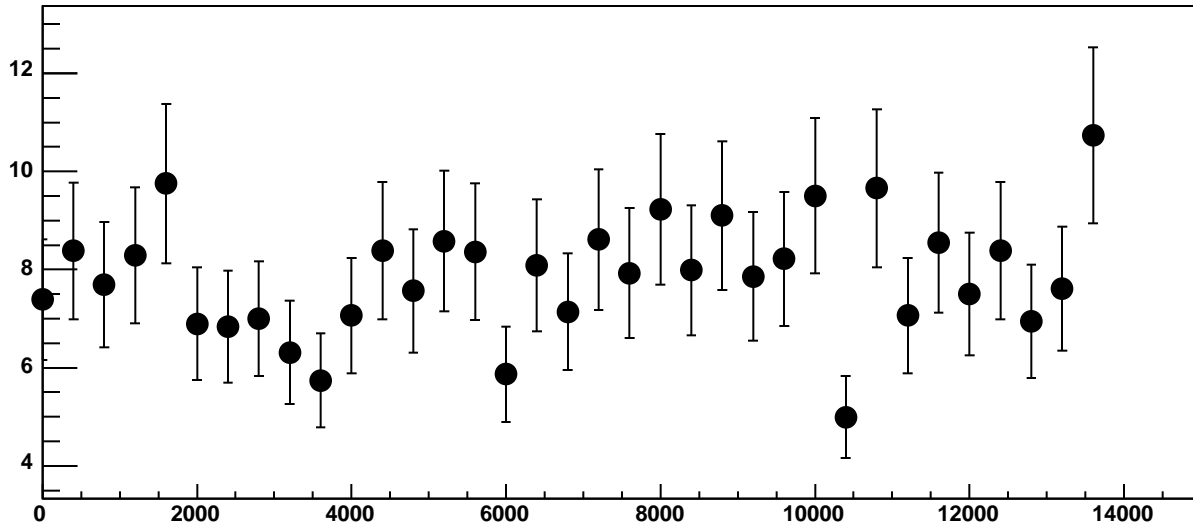


Chip 9, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

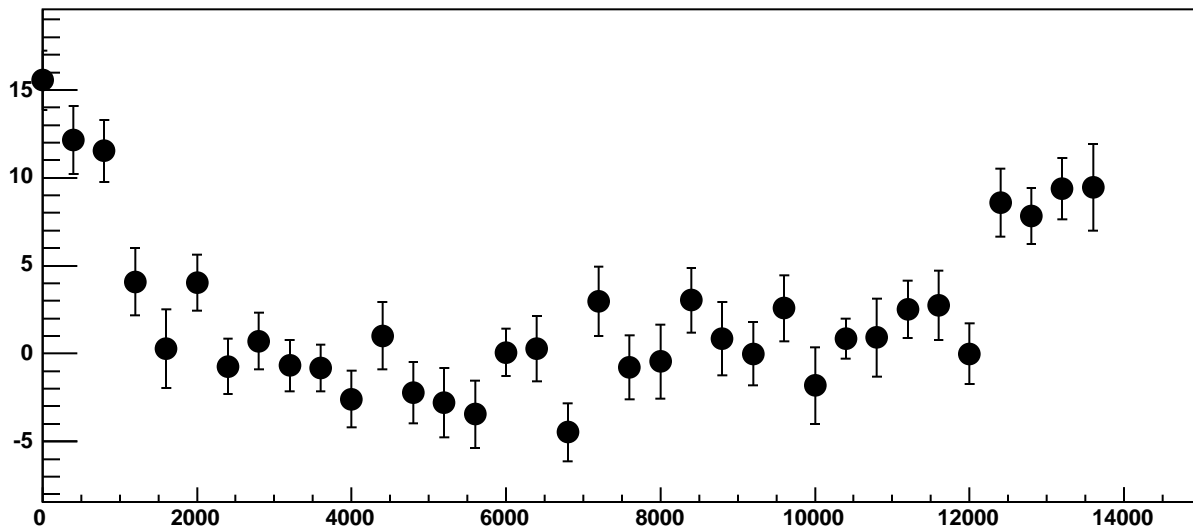


$\chi^2 / \text{ndf}$  38.06 / 23  
p0  $-492.5 \pm 0.7773$   
p1  $0.0102 \pm 0.0001185$

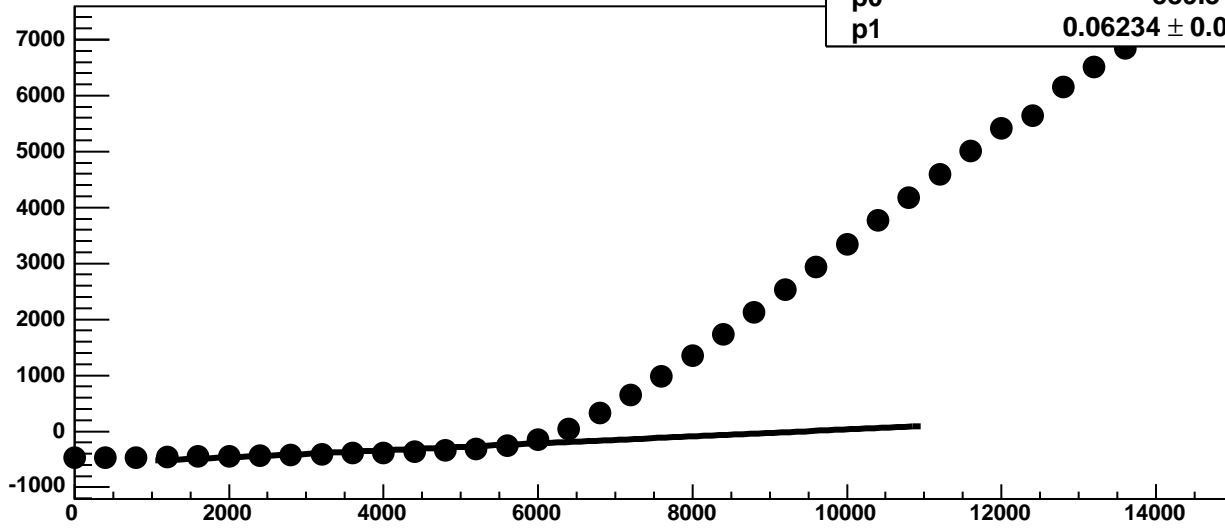
Chip 9, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 9, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

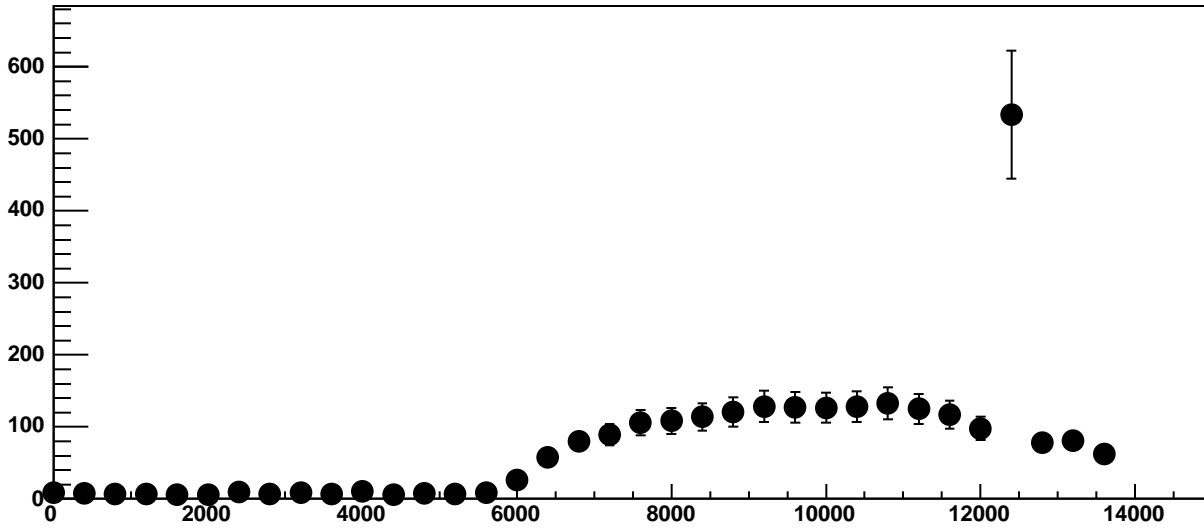


Chip 9, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC

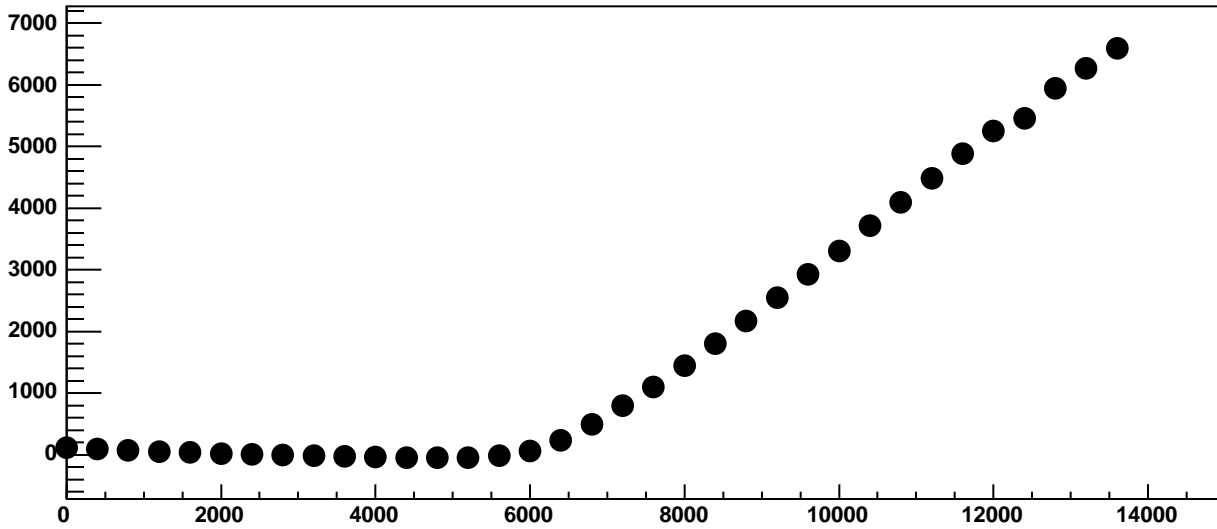


$\chi^2 / \text{ndf}$  8.904e+04 / 23  
p0 -589.3 ± 1.177  
p1 0.06234 ± 0.0003255

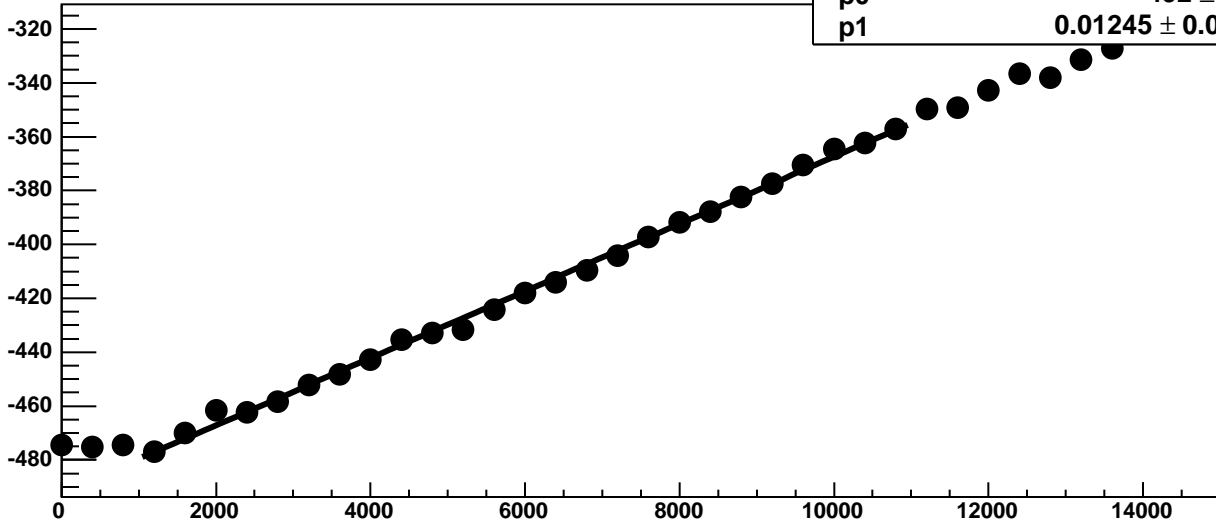
Chip 9, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



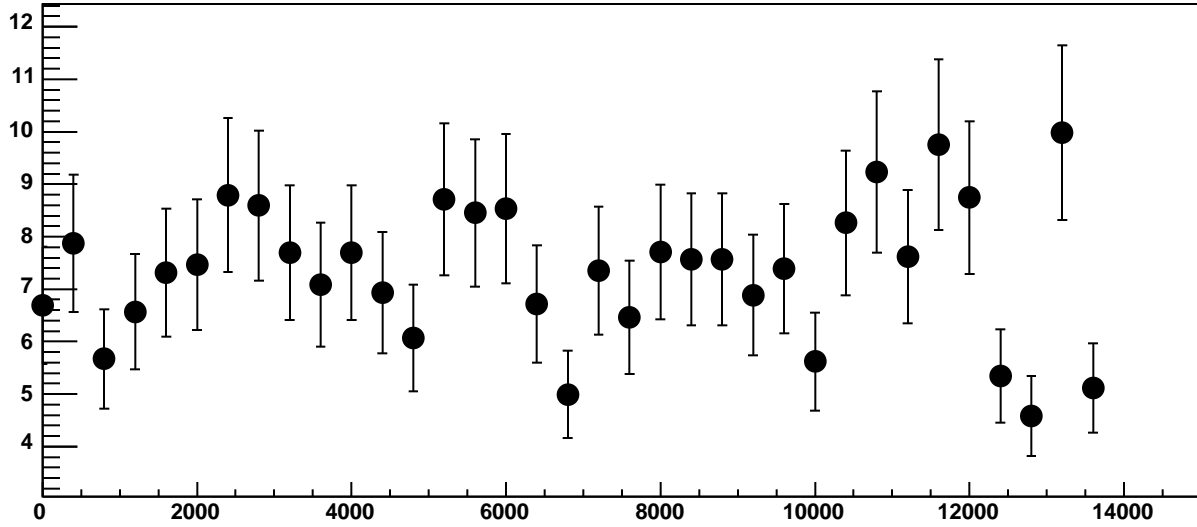
Chip 9, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



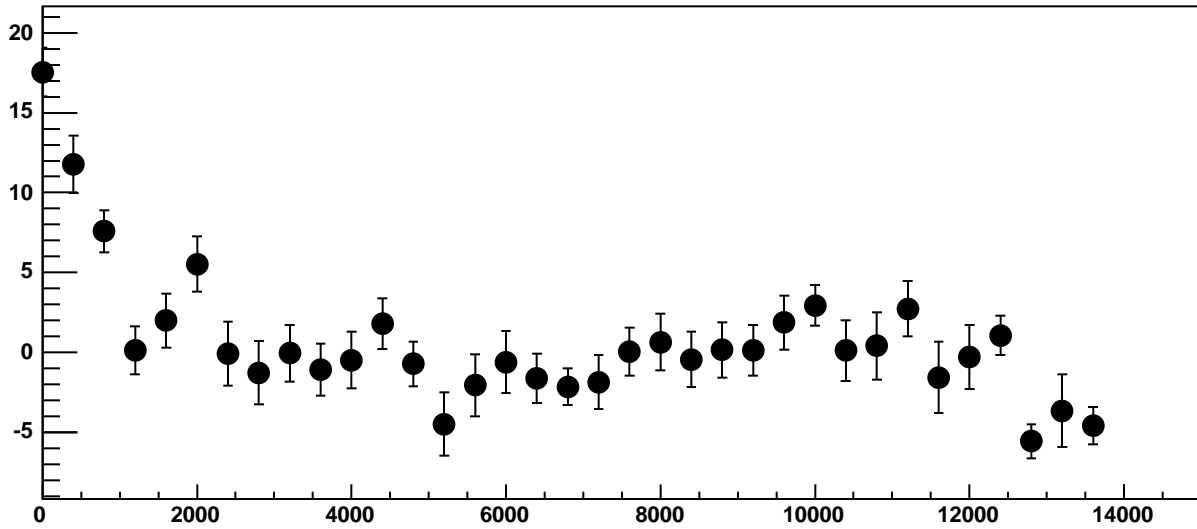
Chip 9, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



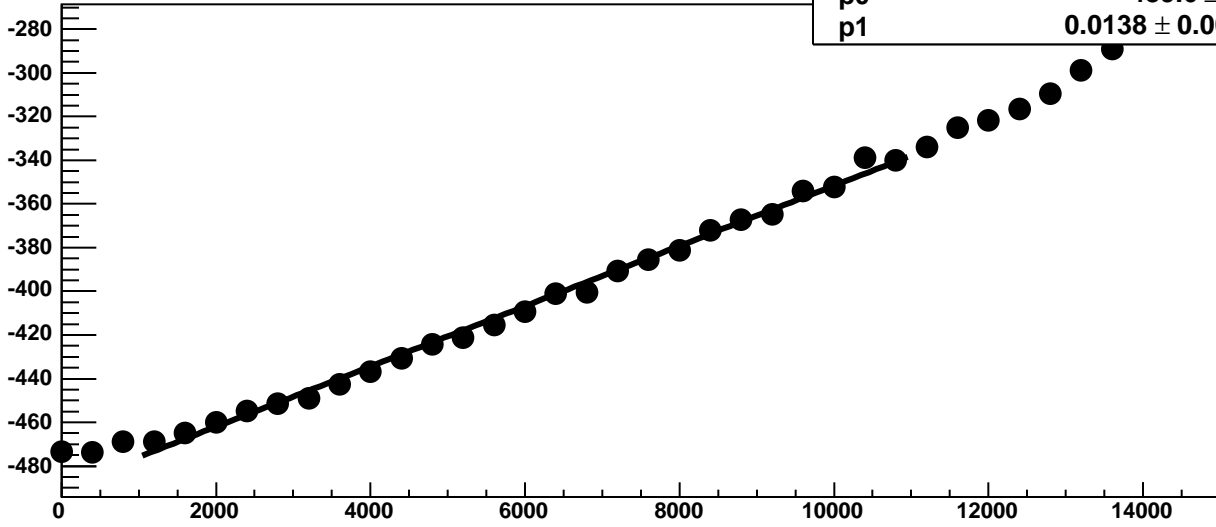
Chip 9, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



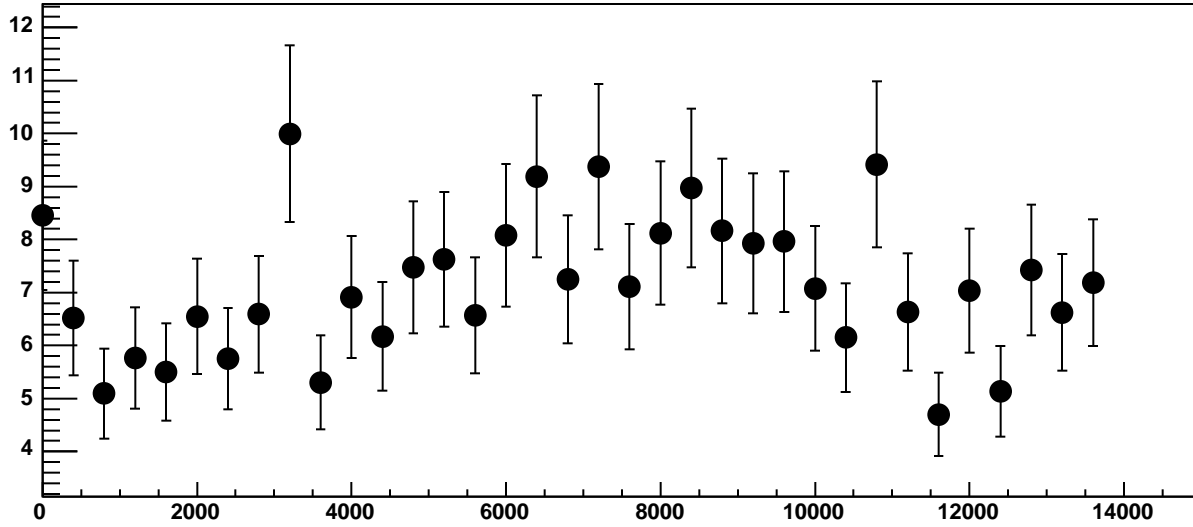
Chip 9, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



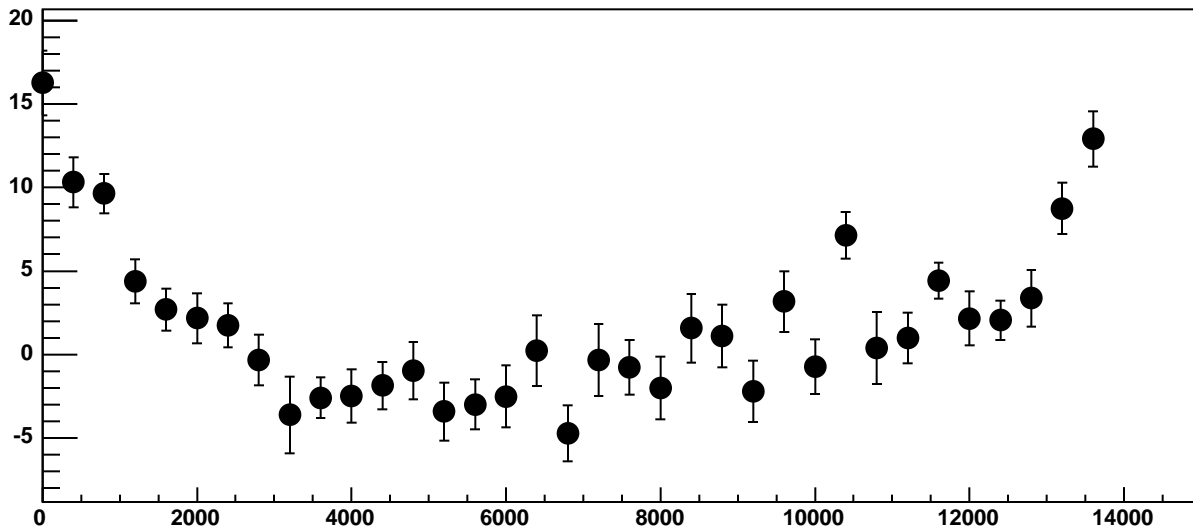
Chip 9, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 9, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

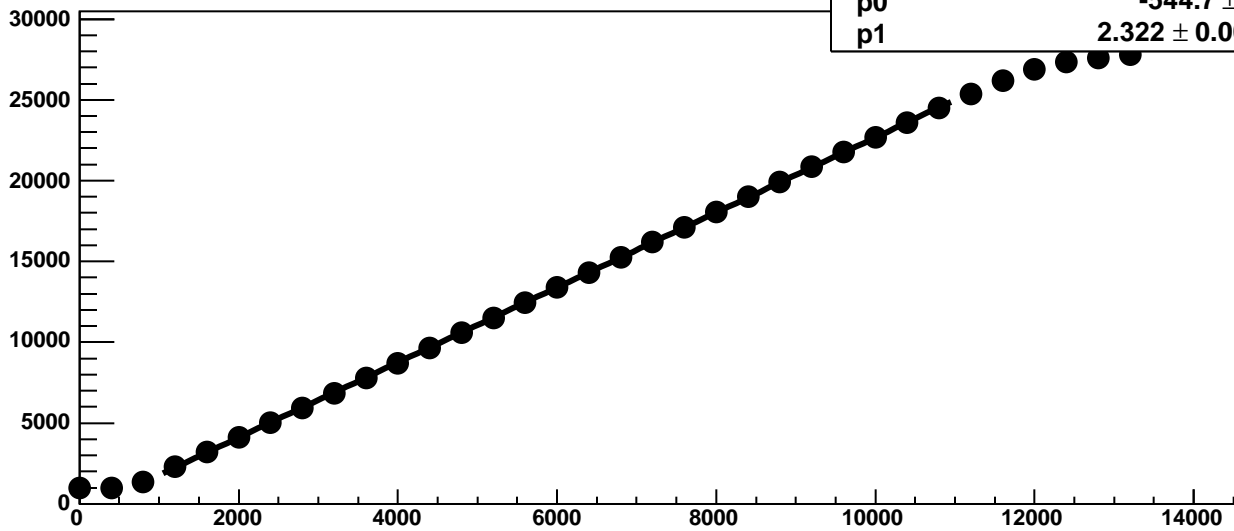


Chip 9, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC





Chip 9, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

2772 / 23

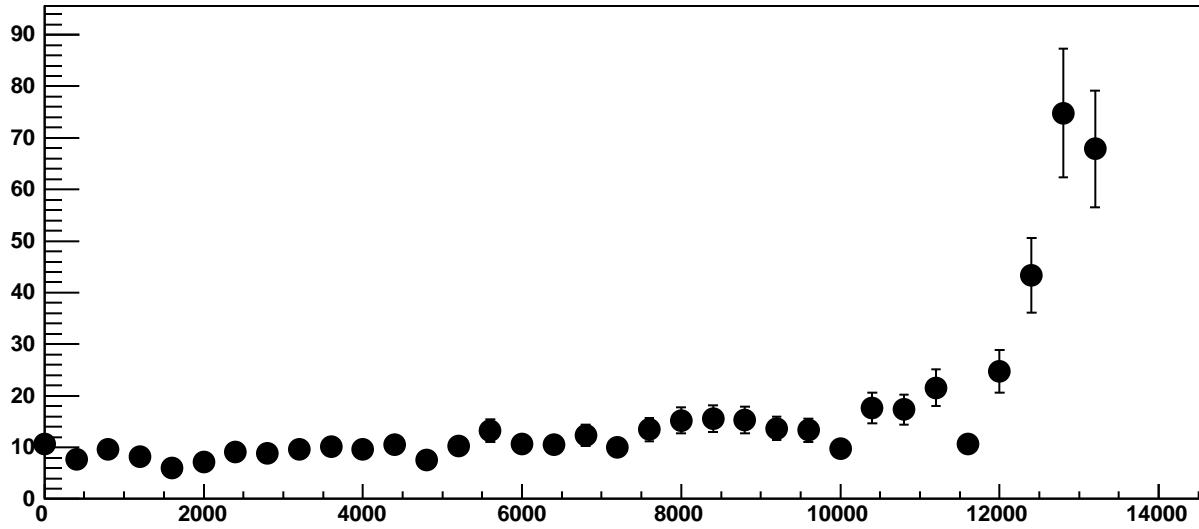
p0

$-544.7 \pm 0.9221$

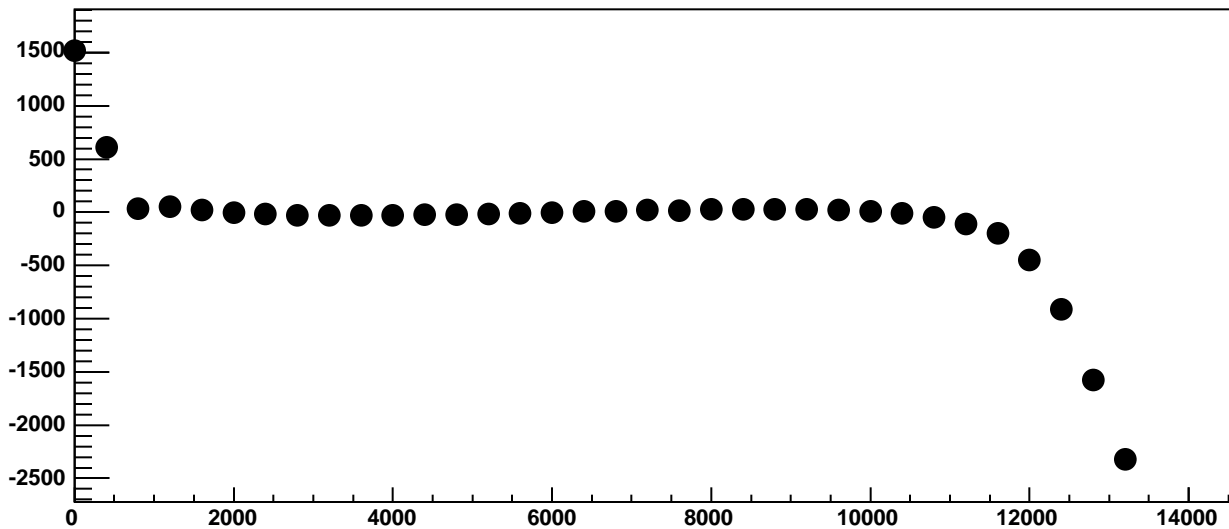
p1

$2.322 \pm 0.0001679$

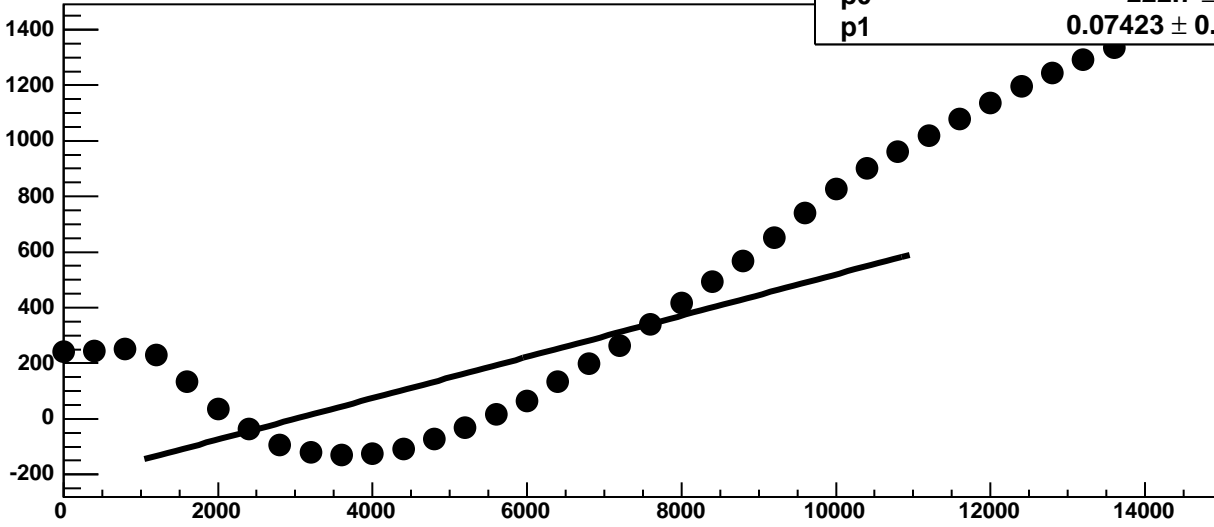
Chip 9, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 9, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1.91e+05 / 23

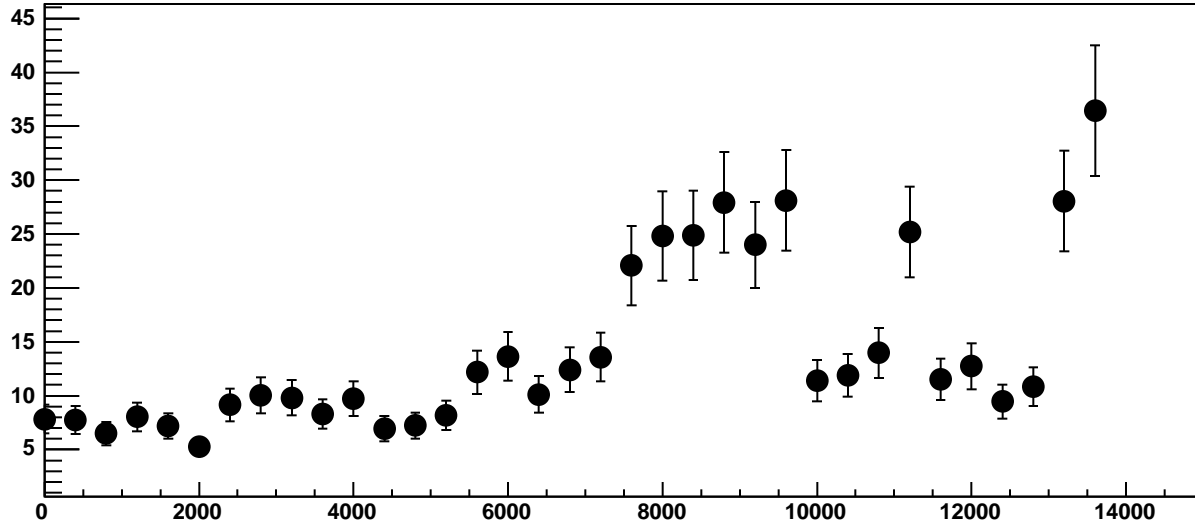
p0

-222.7 ± 0.9054

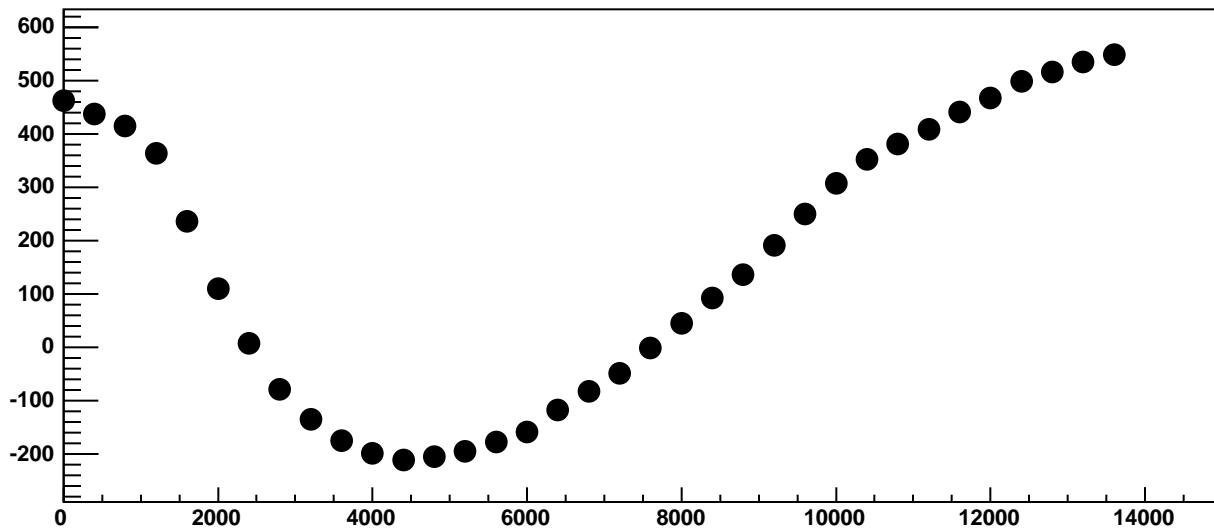
p1

0.07423 ± 0.000179

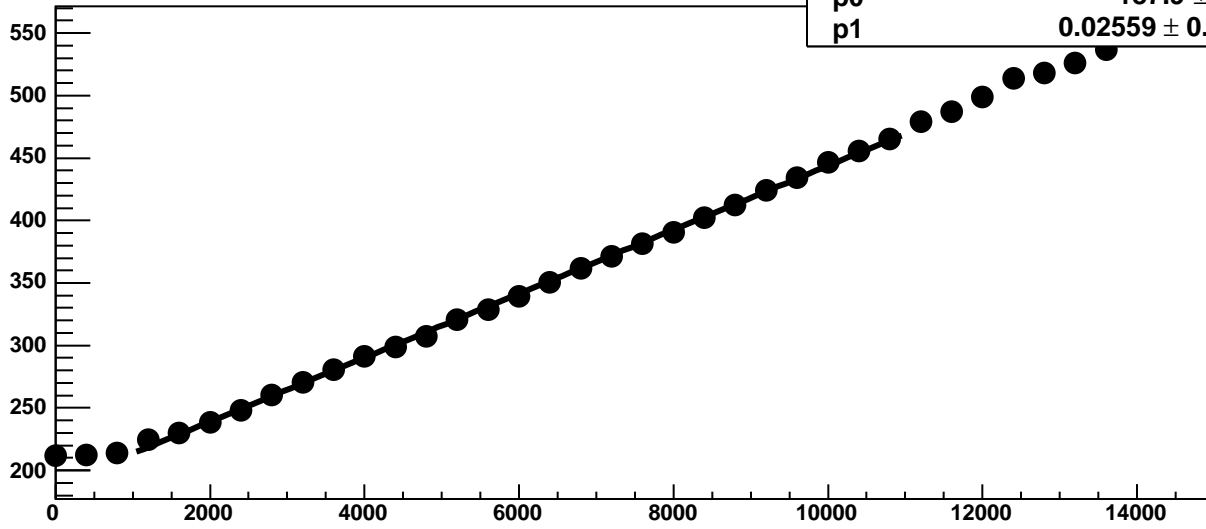
Chip 10, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

26.46 / 23

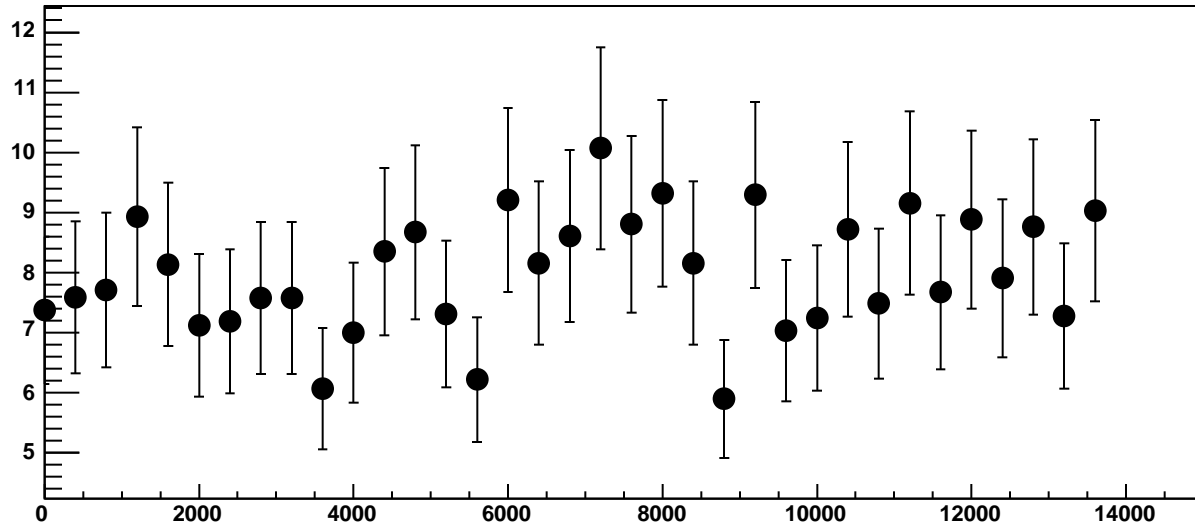
p0

$187.9 \pm 0.8046$

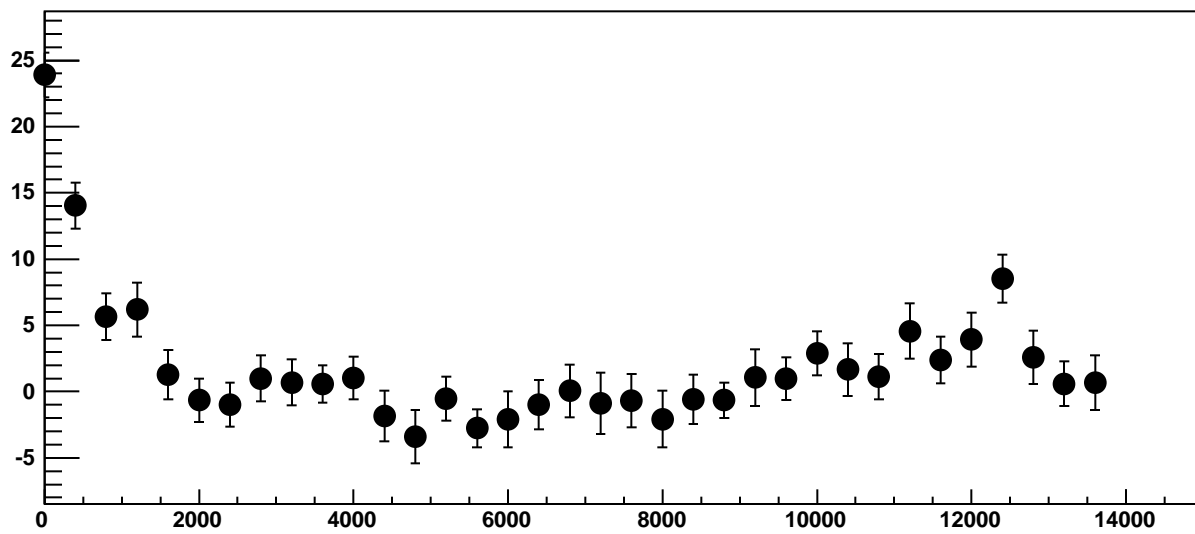
p1

$0.02559 \pm 0.000122$

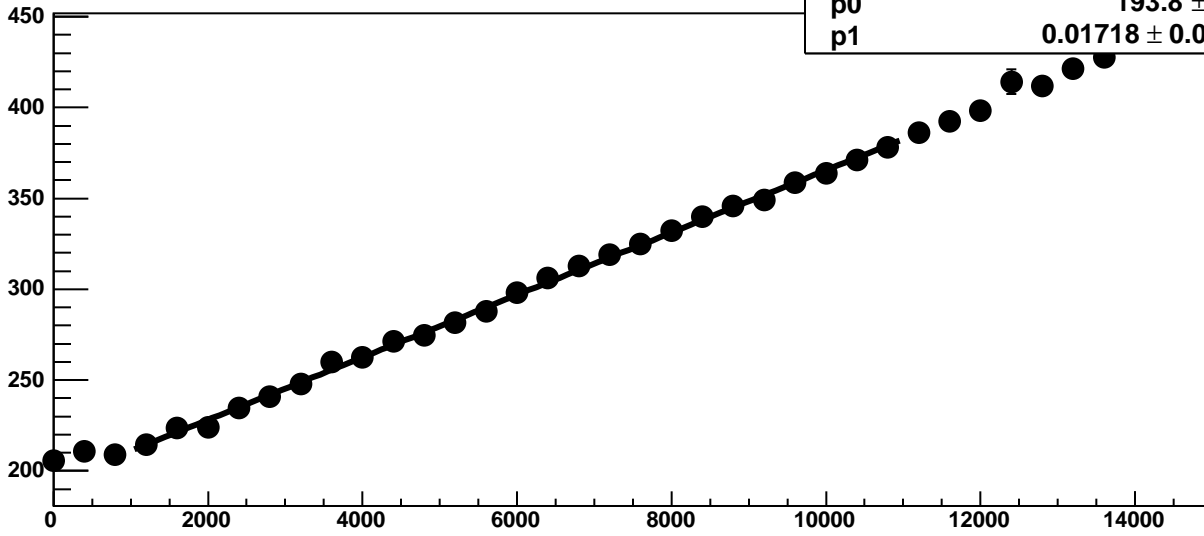
Chip 10, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

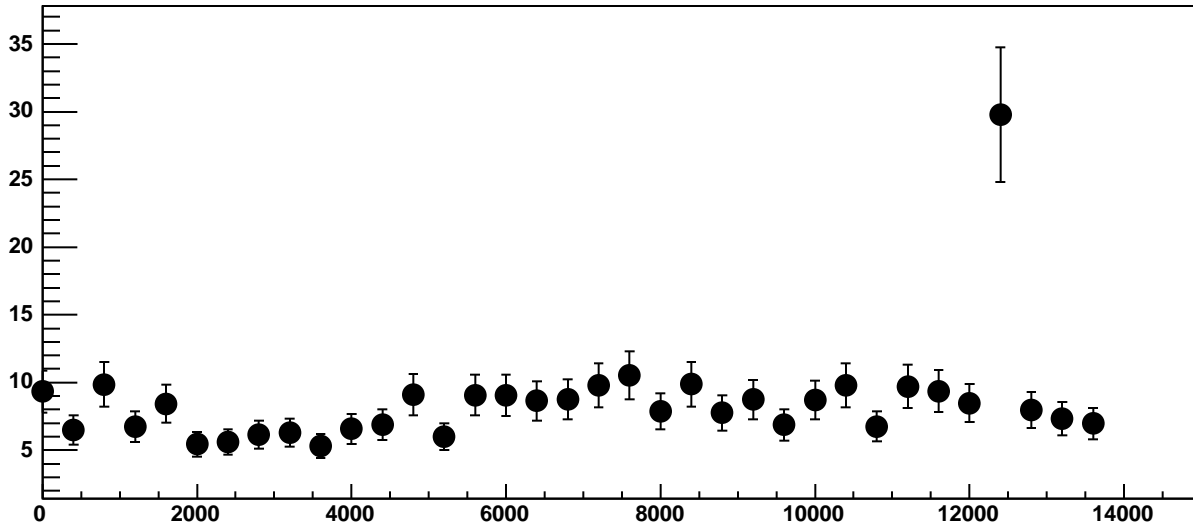


Chip 10, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

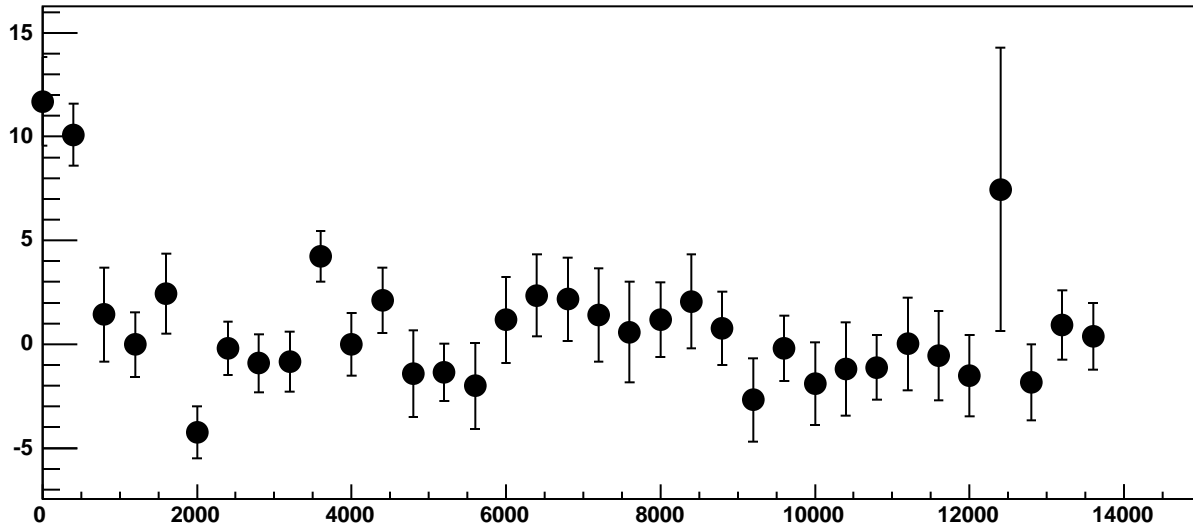


$\chi^2 / \text{ndf}$  38.37 / 23  
p0  $193.8 \pm 0.6977$   
p1  $0.01718 \pm 0.0001146$

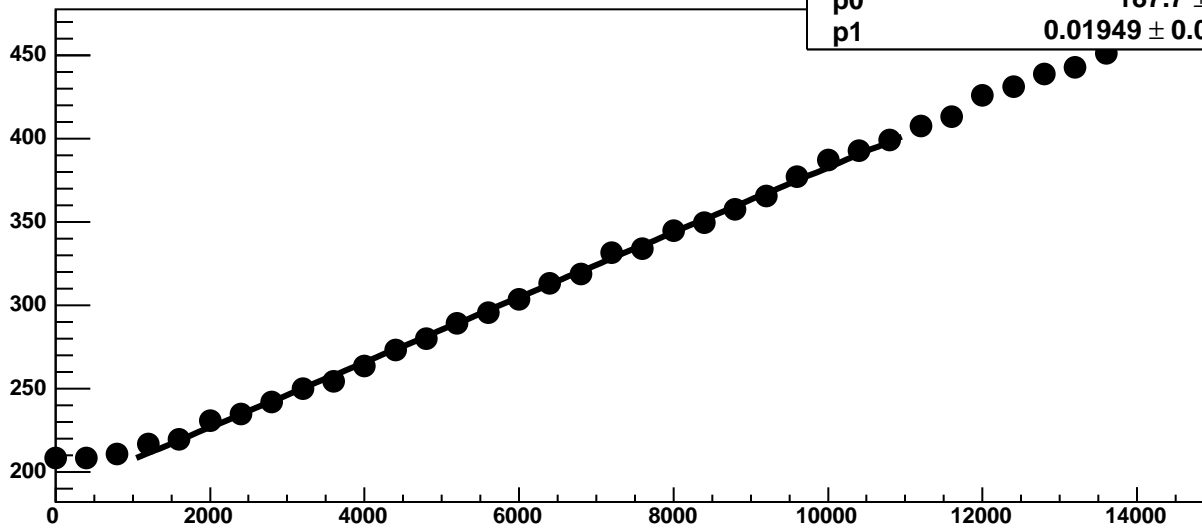
Chip 10, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

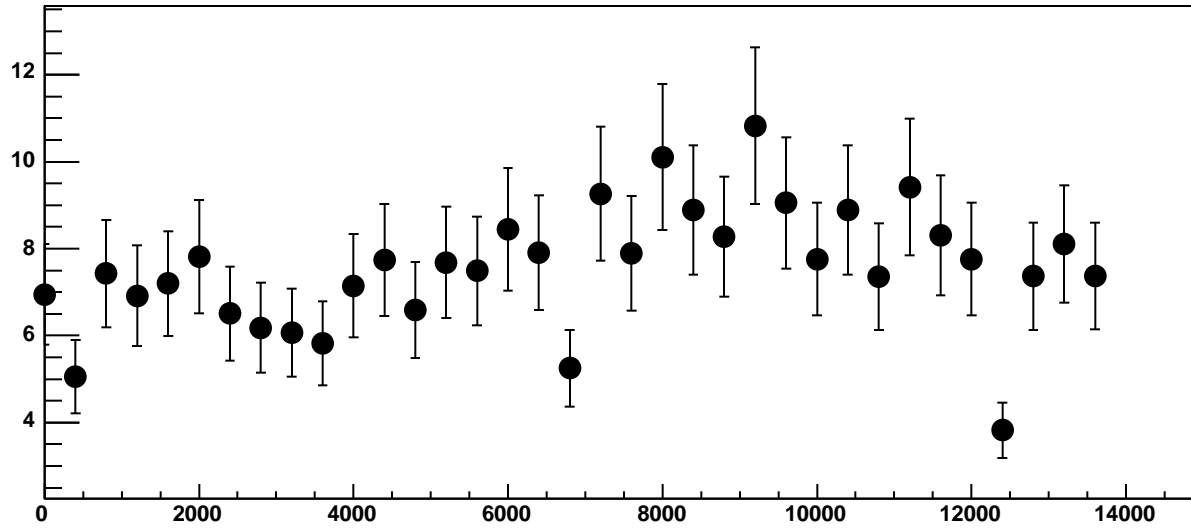


Chip 10, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

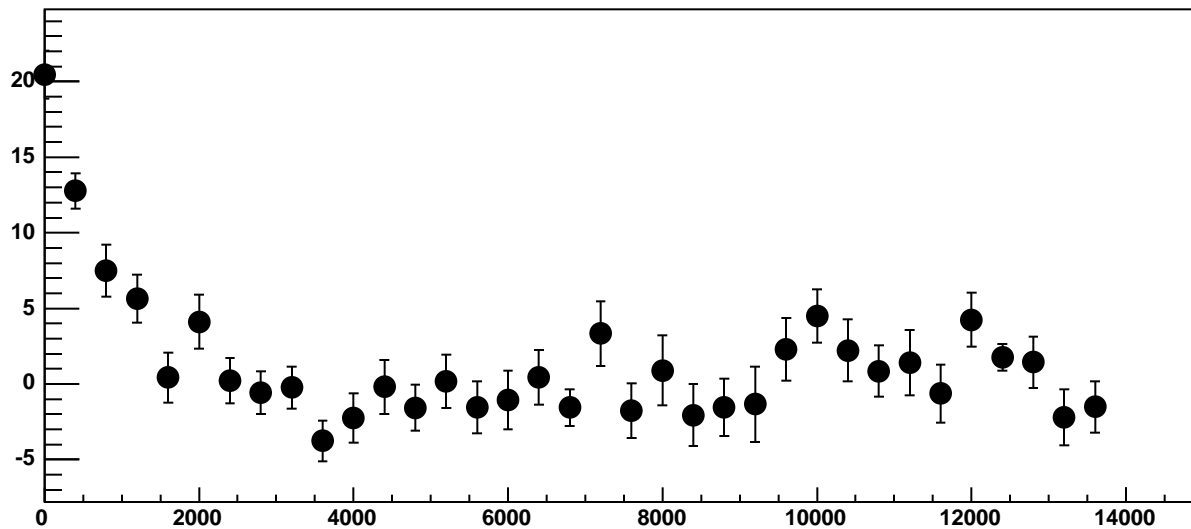


$\chi^2 / \text{ndf}$  46.5 / 23  
p0  $187.7 \pm 0.7444$   
p1  $0.01949 \pm 0.0001207$

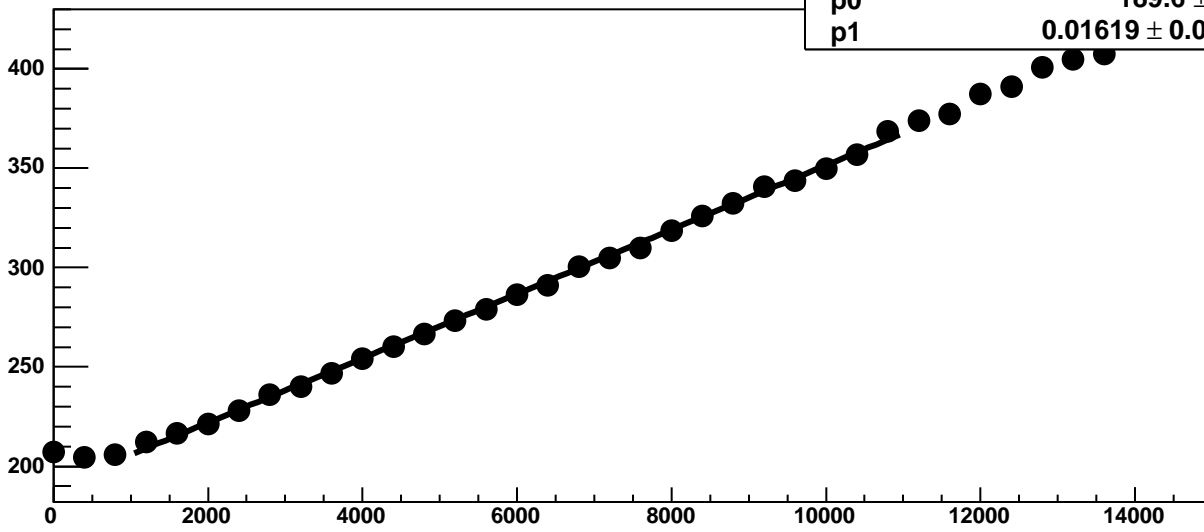
Chip 10, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

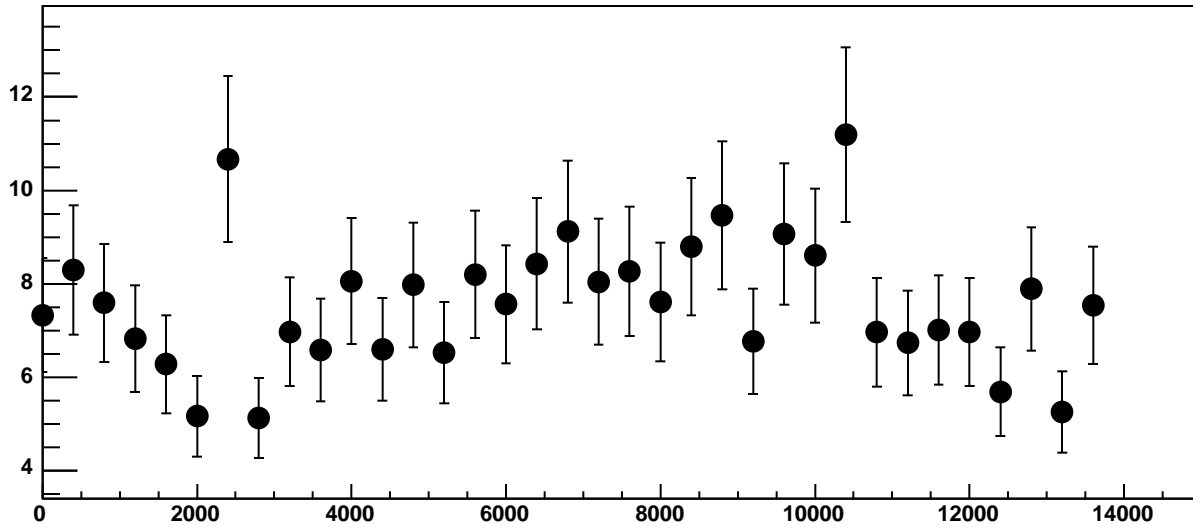


Chip 10, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

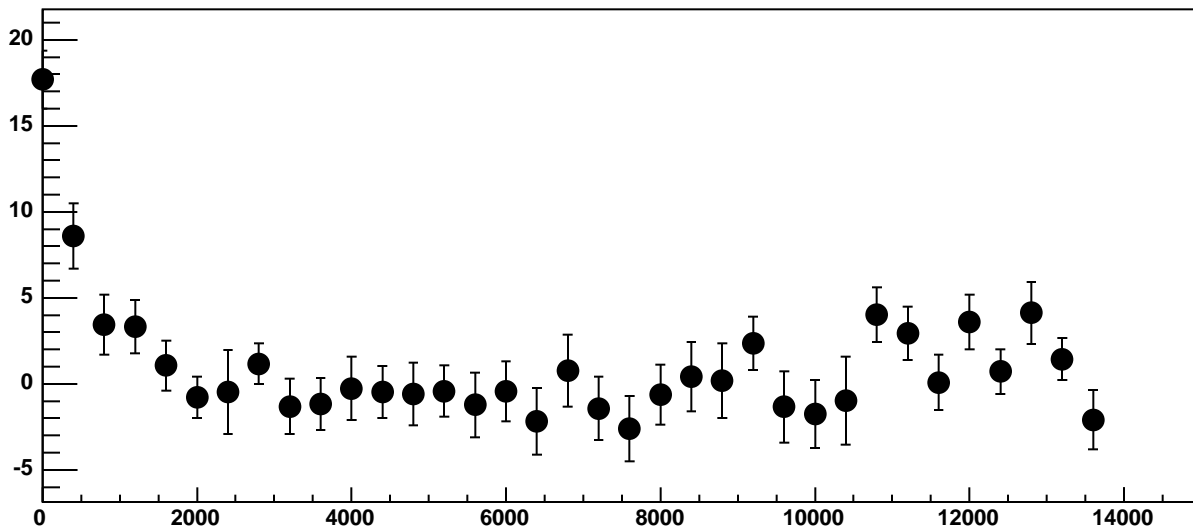


$\chi^2 / \text{ndf}$  22.58 / 23  
p0  $189.6 \pm 0.7114$   
p1  $0.01619 \pm 0.0001163$

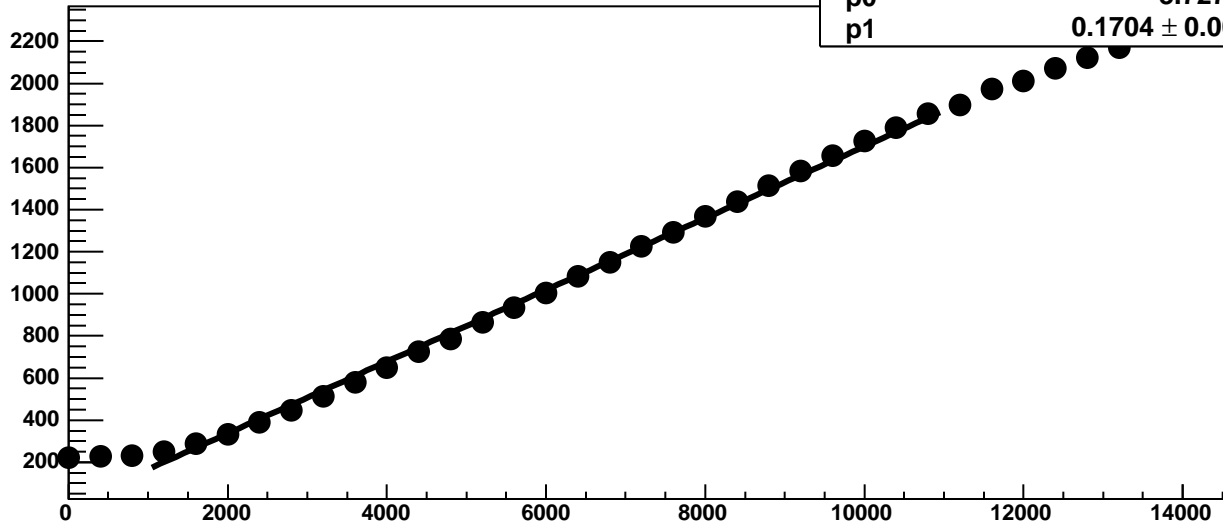
Chip 10, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

1837 / 23

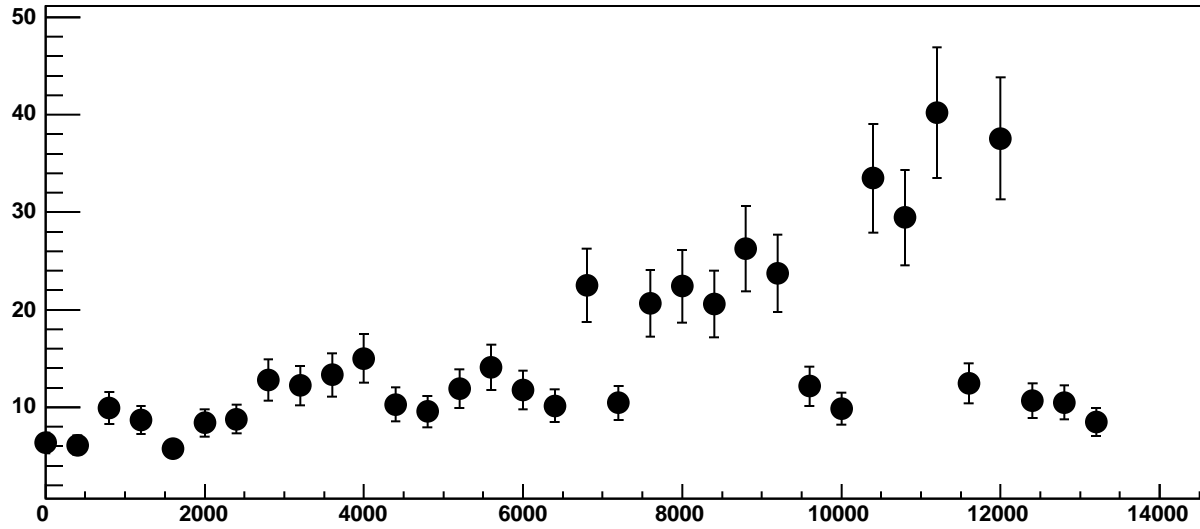
p0

$-3.727 \pm 1.01$

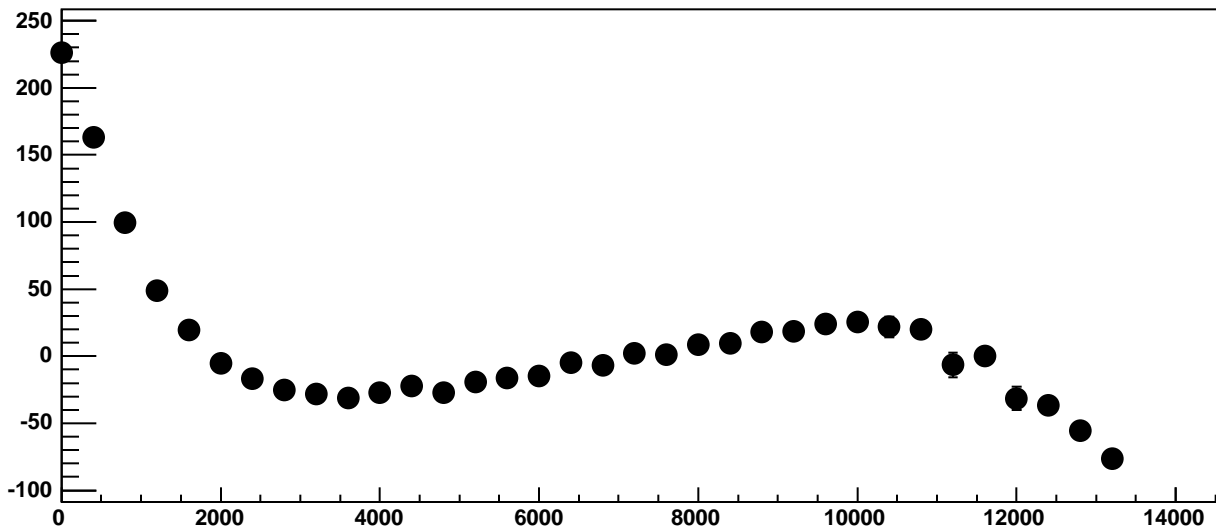
p1

$0.1704 \pm 0.0001918$

Chip 10, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

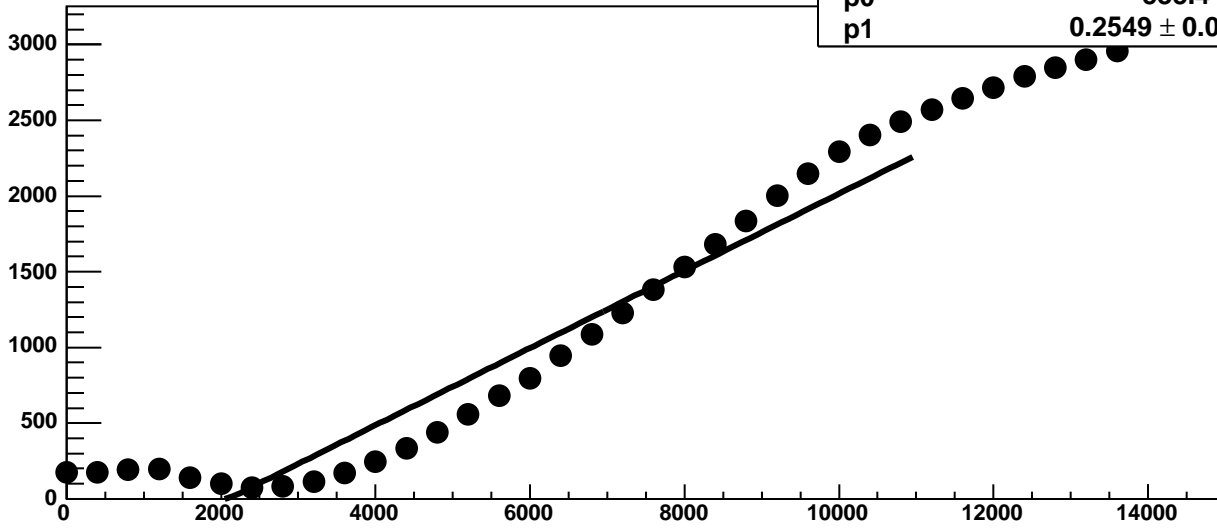


Chip 10, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

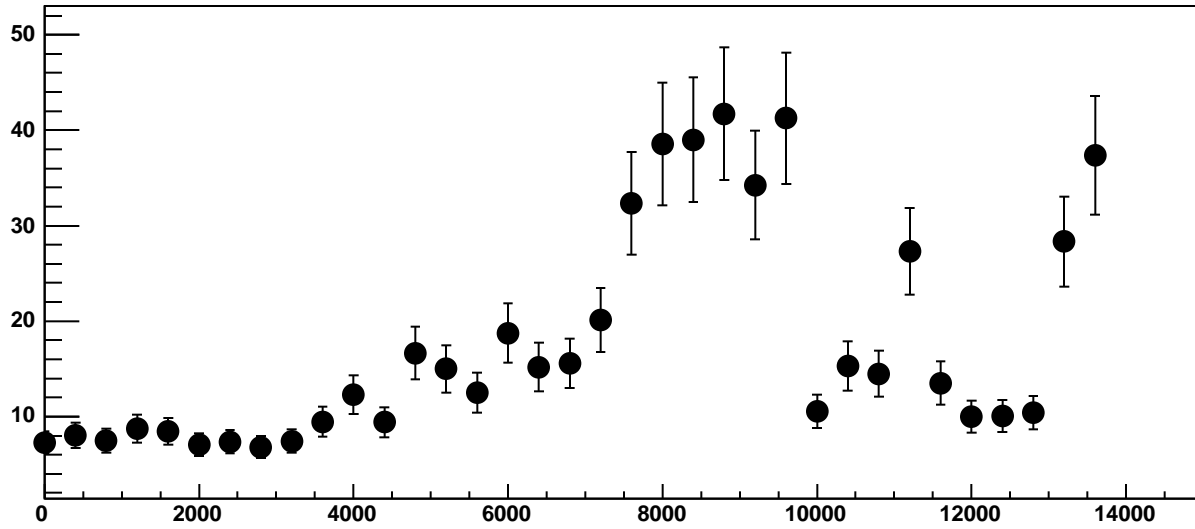


Chip 10, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

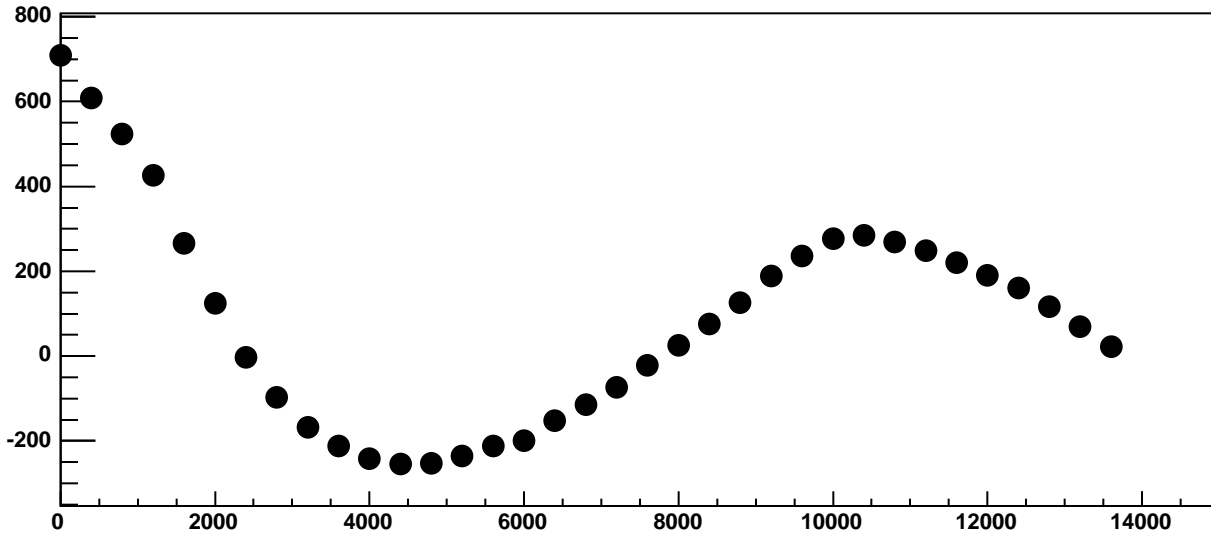
$\chi^2 / \text{ndf}$  1.619e+05 / 23  
p0 -533.4 ± 0.976  
p1 0.2549 ± 0.0002025



Chip 10, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

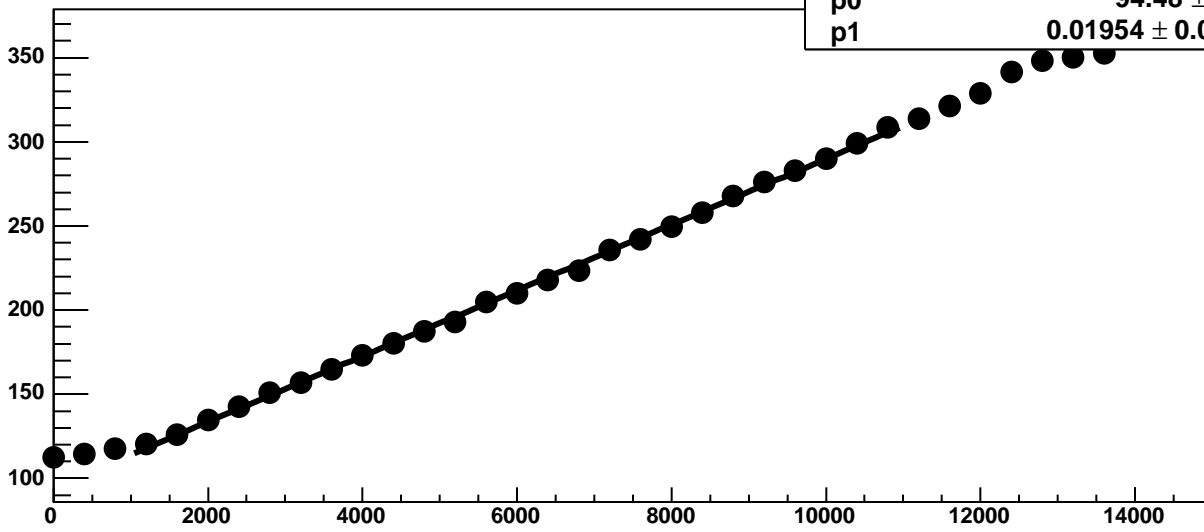


Chip 10, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

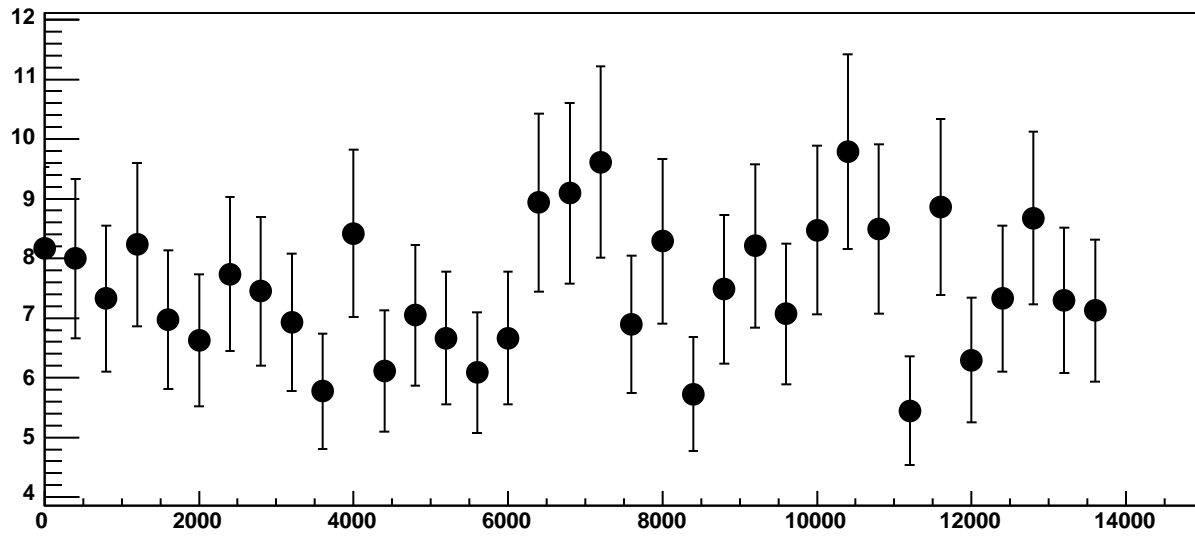




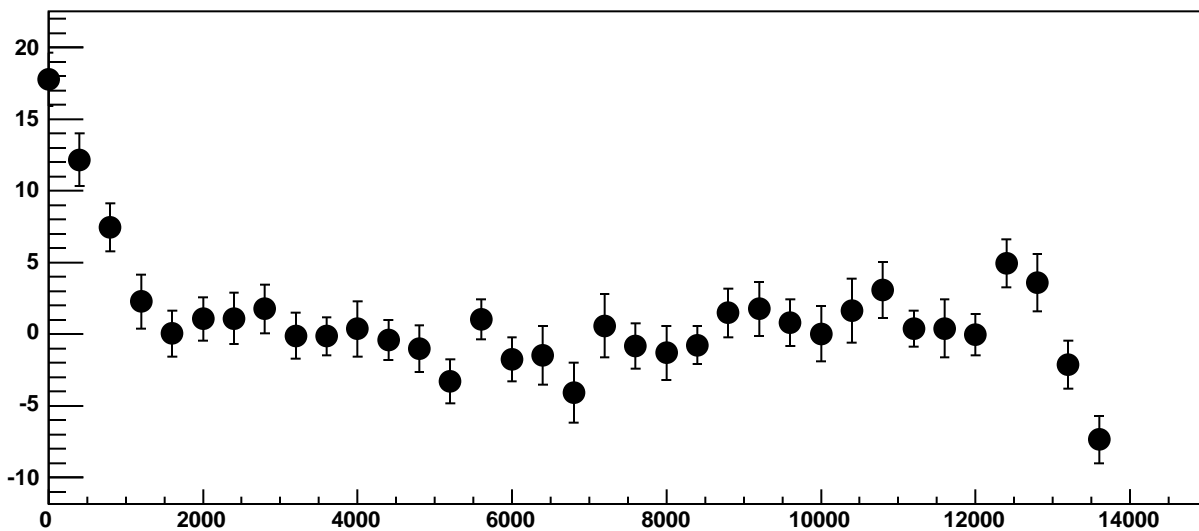
Chip 10, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



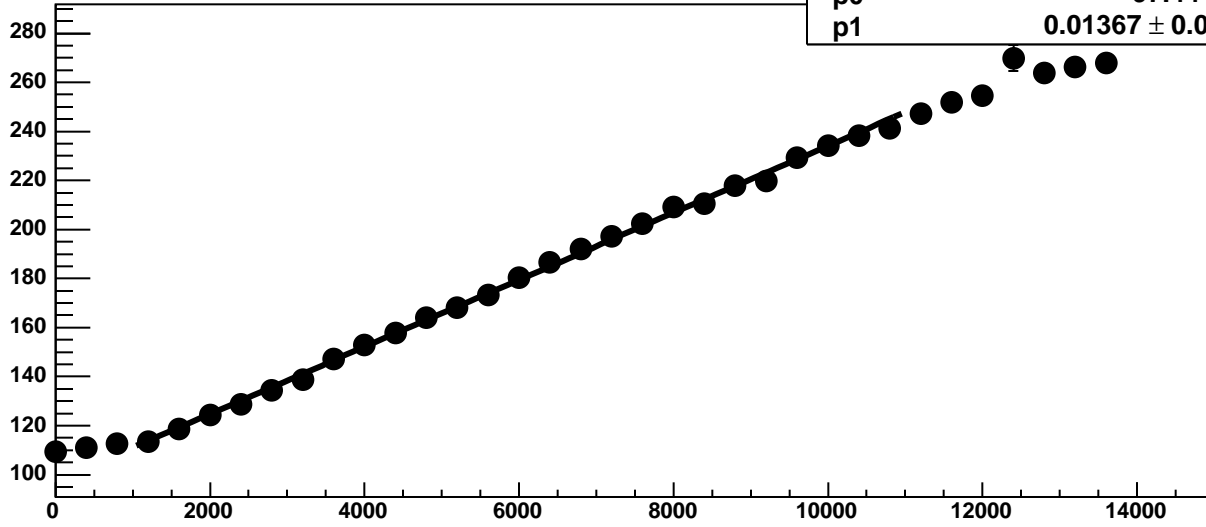
Chip 10, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



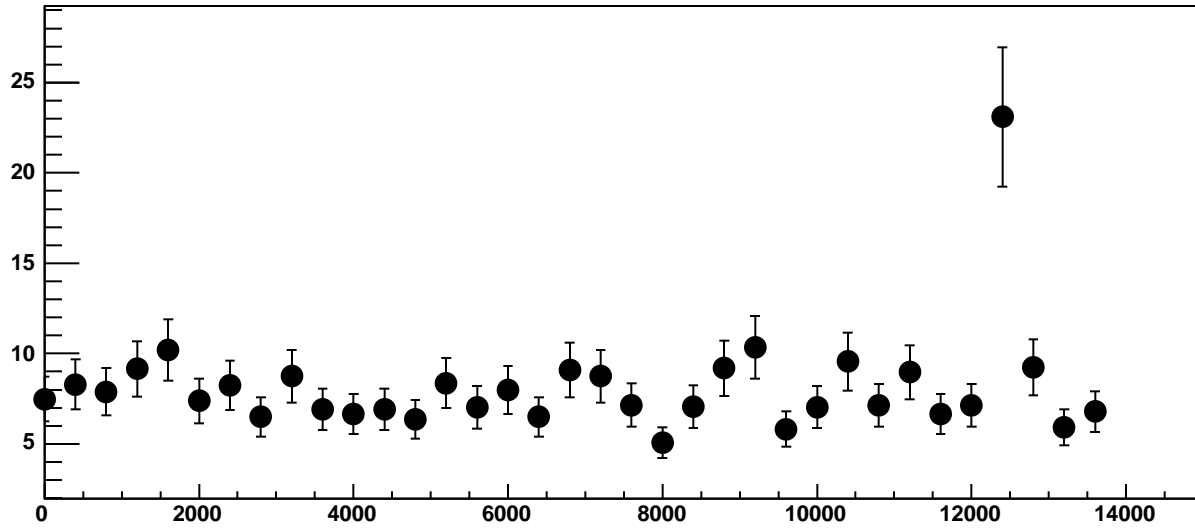
Chip 10, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



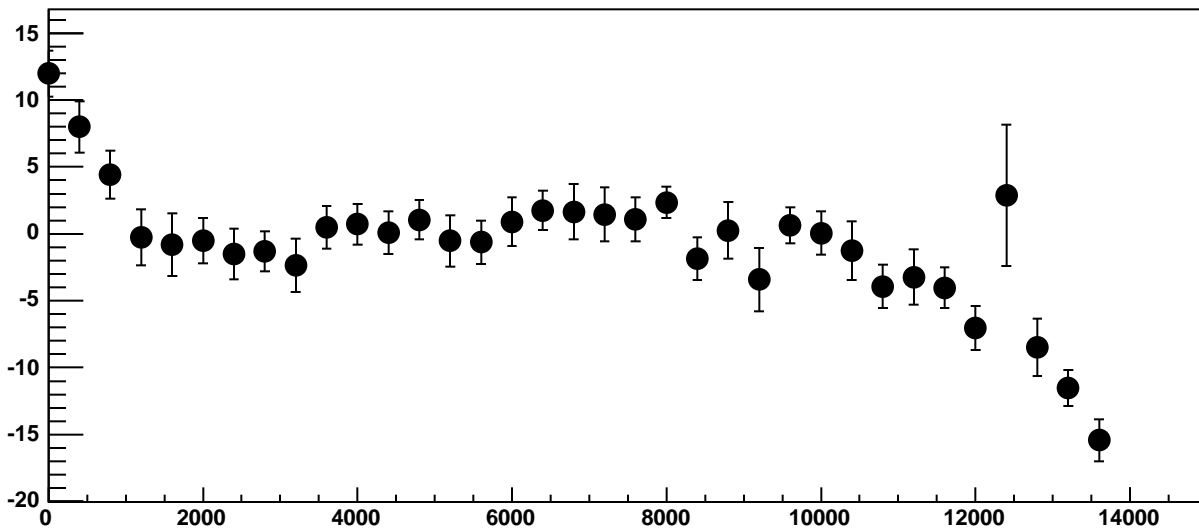
Chip 10, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



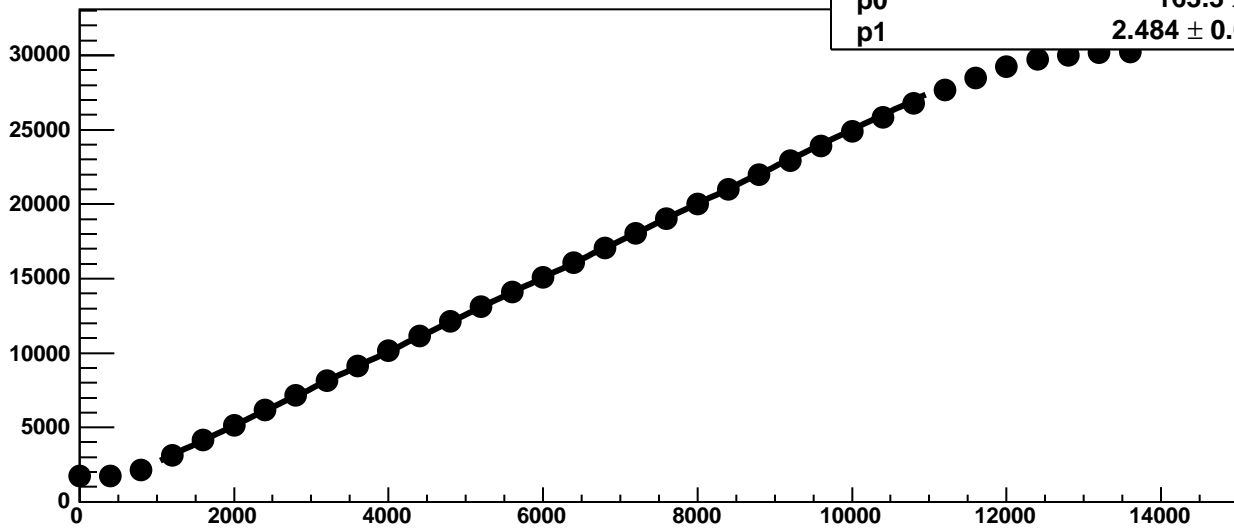
Chip 10, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

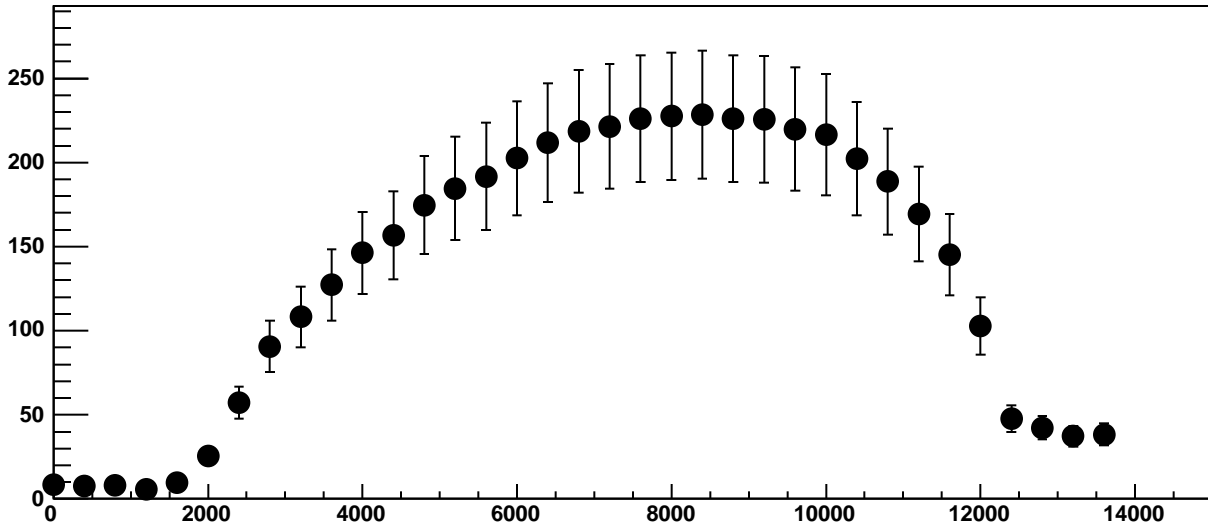


Chip 10, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC

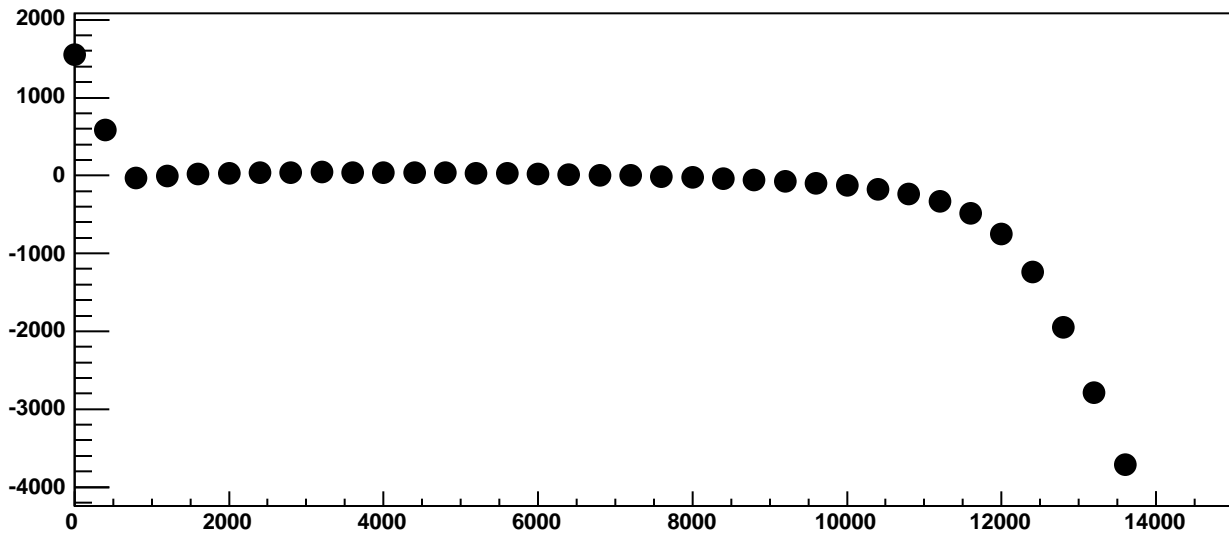


$\chi^2 / \text{ndf}$	206.6 / 23
p0	$163.3 \pm 2.548$
p1	$2.484 \pm 0.001628$

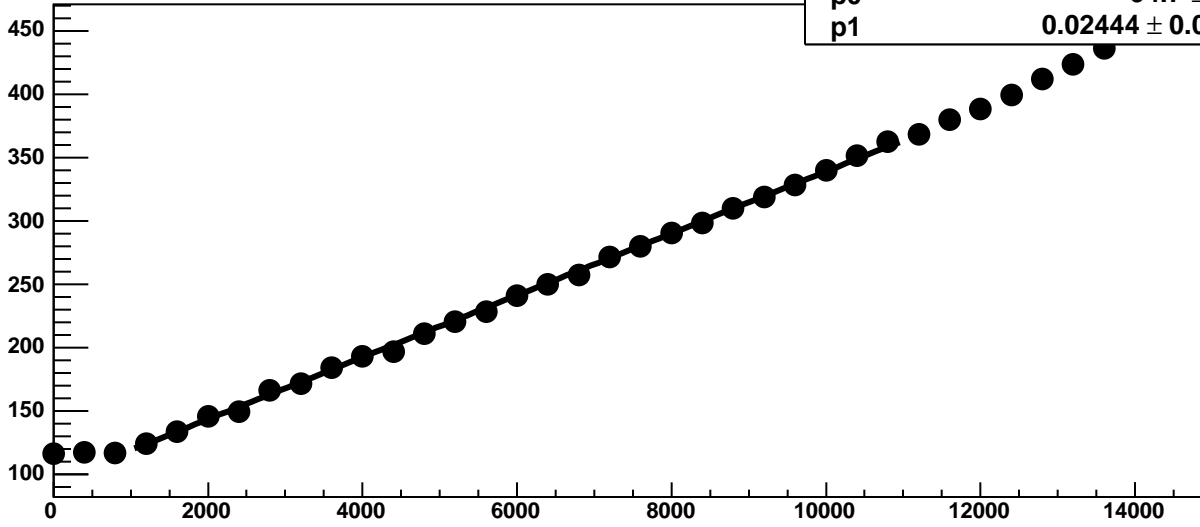
Chip 10, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC

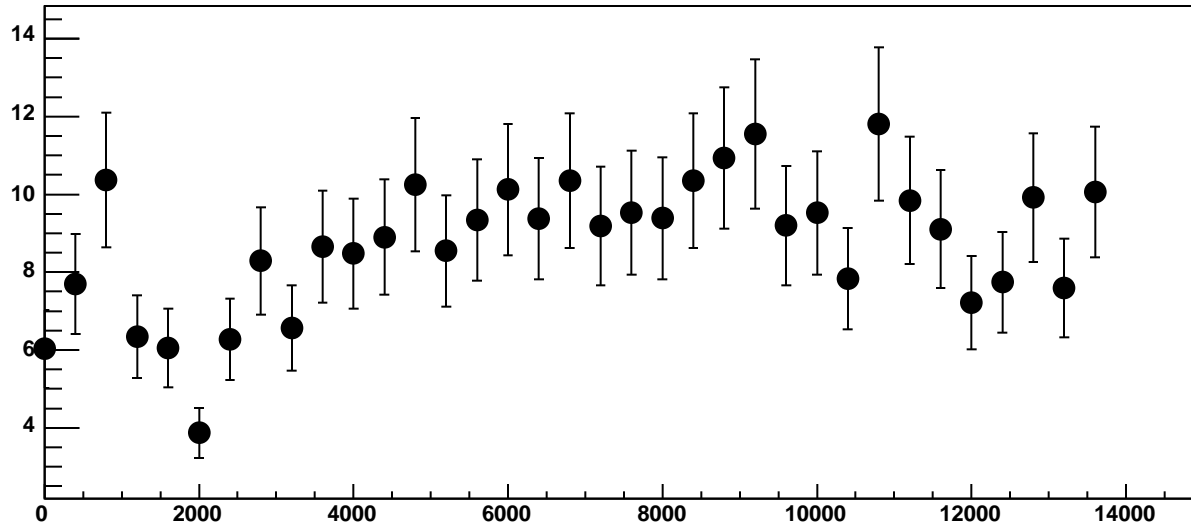


Chip 10, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

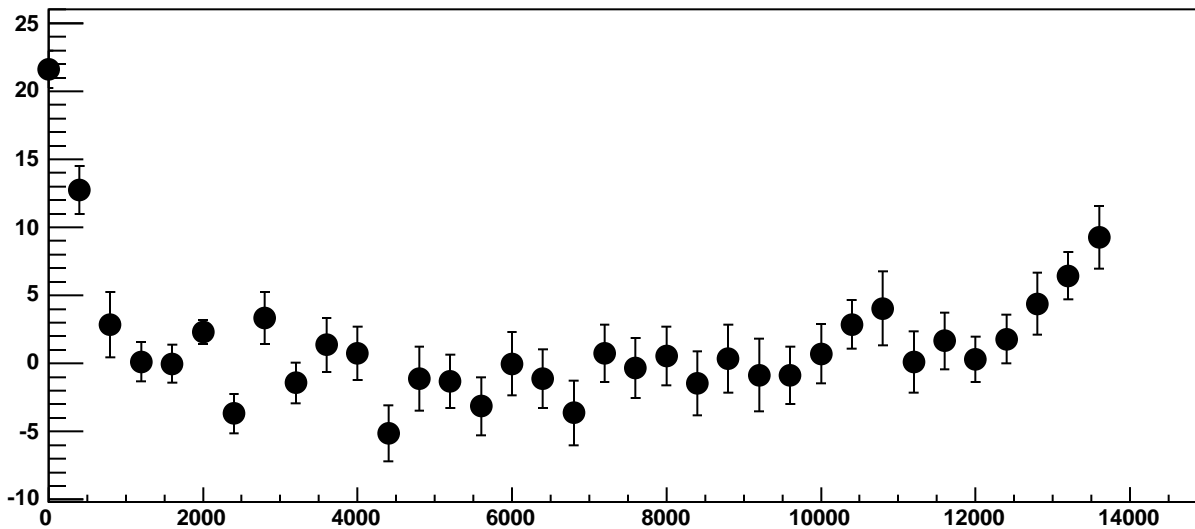


$\chi^2 / \text{ndf}$  35.42 / 23  
p0  $94.7 \pm 0.6877$   
p1  $0.02444 \pm 0.0001223$

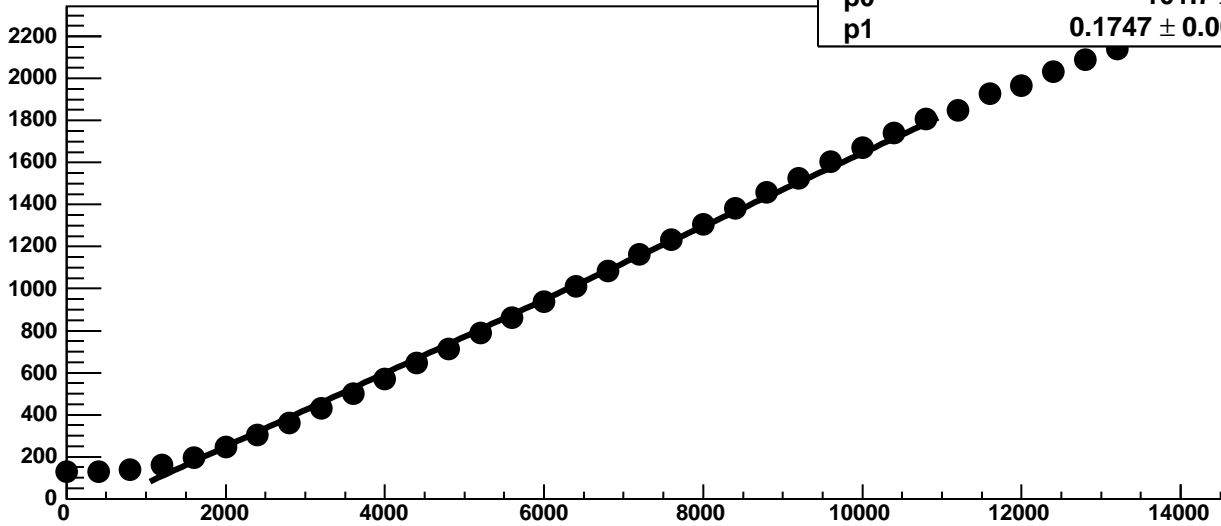
Chip 10, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

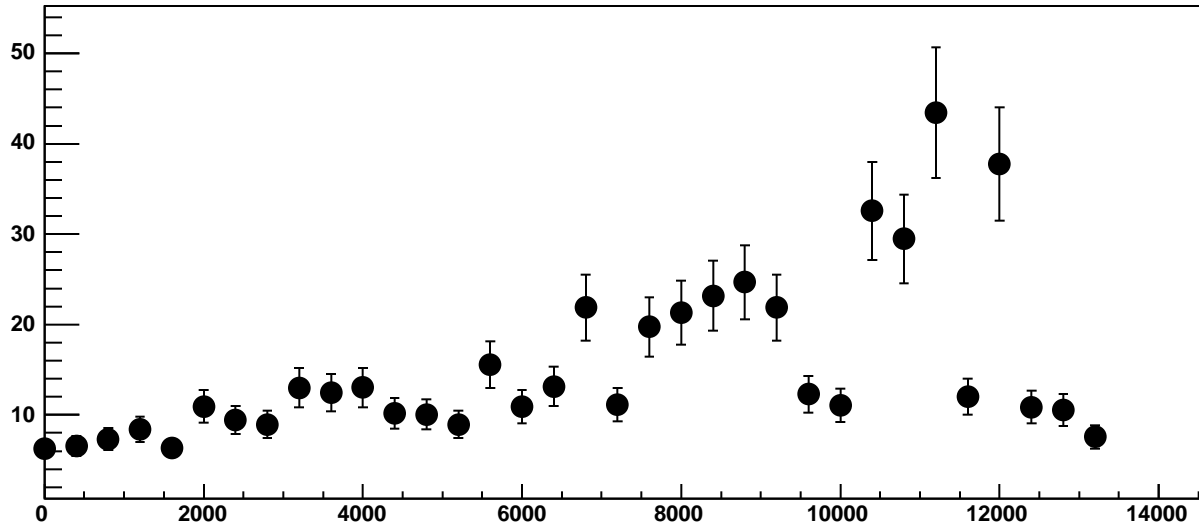


Chip 10, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

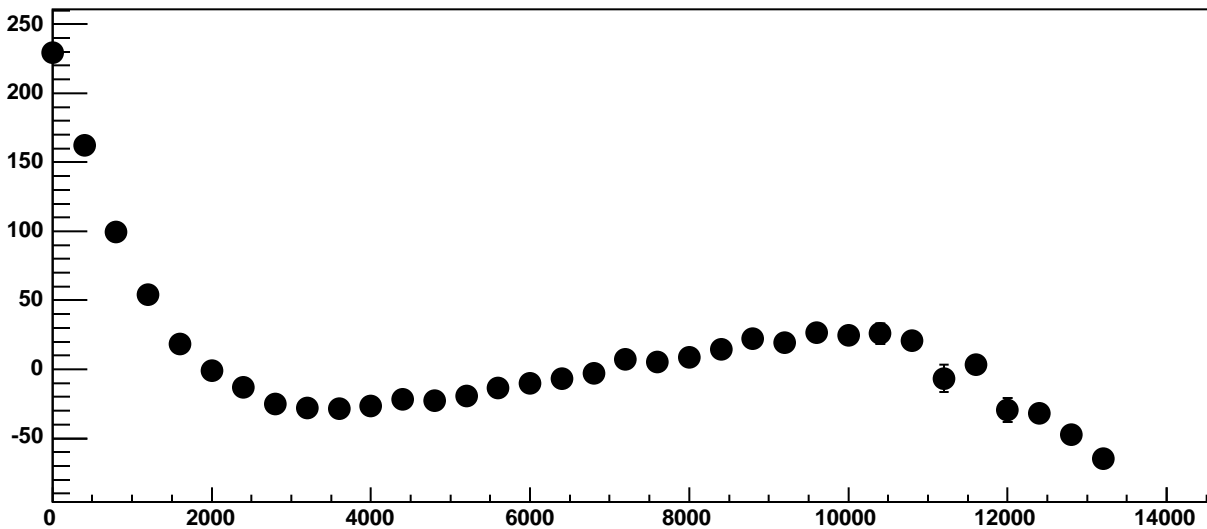


$\chi^2 / \text{ndf}$  1968 / 23  
p0  $-101.7 \pm 1.055$   
p1  $0.1747 \pm 0.0002017$

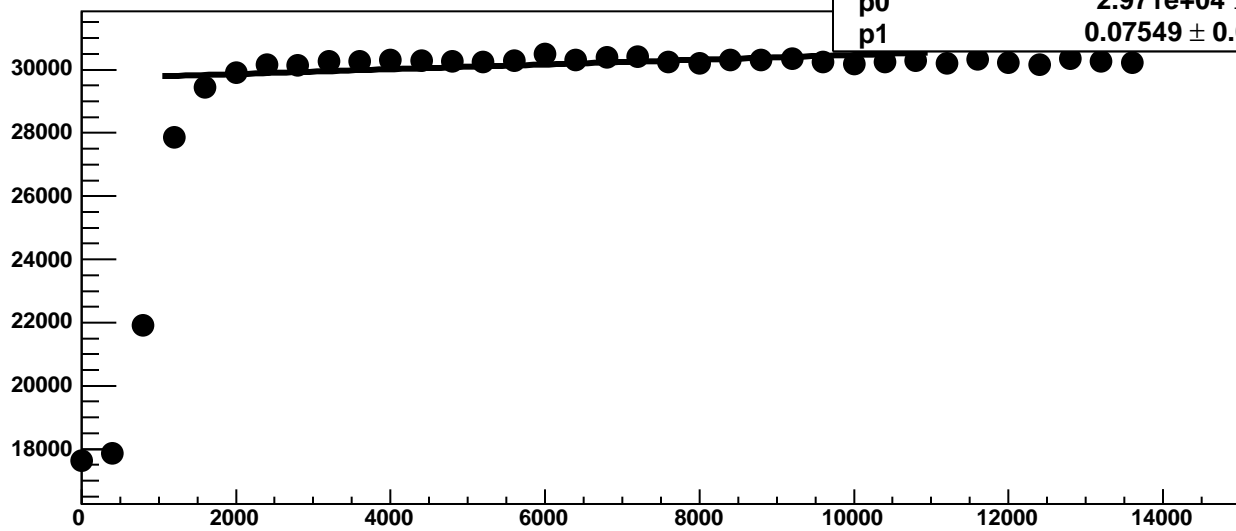
Chip 10, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



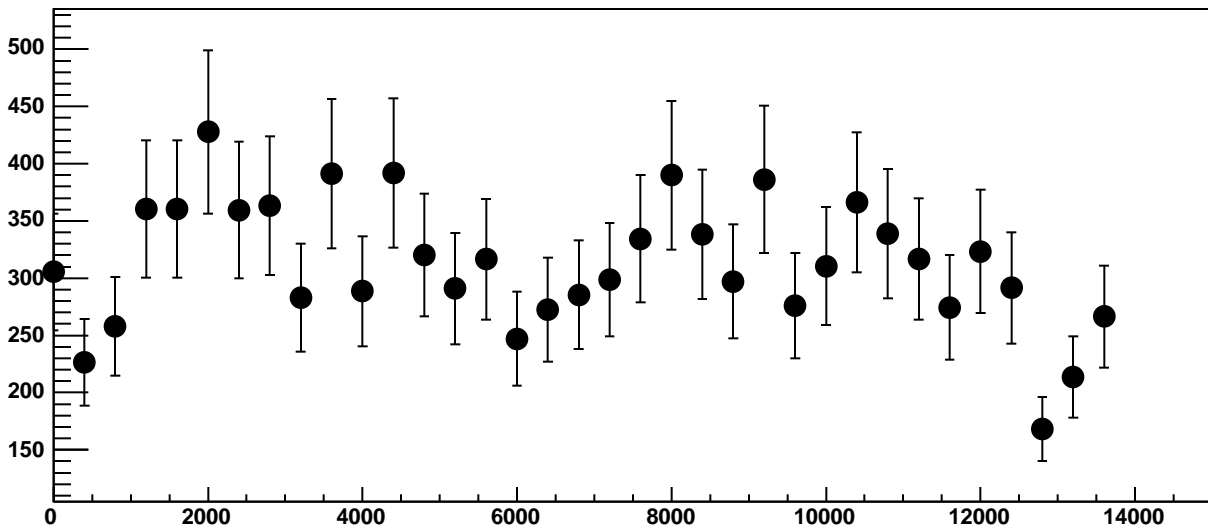
Chip 10, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



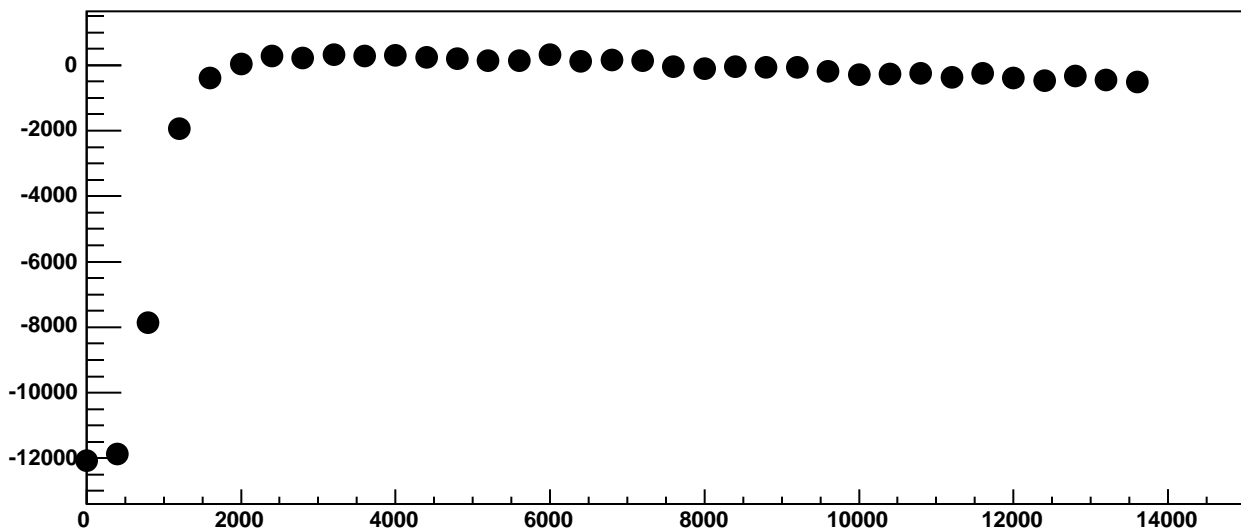
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



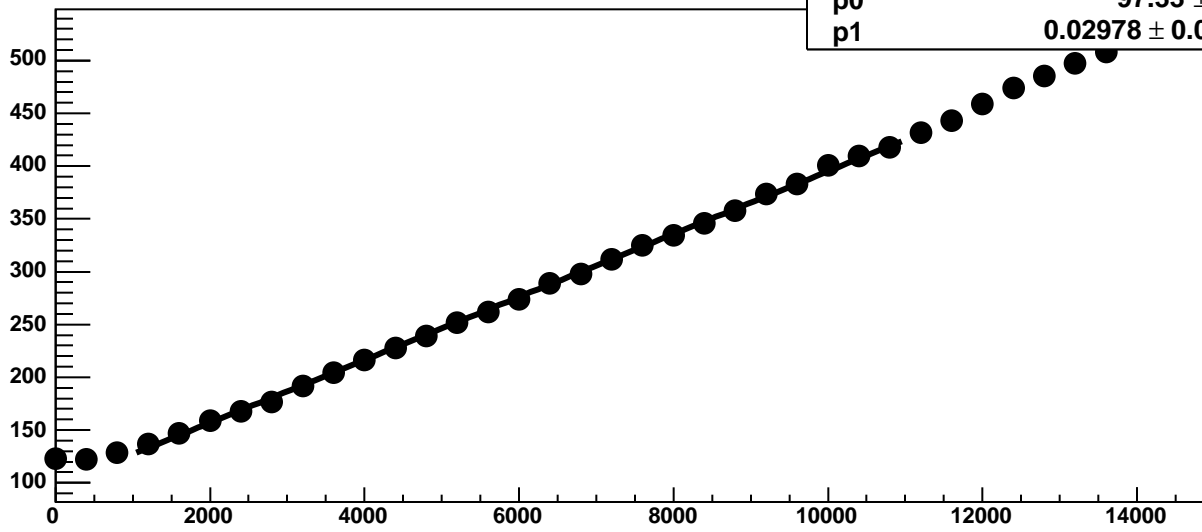
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

36.85 / 23

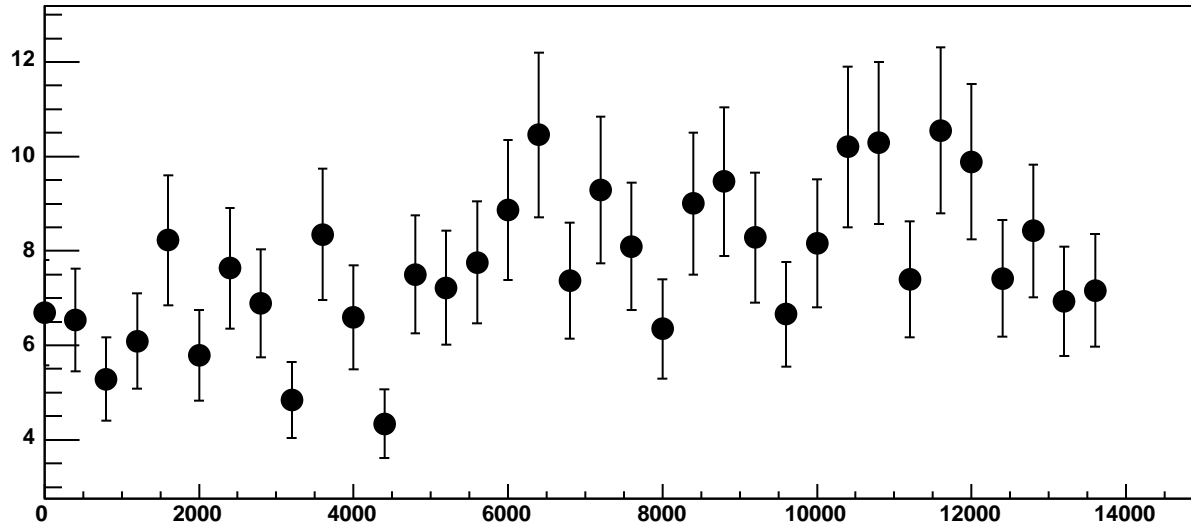
p0

$97.33 \pm 0.7154$

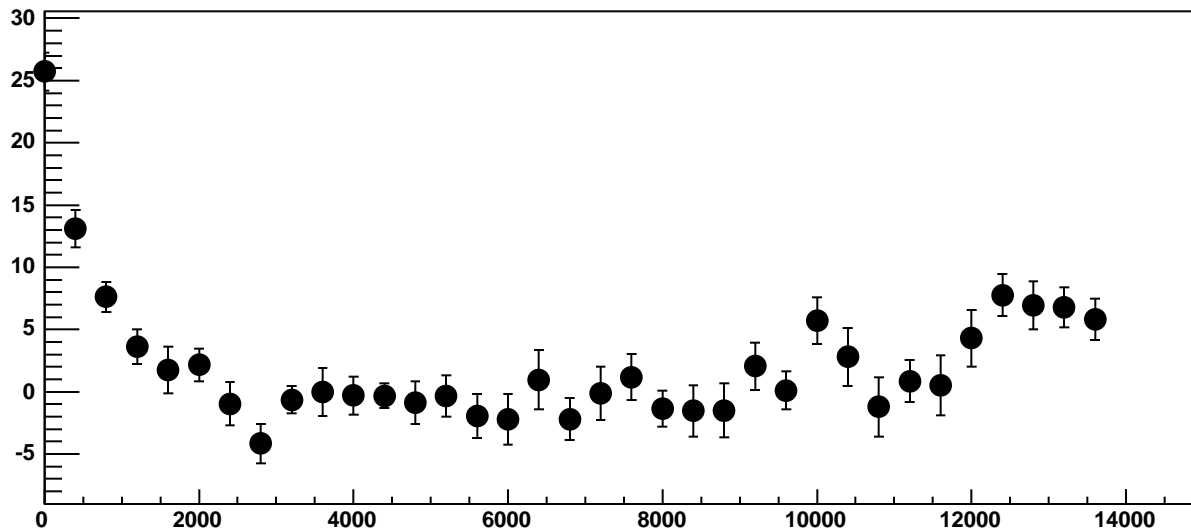
p1

$0.02978 \pm 0.0001193$

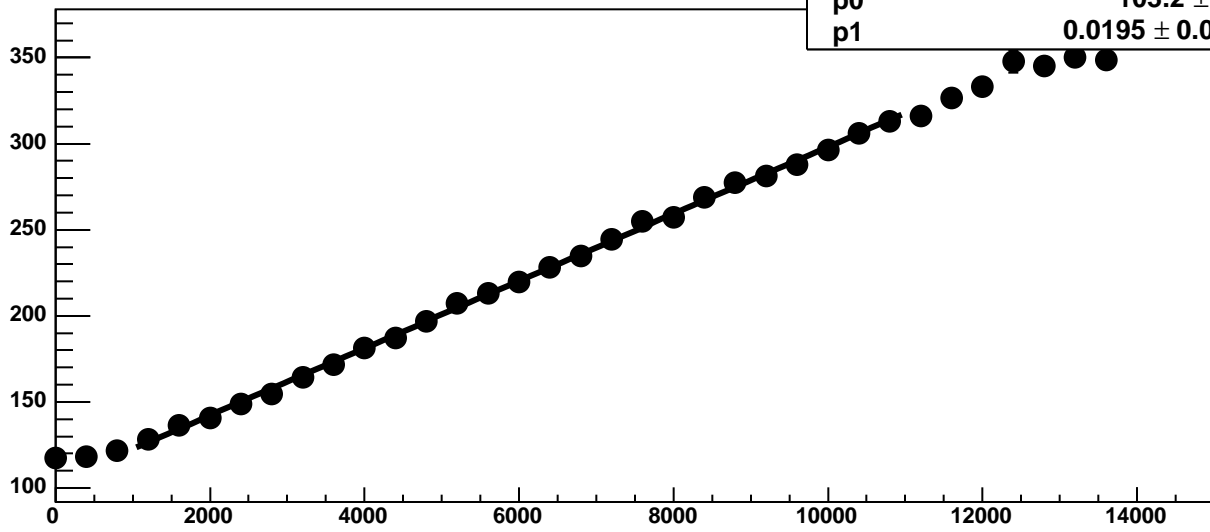
Chip 10, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



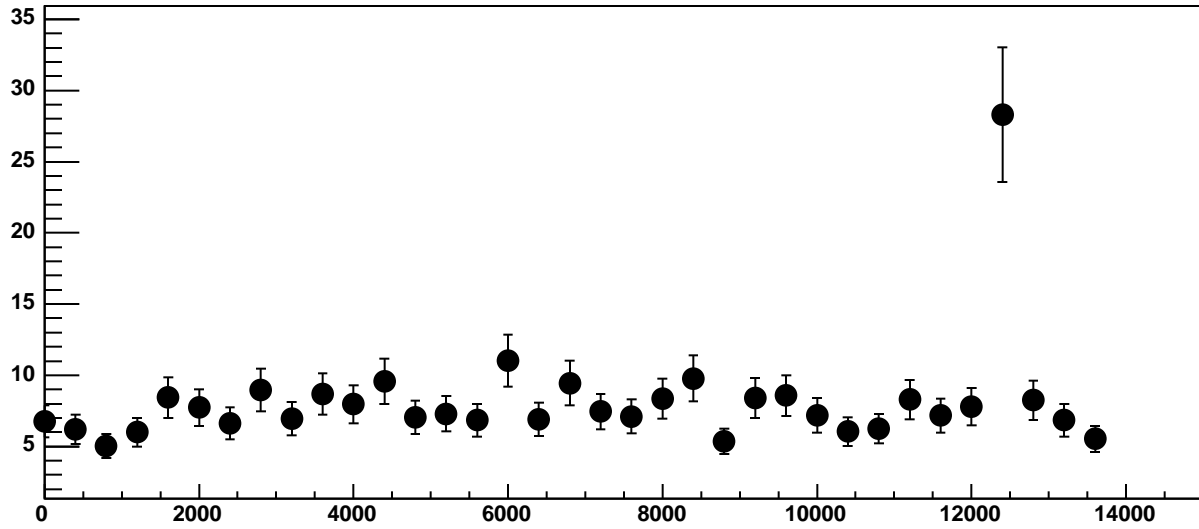
Chip 10, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



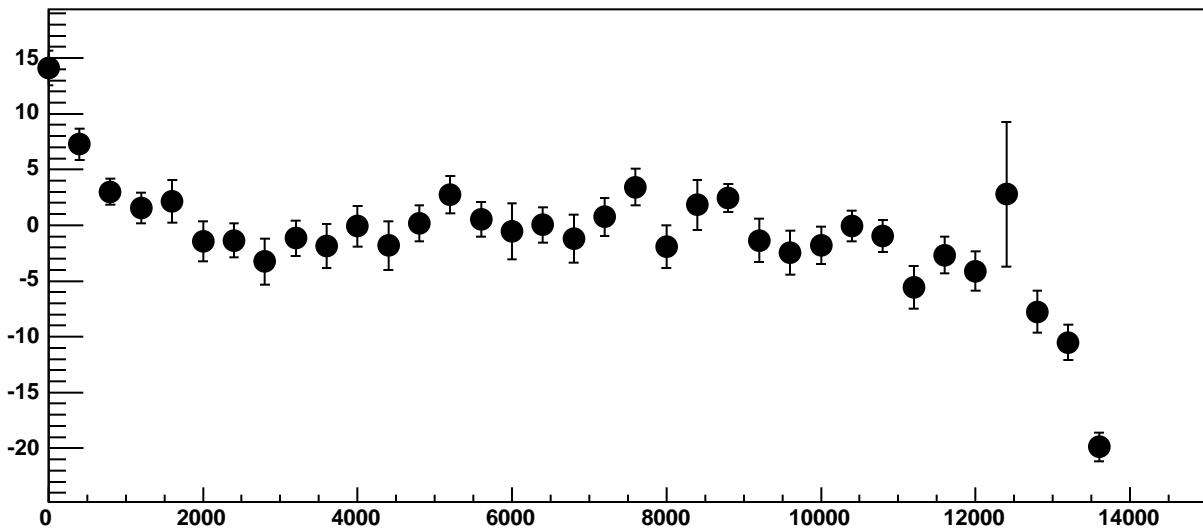
Chip 10, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



Chip 10, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

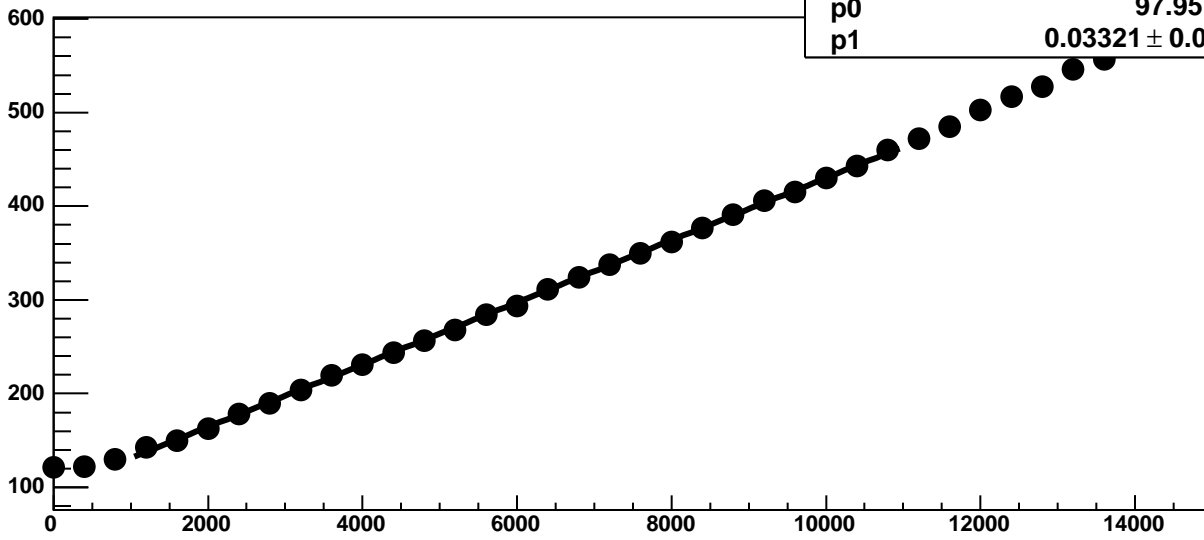


Chip 10, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



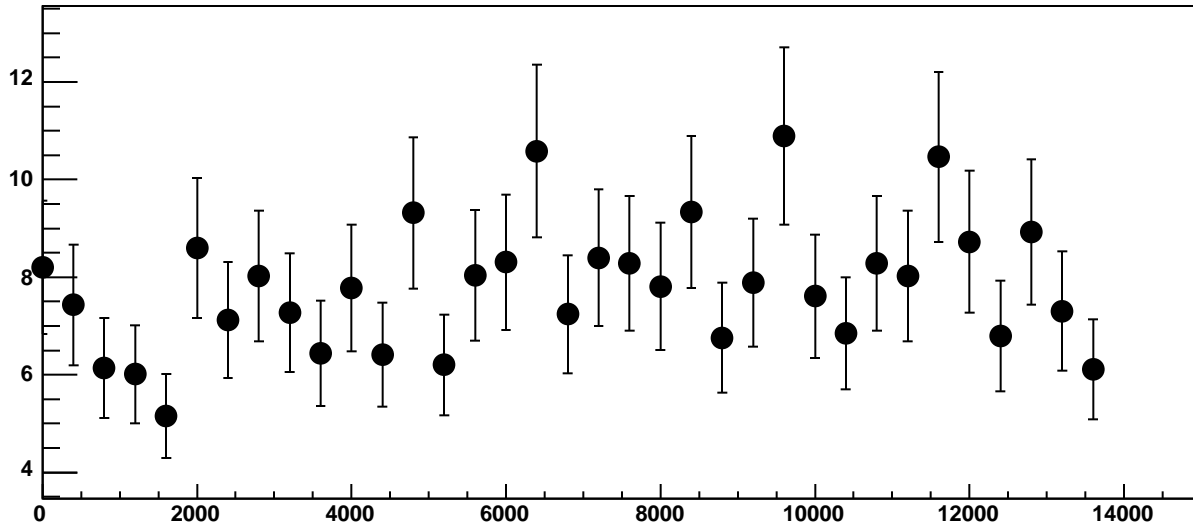


Chip 10, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

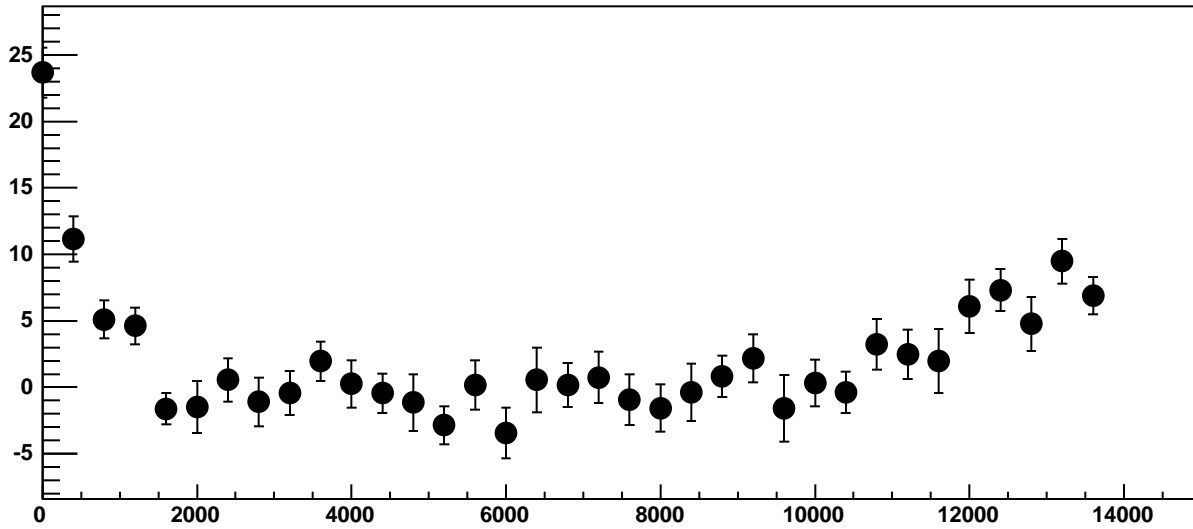


$\chi^2 / \text{ndf}$  29.9 / 23  
p0  $97.95 \pm 0.721$   
p1  $0.03321 \pm 0.0001143$

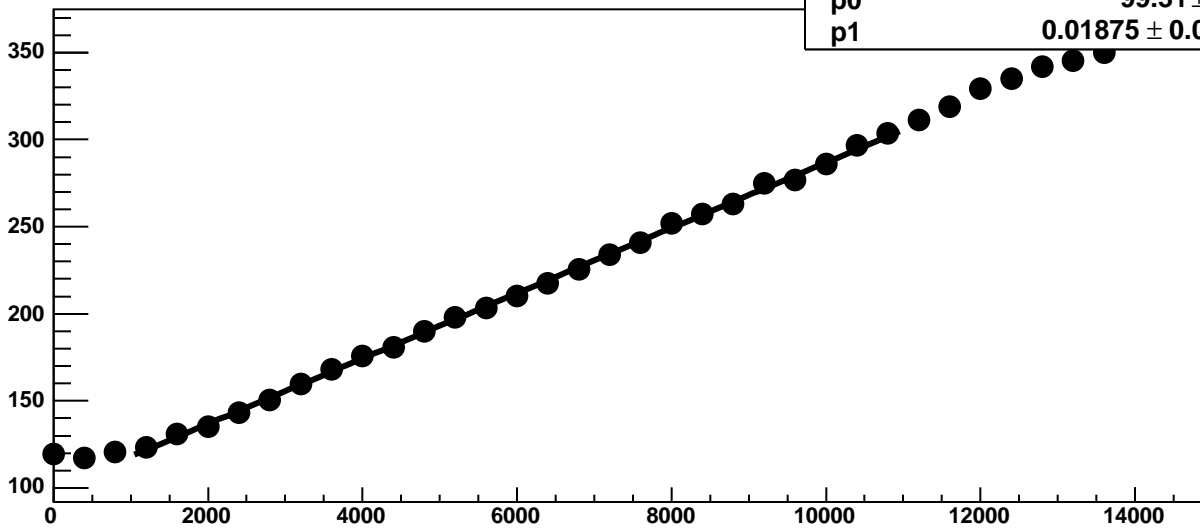
Chip 10, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

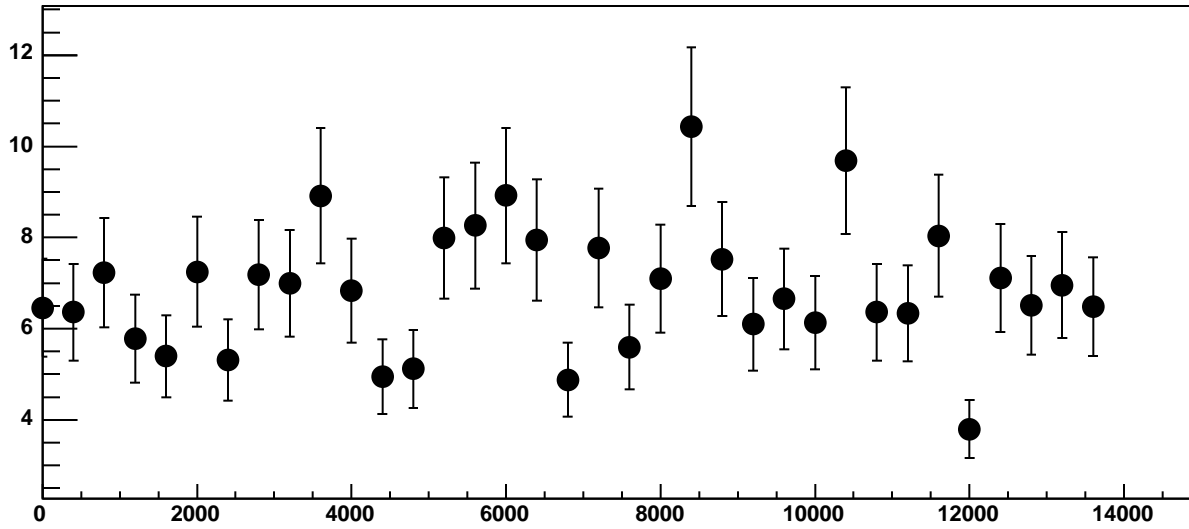


Chip 10, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

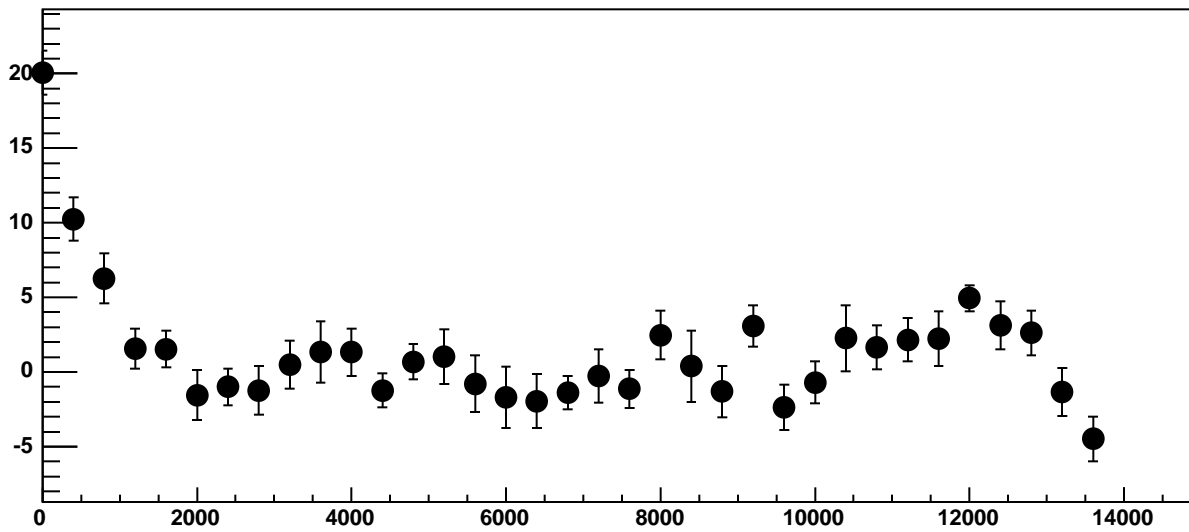


$\chi^2 / \text{ndf}$  25.16 / 23  
p0  $99.31 \pm 0.6638$   
p1  $0.01875 \pm 0.0001033$

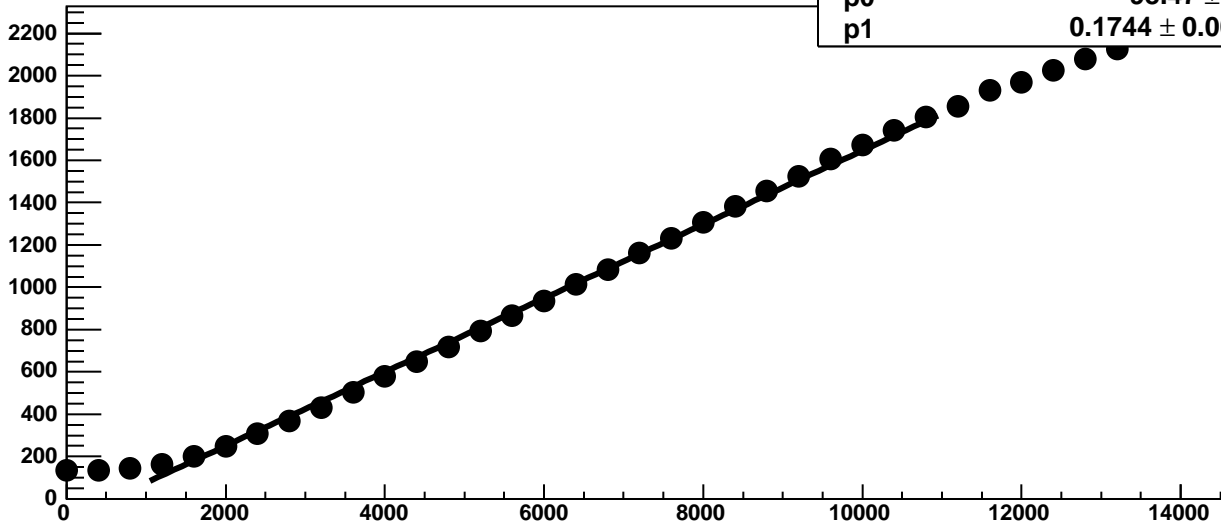
Chip 10, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



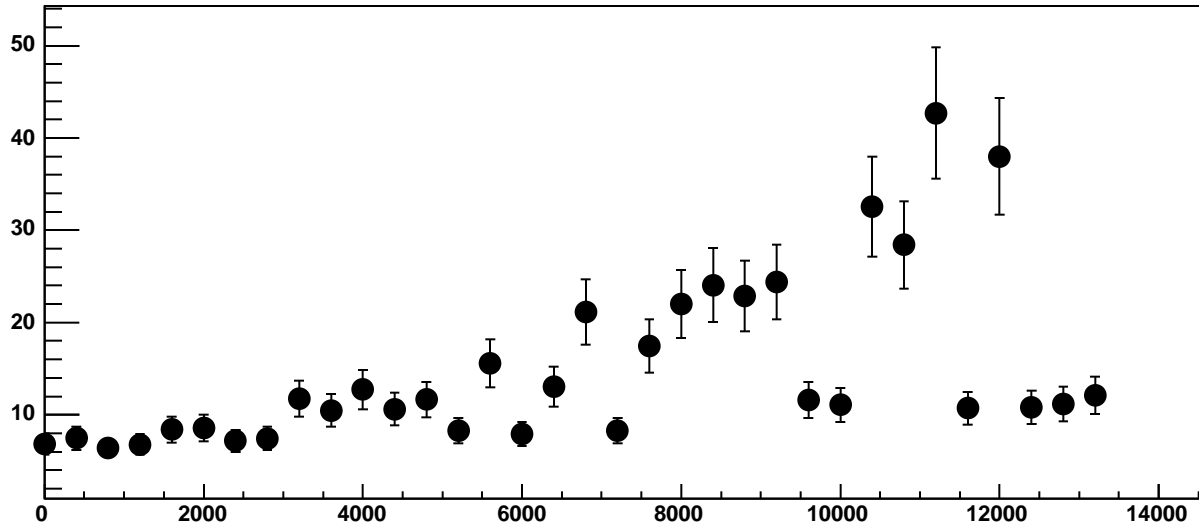
Chip 10, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



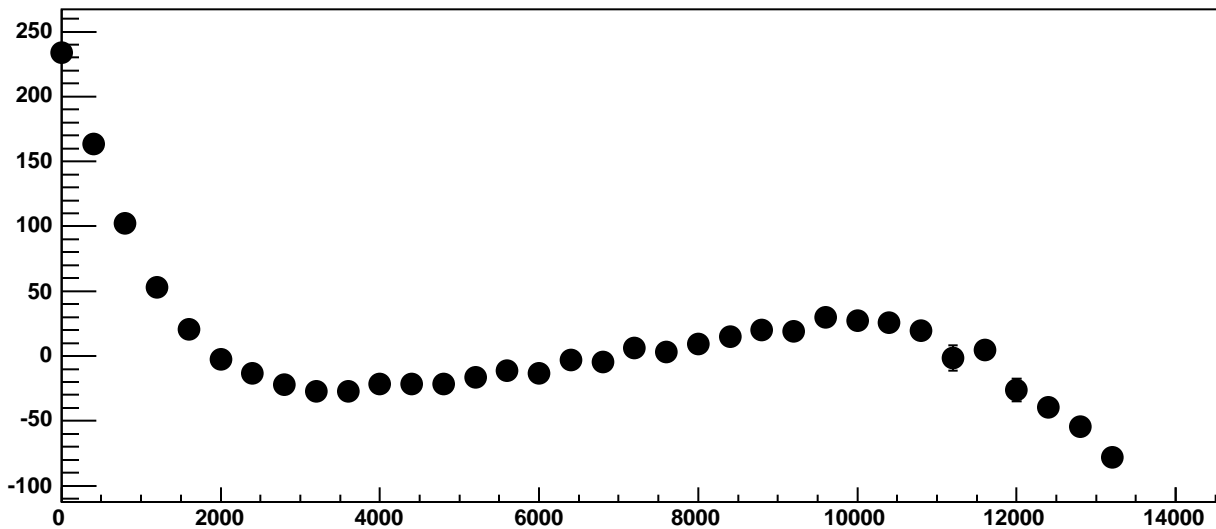
Chip 10, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



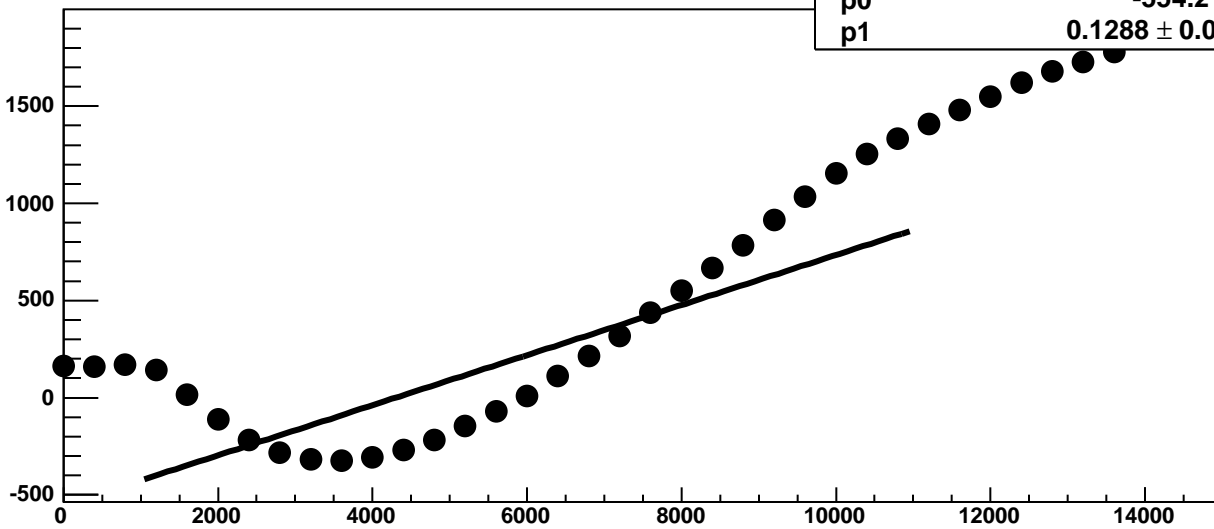
Chip 10, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



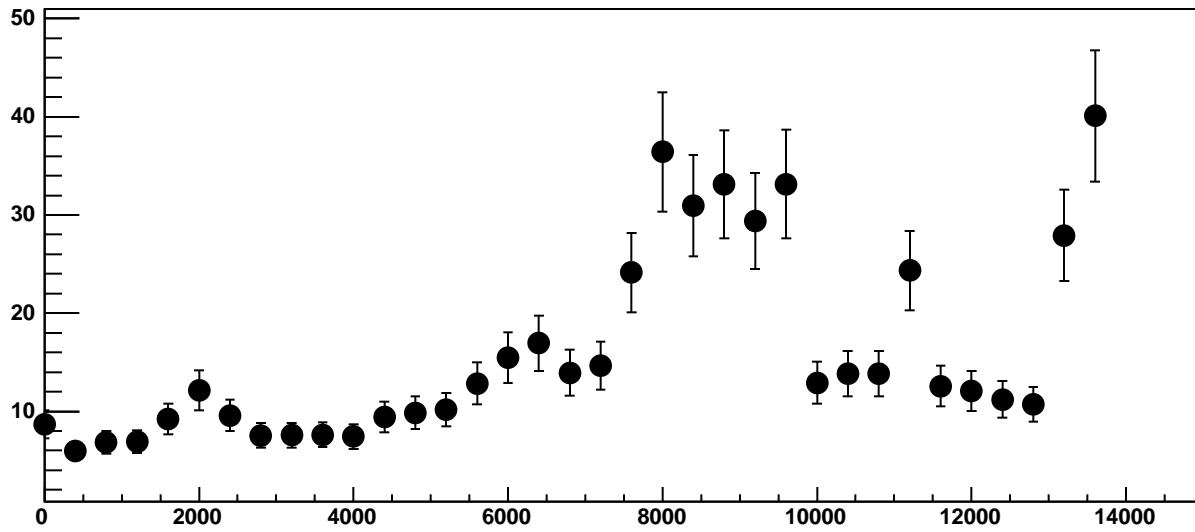
Chip 10, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



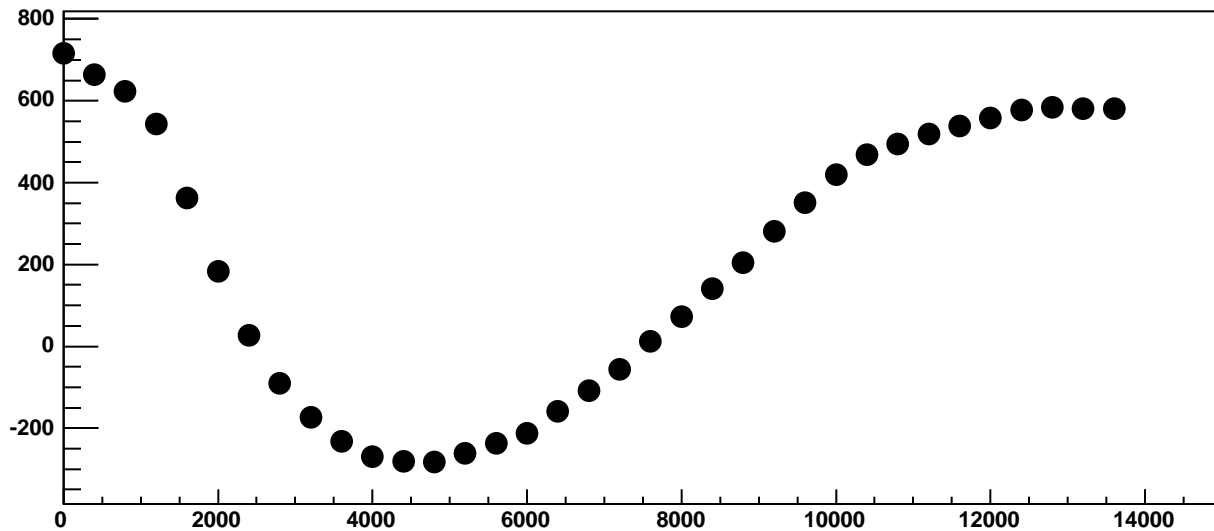
Chip 10, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



Chip 10, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

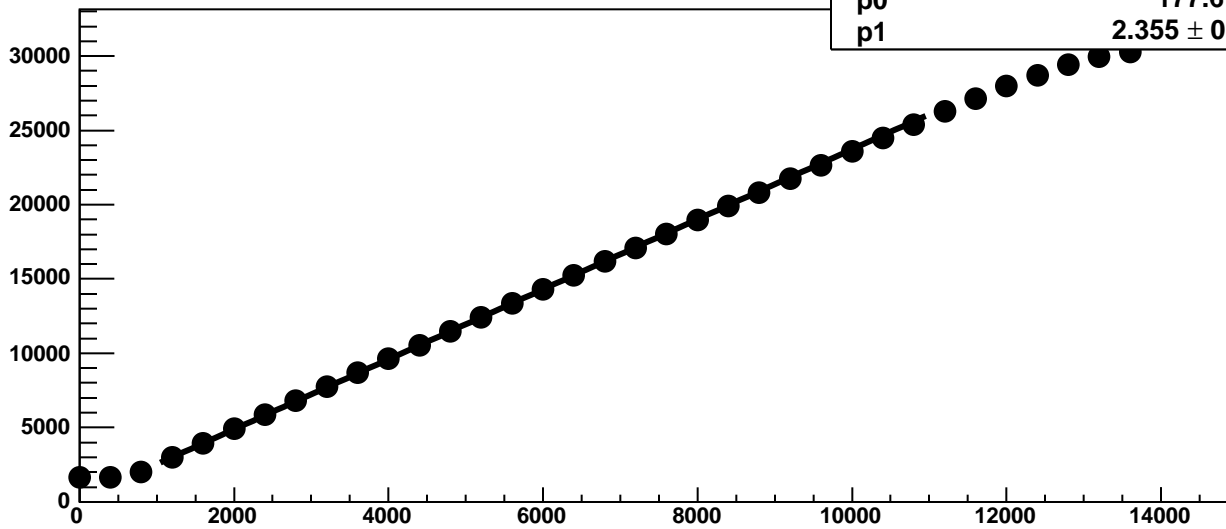


Chip 10, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

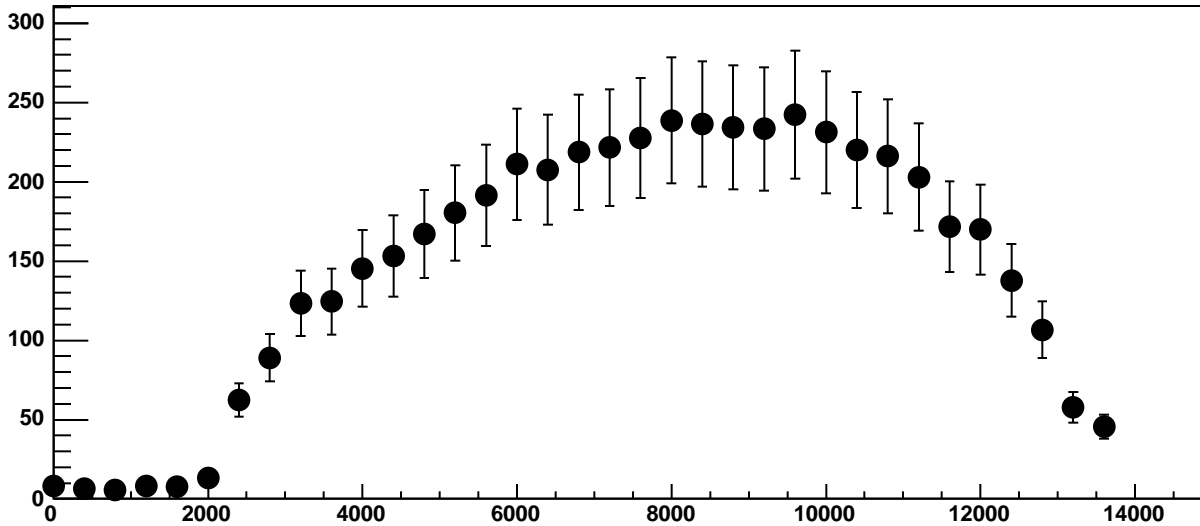


Chip 10, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

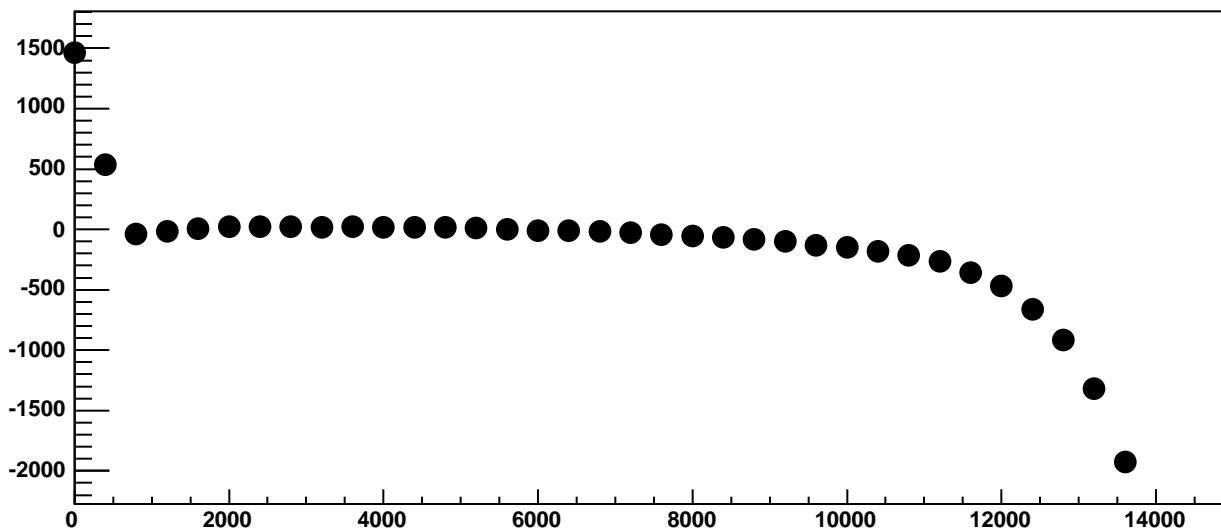
$\chi^2 / \text{ndf}$  203.3 / 23  
p0  $177.6 \pm 2.934$   
p1  $2.355 \pm 0.001705$



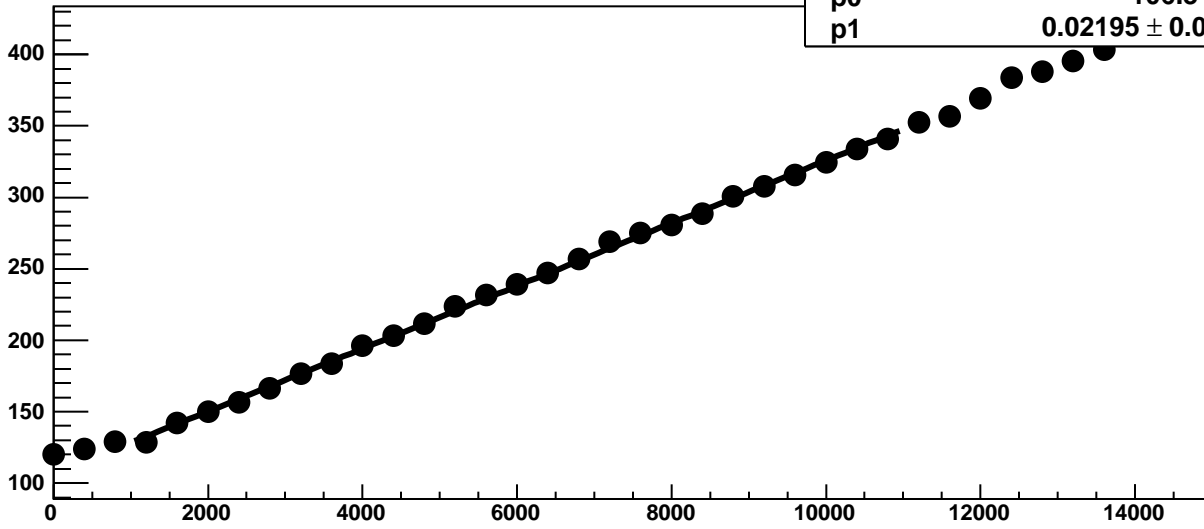
Chip 10, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

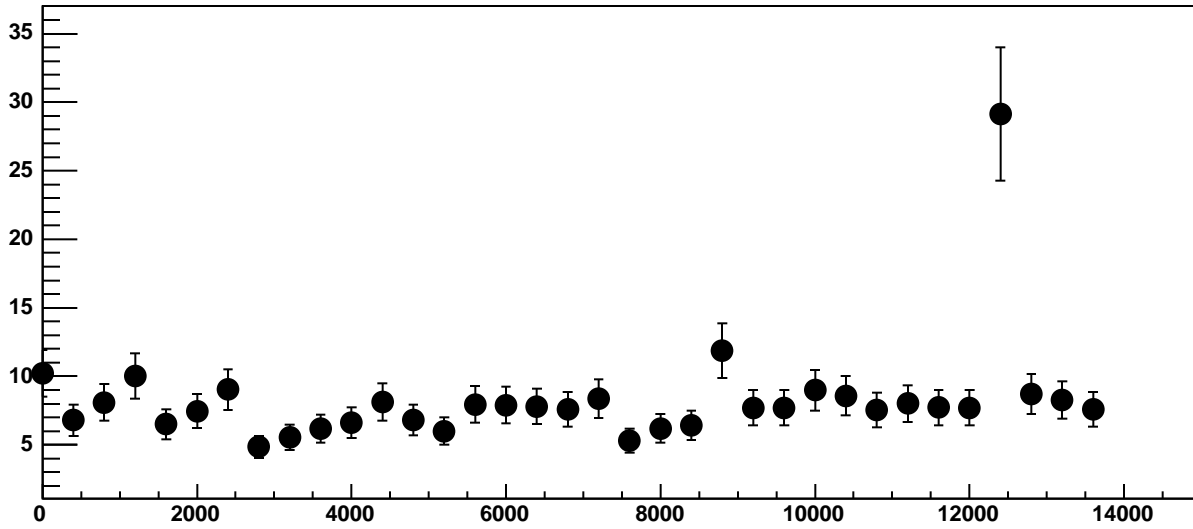


Chip 10, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

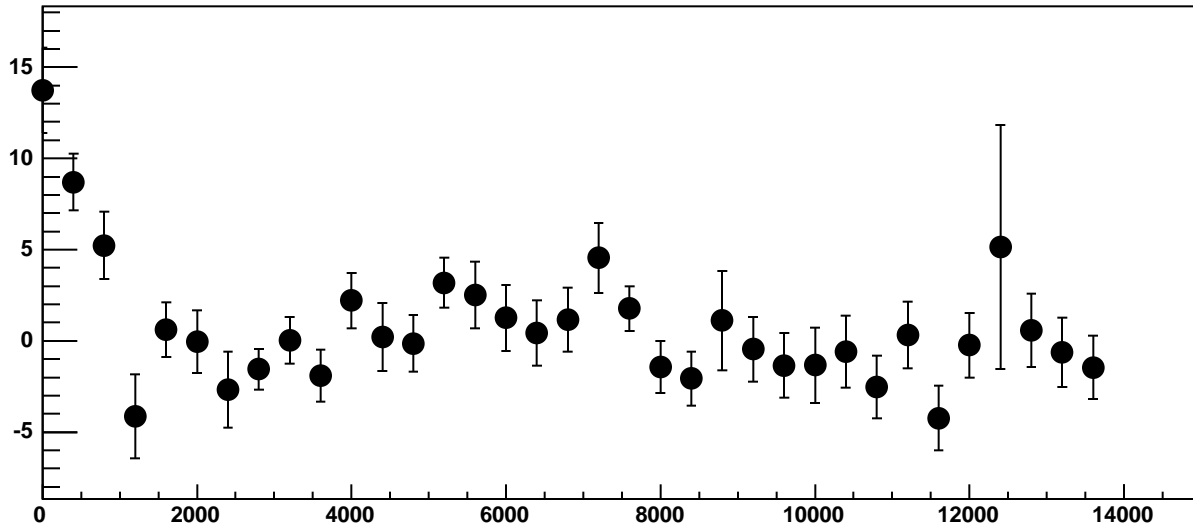


$\chi^2 / \text{ndf}$  33.34 / 23  
p0 106.3 ± 0.745  
p1 0.02195 ± 0.0001177

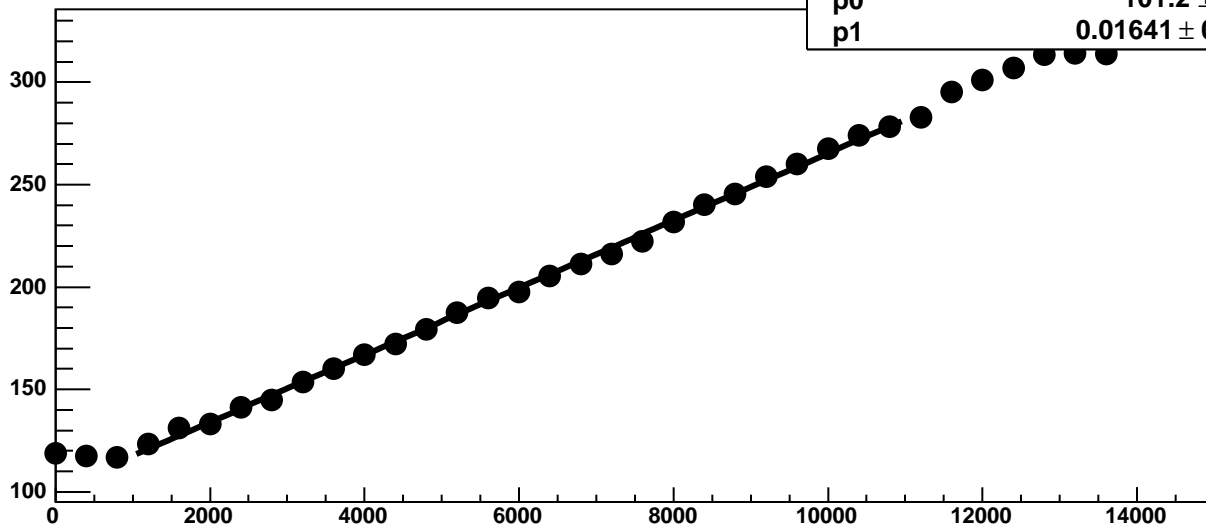
Chip 10, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

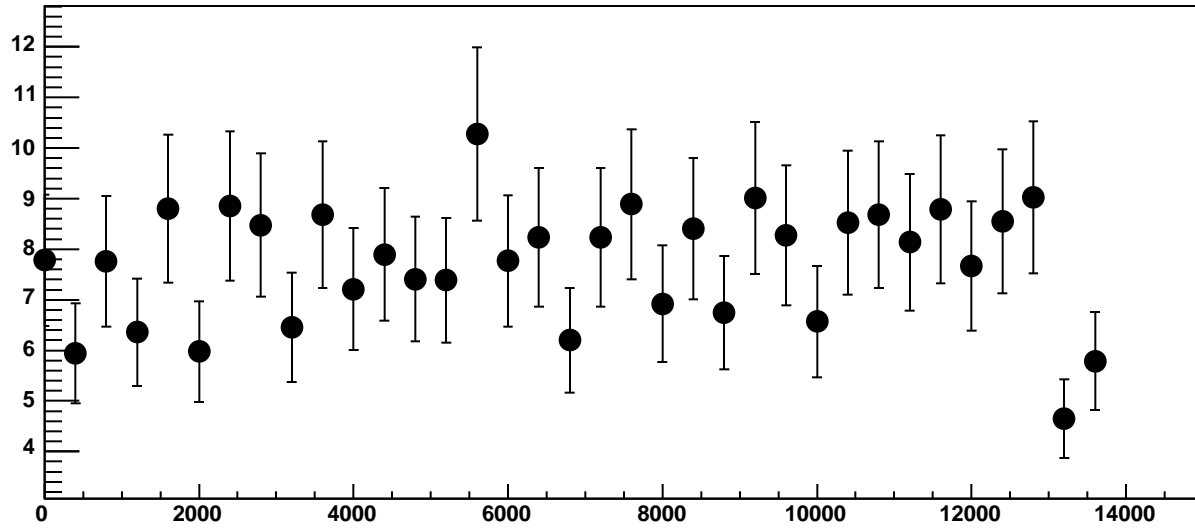


Chip 10, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

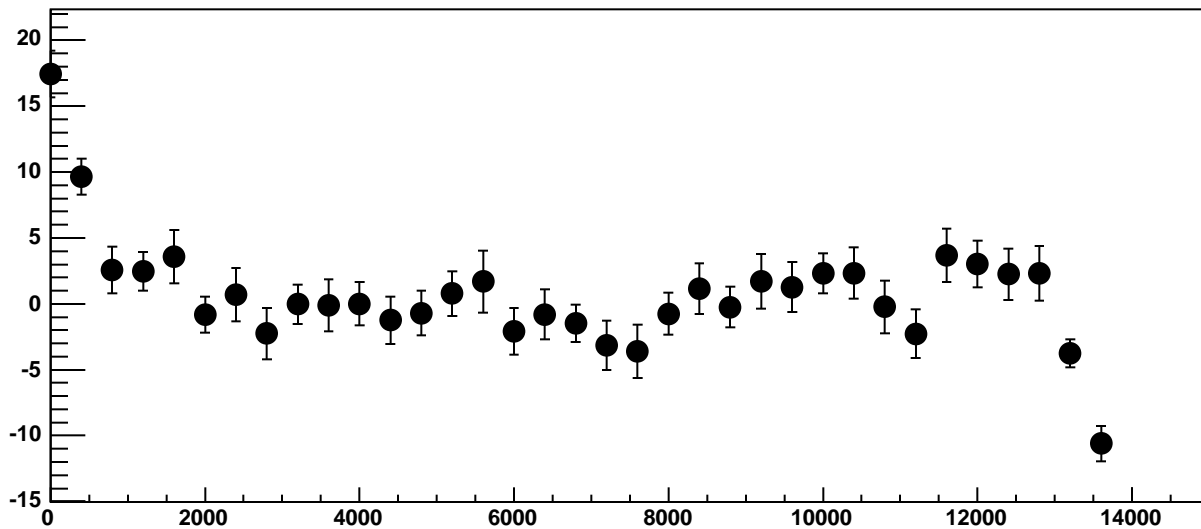


$\chi^2 / \text{ndf}$  23.33 / 23  
p0  $101.2 \pm 0.7831$   
p1  $0.01641 \pm 0.00012$

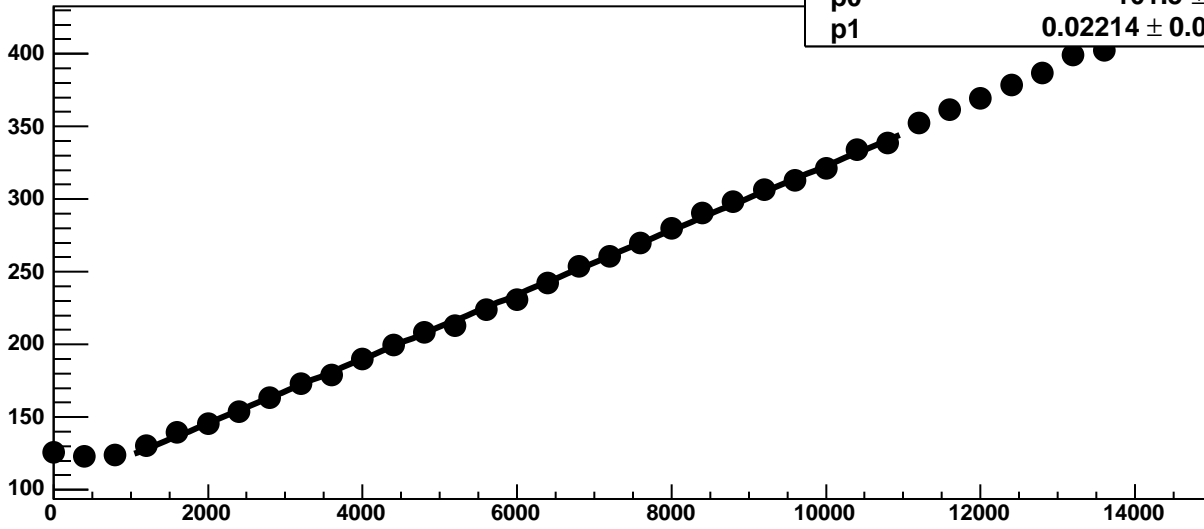
Chip 10, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

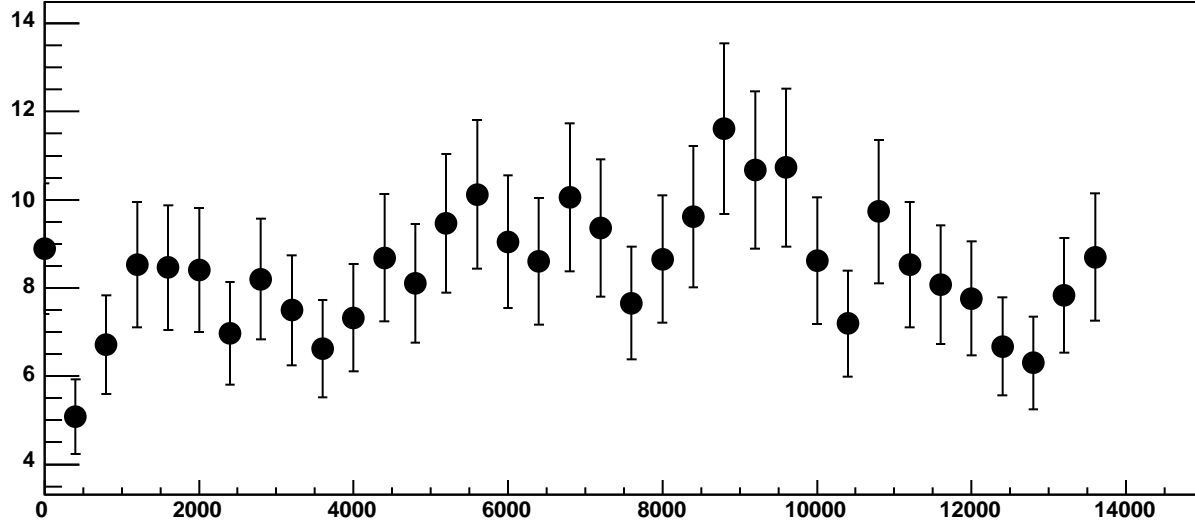


Chip 10, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

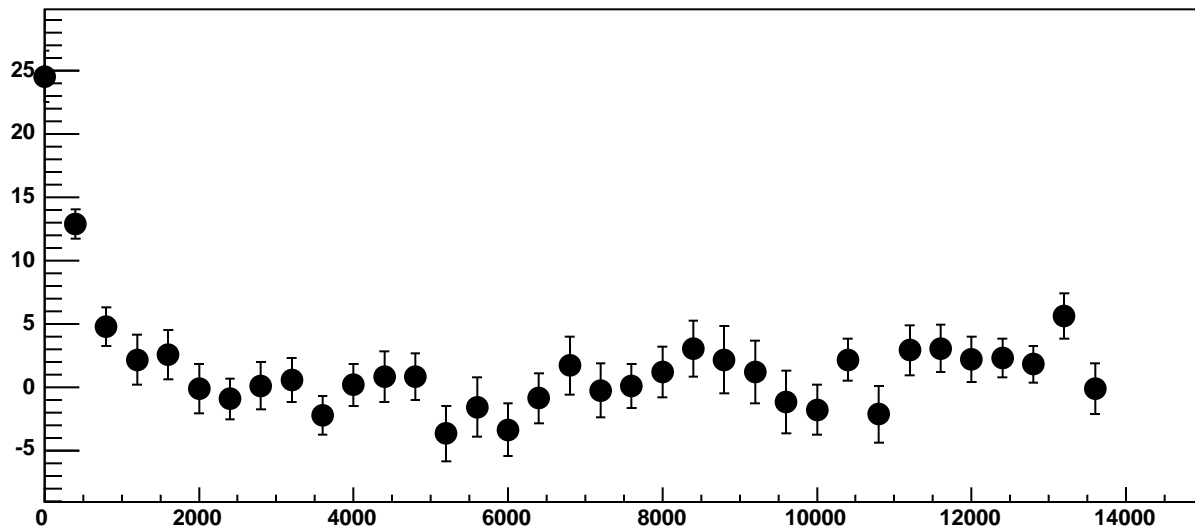


$\chi^2 / \text{ndf}$  19.45 / 23  
p0  $101.5 \pm 0.8578$   
p1  $0.02214 \pm 0.0001353$

Chip 10, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

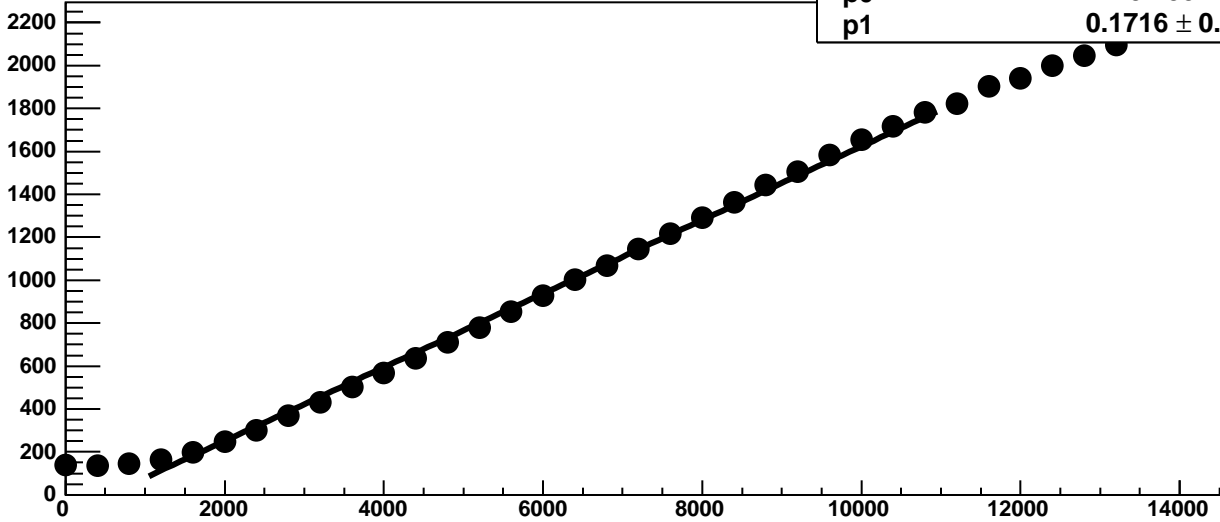


Chip 10, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



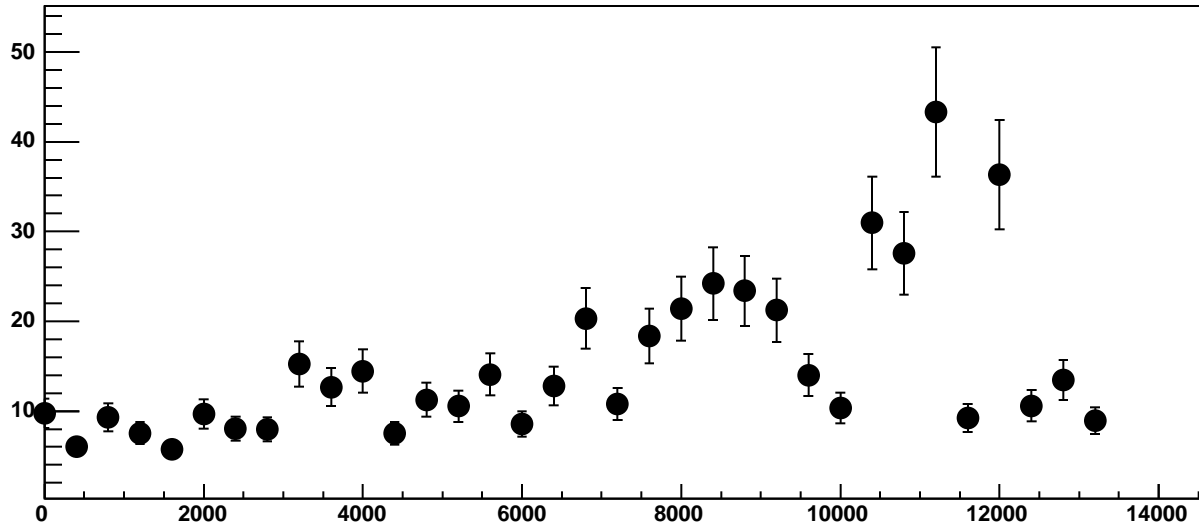


Chip 10, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

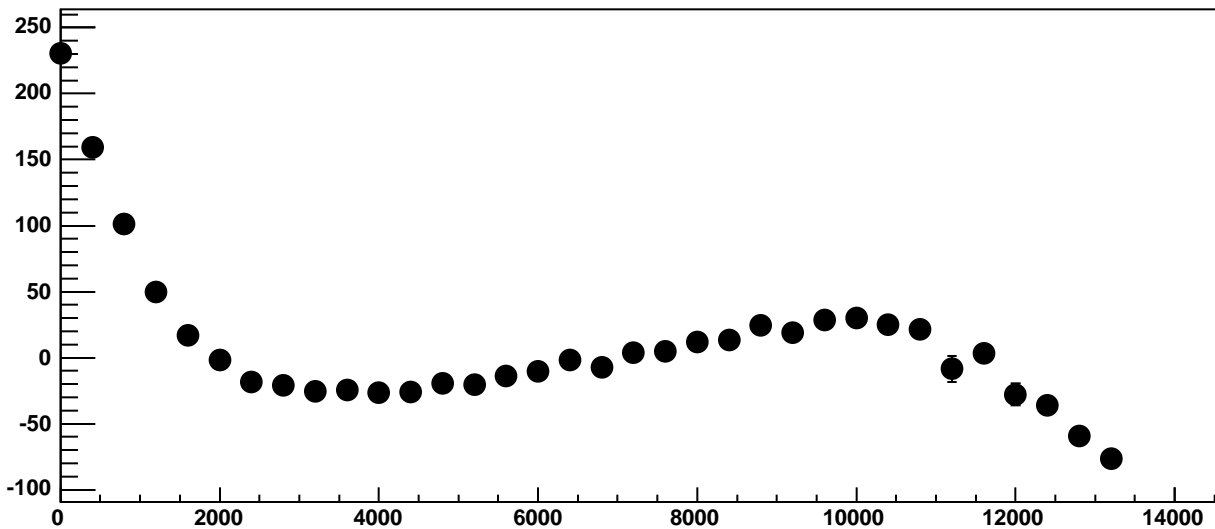


$\chi^2 / \text{ndf}$  2129 / 23  
p0  $-92.35 \pm 0.9676$   
p1  $0.1716 \pm 0.000191$

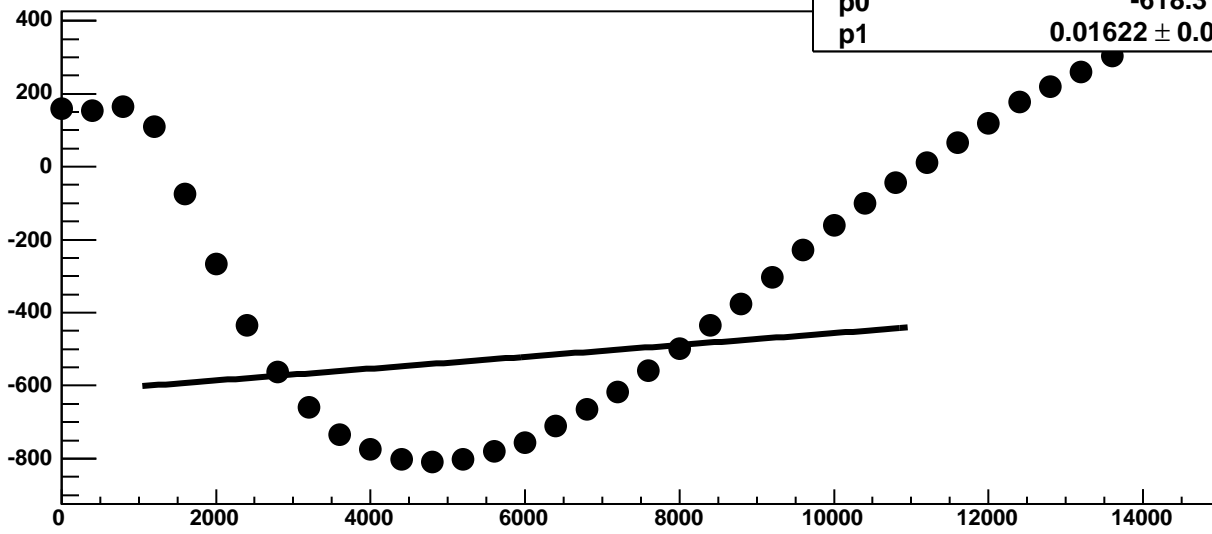
Chip 10, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

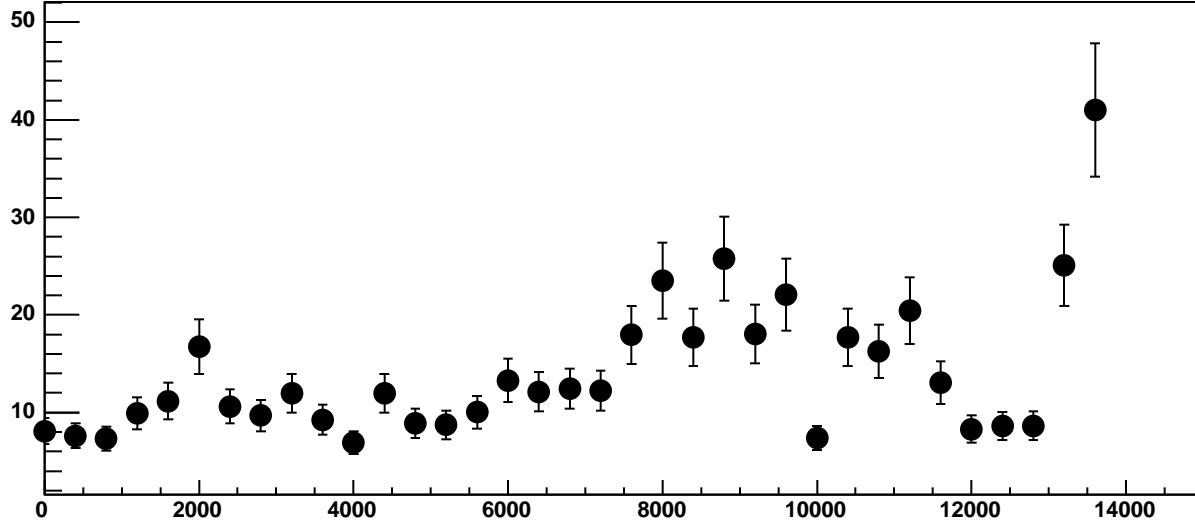


Chip 10, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

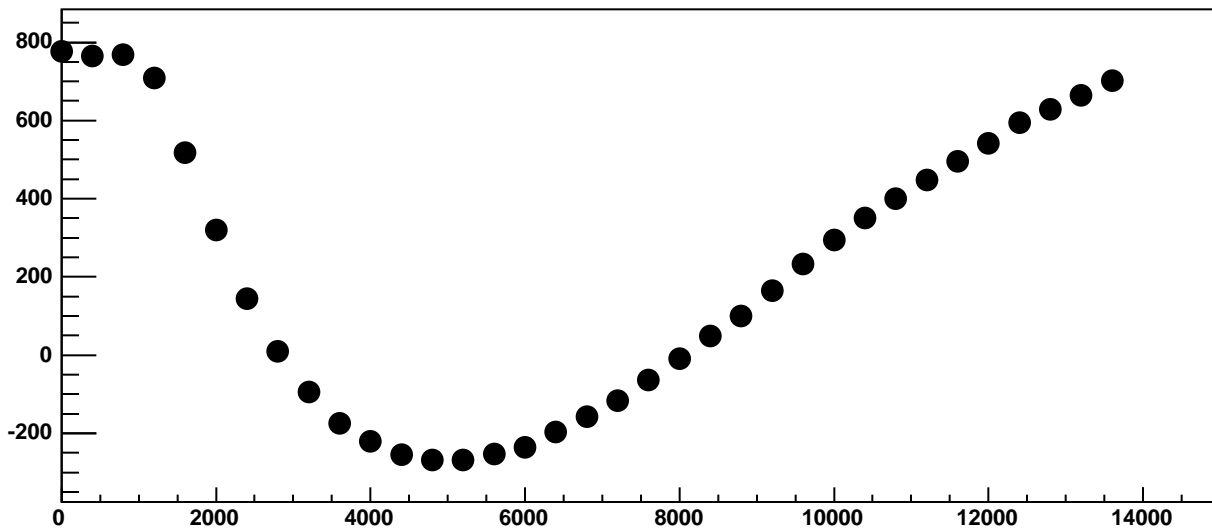


$\chi^2 / \text{ndf}$  3.009e+05 / 23  
p0 -618.3 ± 1.157  
p1 0.01622 ± 0.0001948

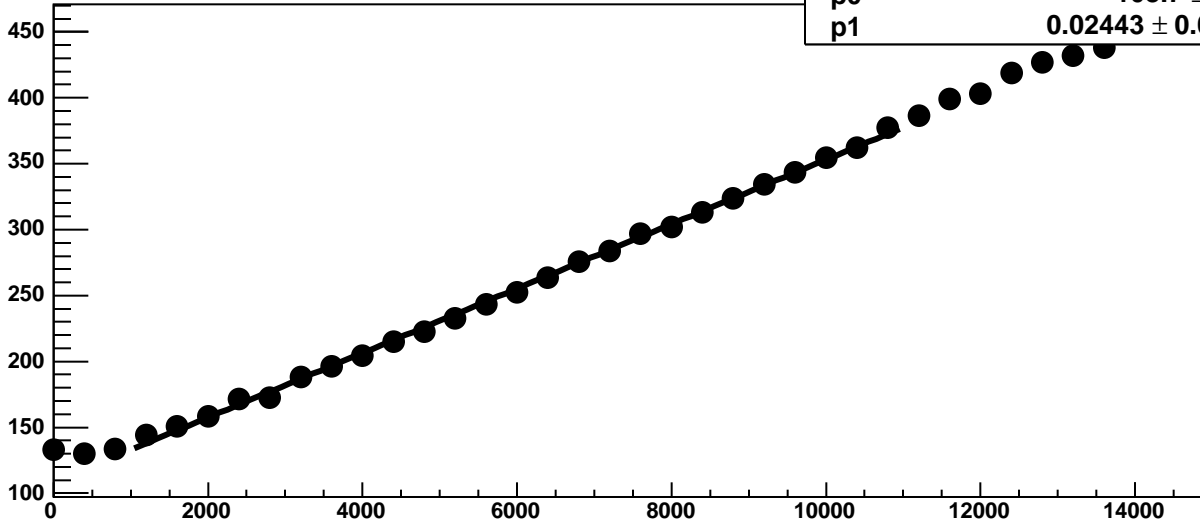
Chip 10, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

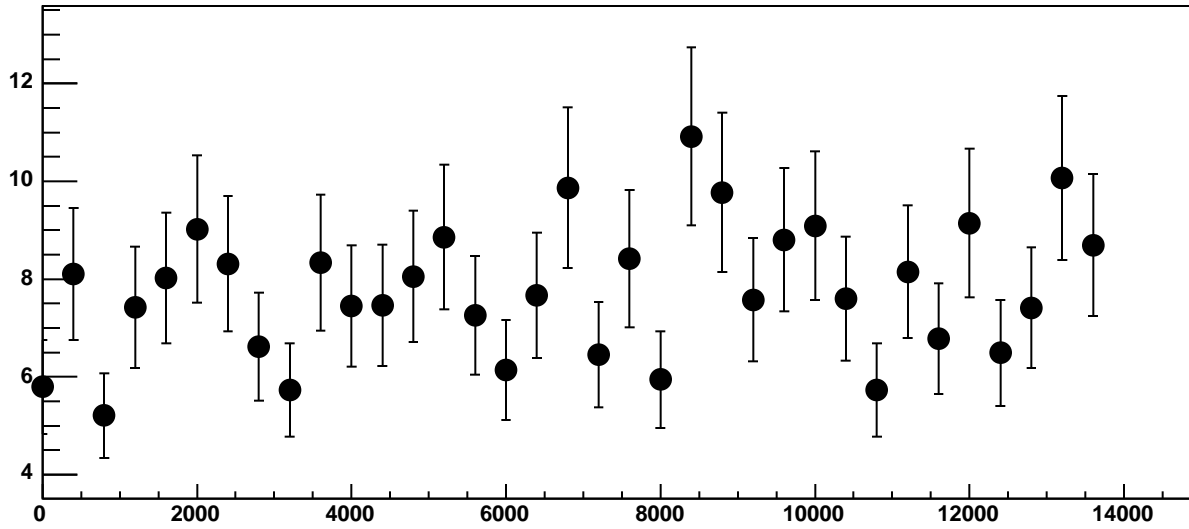


Chip 10, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

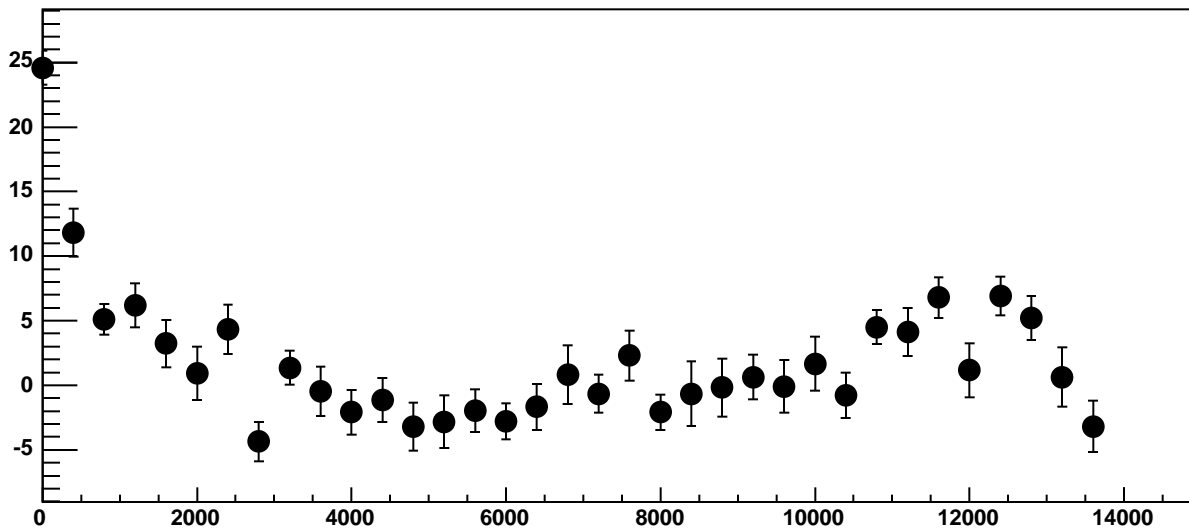


$\chi^2 / \text{ndf}$  60.88 / 23  
p0 108.7 ± 0.7926  
p1 0.02443 ± 0.0001191

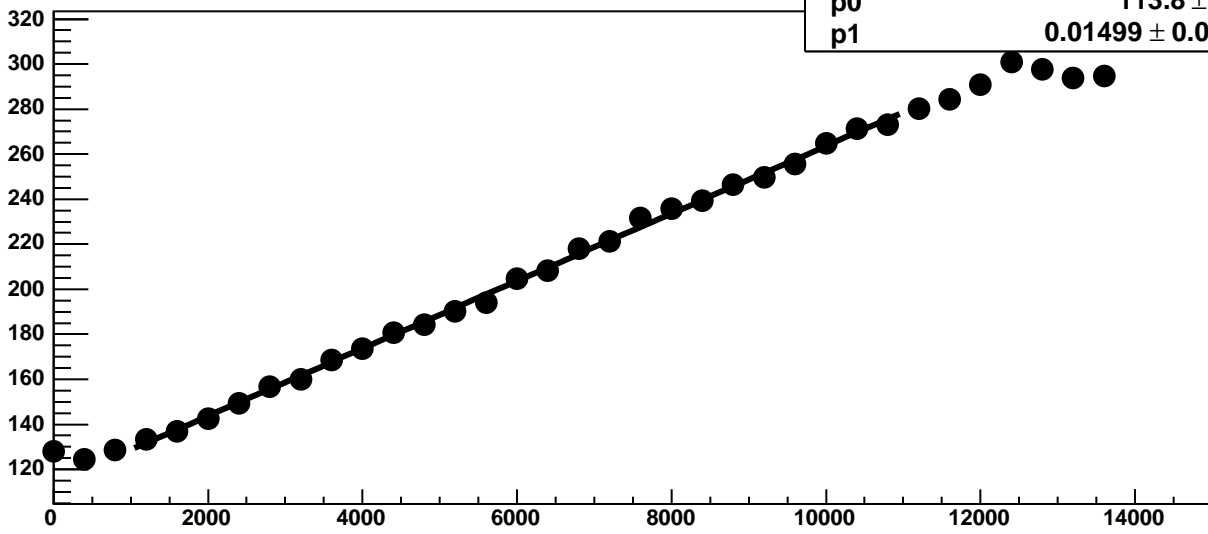
Chip 10, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



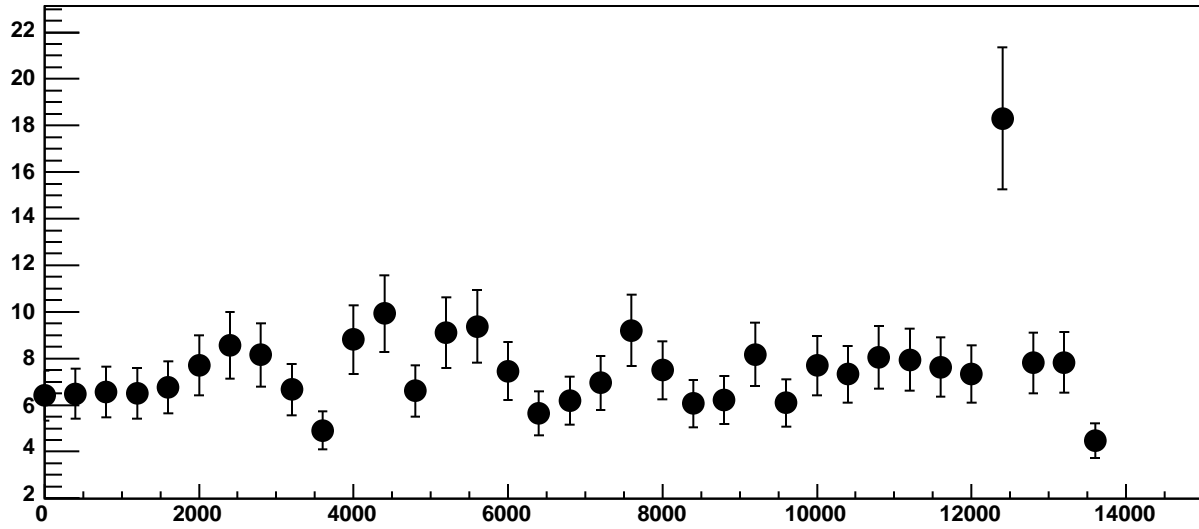
Chip 10, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



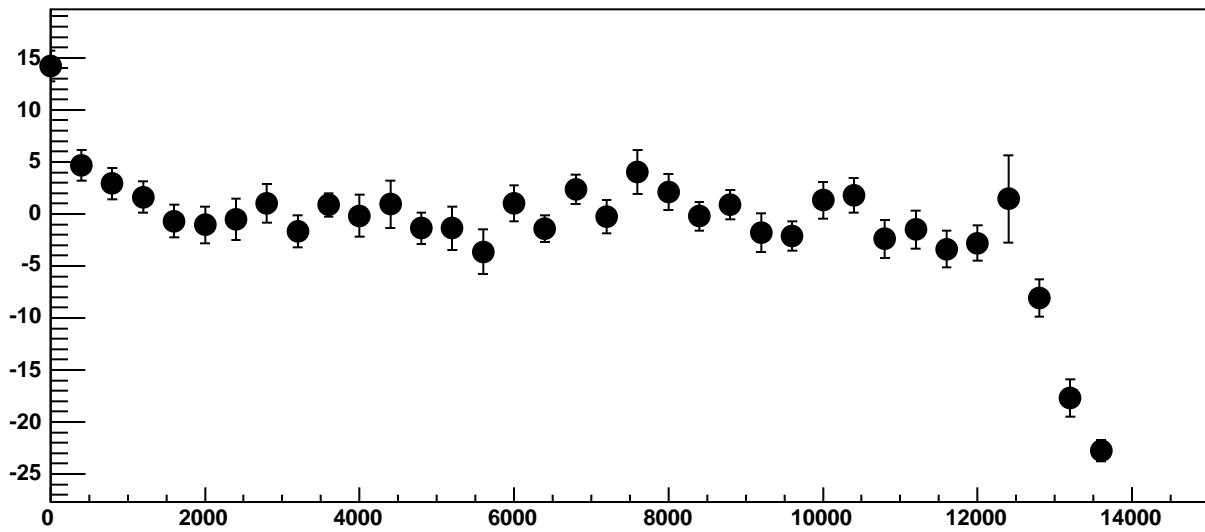
Chip 10, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



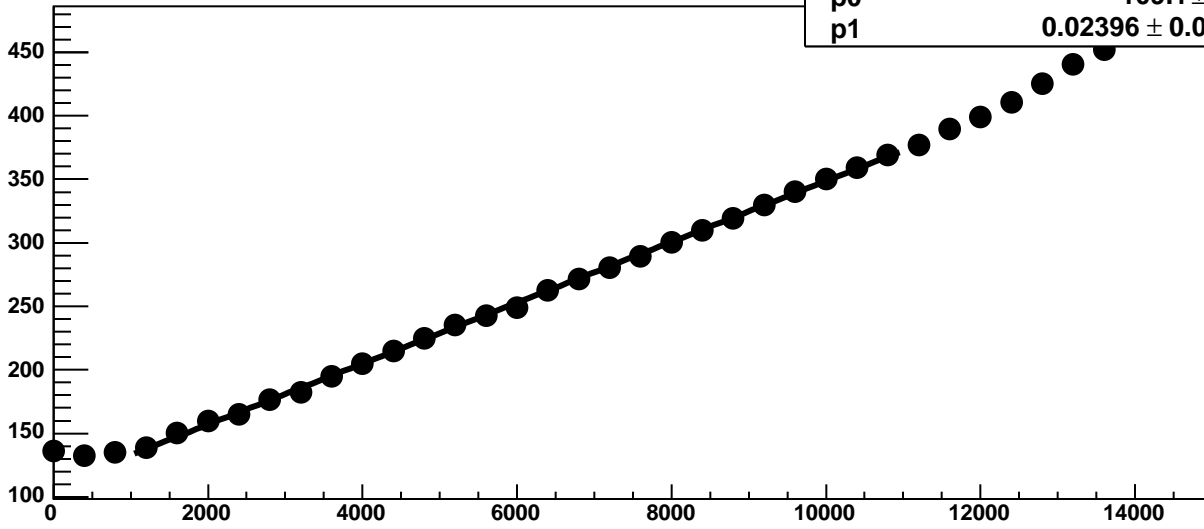
Chip 10, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

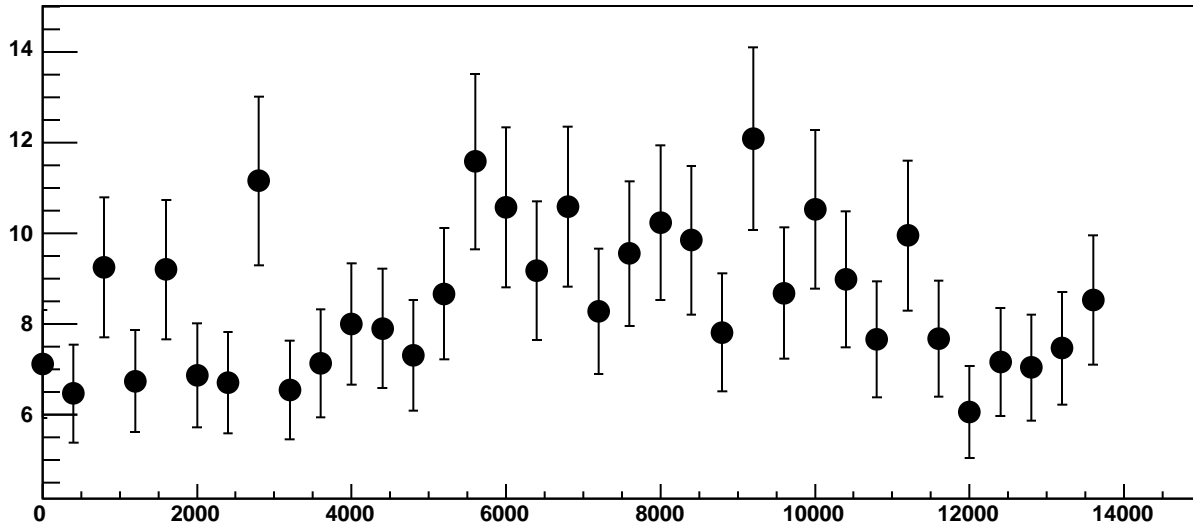


Chip 10, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

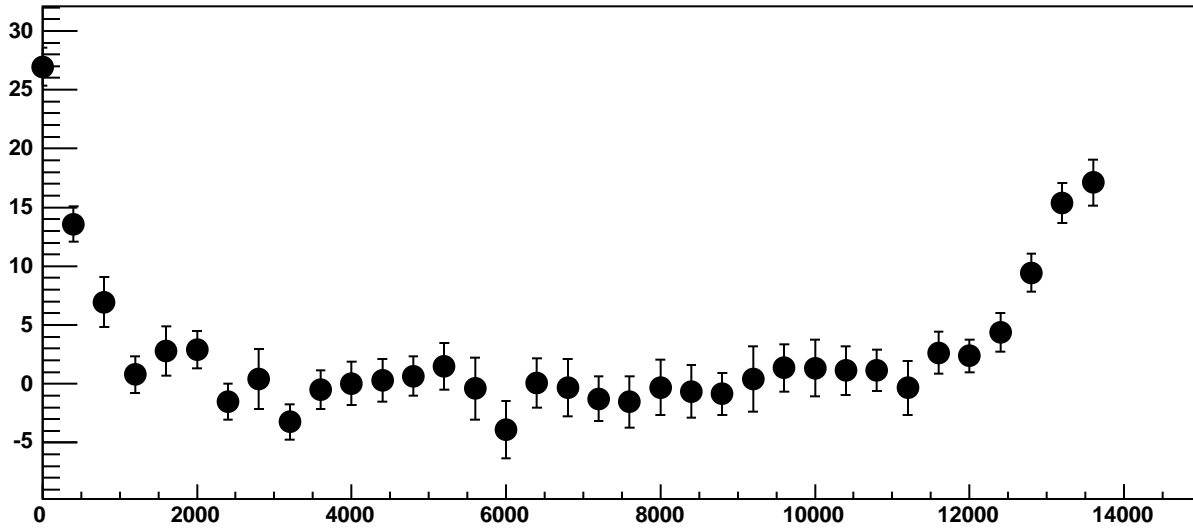


$\chi^2 / \text{ndf}$  17.19 / 23  
p0  $109.1 \pm 0.8156$   
p1  $0.02396 \pm 0.0001304$

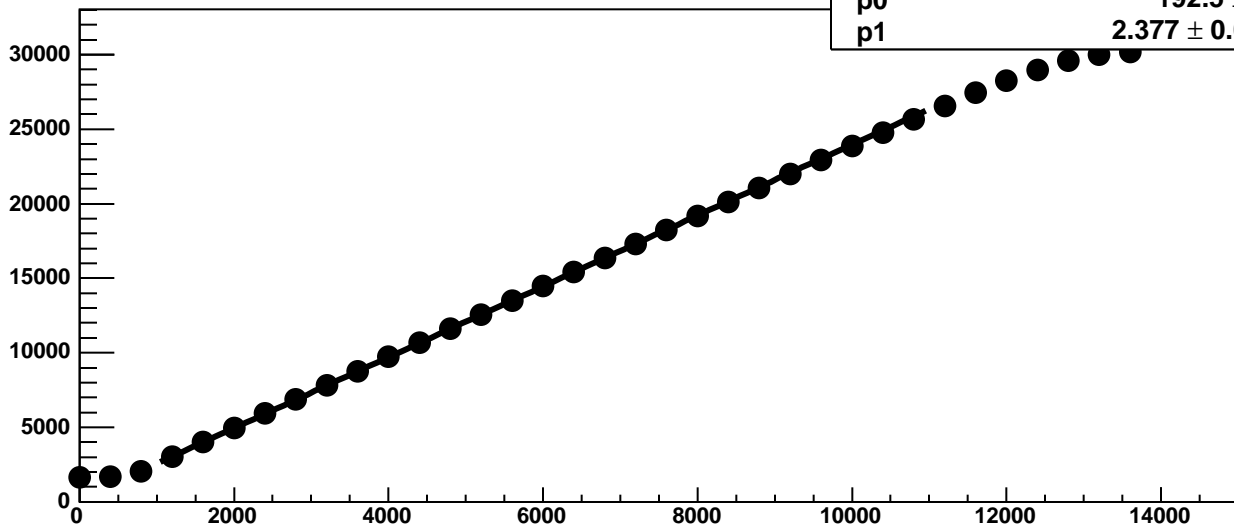
Chip 10, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

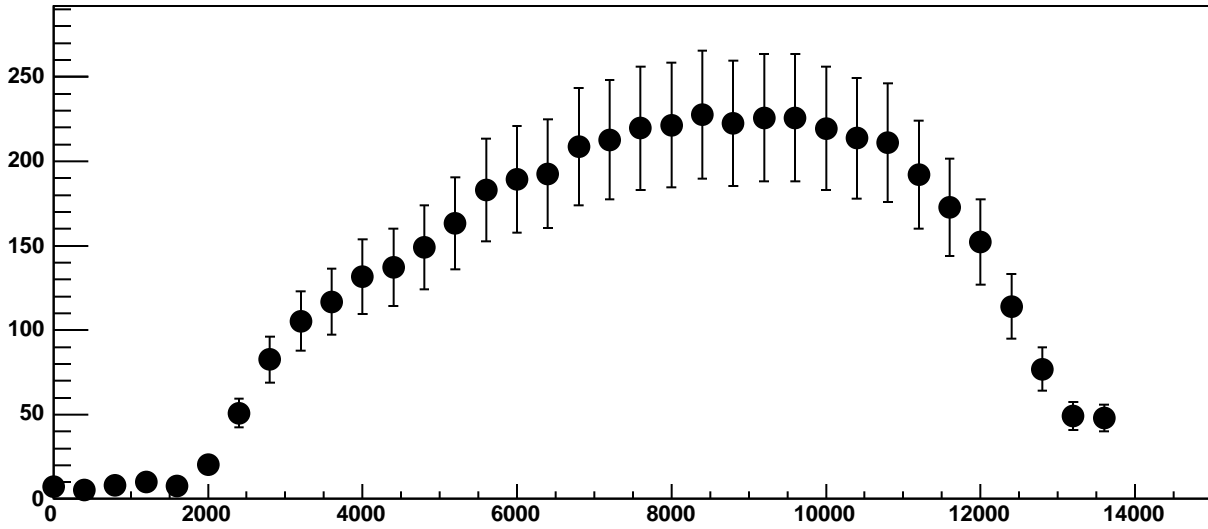


Chip 10, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

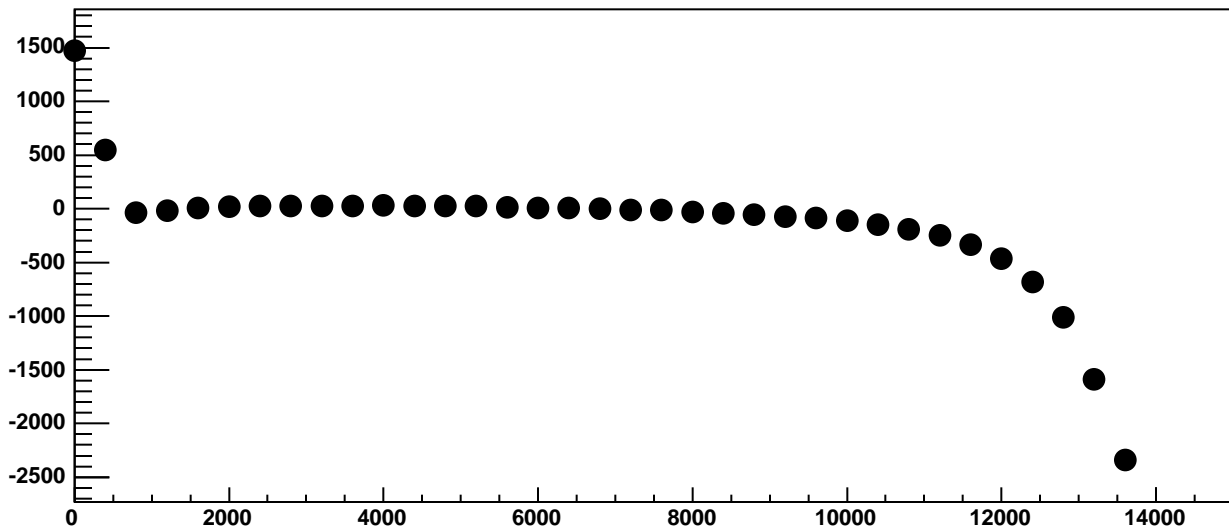


$\chi^2 / \text{ndf}$  151.6 / 23  
p0  $192.5 \pm 3.009$   
p1  $2.377 \pm 0.001683$

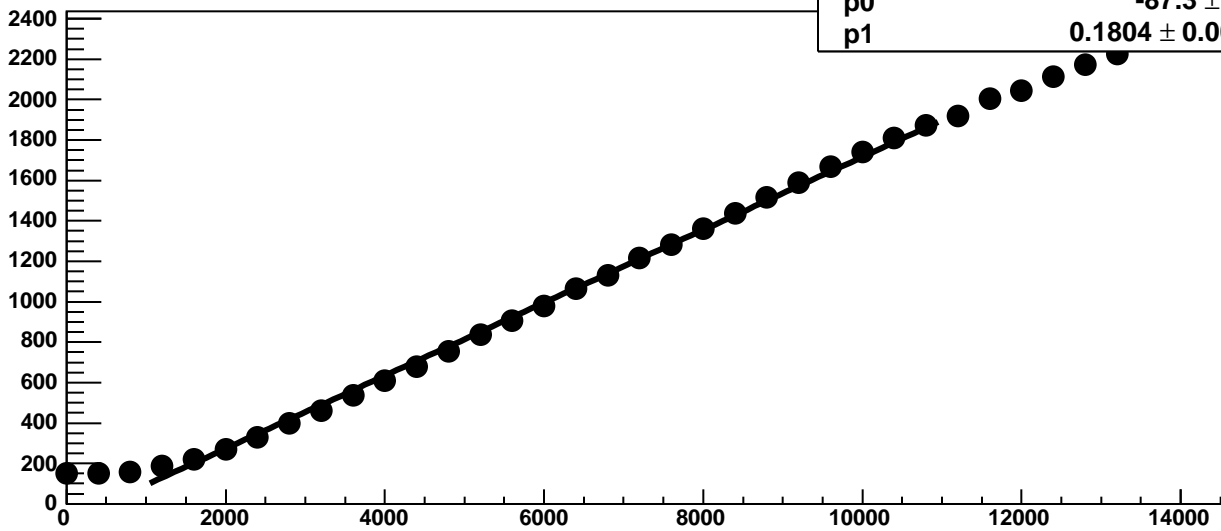
Chip 10, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC



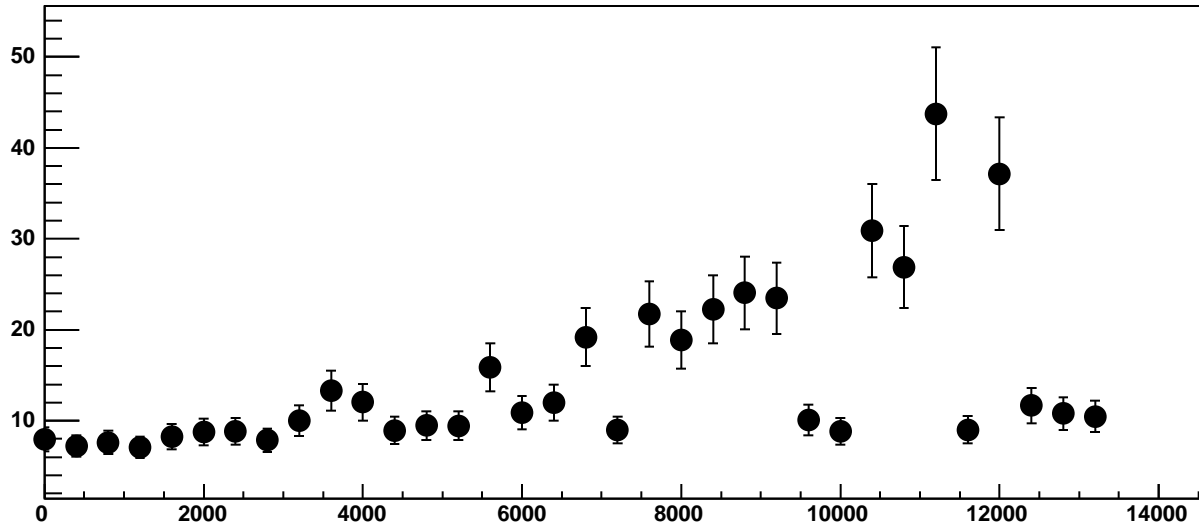
Chip 10, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC



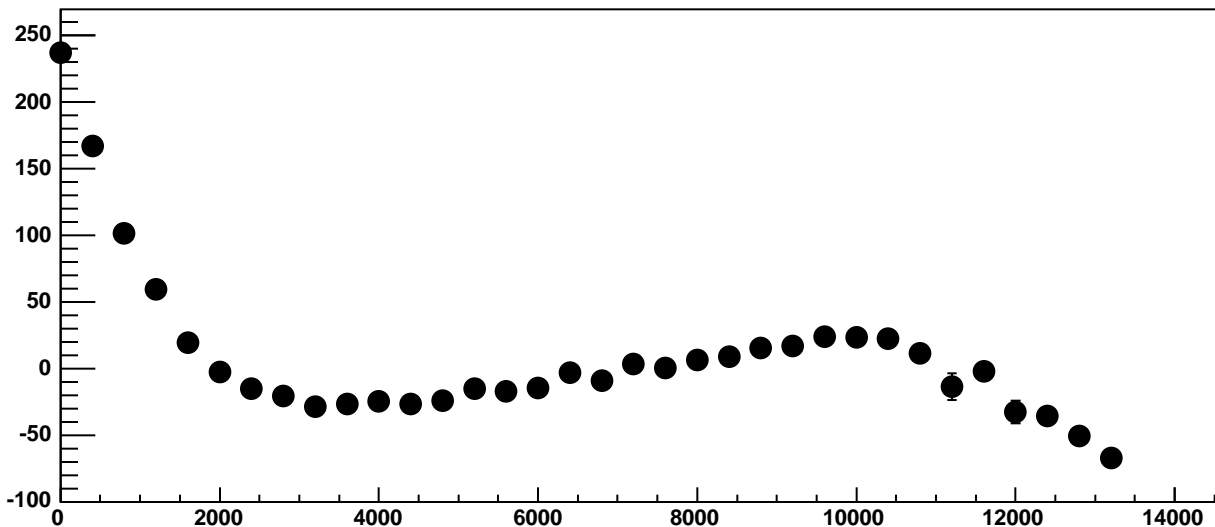
Chip 10, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

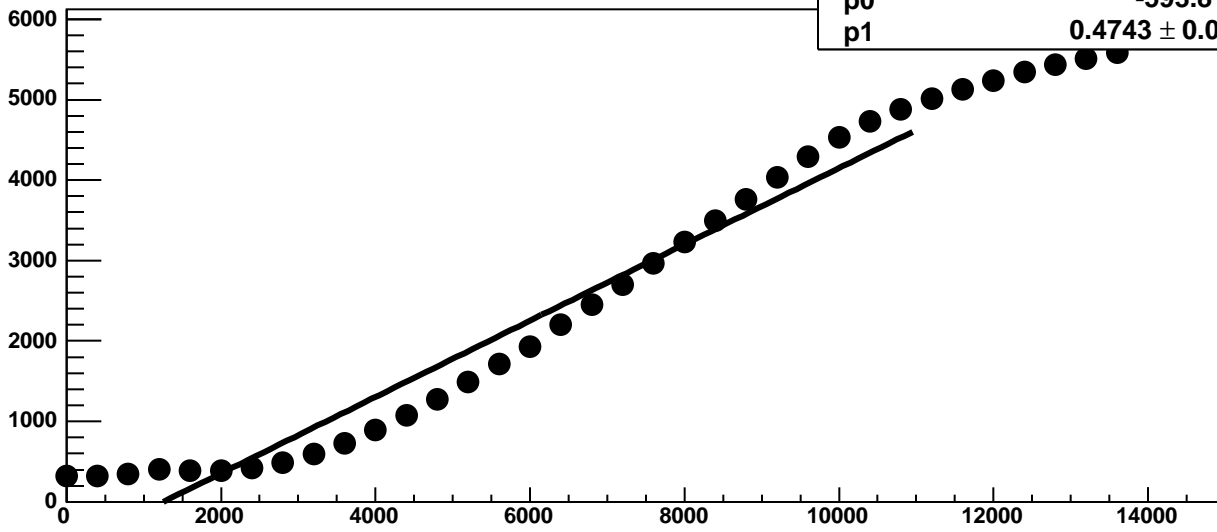


Chip 10, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

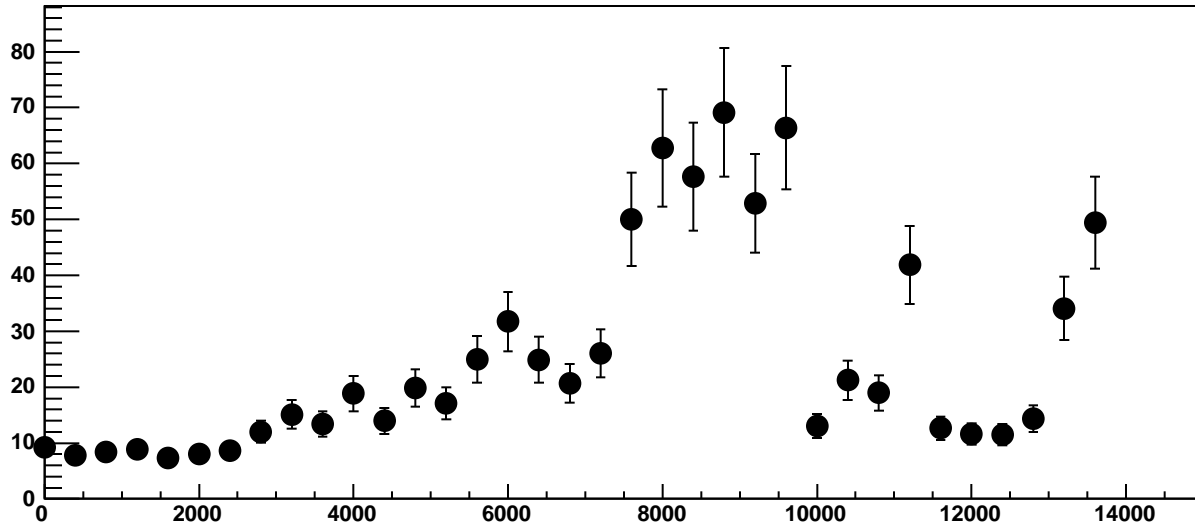


Chip 10, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

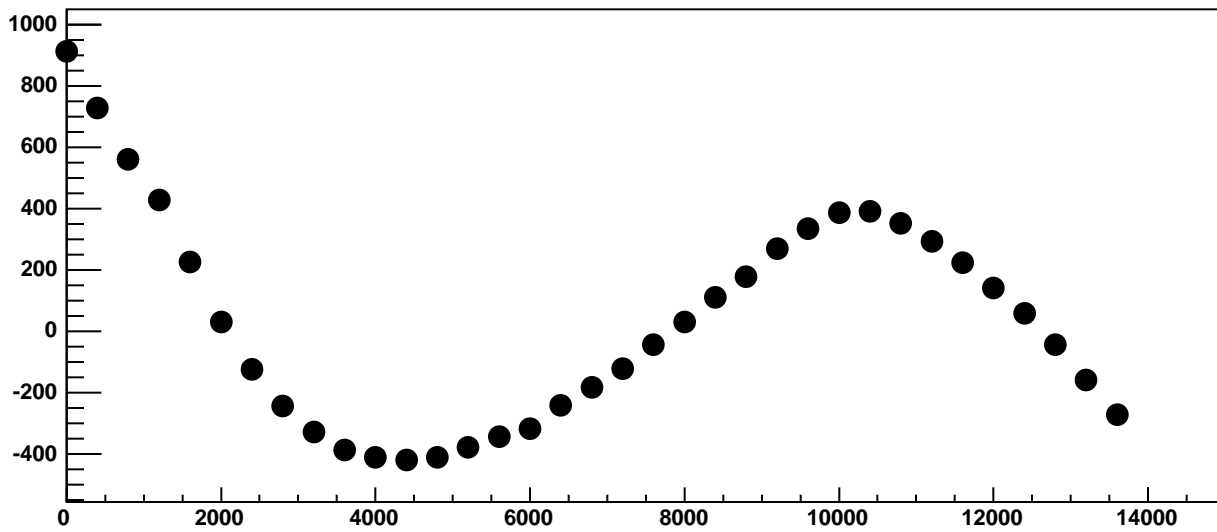
$\chi^2 / \text{ndf}$  1.822e+05 / 23  
p0 -593.8 ± 1.144  
p1 0.4743 ± 0.0002525



Chip 10, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

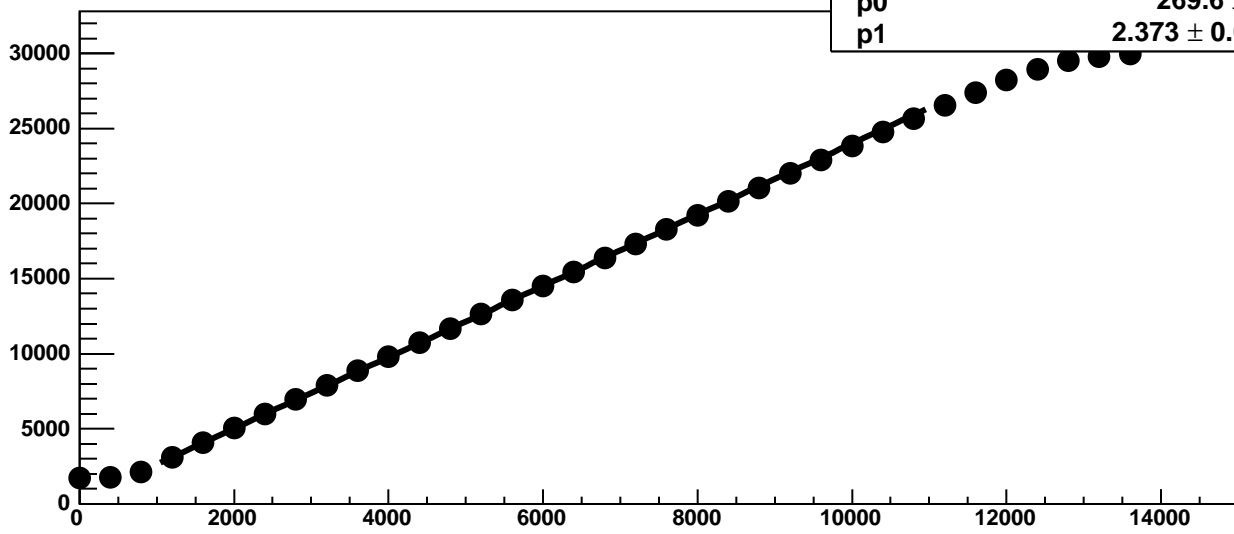


Chip 10, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

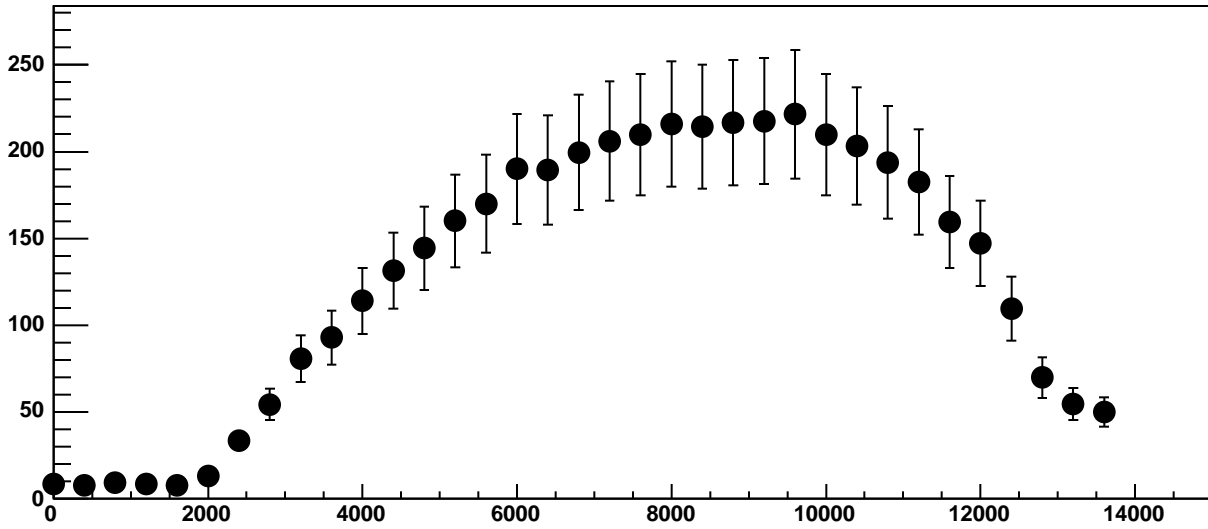




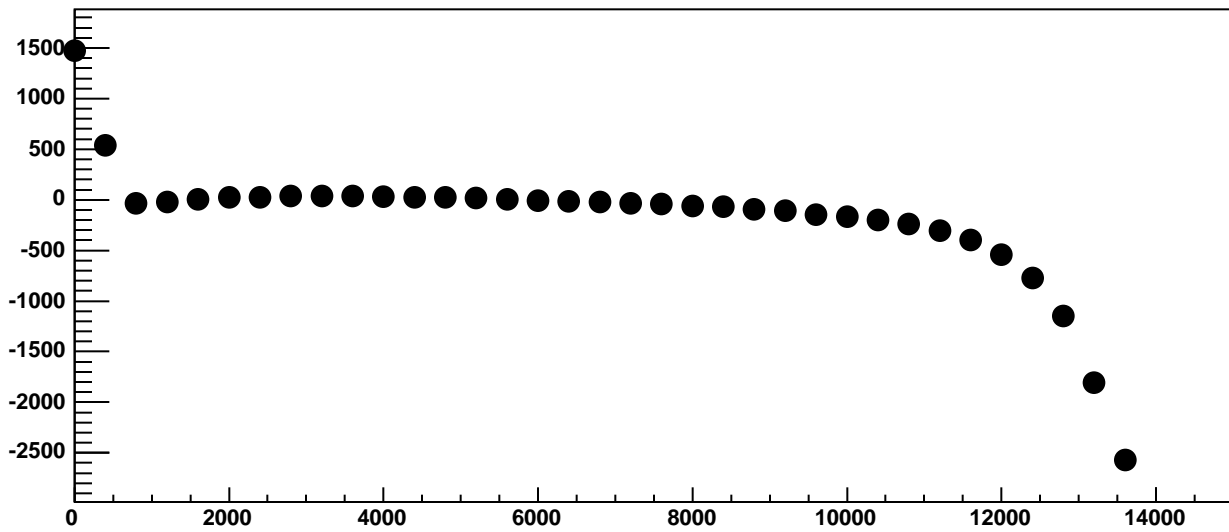
Chip 10, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC



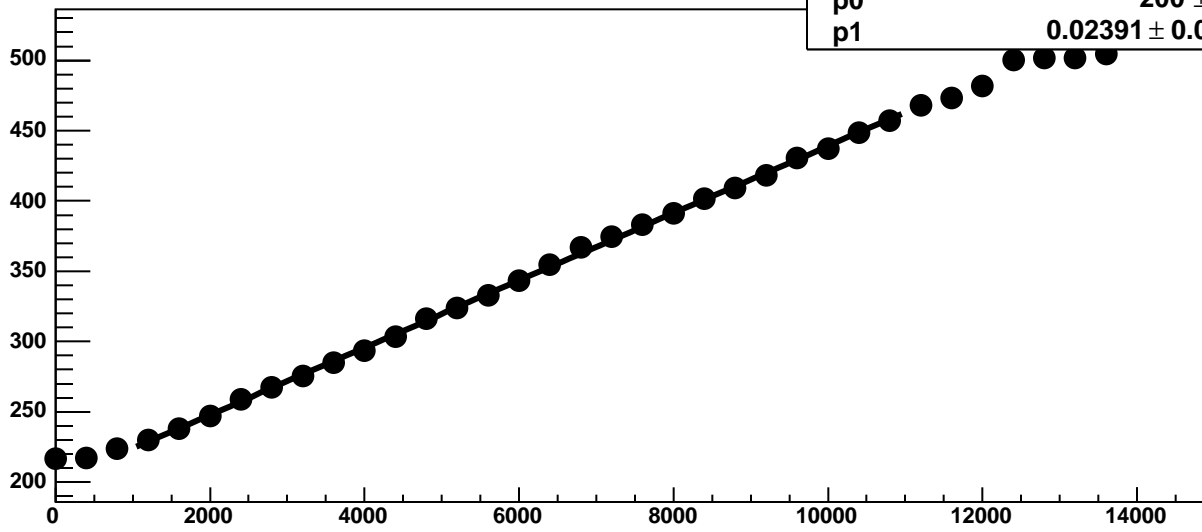
Chip 10, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC

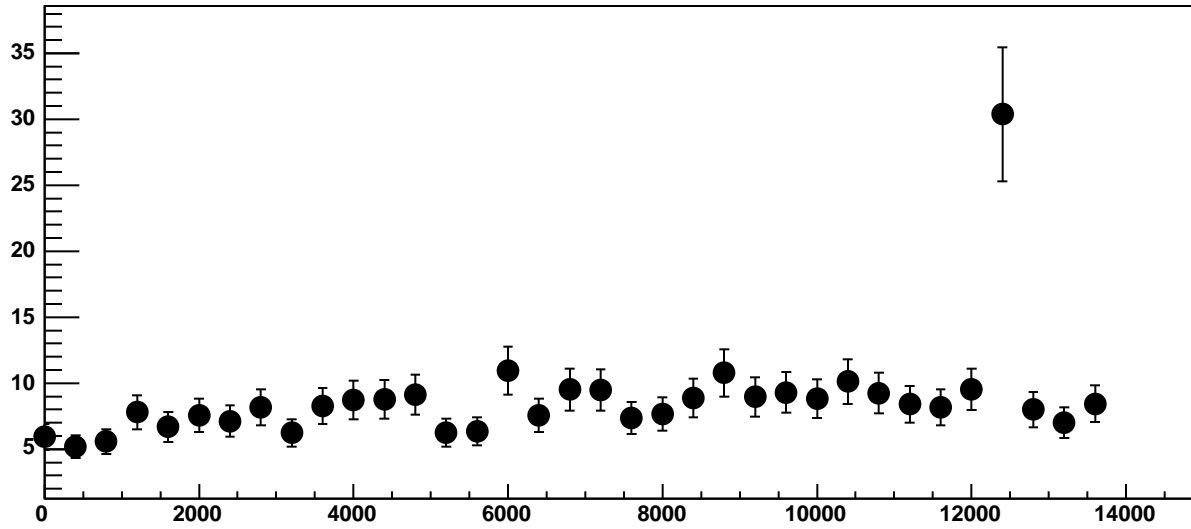


Chip 10, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

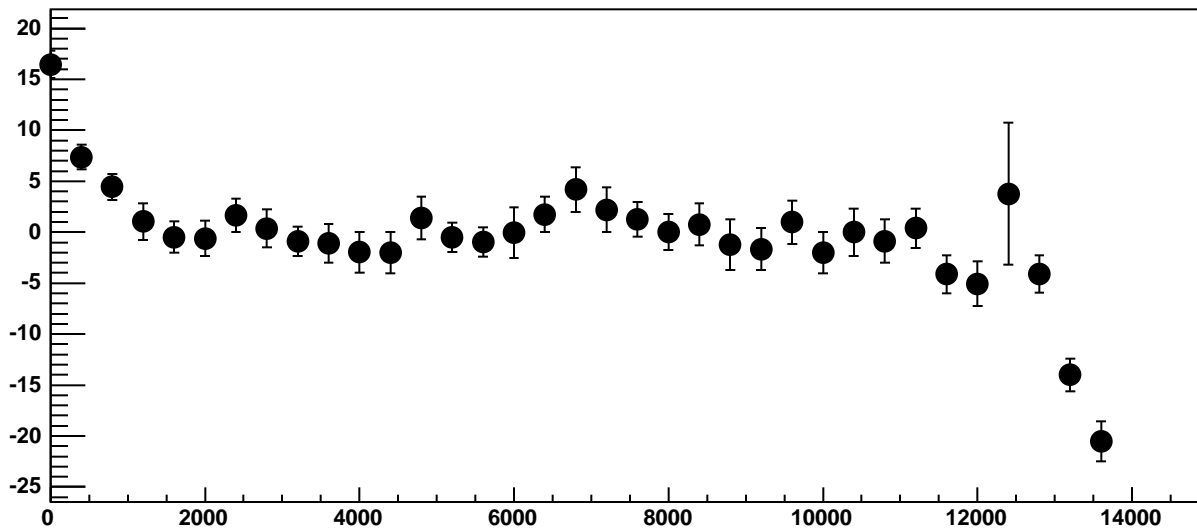


$\chi^2 / \text{ndf}$  14 / 23  
p0  $200 \pm 0.8146$   
p1  $0.02391 \pm 0.0001315$

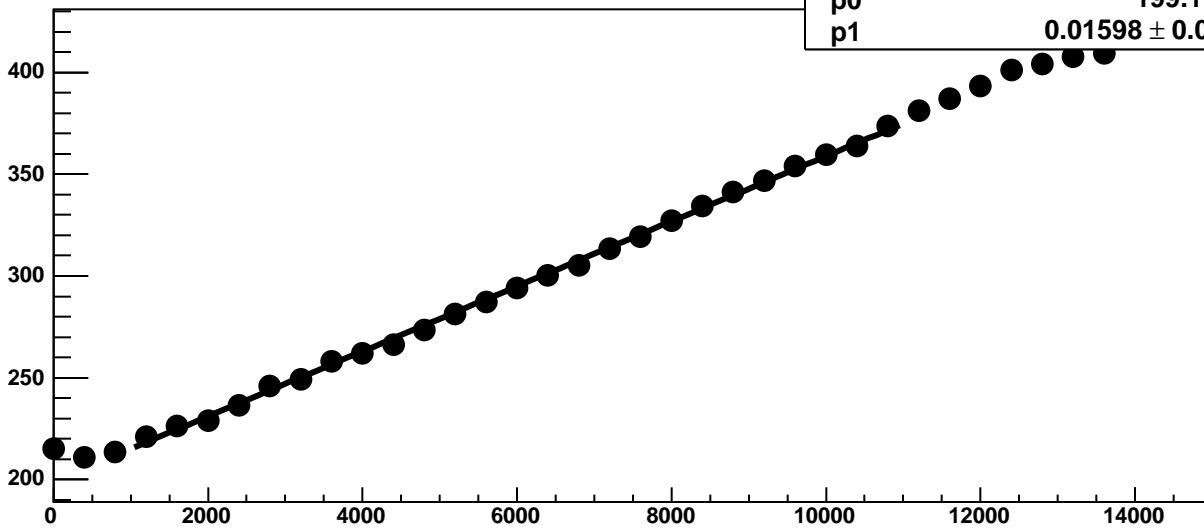
Chip 10, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

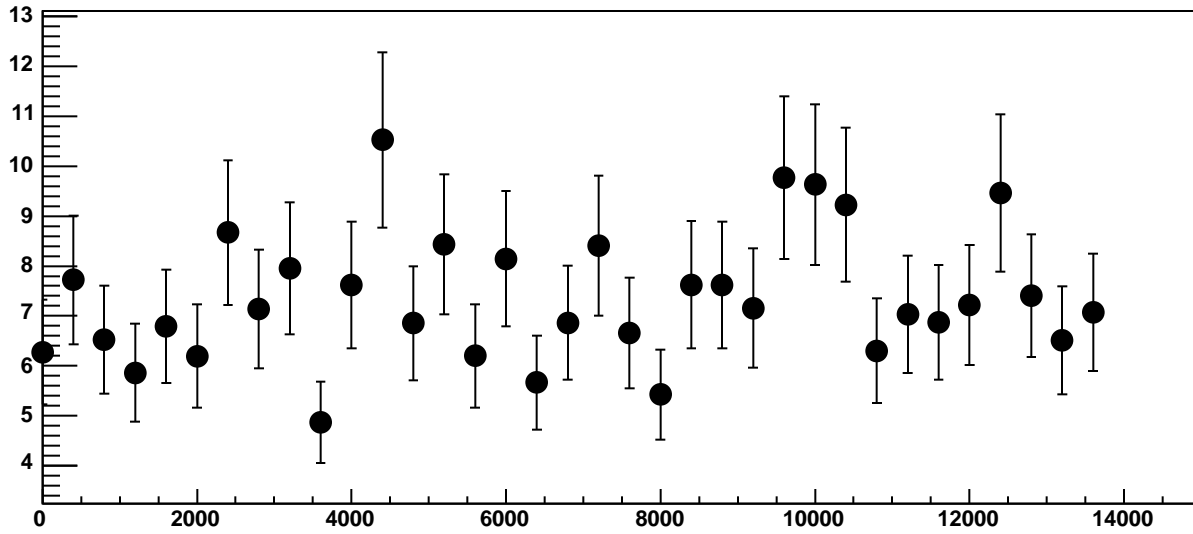


Chip 10, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

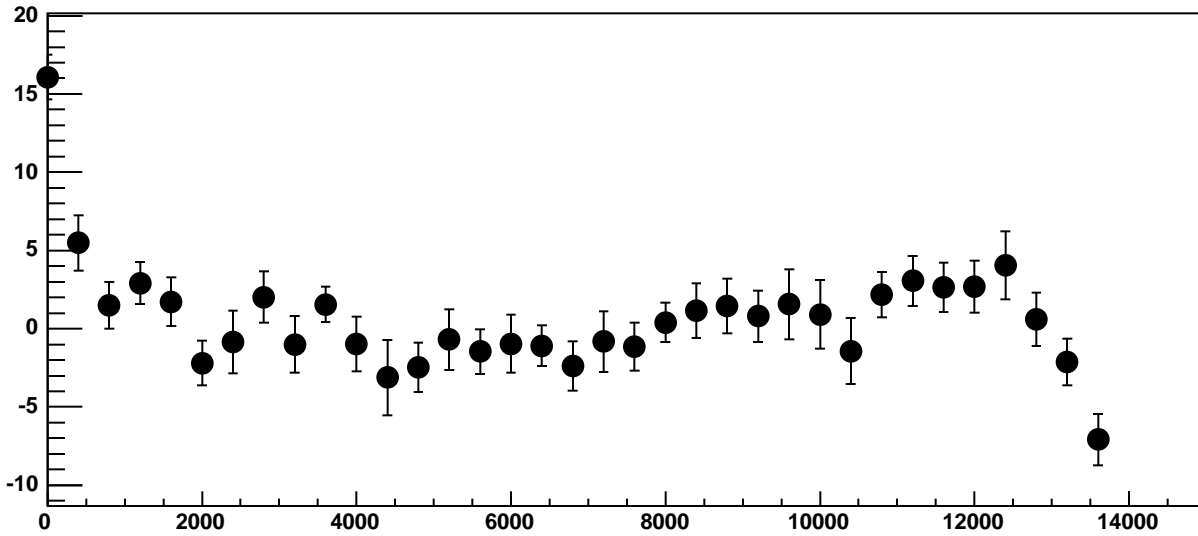


$\chi^2 / \text{ndf}$  26.71 / 23  
p0 199.1 ± 0.723  
p1 0.01598 ± 0.0001129

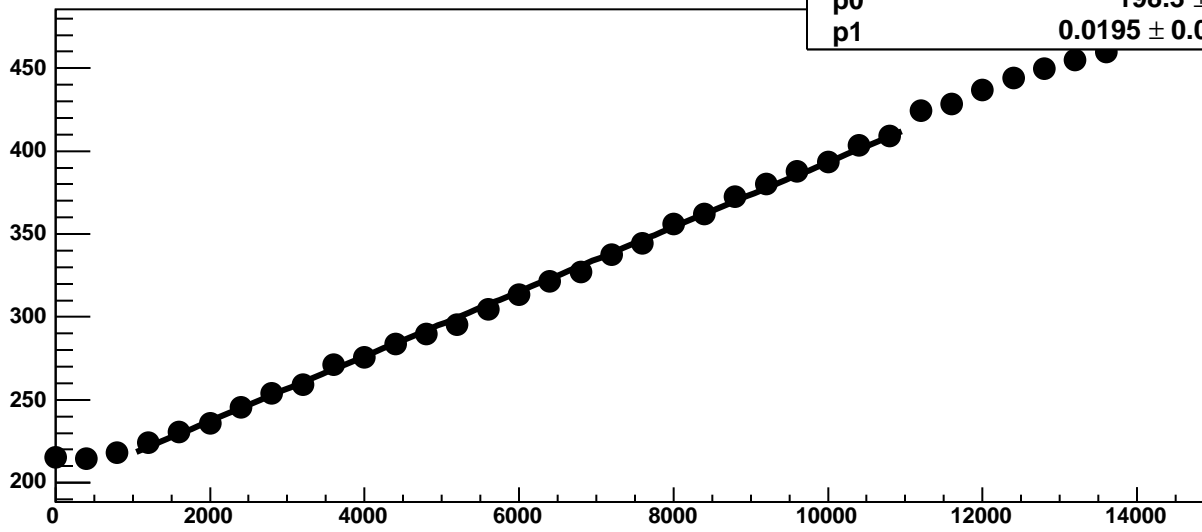
Chip 10, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

32.28 / 23

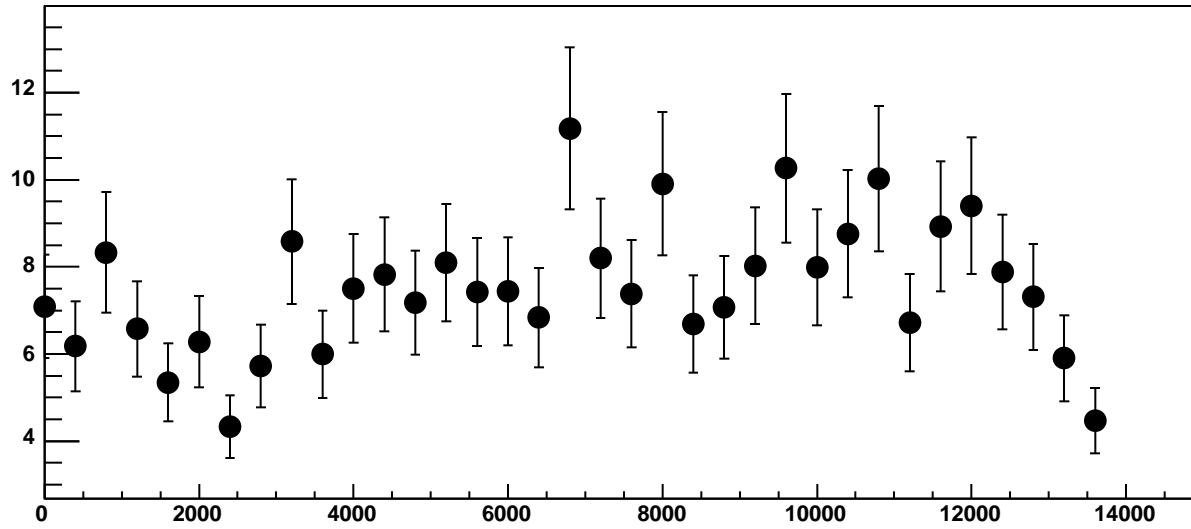
p0

$198.3 \pm 0.6684$

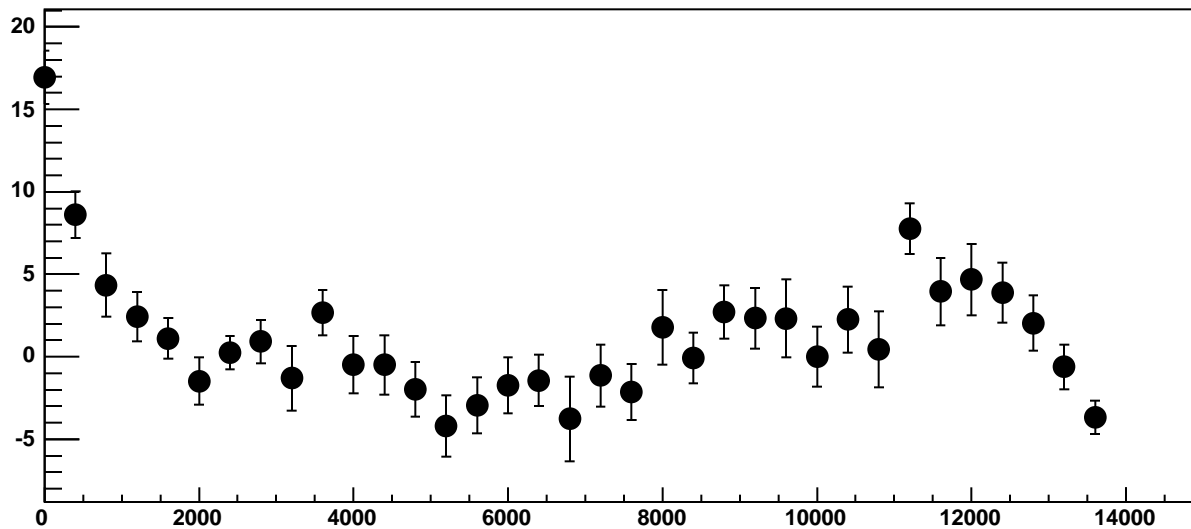
p1

$0.0195 \pm 0.0001133$

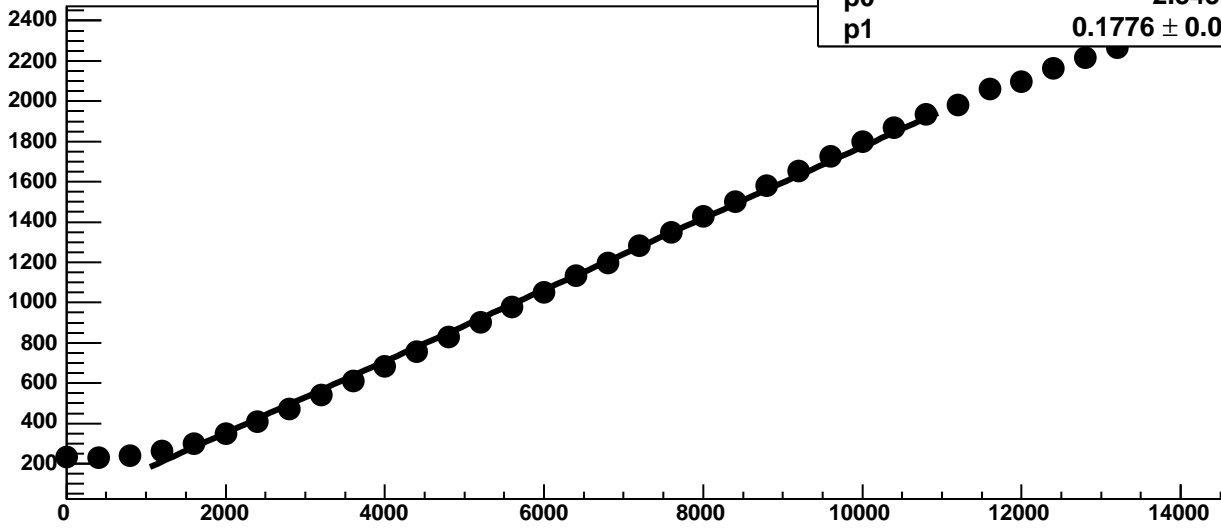
Chip 10, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



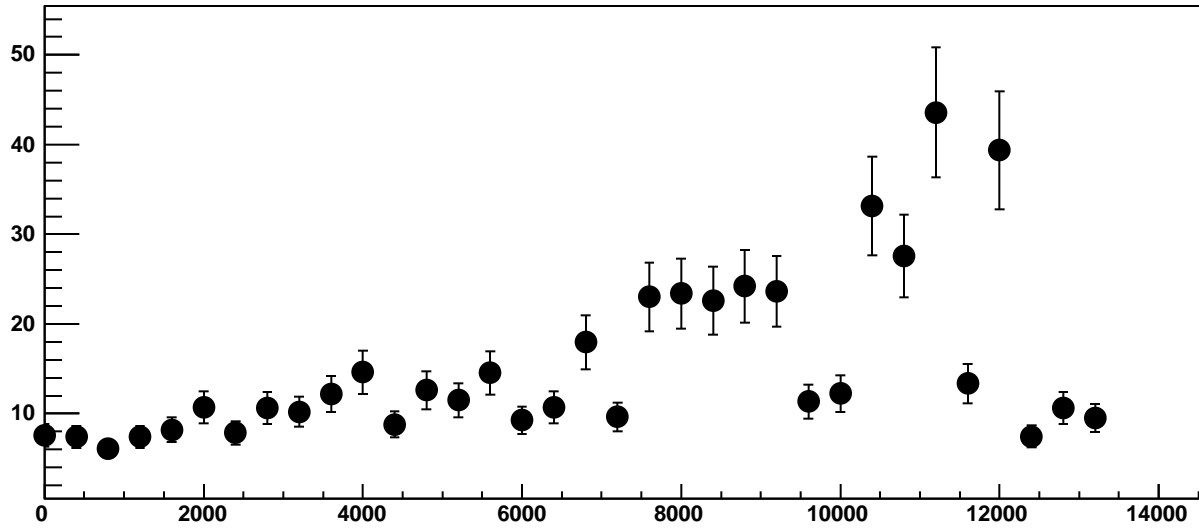
Chip 10, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



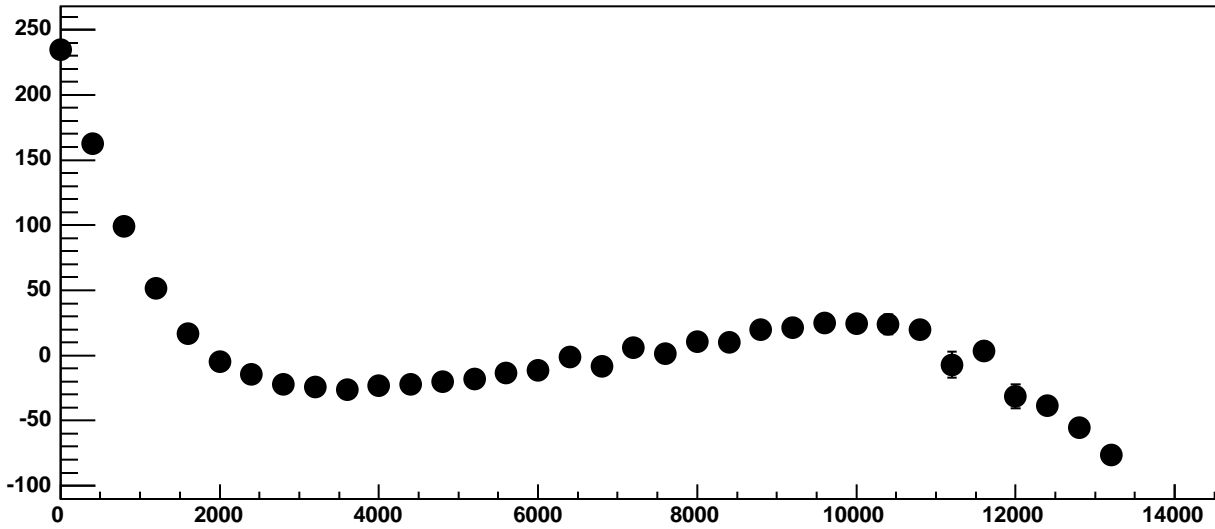
Chip 10, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



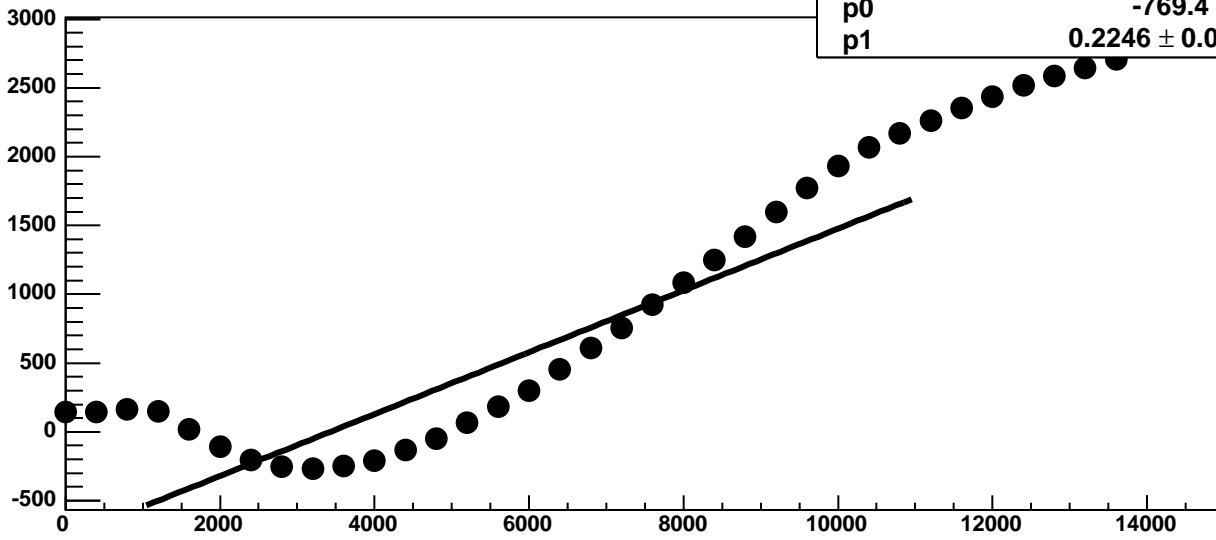
Chip 10, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

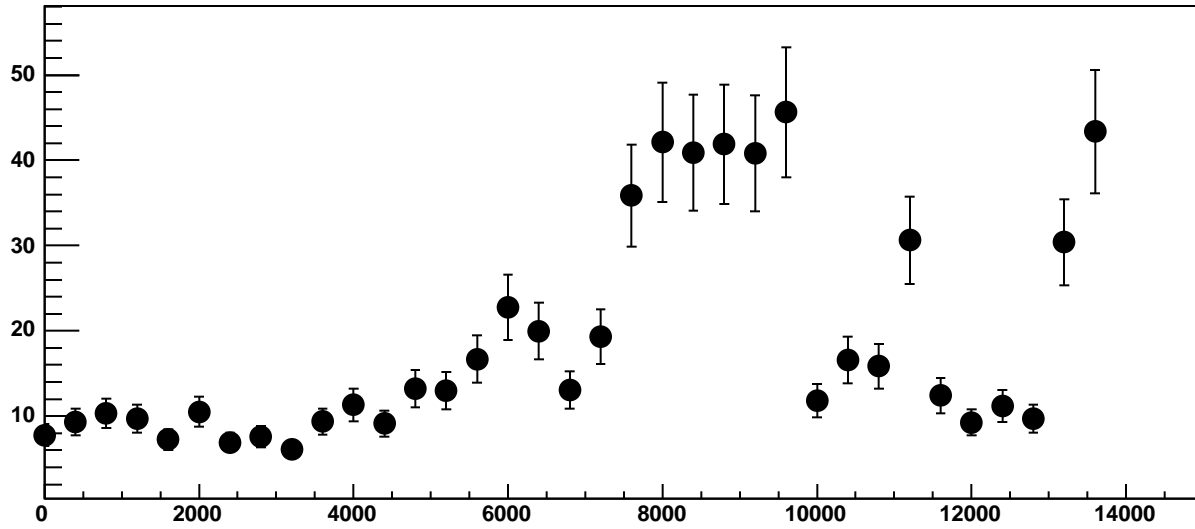


Chip 10, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

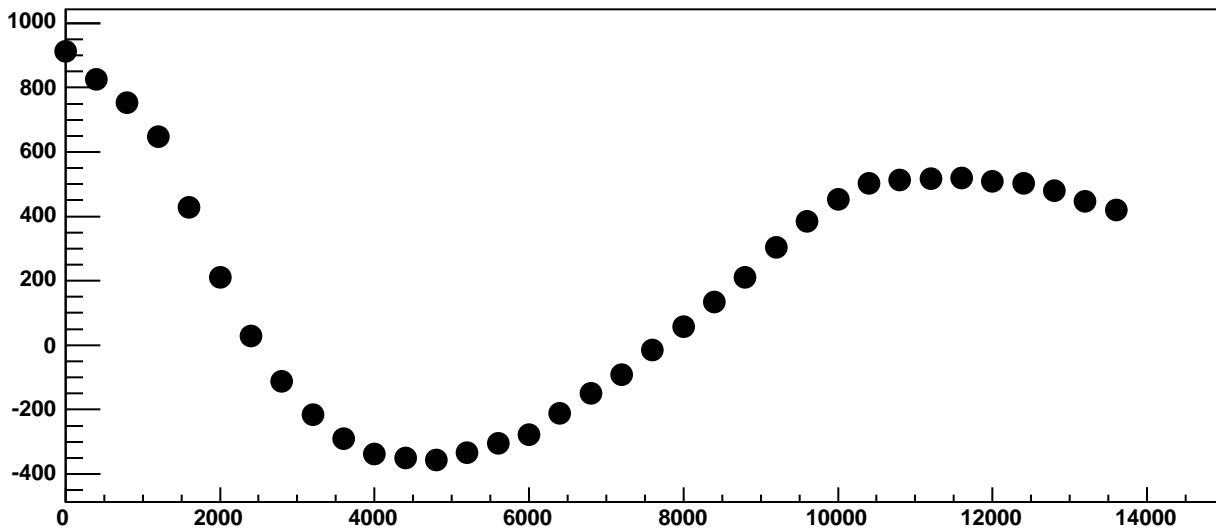


$\chi^2 / \text{ndf}$  3.576e+05 / 23  
p0 -769.4 ± 1.017  
p1 0.2246 ± 0.0002177

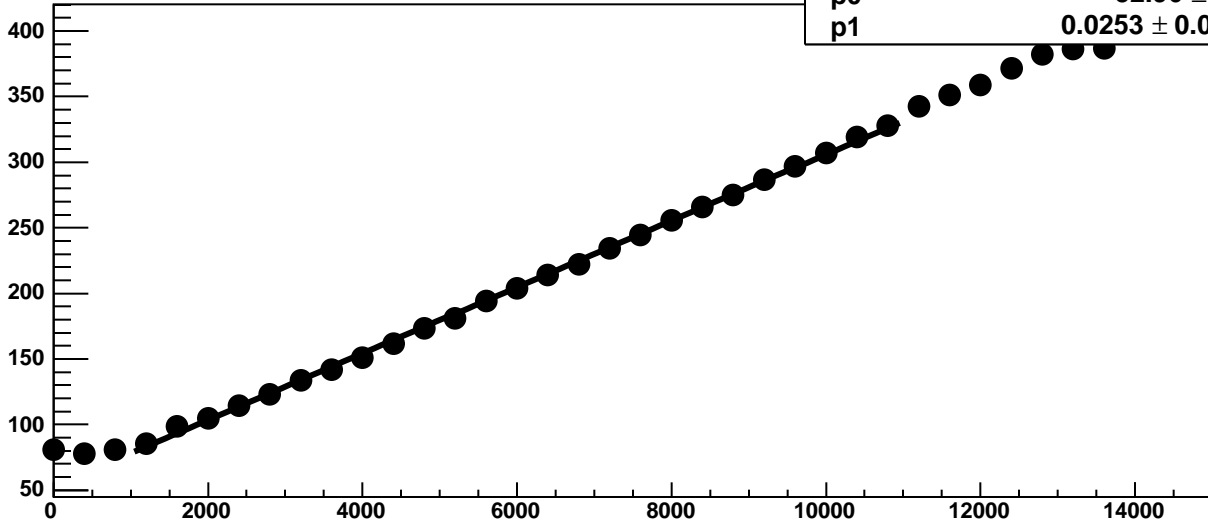
Chip 10, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

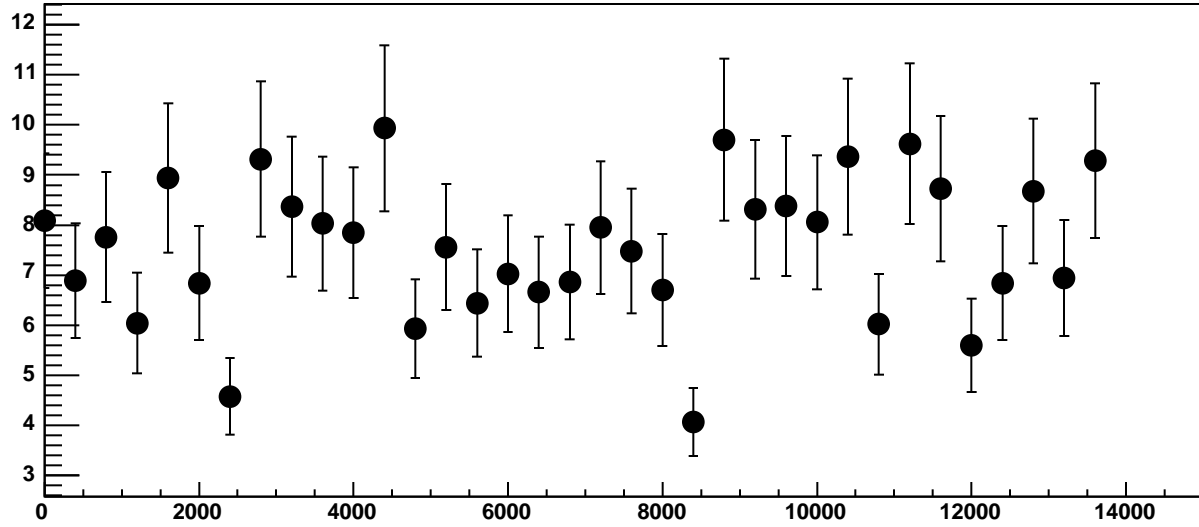


Chip 10, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

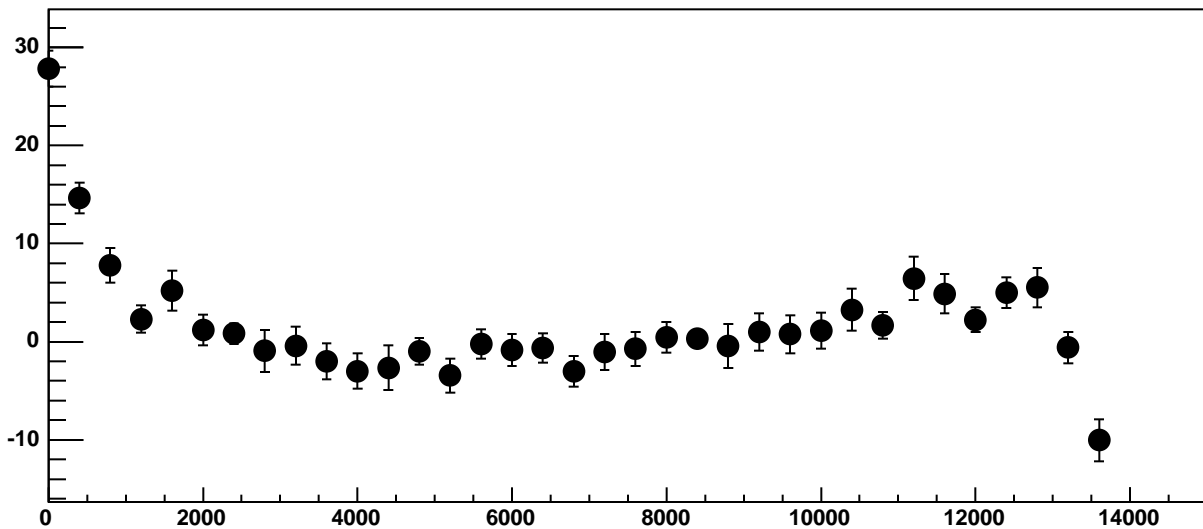


$\chi^2 / \text{ndf}$  29.85 / 23  
p0  $52.96 \pm 0.7273$   
p1  $0.0253 \pm 0.0001101$

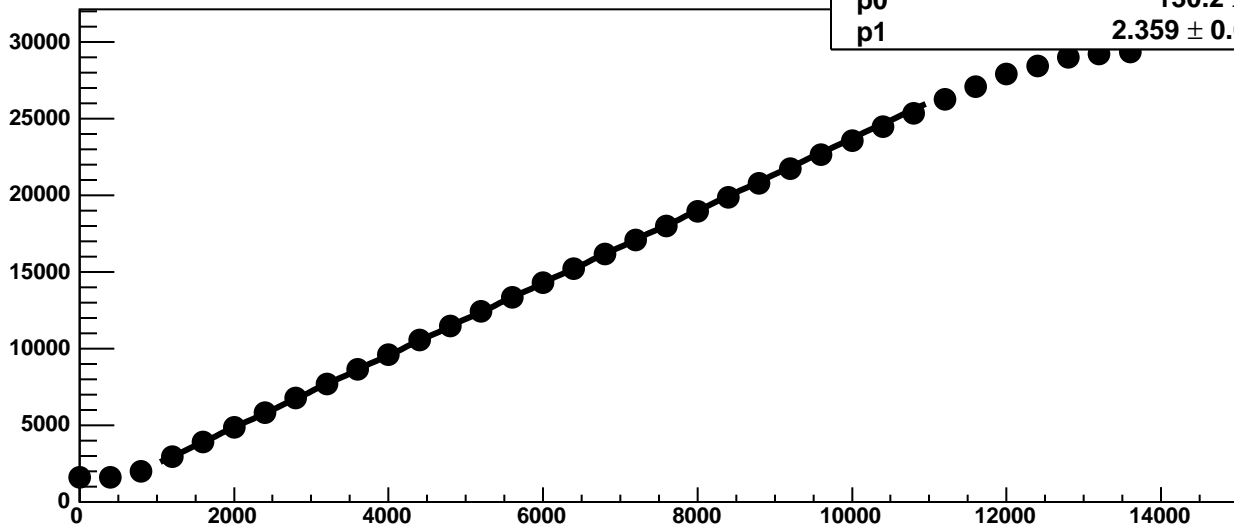
Chip 10, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



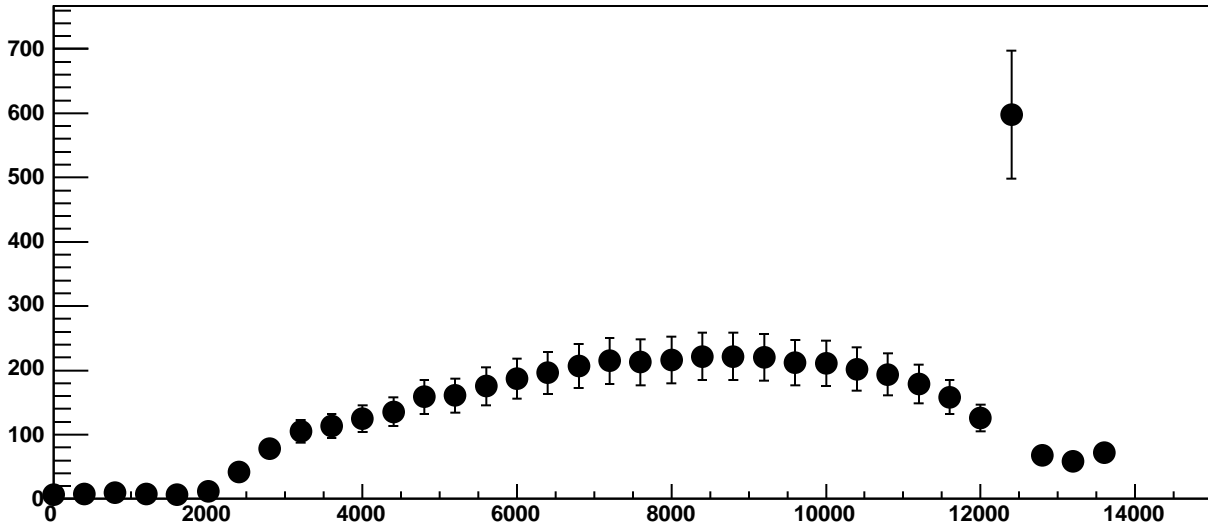
Chip 10, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



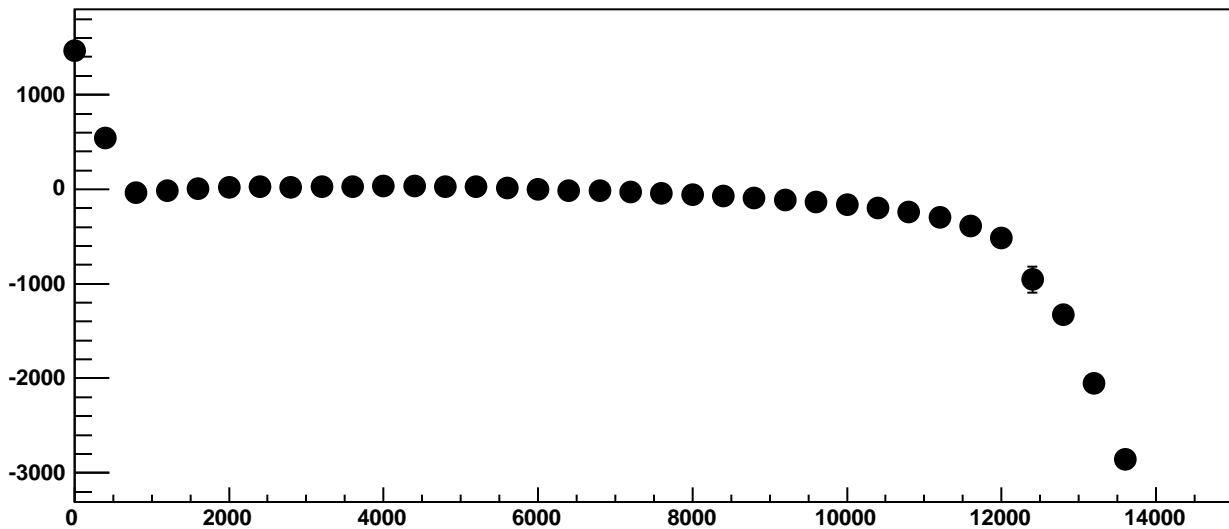
Chip 10, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



Chip 10, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

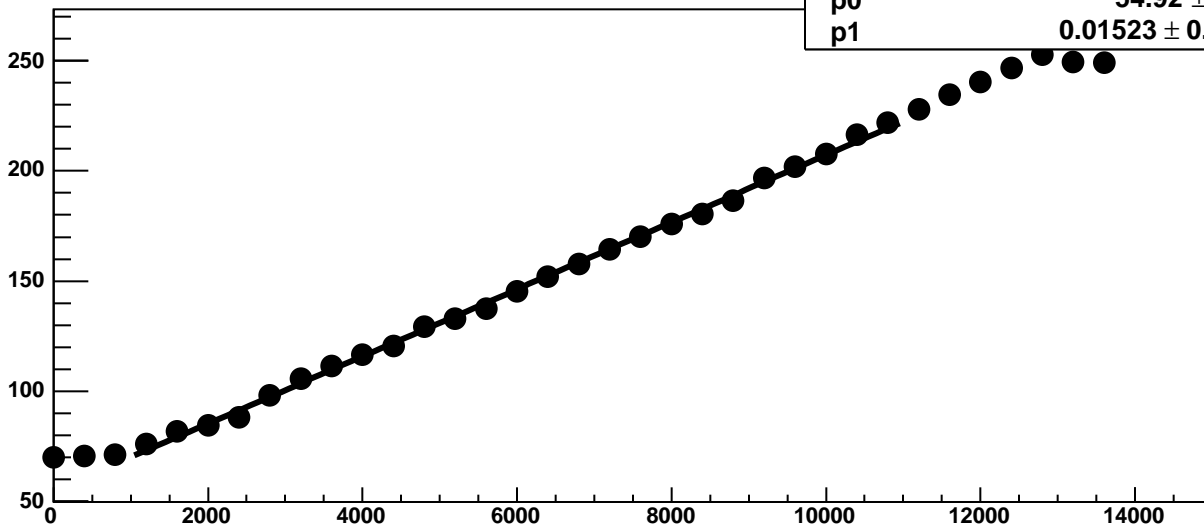


Chip 10, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



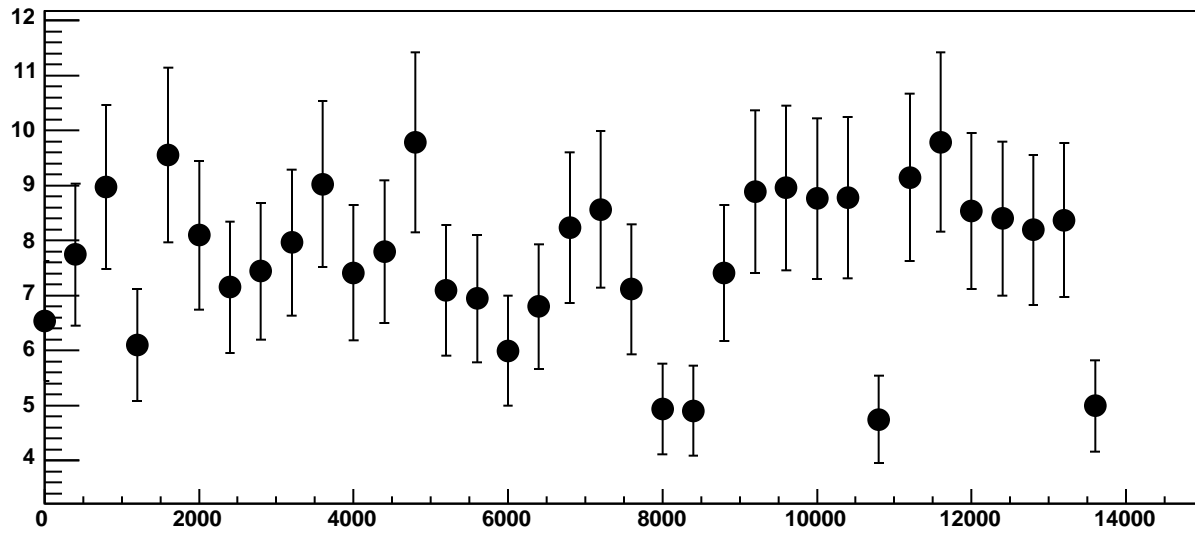


Chip 10, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

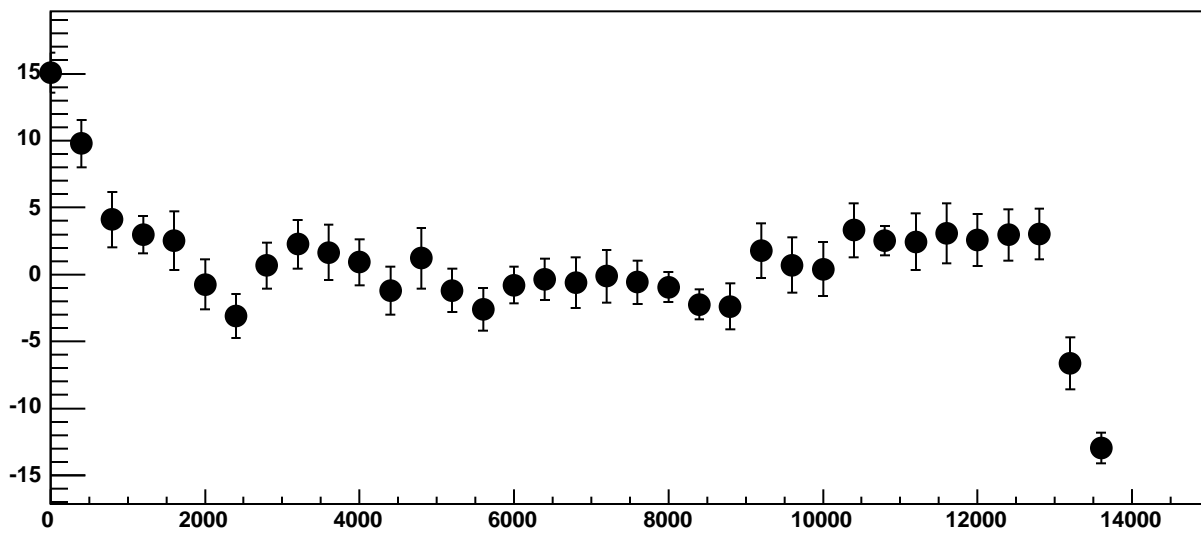


$\chi^2 / \text{ndf}$  32.36 / 23  
p0 54.92 ± 0.7808  
p1 0.01523 ± 0.000112

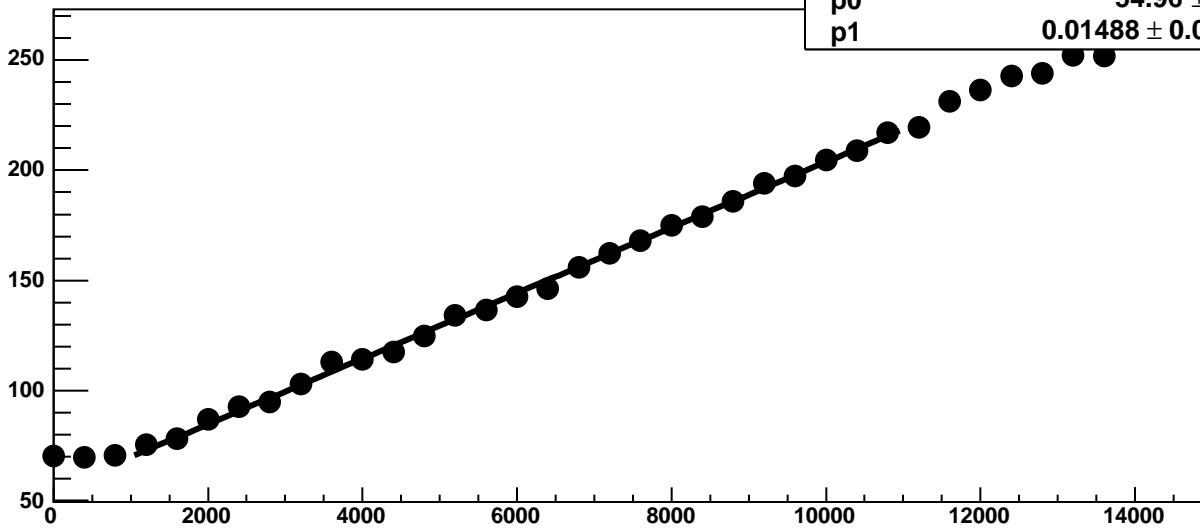
Chip 10, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

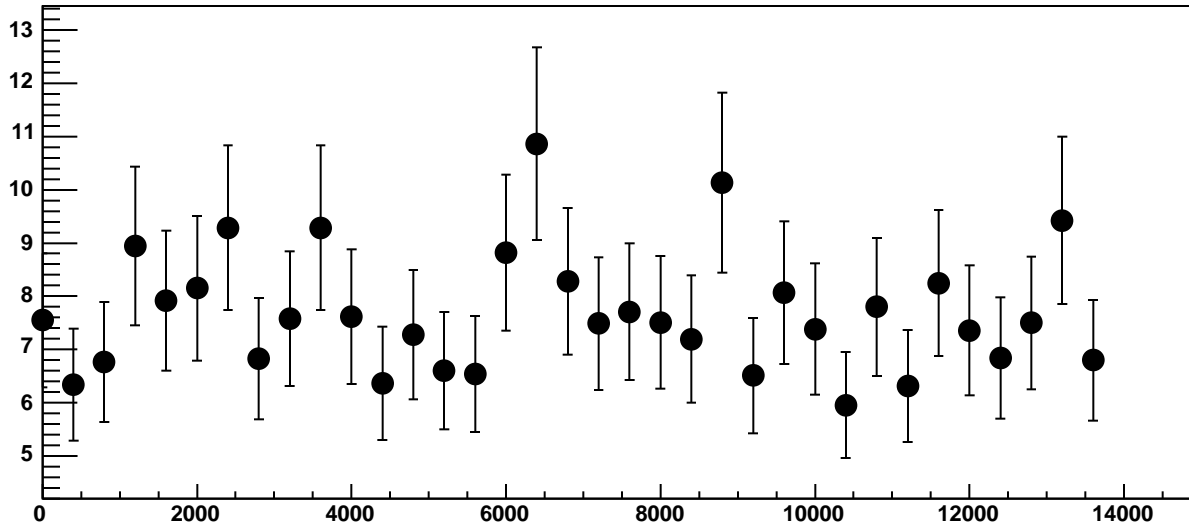


Chip 10, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

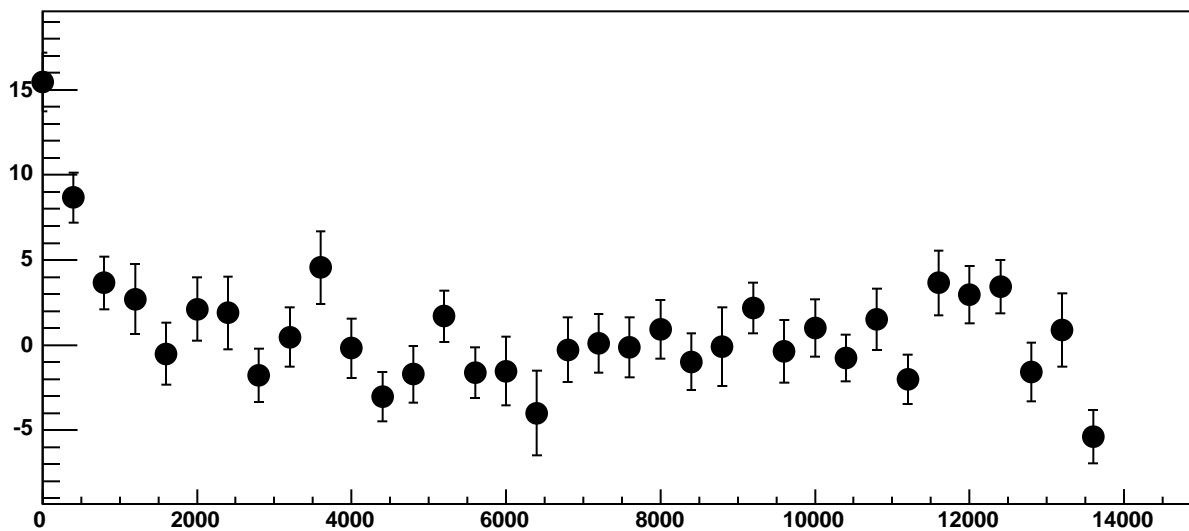


$\chi^2 / \text{ndf}$  24.97 / 23  
p0  $54.96 \pm 0.8275$   
p1  $0.01488 \pm 0.0001214$

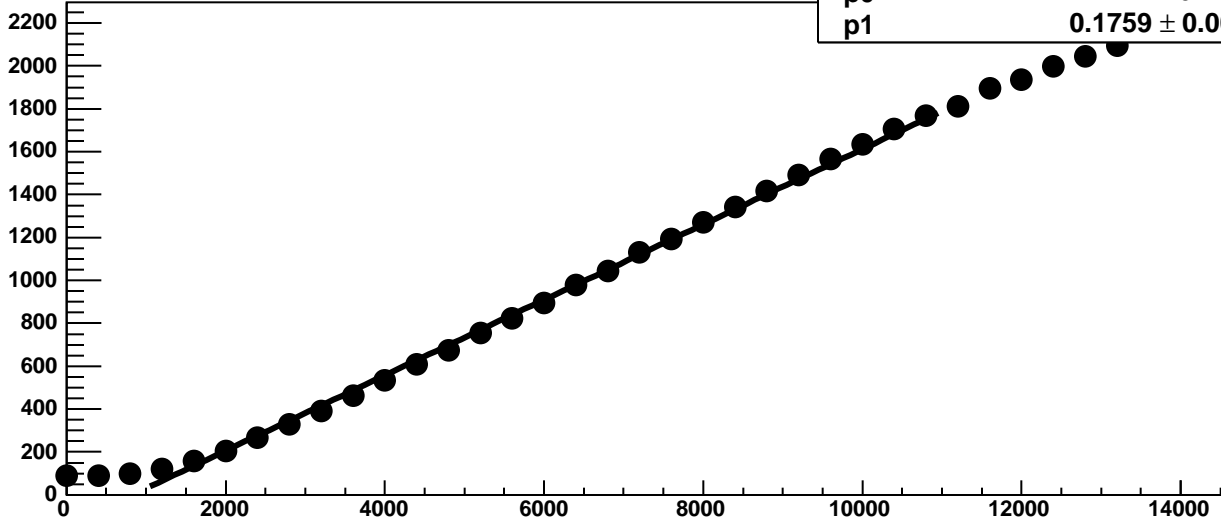
Chip 10, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

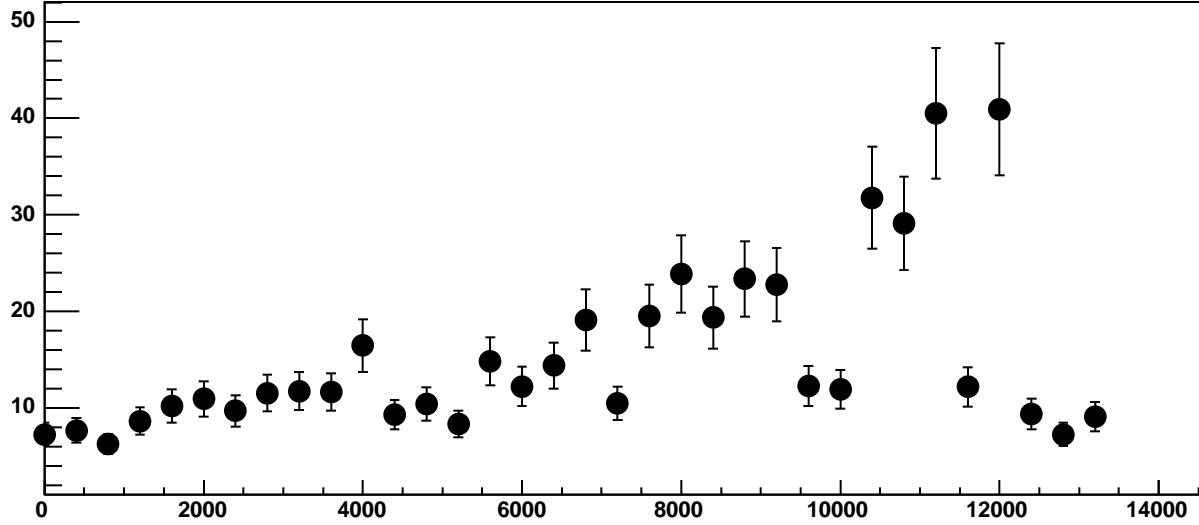


Chip 10, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

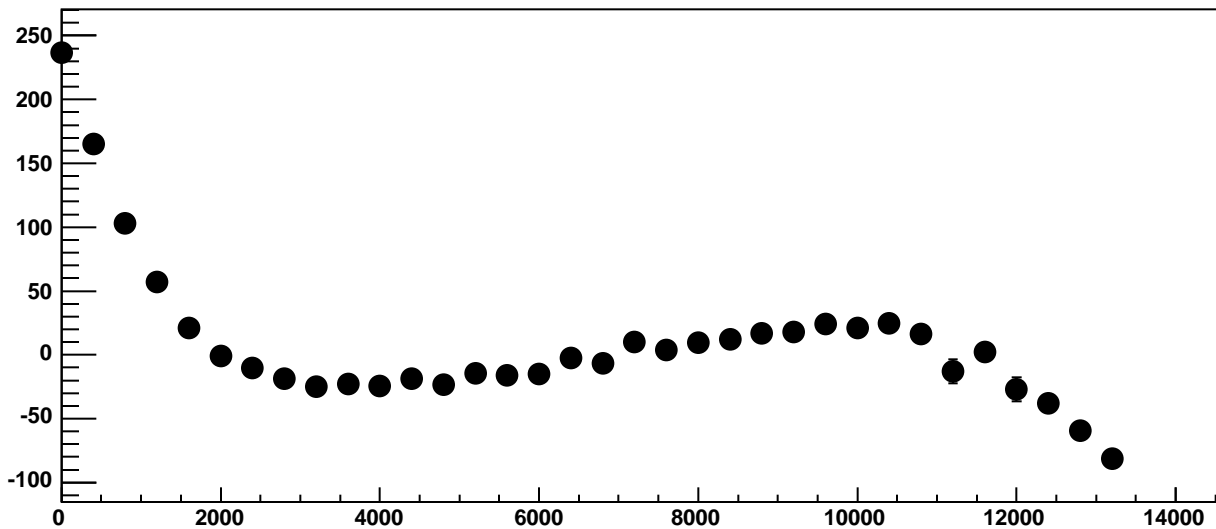


$\chi^2 / \text{ndf}$  1653 / 23  
p0  $-146.1 \pm 1.197$   
p1  $0.1759 \pm 0.0002179$

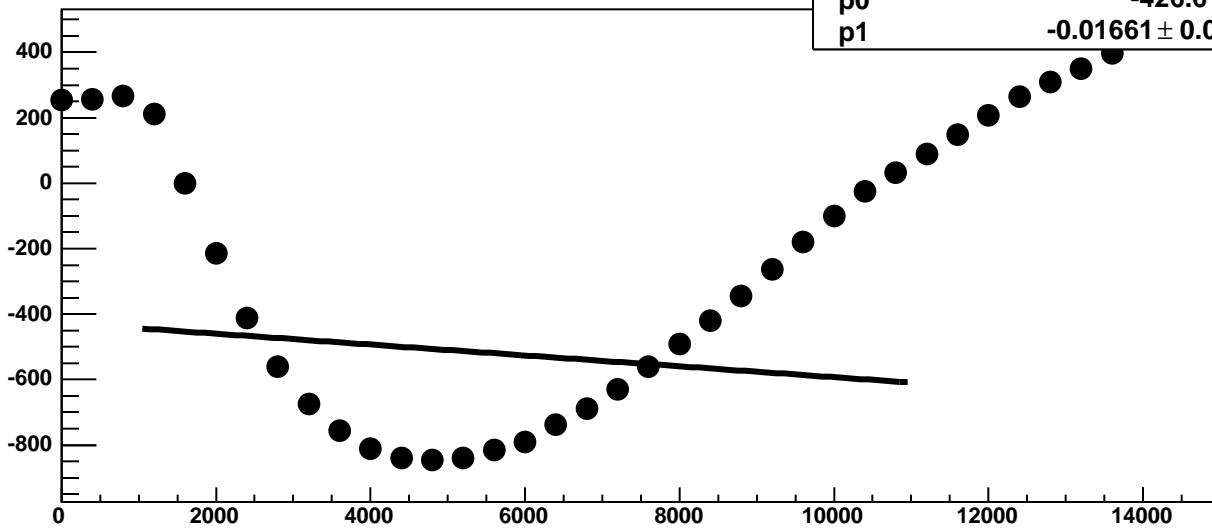
Chip 10, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

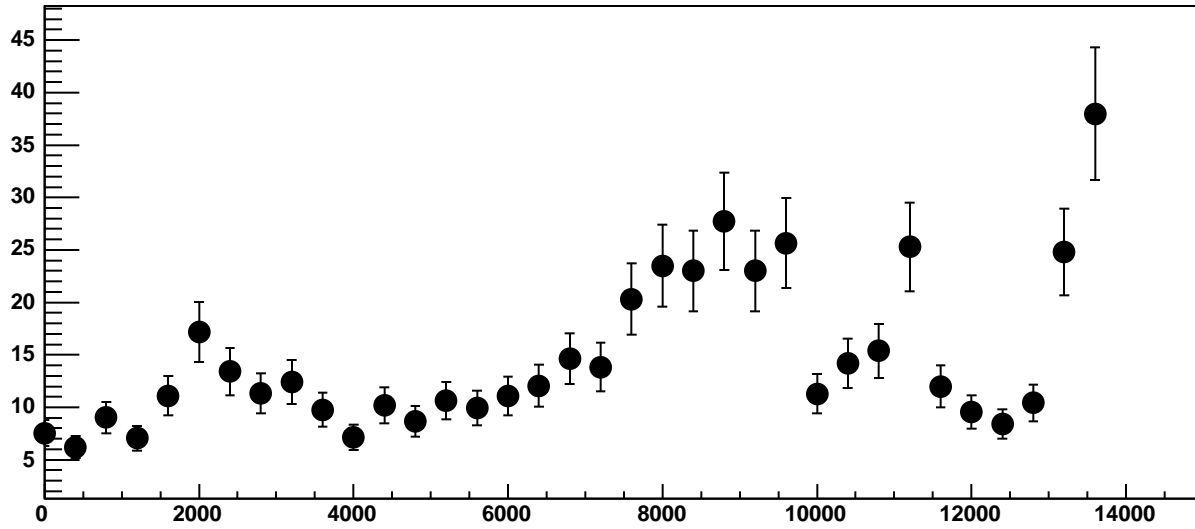


Chip 10, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

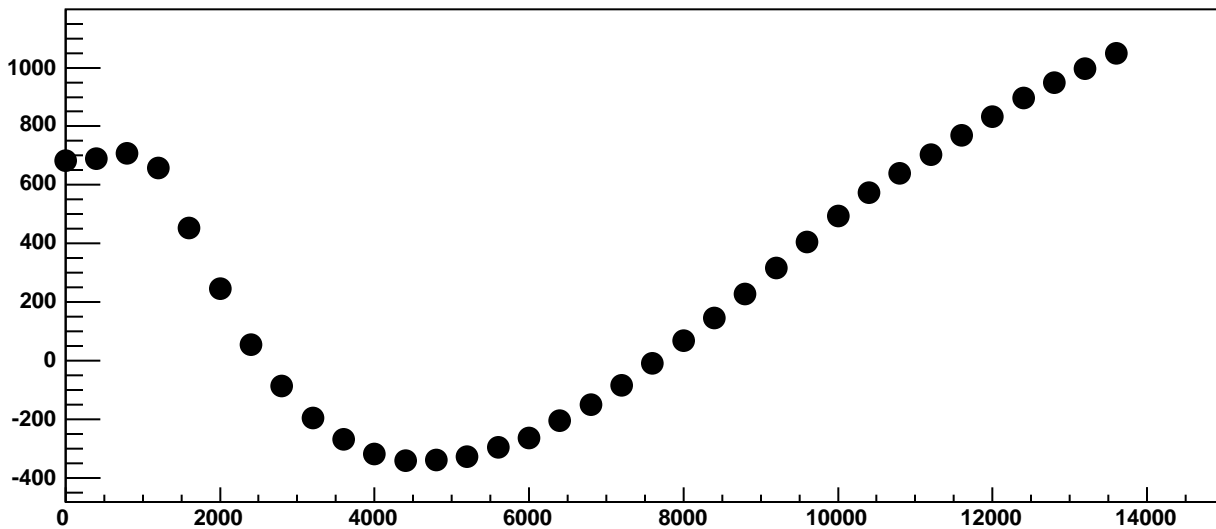


$\chi^2 / \text{ndf}$  4.742e+05 / 23  
p0 -426.6 ± 1.139  
p1 -0.01661 ± 0.0002061

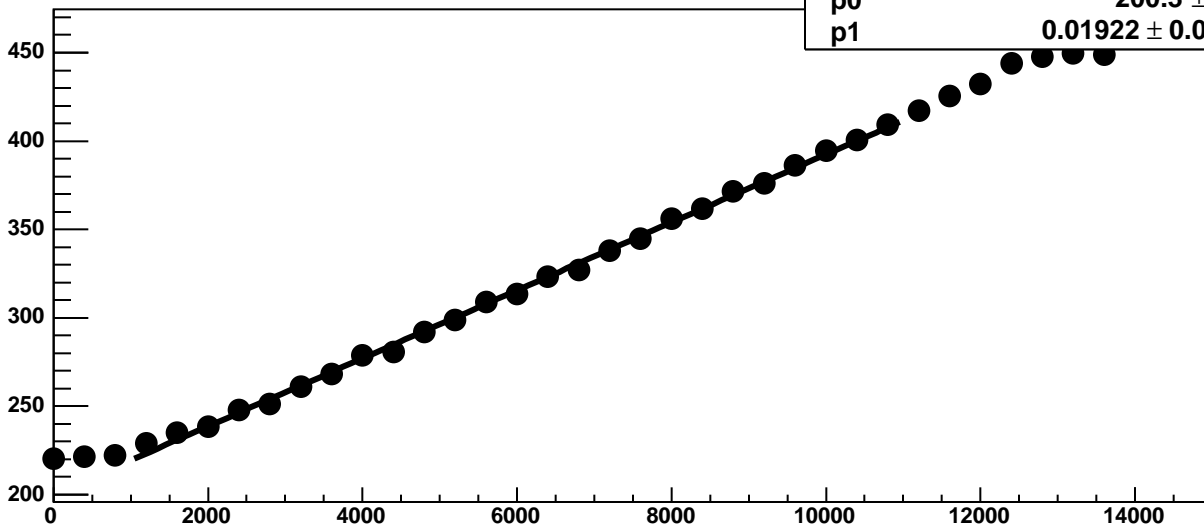
Chip 10, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

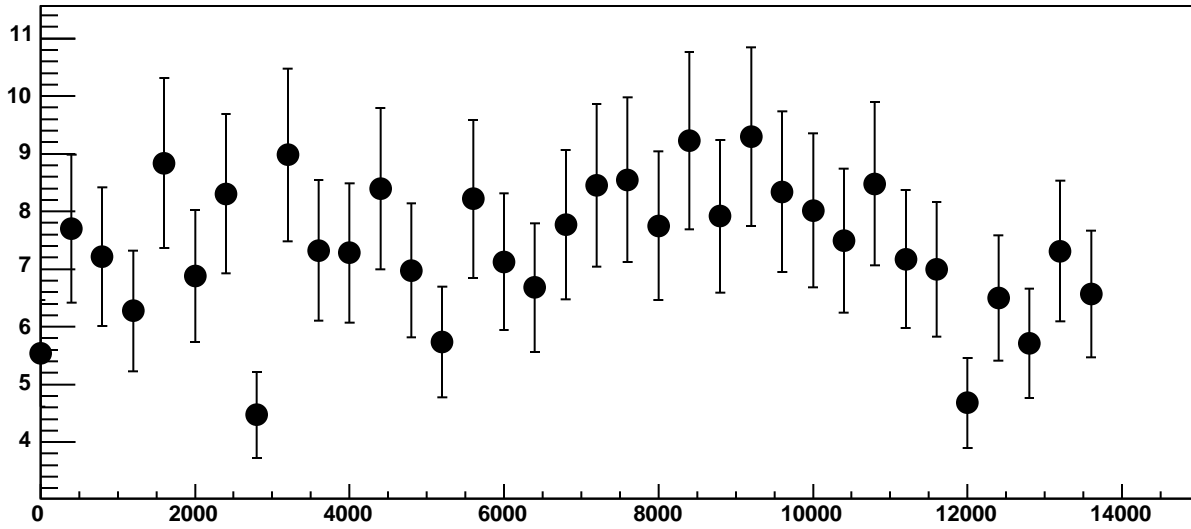


Chip 10, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

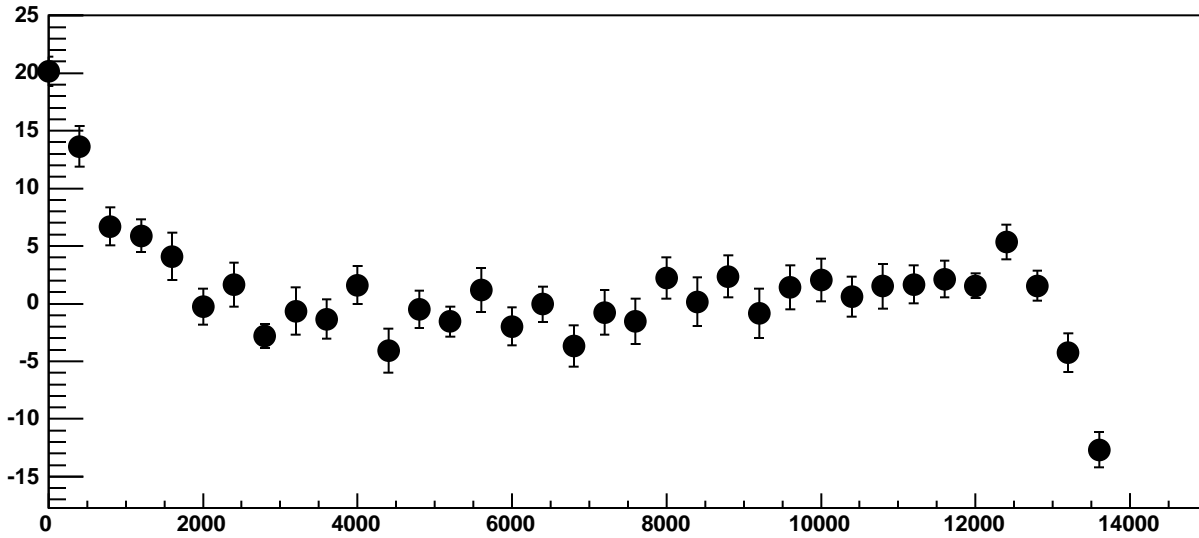


$\chi^2 / \text{ndf}$  49.48 / 23  
p0  $200.3 \pm 0.7442$   
p1  $0.01922 \pm 0.0001194$

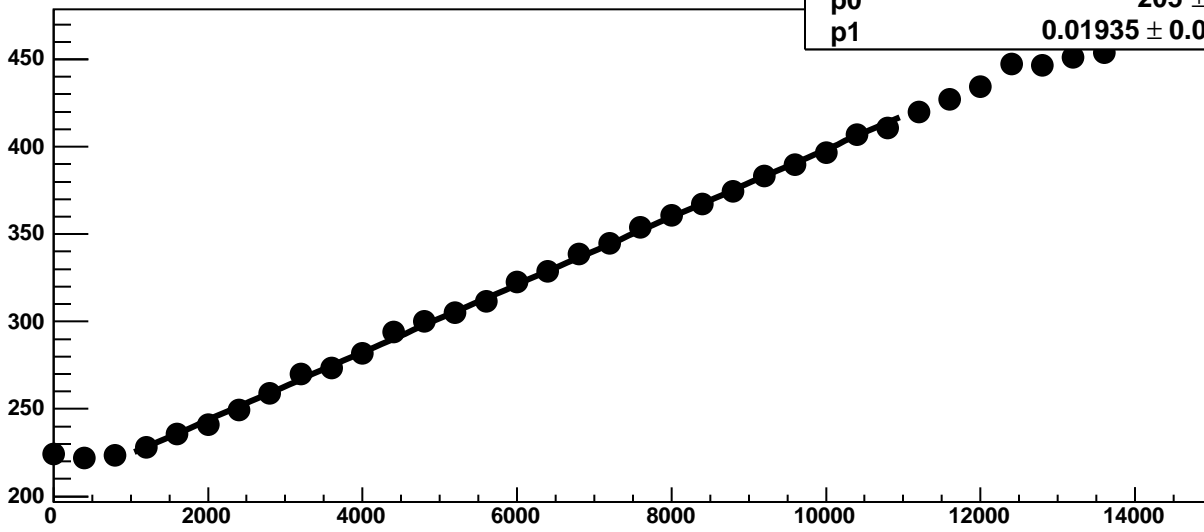
Chip 10, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

26.89 / 23

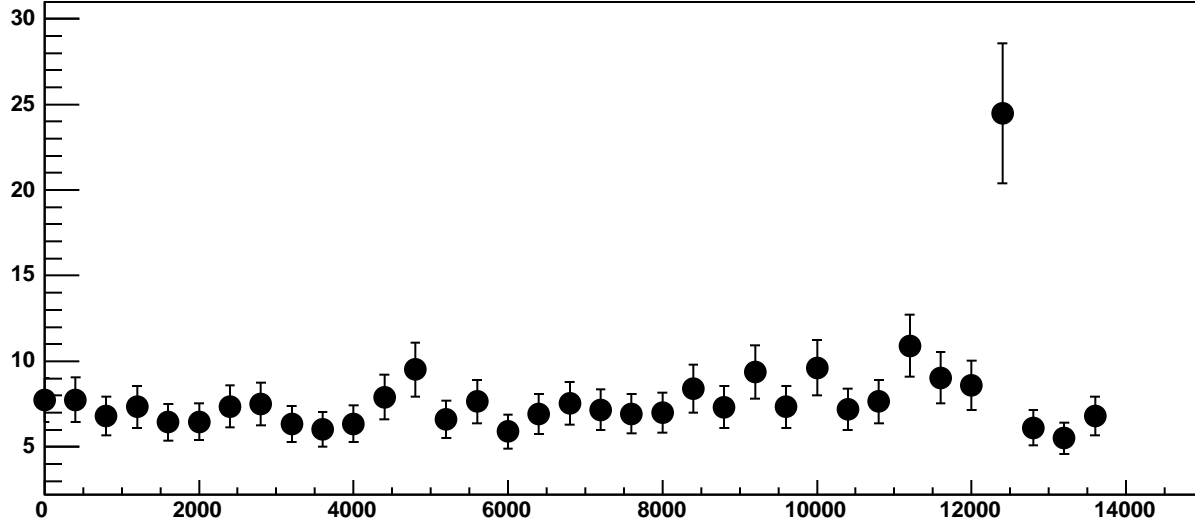
p0

$205 \pm 0.7407$

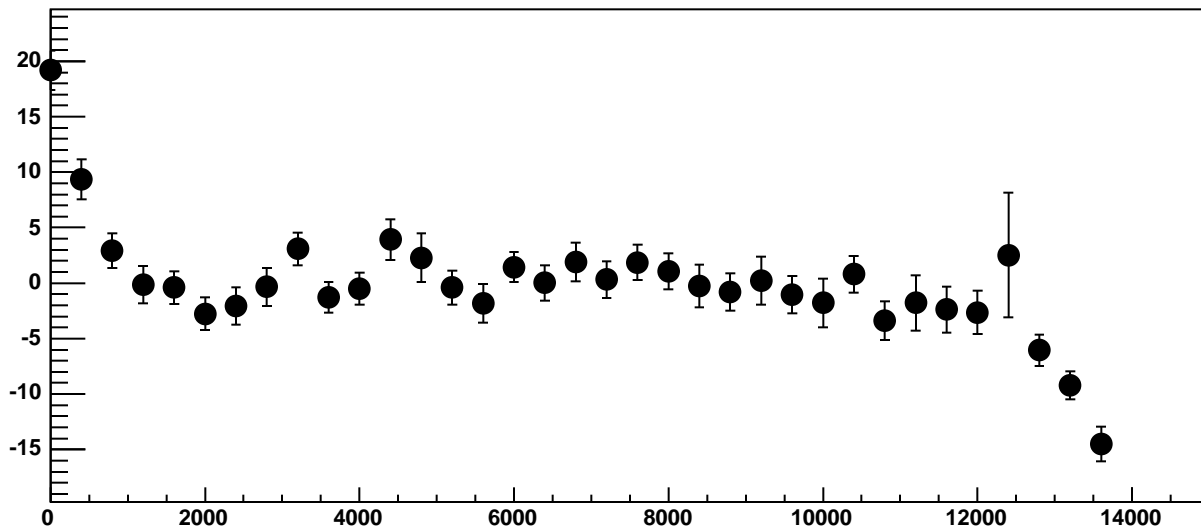
p1

$0.01935 \pm 0.0001163$

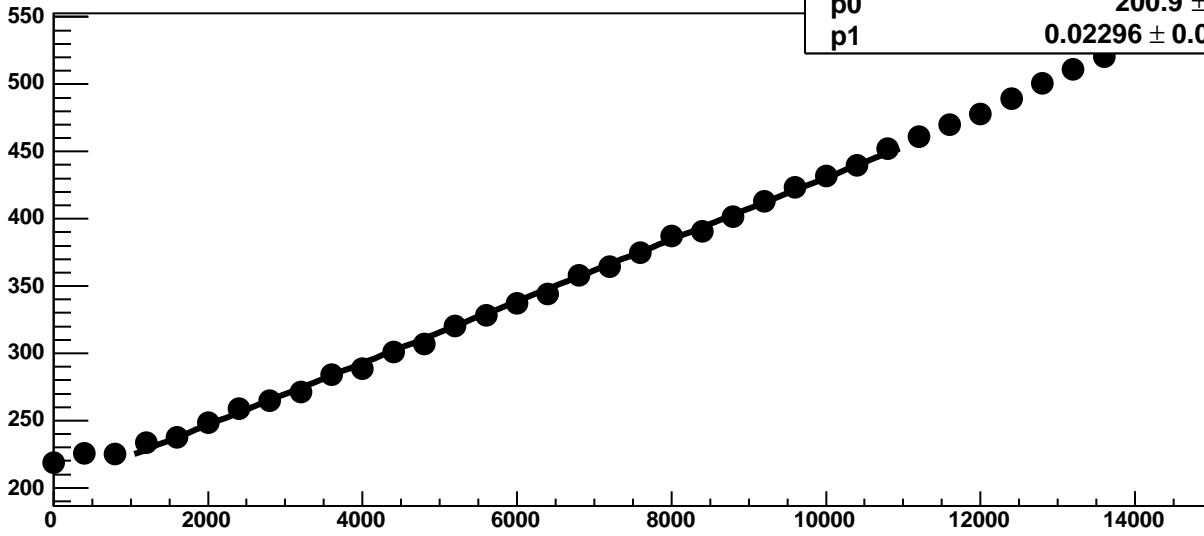
Chip 10, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

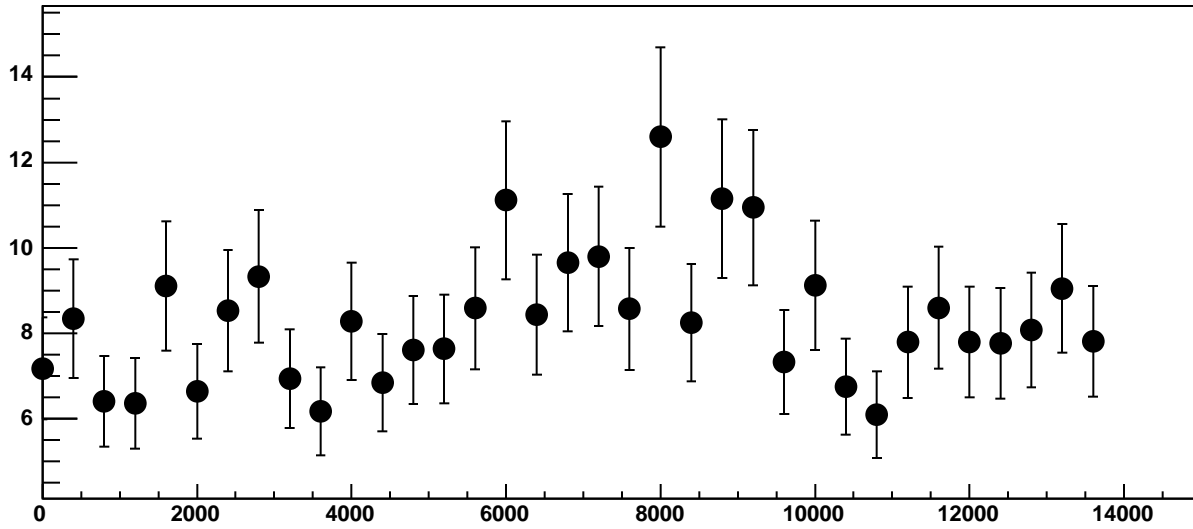


Chip 10, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

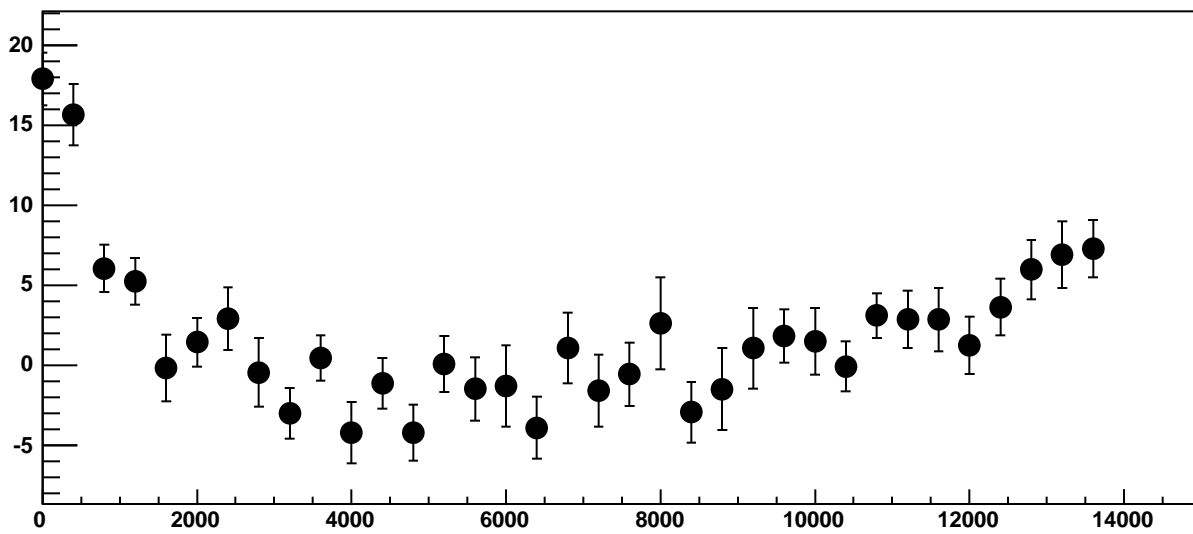


$\chi^2 / \text{ndf}$  47.09 / 23  
p0  $200.9 \pm 0.7804$   
p1  $0.02296 \pm 0.0001192$

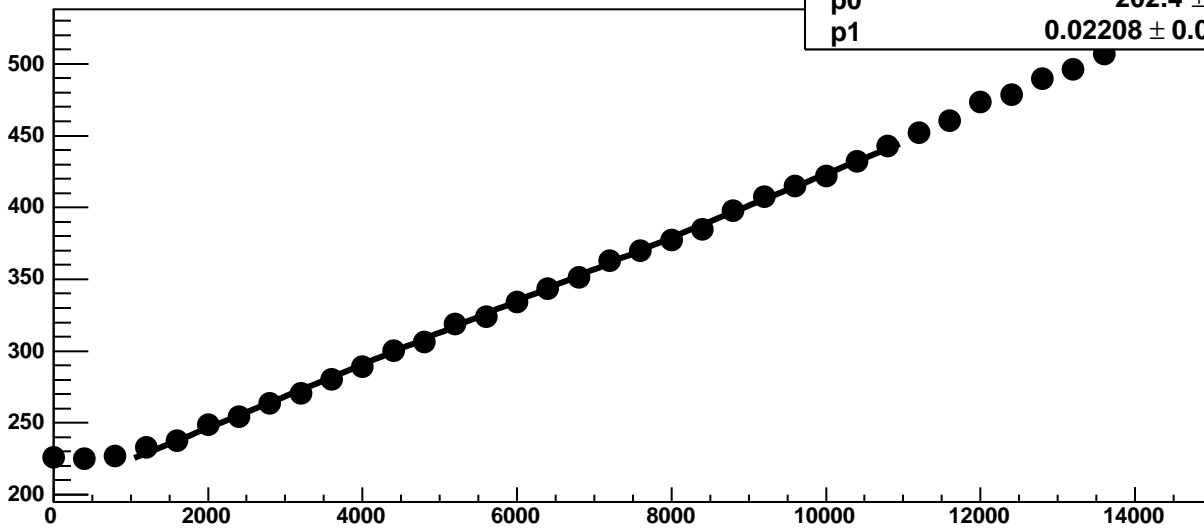
Chip 10, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

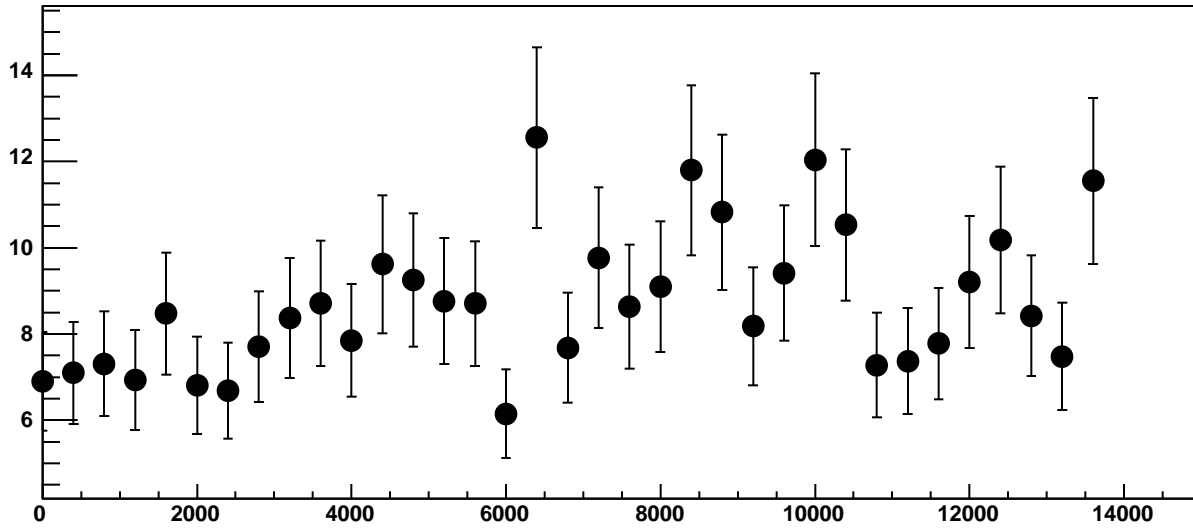


Chip 10, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

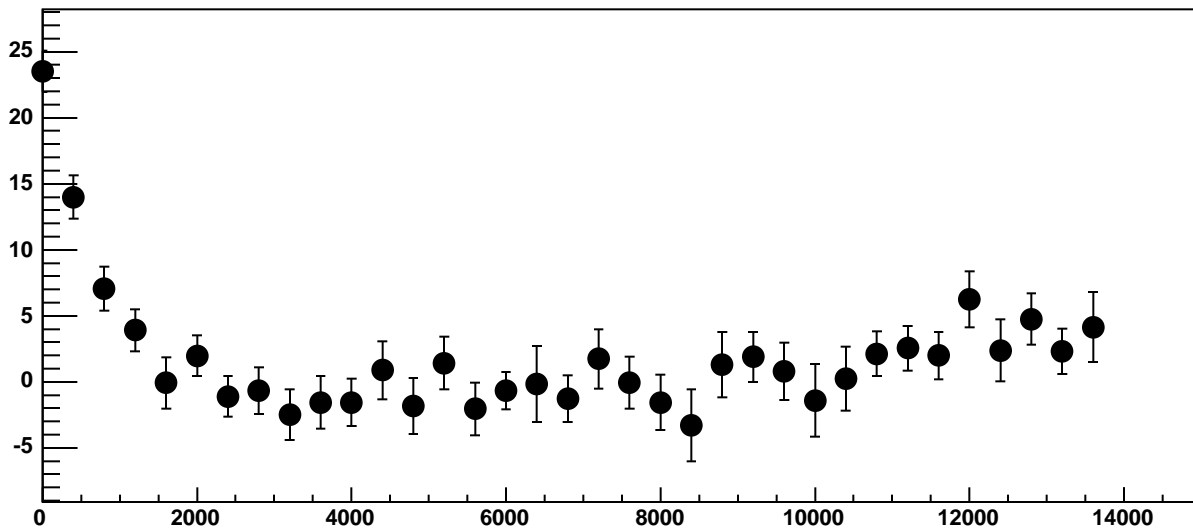


$\chi^2 / \text{ndf}$  20.5 / 23  
p0  $202.4 \pm 0.8246$   
p1  $0.02208 \pm 0.0001321$

Chip 10, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

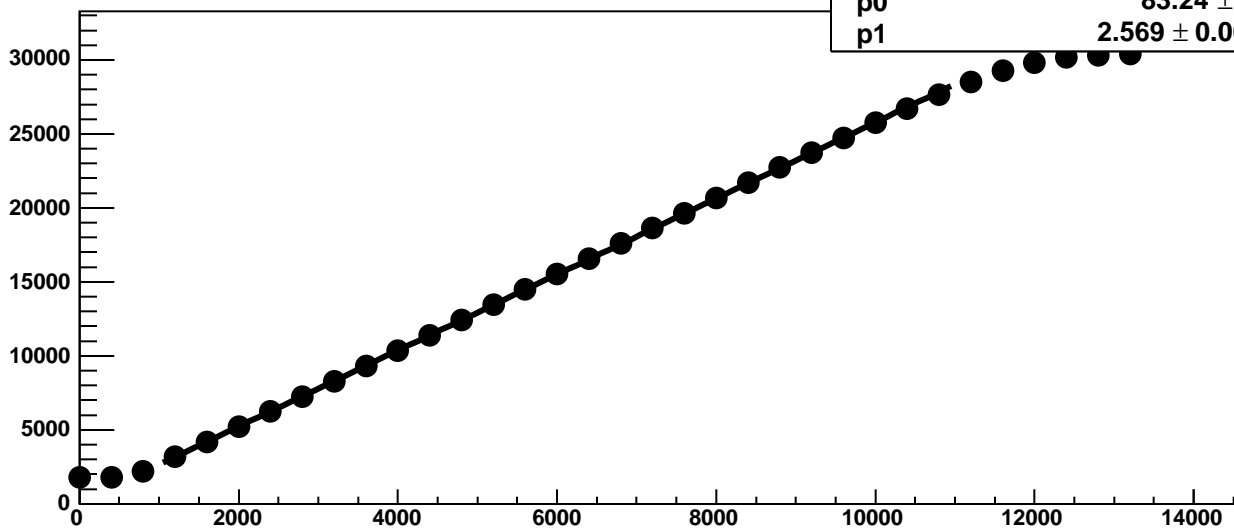


Chip 10, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

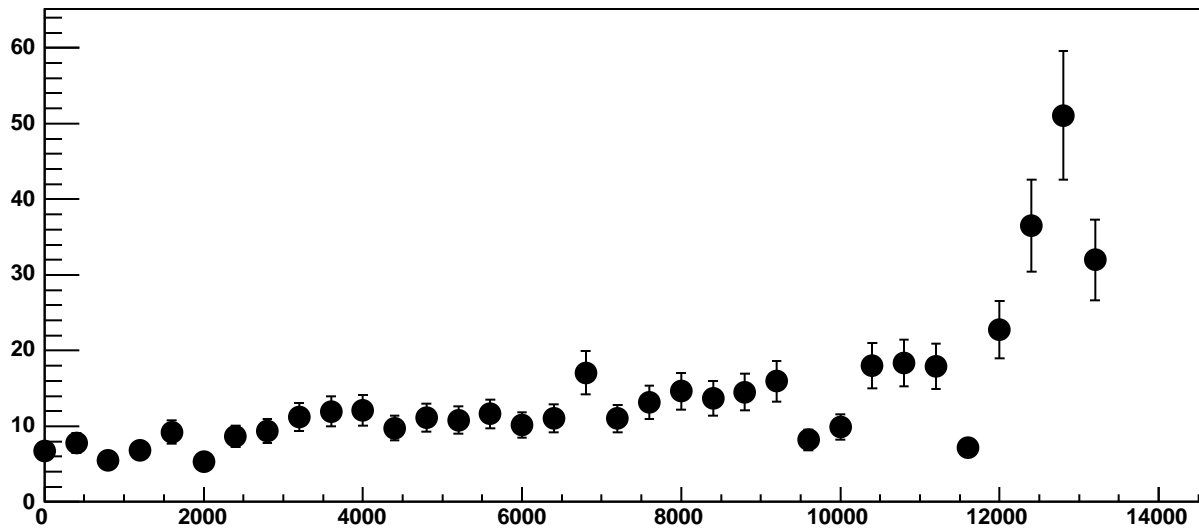




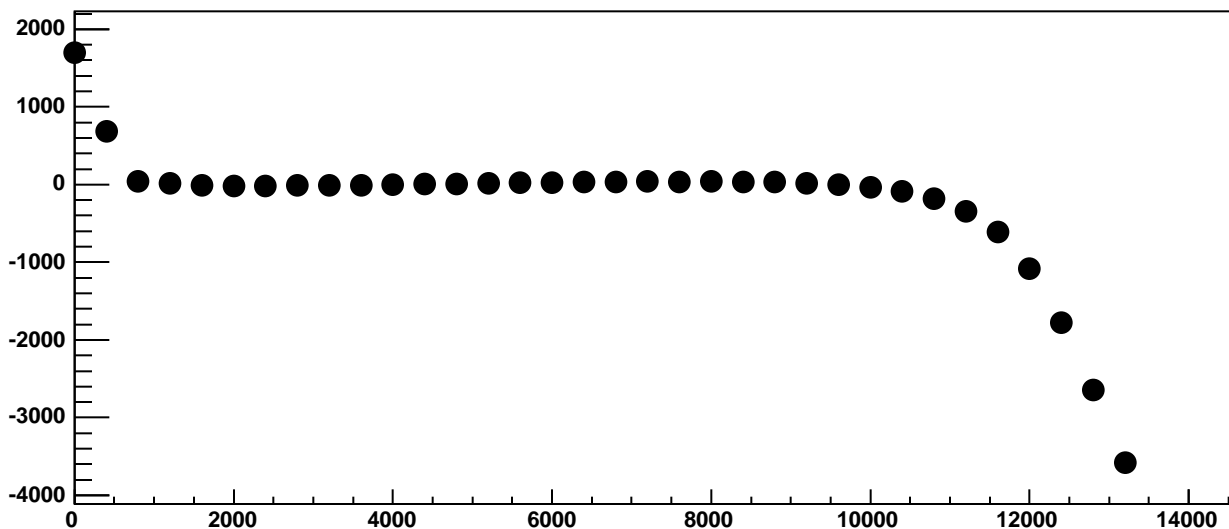
Chip 10, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC



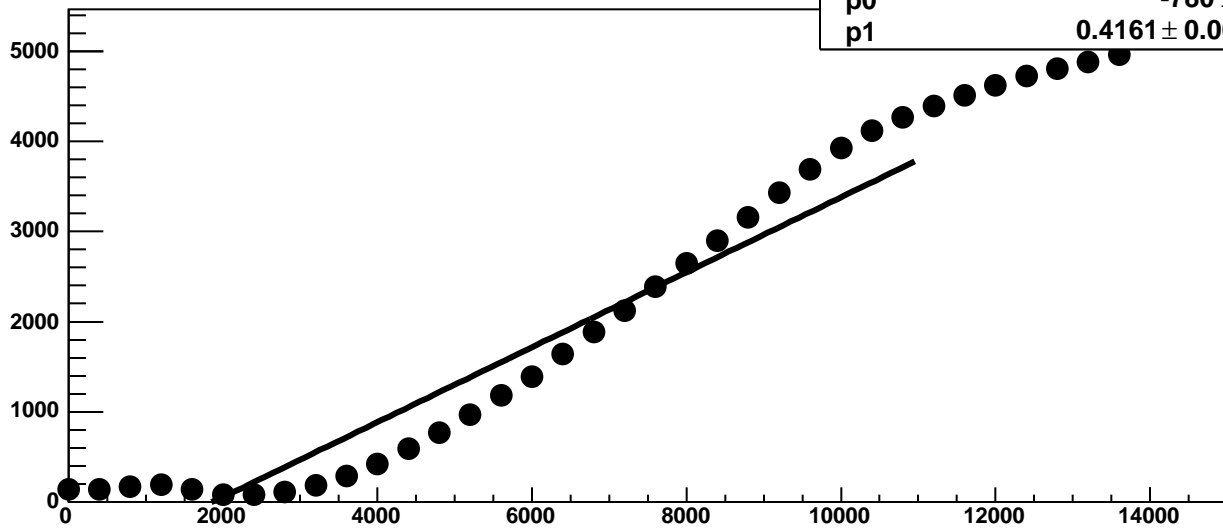
Chip 10, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

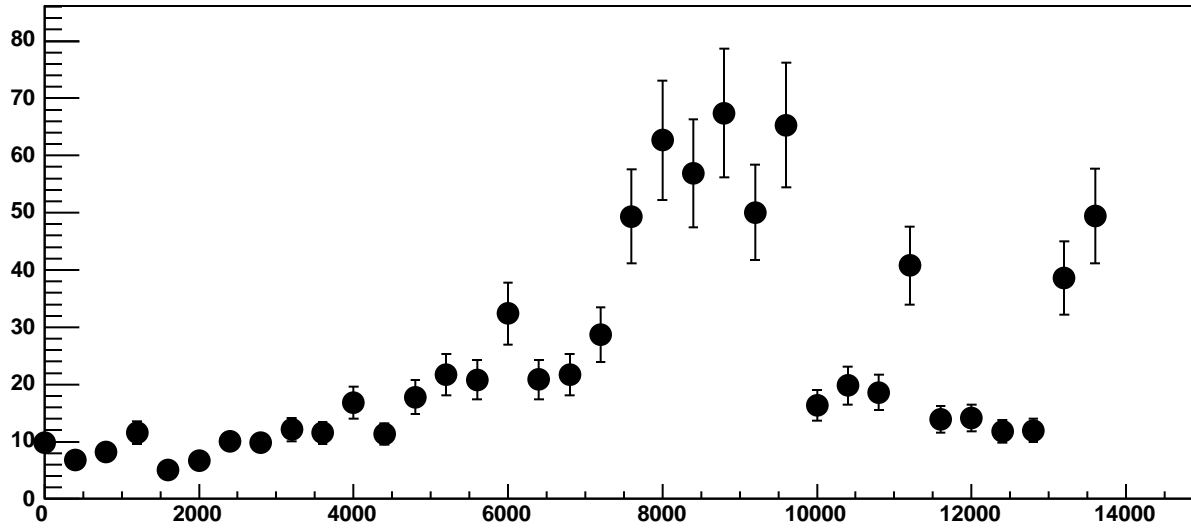


Chip 10, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

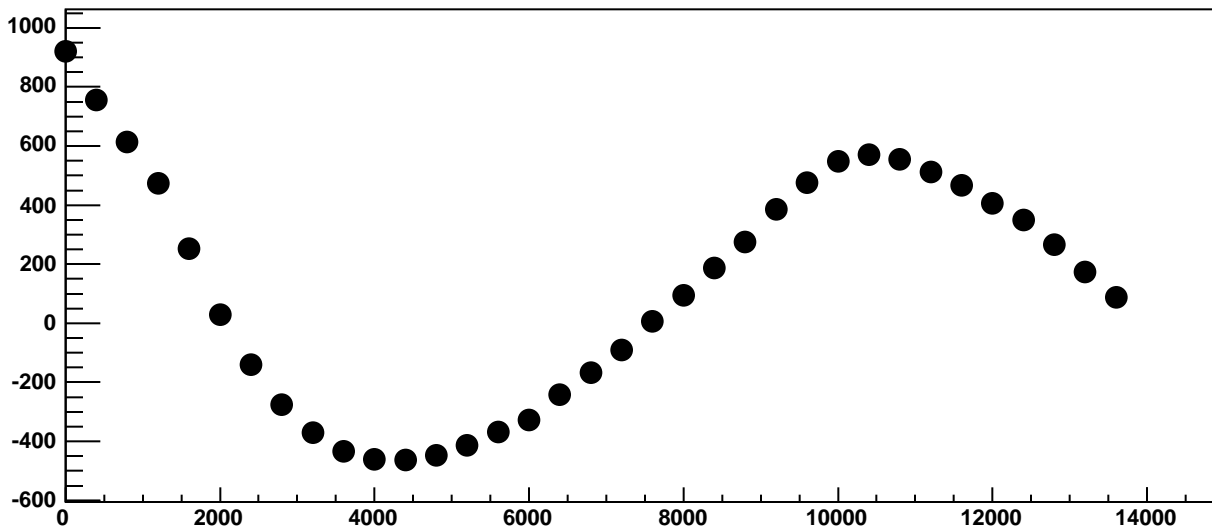


$\chi^2 / \text{ndf}$  2.767e+05 / 23  
p0 -780 ± 1.034  
p1 0.4161 ± 0.0002538

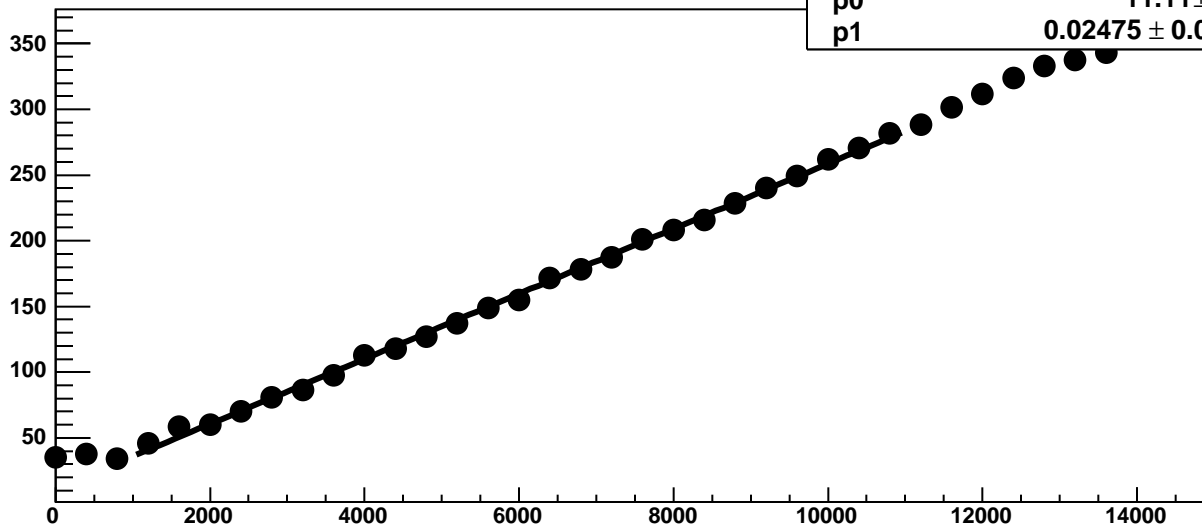
Chip 10, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

98.78 / 23

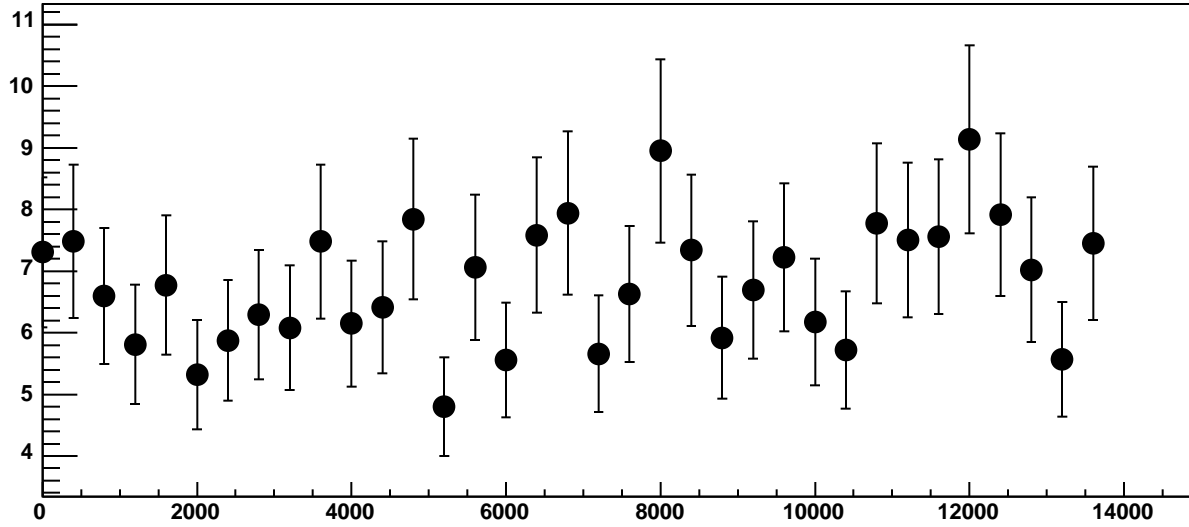
p0

$11.11 \pm 0.6524$

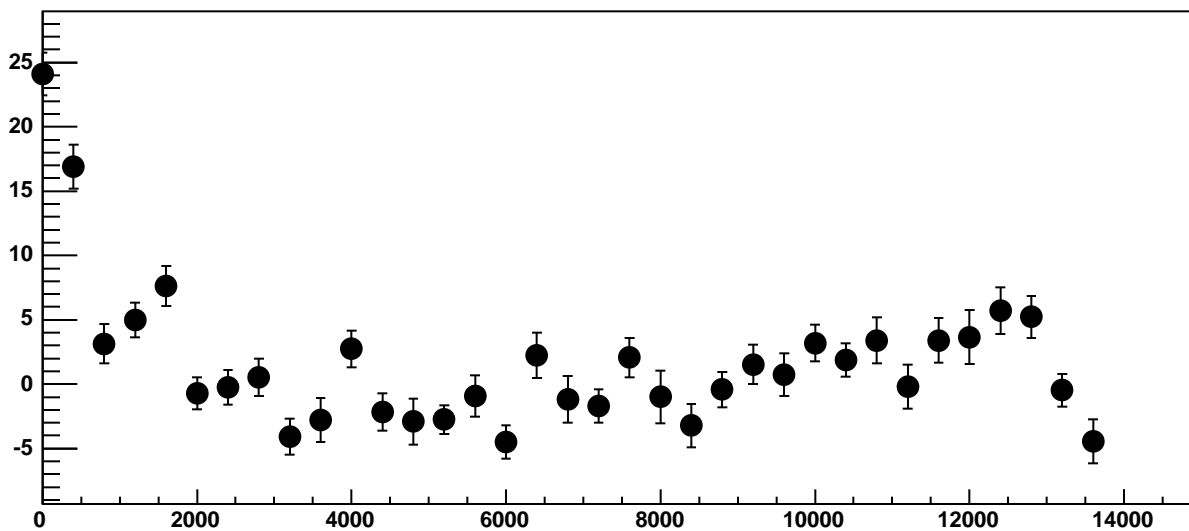
p1

$0.02475 \pm 0.0001008$

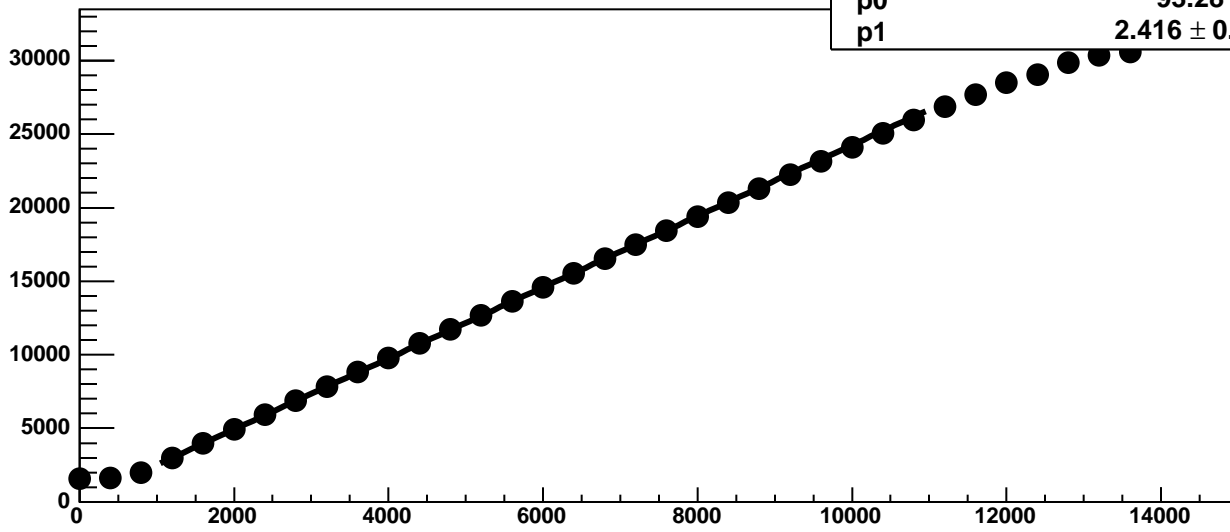
Chip 10, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

210 / 23

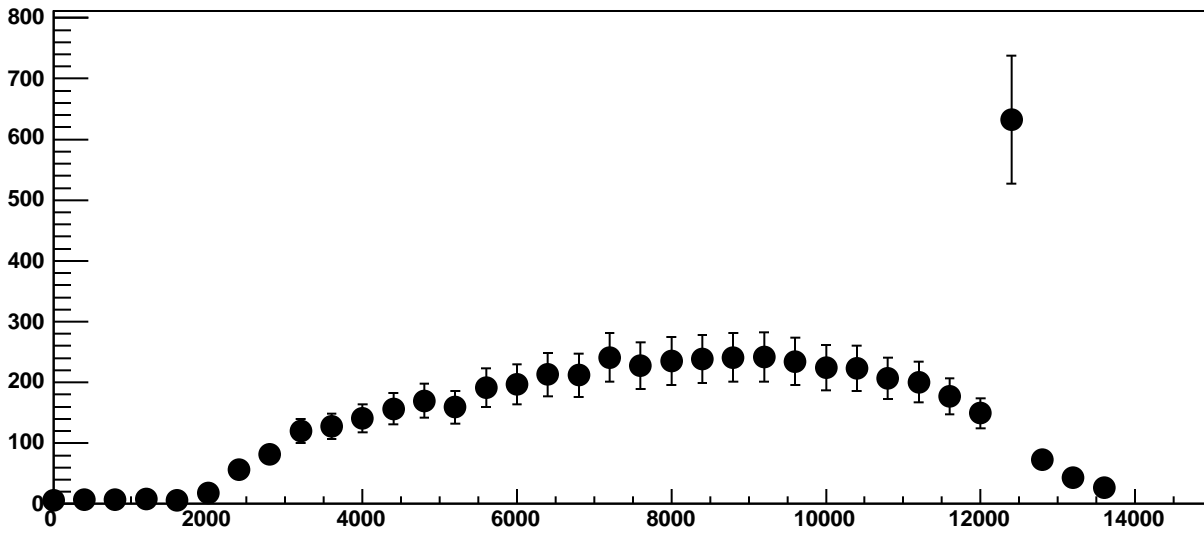
p0

$93.28 \pm 2.817$

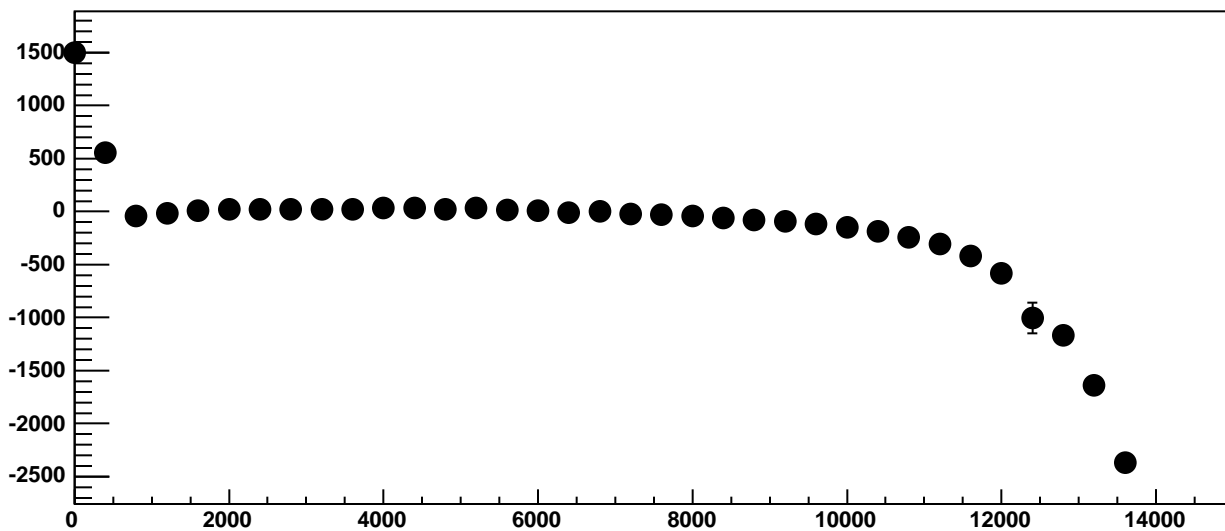
p1

$2.416 \pm 0.001701$

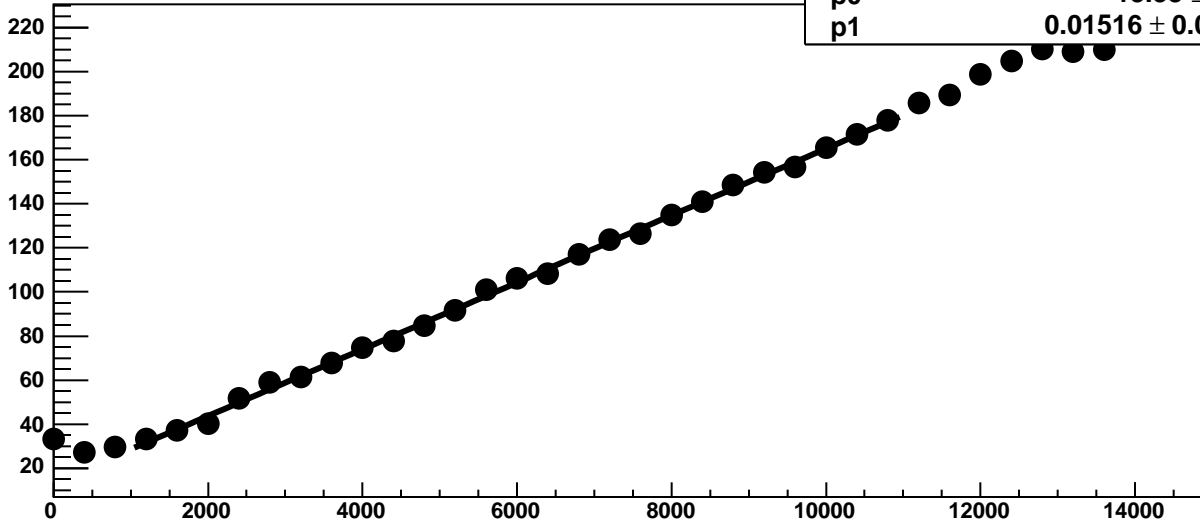
Chip 10, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

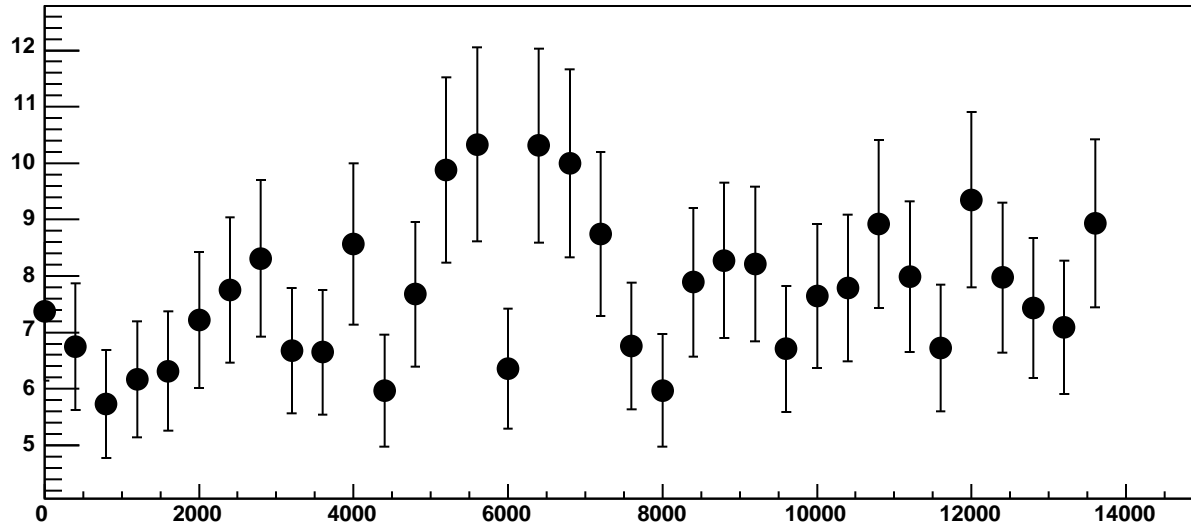


Chip 10, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

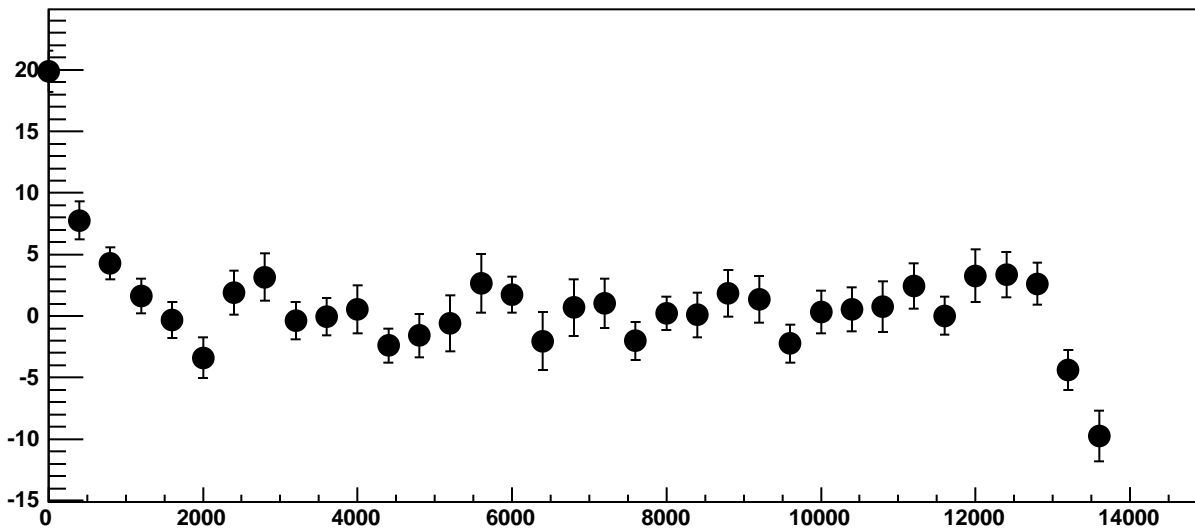


$\chi^2 / \text{ndf}$  22.97 / 23  
p0  $13.38 \pm 0.7489$   
p1  $0.01516 \pm 0.0001159$

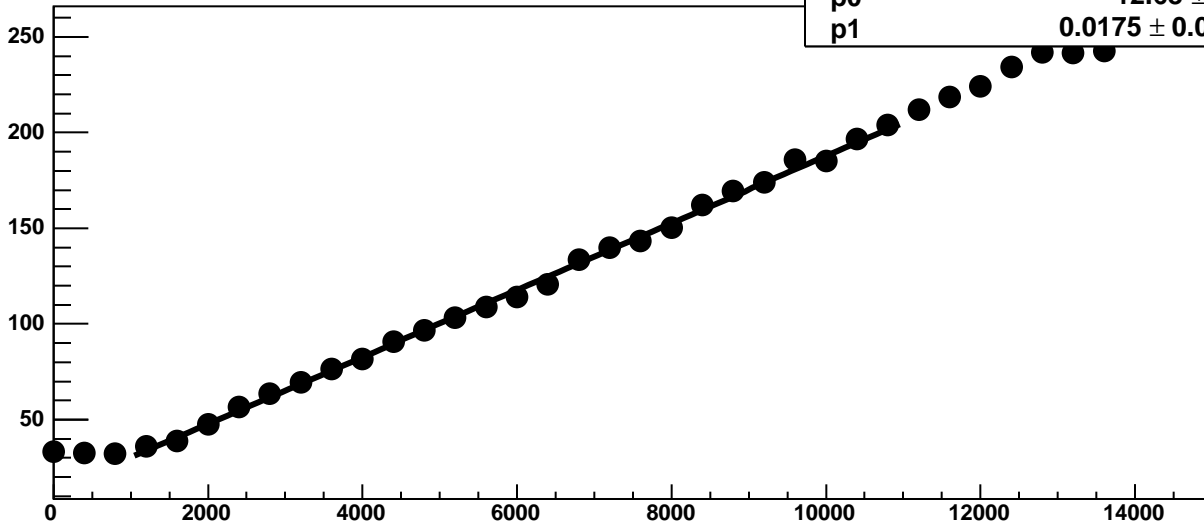
Chip 10, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

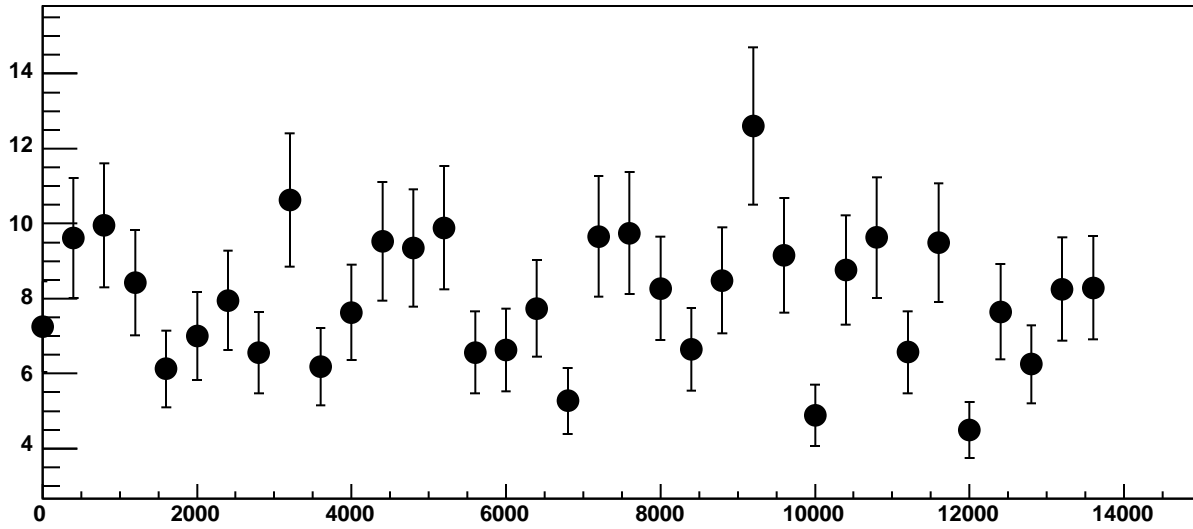


Chip 10, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

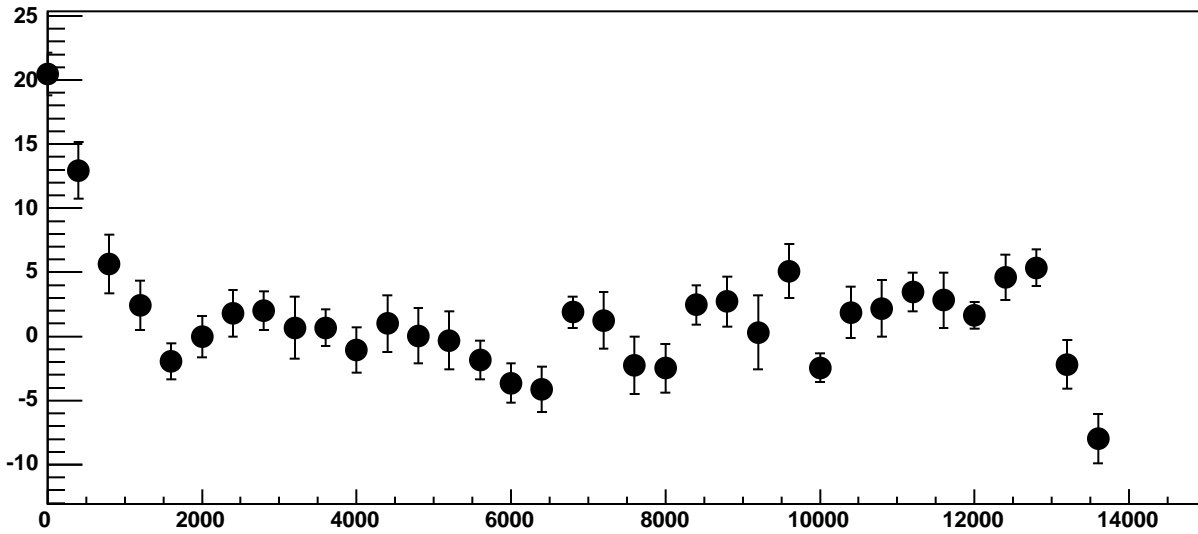


$\chi^2 / \text{ndf}$  42.27 / 23  
p0 12.68 ± 0.7843  
p1 0.0175 ± 0.0001192

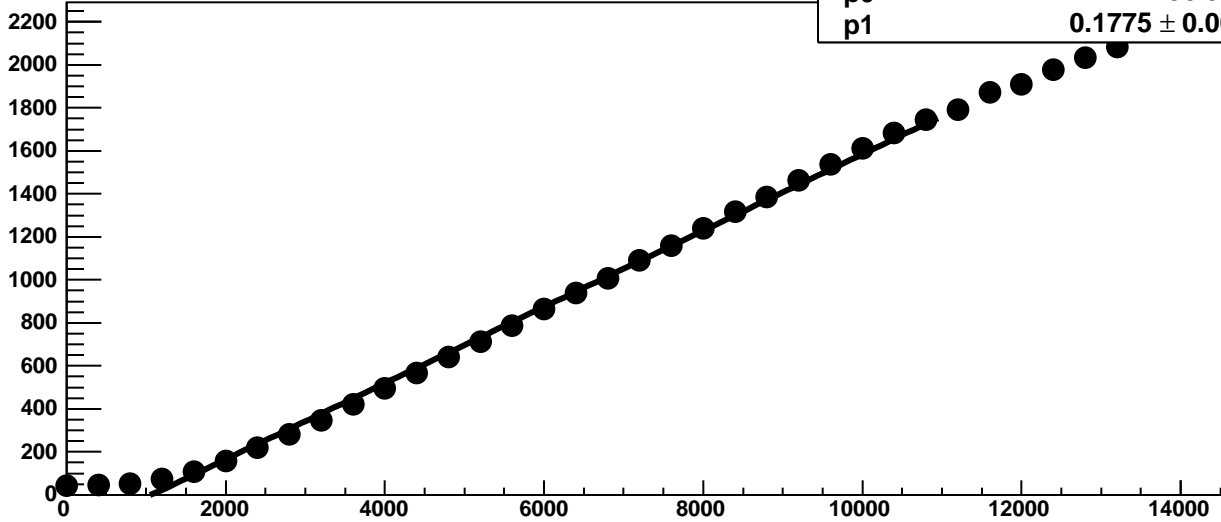
Chip 10, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

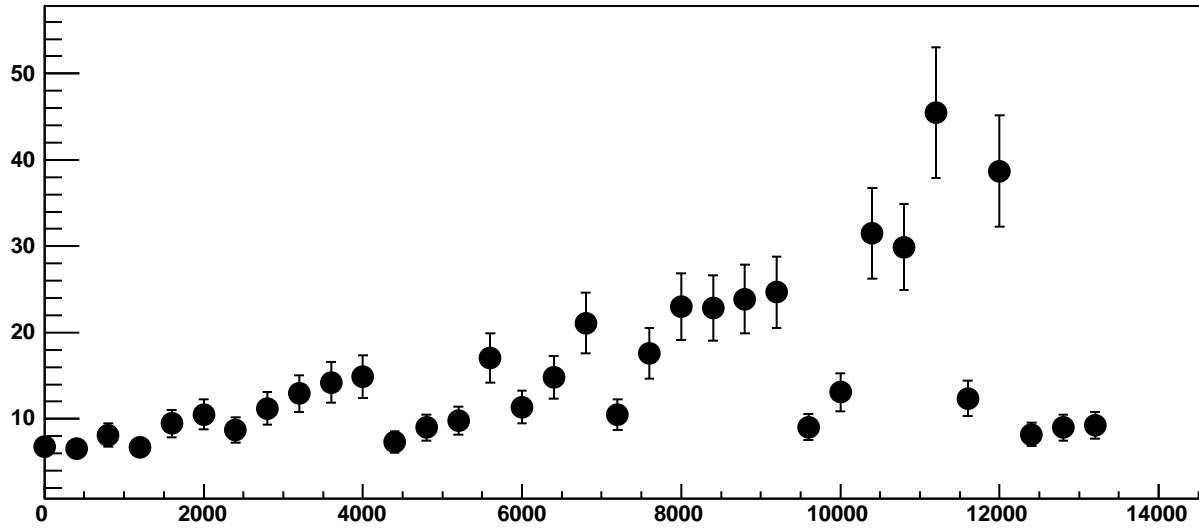


Chip 10, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

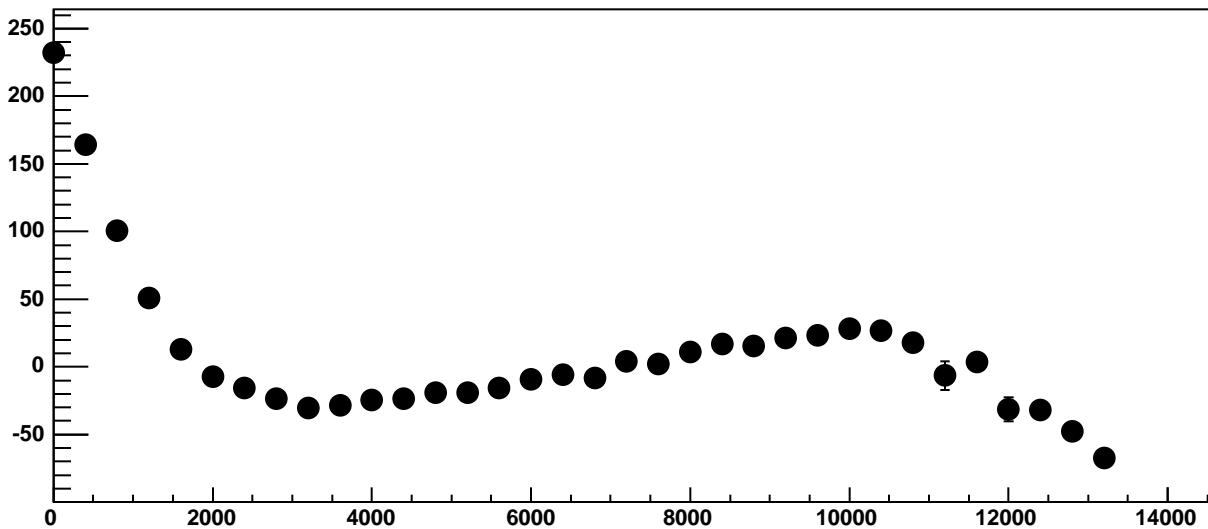


$\chi^2 / \text{ndf}$  2184 / 23  
p0  $-190.5 \pm 1.07$   
p1  $0.1775 \pm 0.0001984$

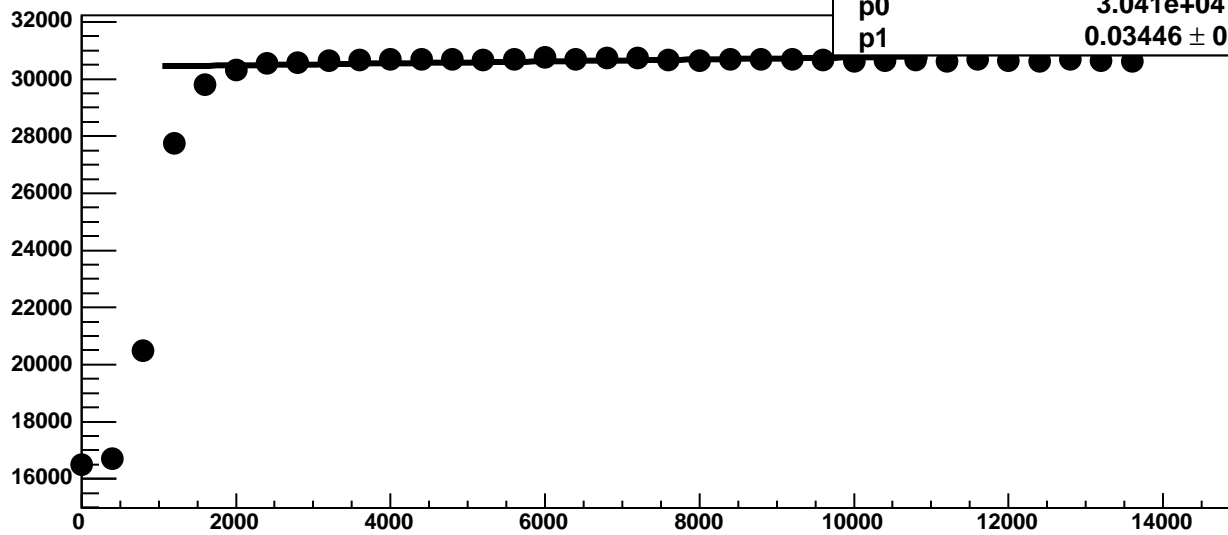
Chip 10, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC



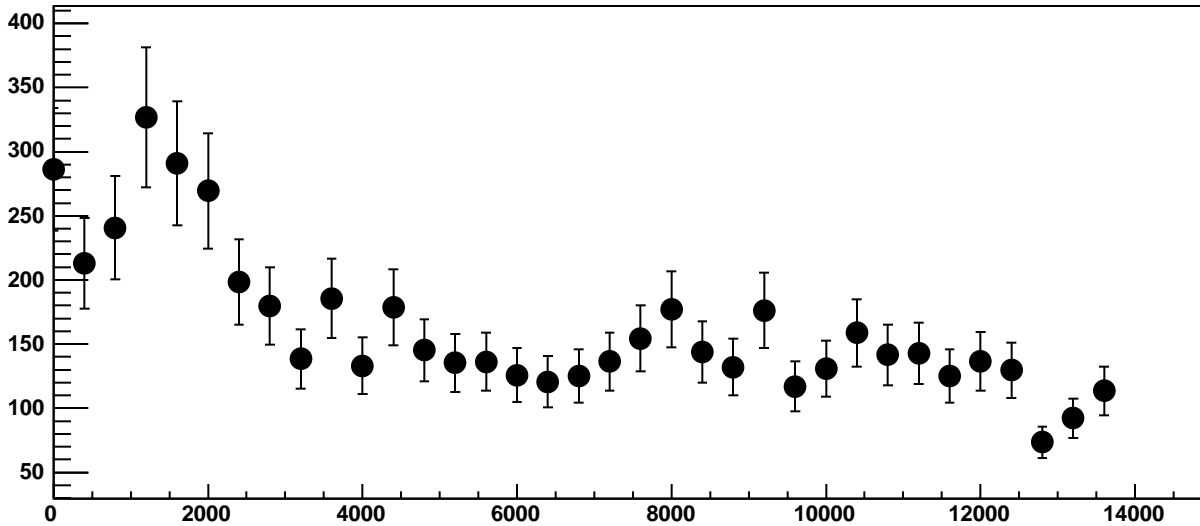
Chip 10, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC



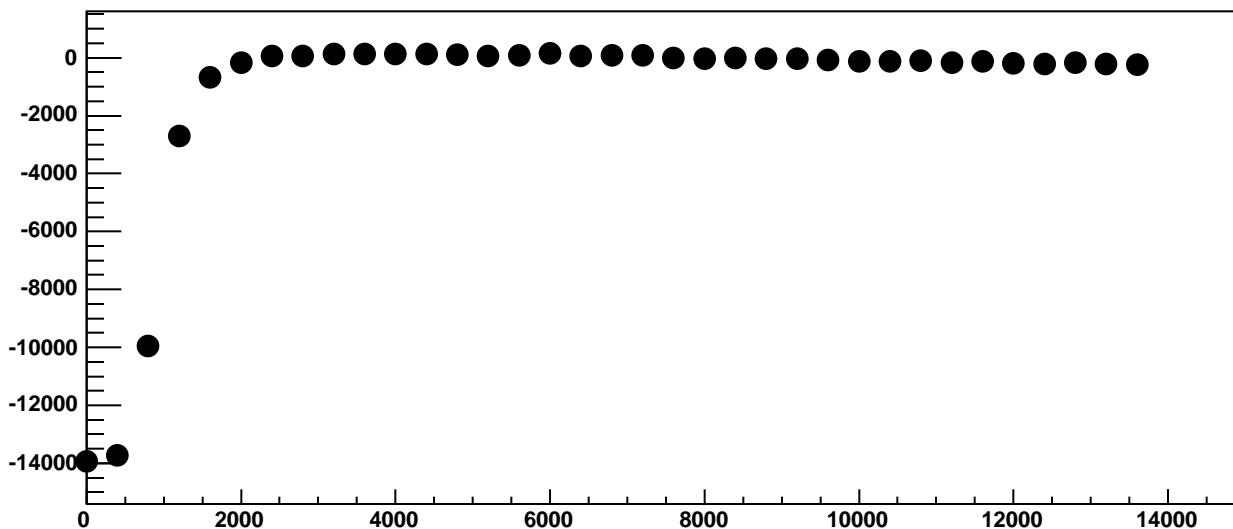
Chip 10, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



Chip 10, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

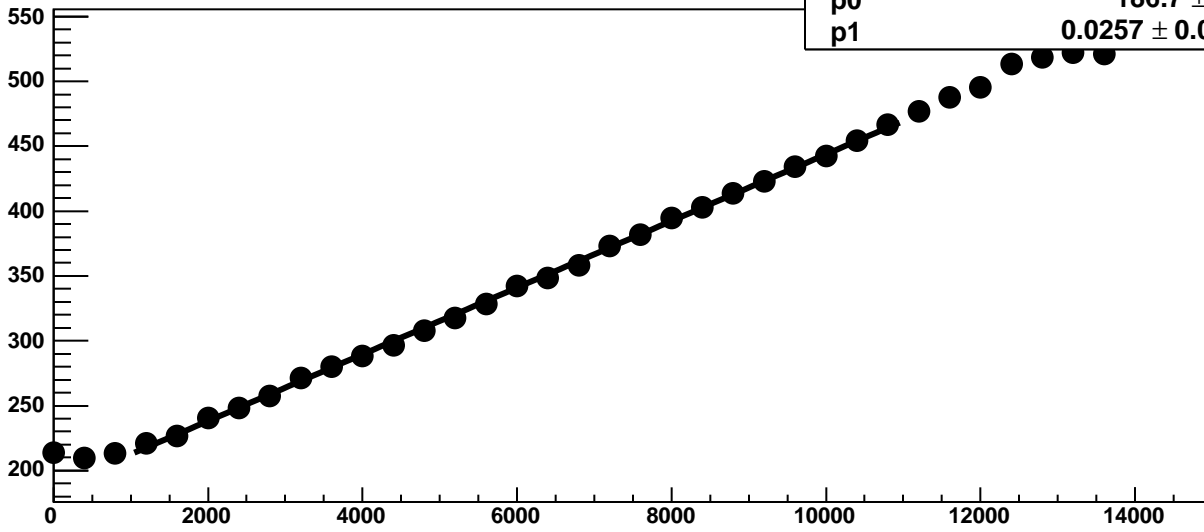


Chip 10, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



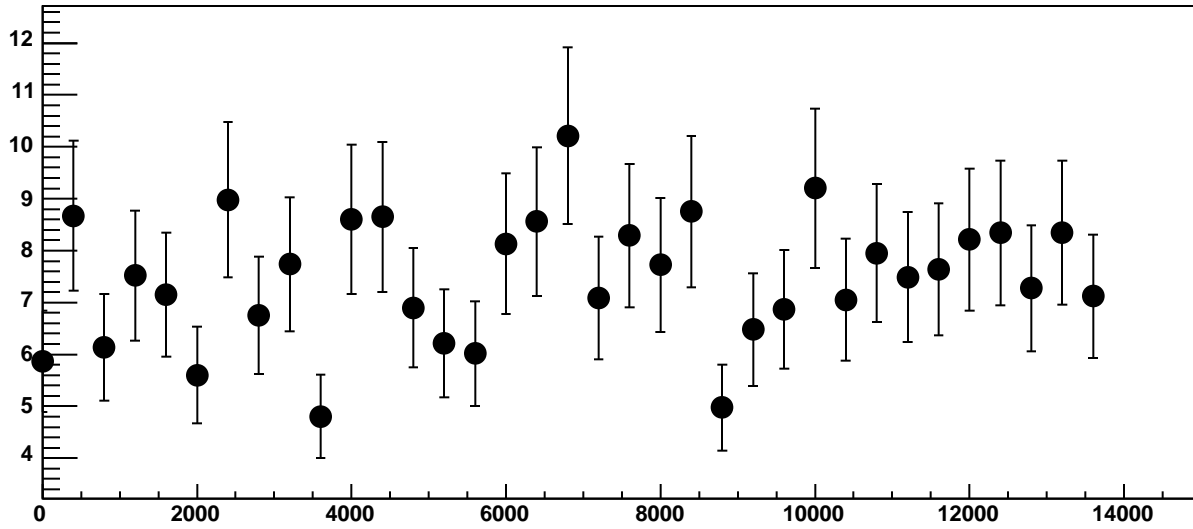


Chip 10, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

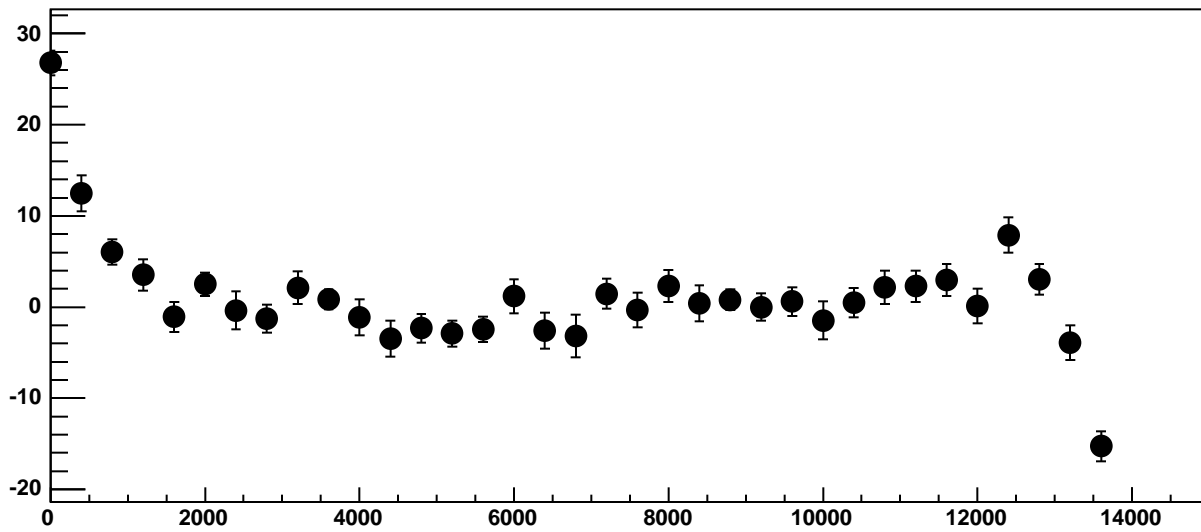


$\chi^2 / \text{ndf}$  33.29 / 23  
p0 186.7  $\pm$  0.7333  
p1 0.0257  $\pm$  0.0001121

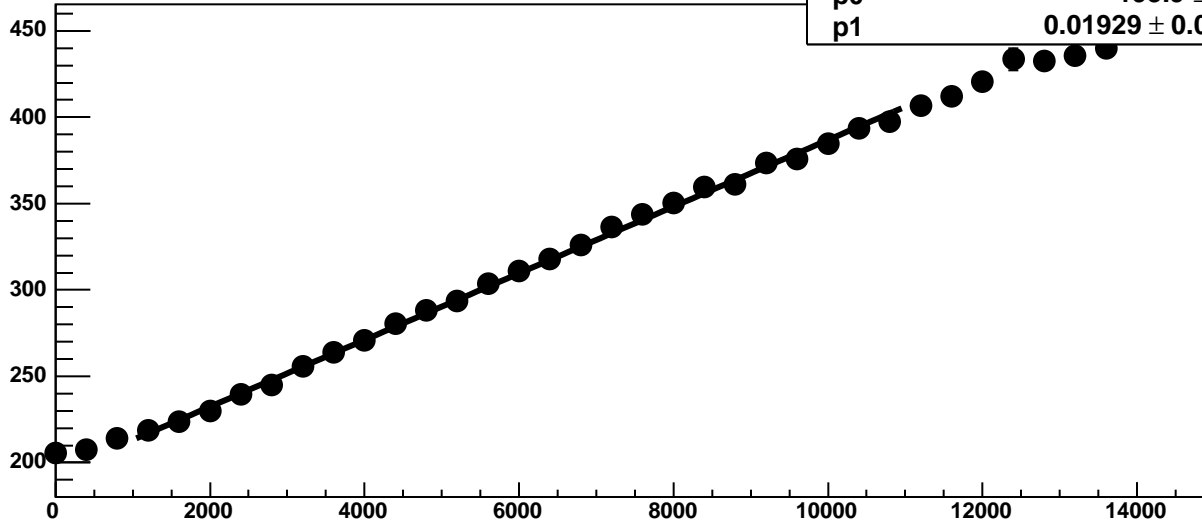
Chip 10, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



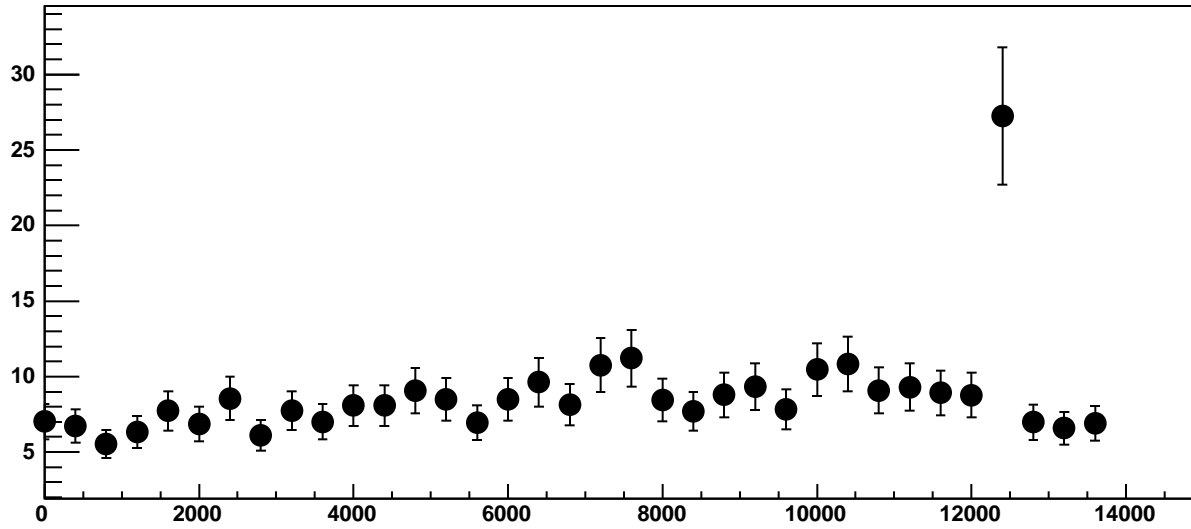
Chip 10, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



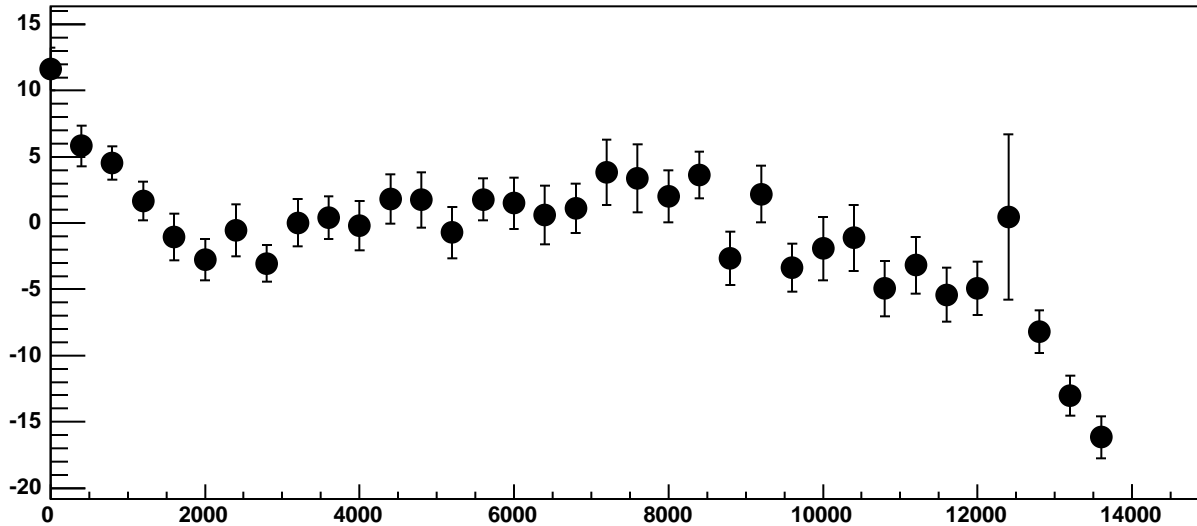
Chip 10, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC



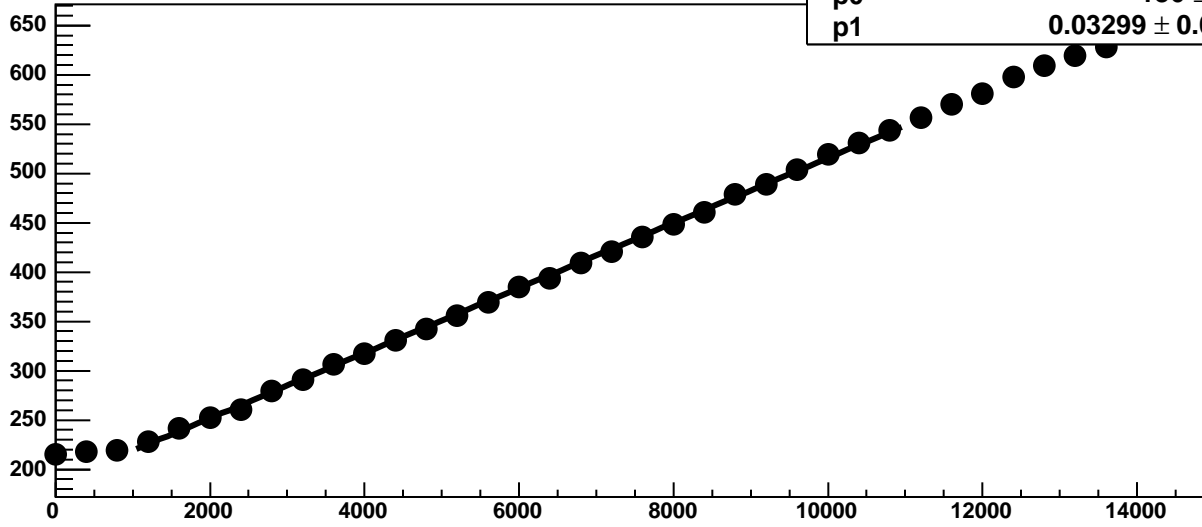
Chip 10, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

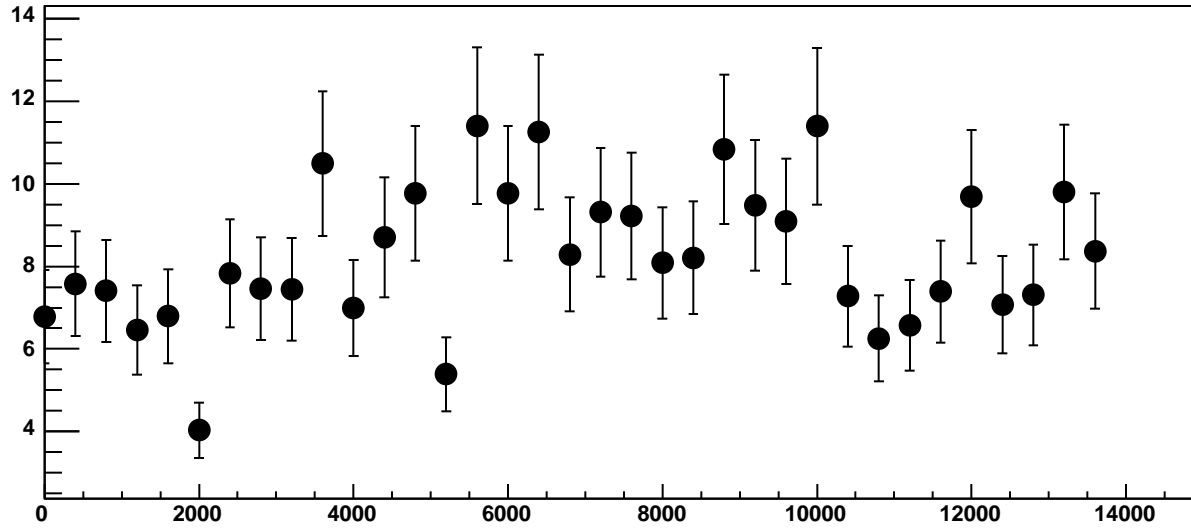


Chip 10, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

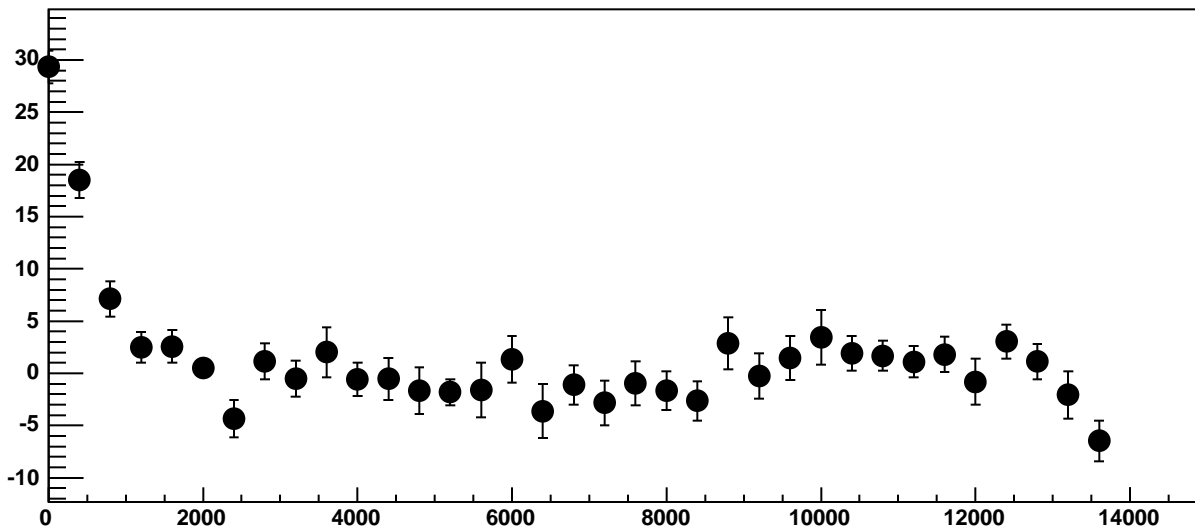


$\chi^2 / \text{ndf}$  29.83 / 23  
p0  $186 \pm 0.6954$   
p1  $0.03299 \pm 0.0001131$

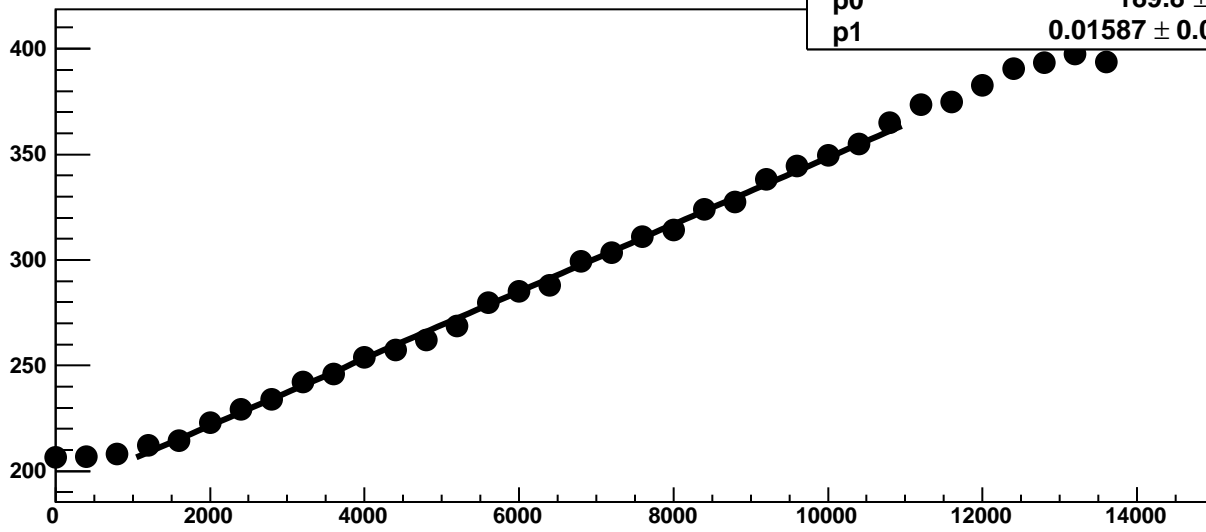
Chip 10, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

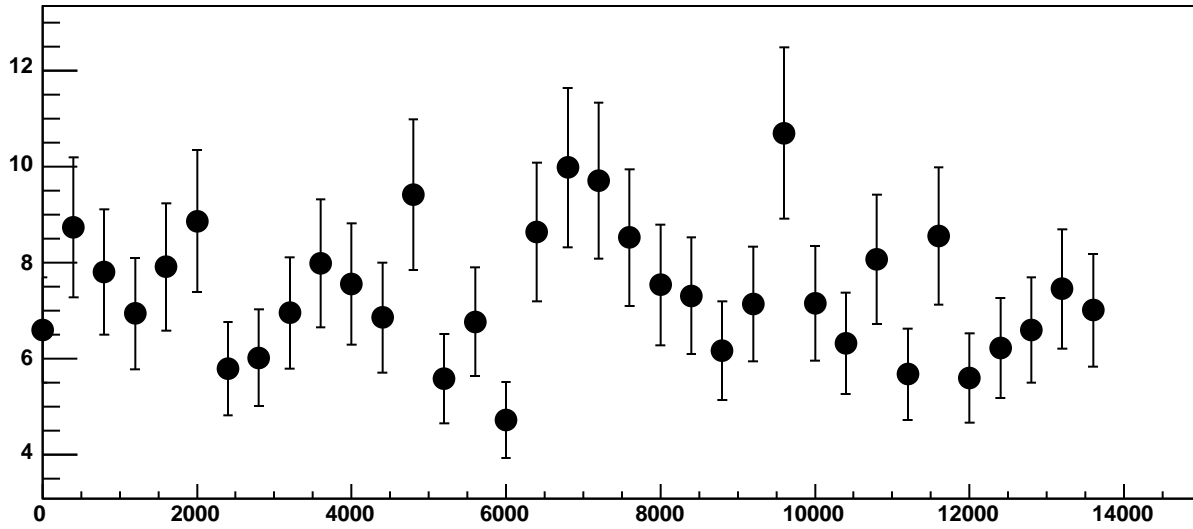


Chip 10, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

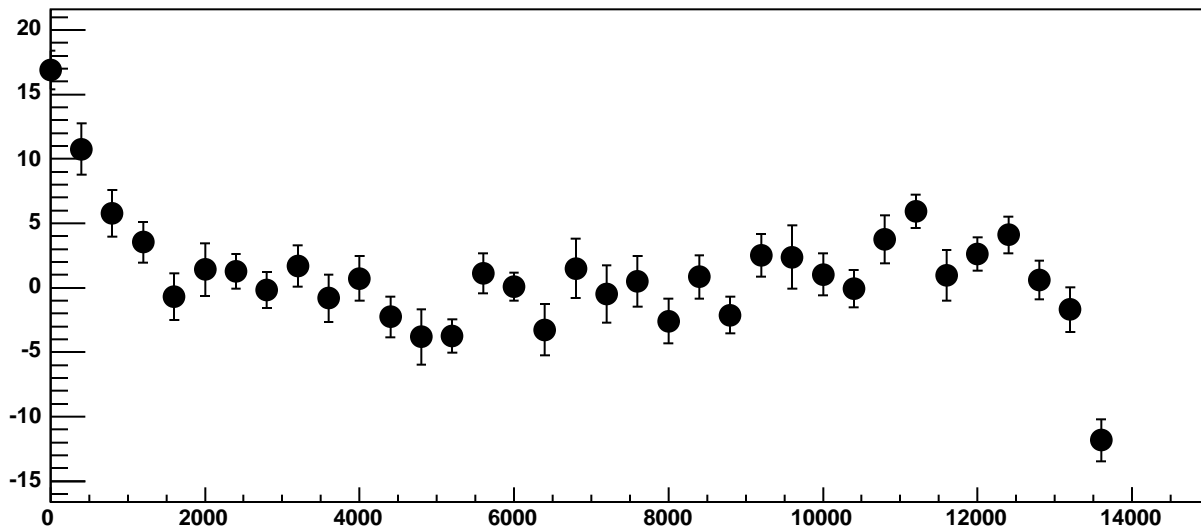


$\chi^2 / \text{ndf}$  38 / 23  
p0  $189.8 \pm 0.7494$   
p1  $0.01587 \pm 0.0001151$

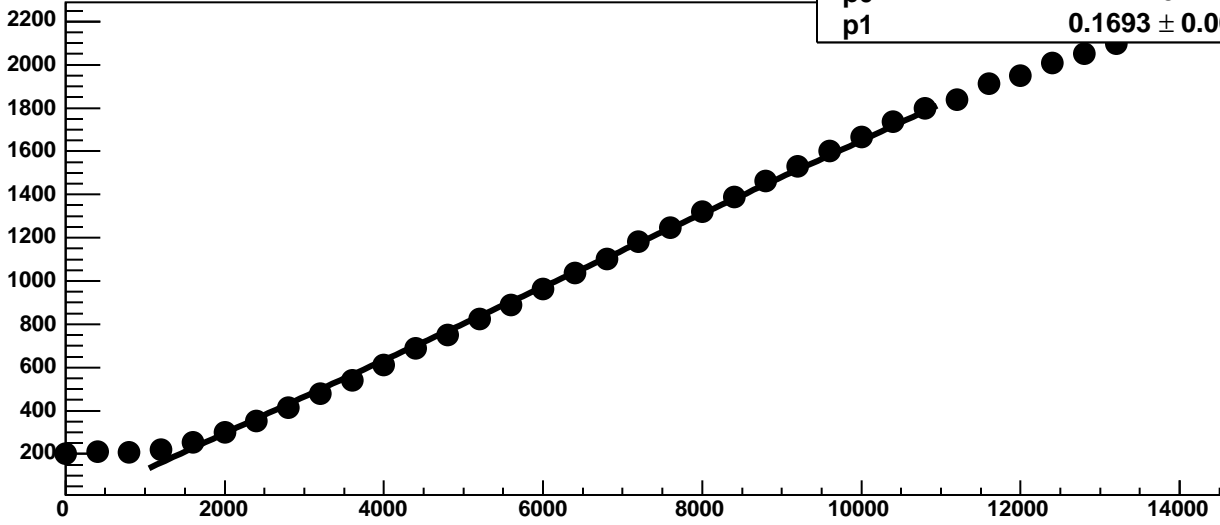
Chip 10, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



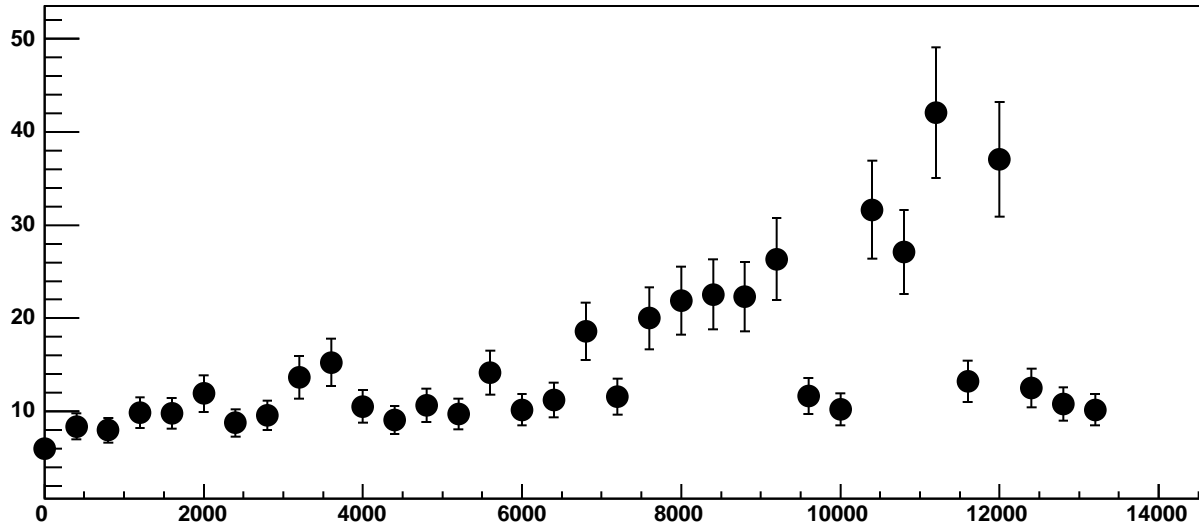
Chip 10, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



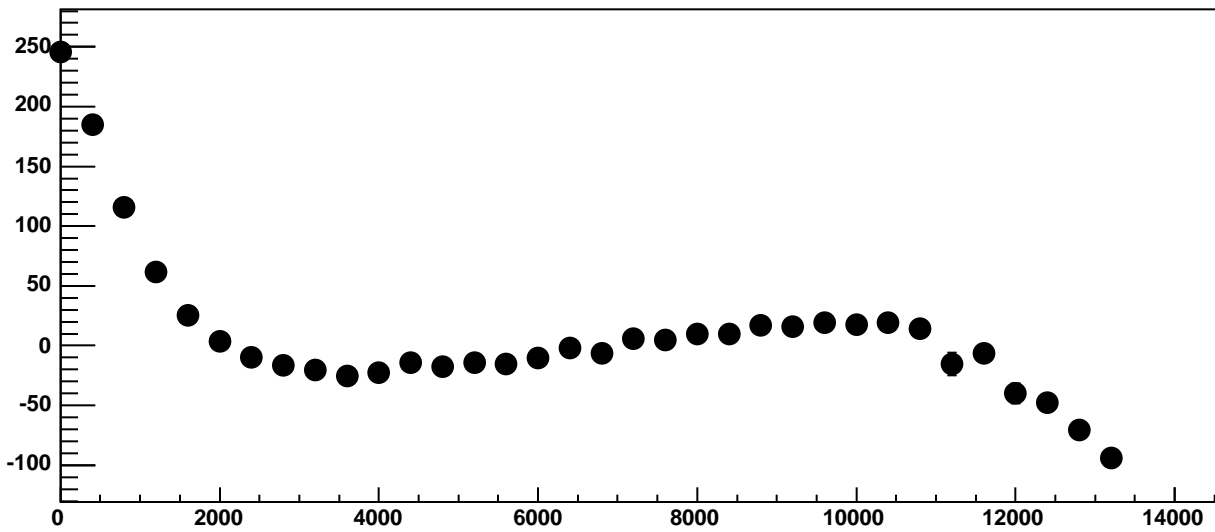
Chip 10, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

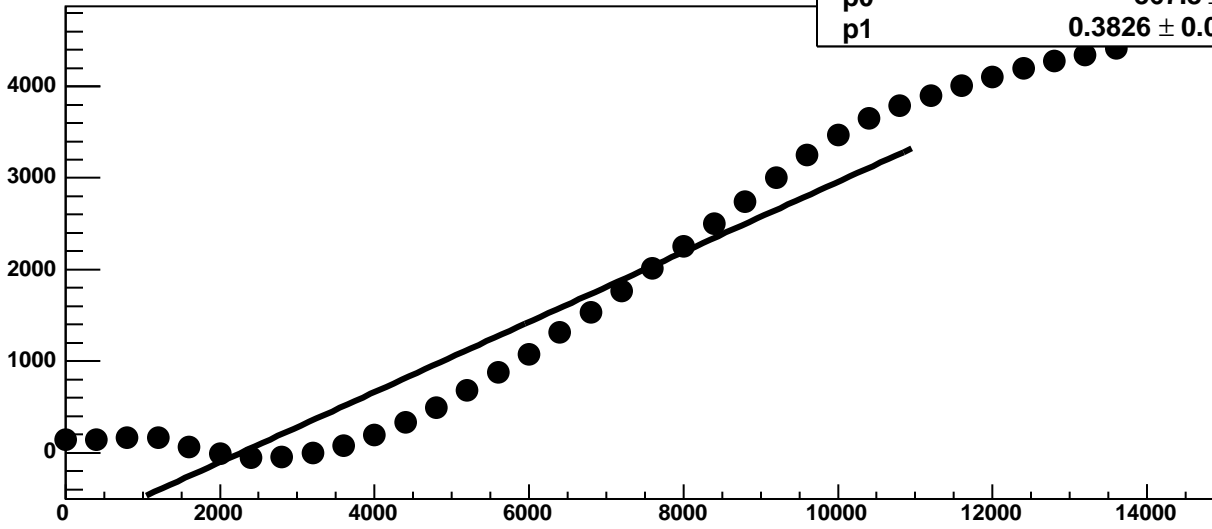


Chip 10, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

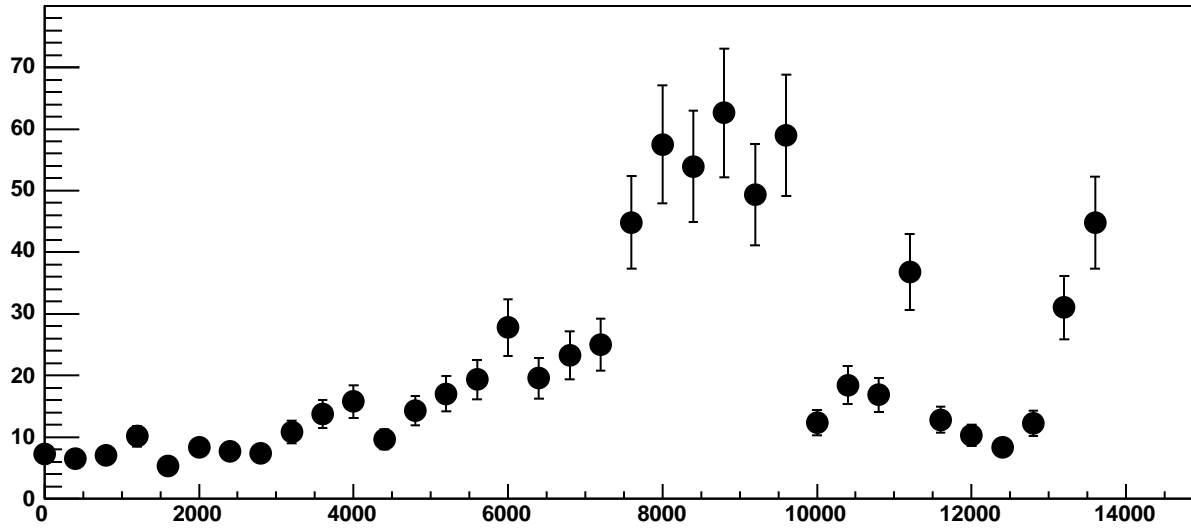


Chip 10, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC

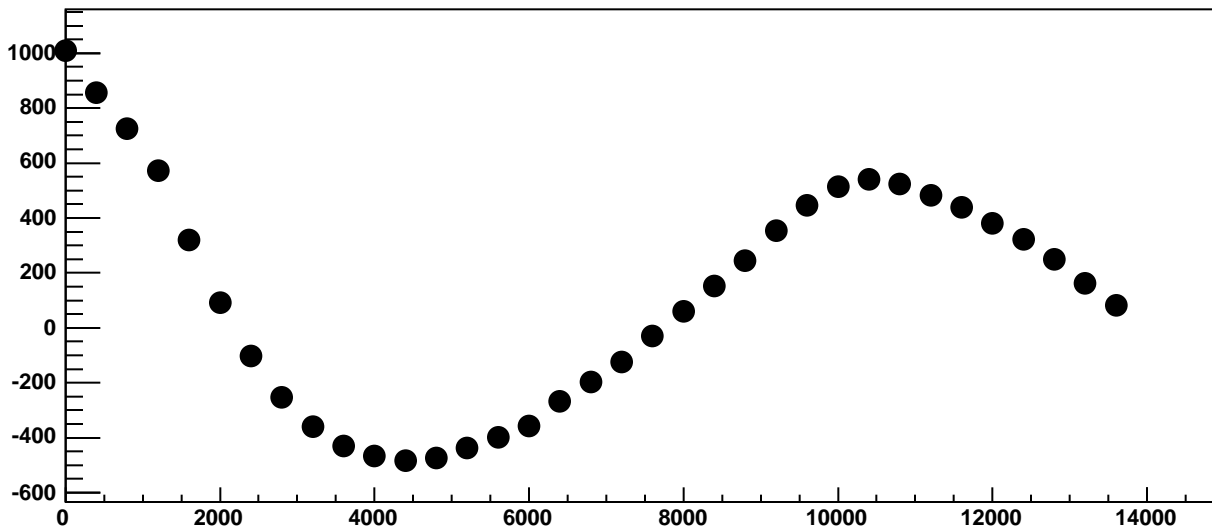
$\chi^2 / \text{ndf}$  3.793e+05 / 23  
p0 -867.8 ± 0.9981  
p1 0.3826 ± 0.0002285



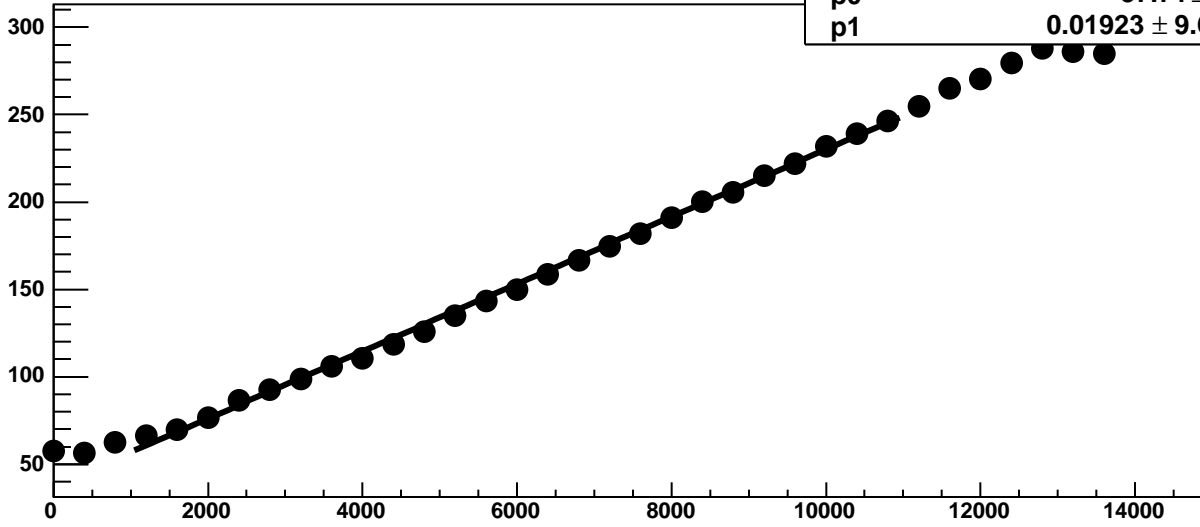
Chip 10, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



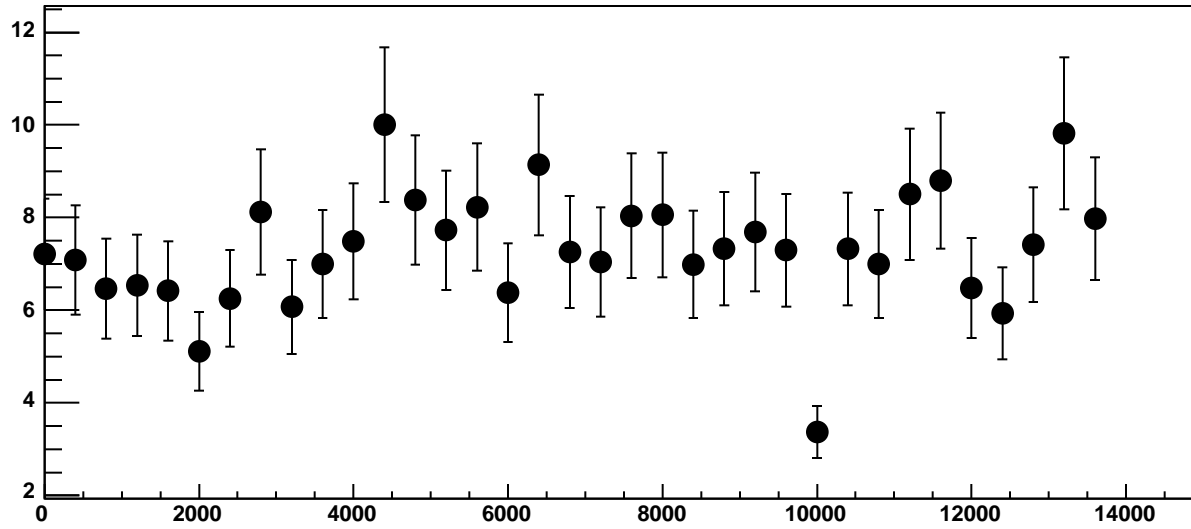
Chip 10, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



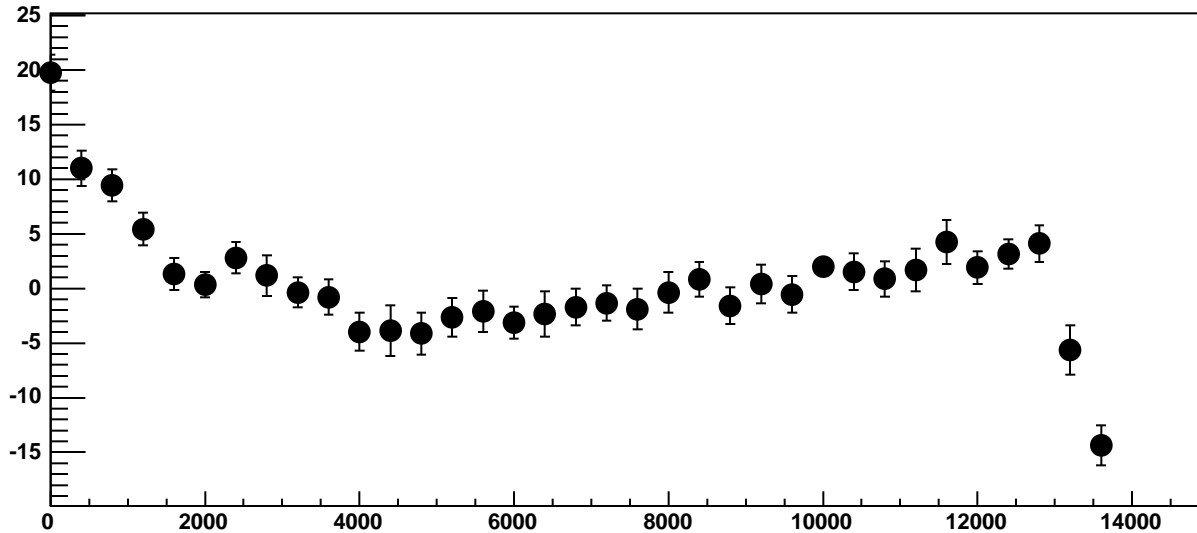
Chip 10, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



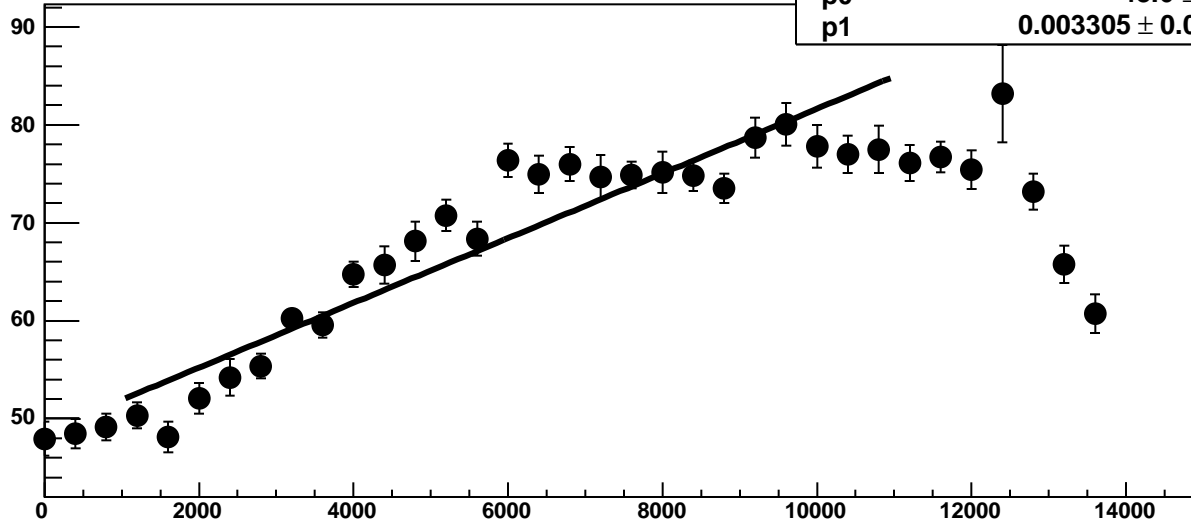
Chip 10, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



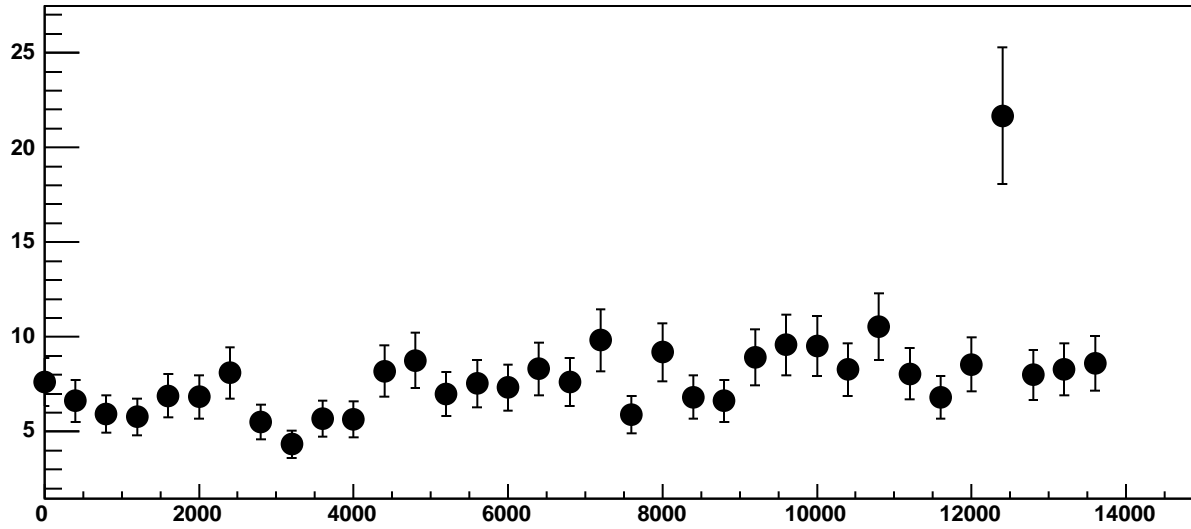
Chip 10, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



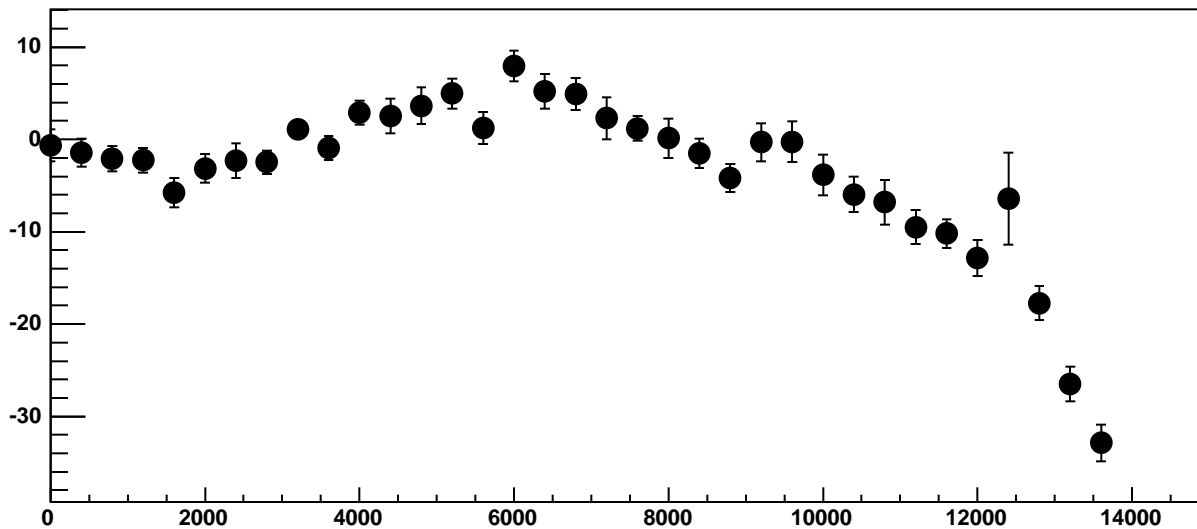
Chip 10, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



Chip 10, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

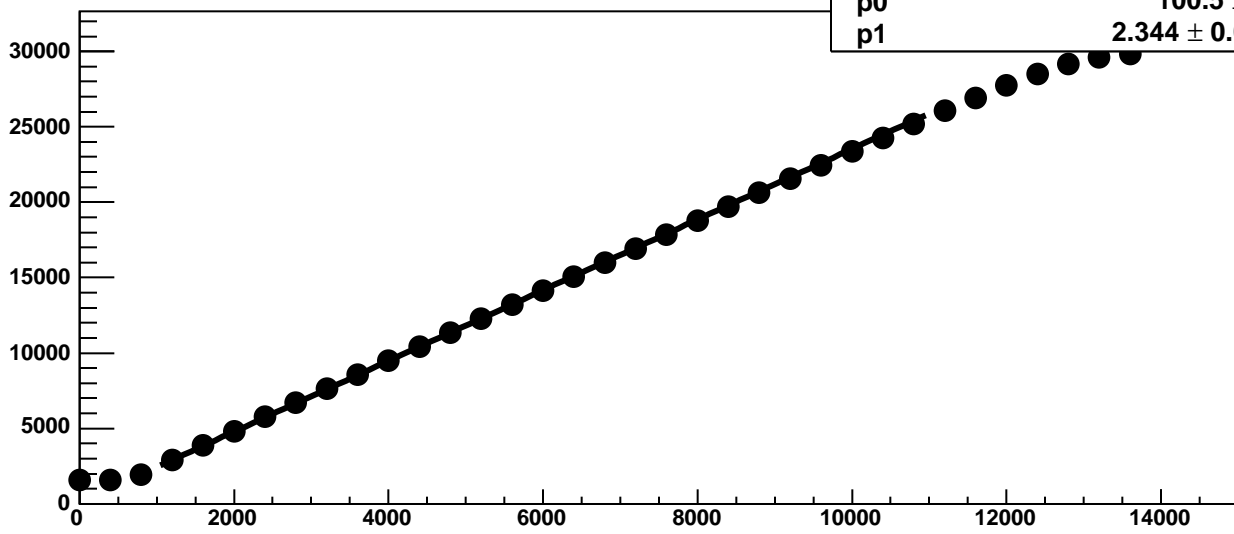


Chip 10, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

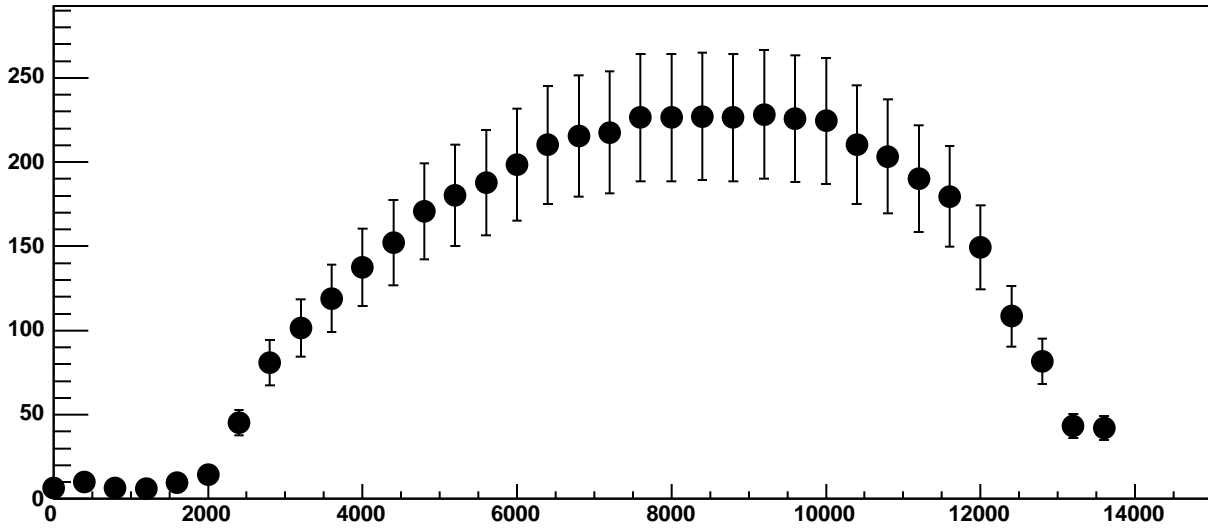




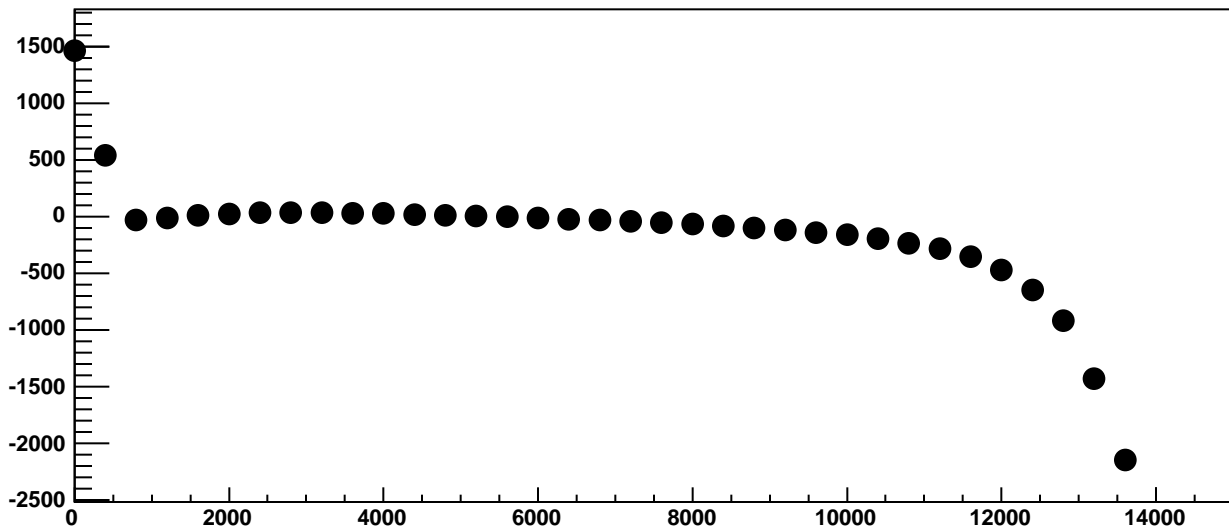
Chip 10, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC



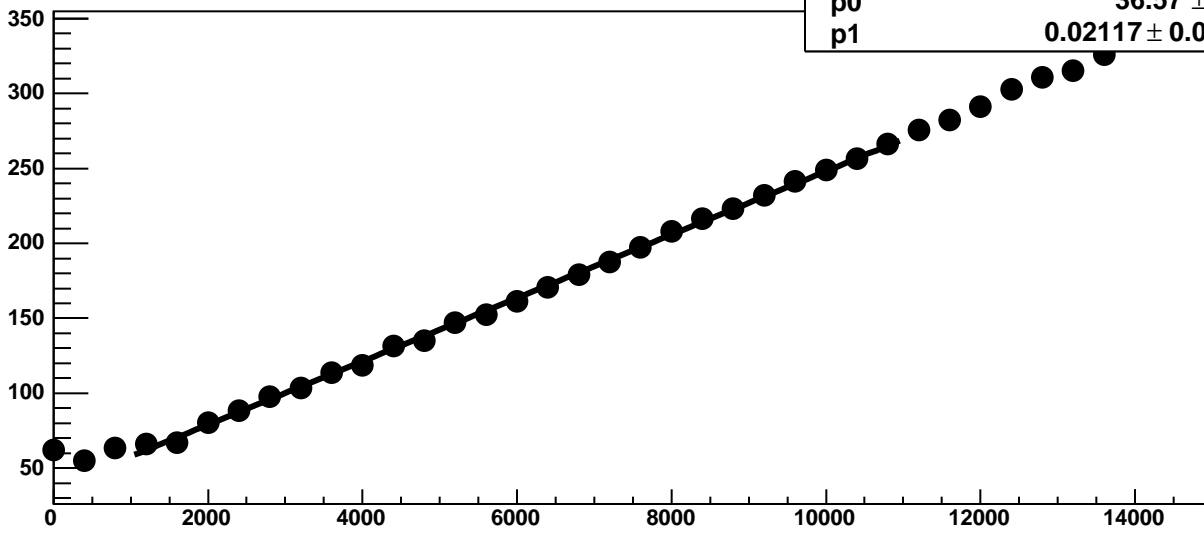
Chip 10, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC

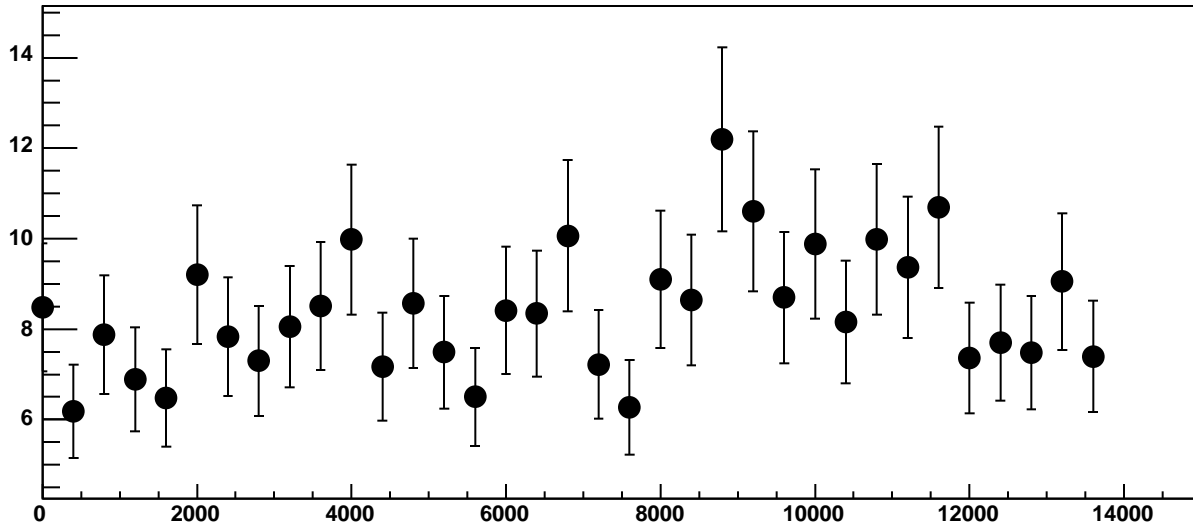


Chip 10, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

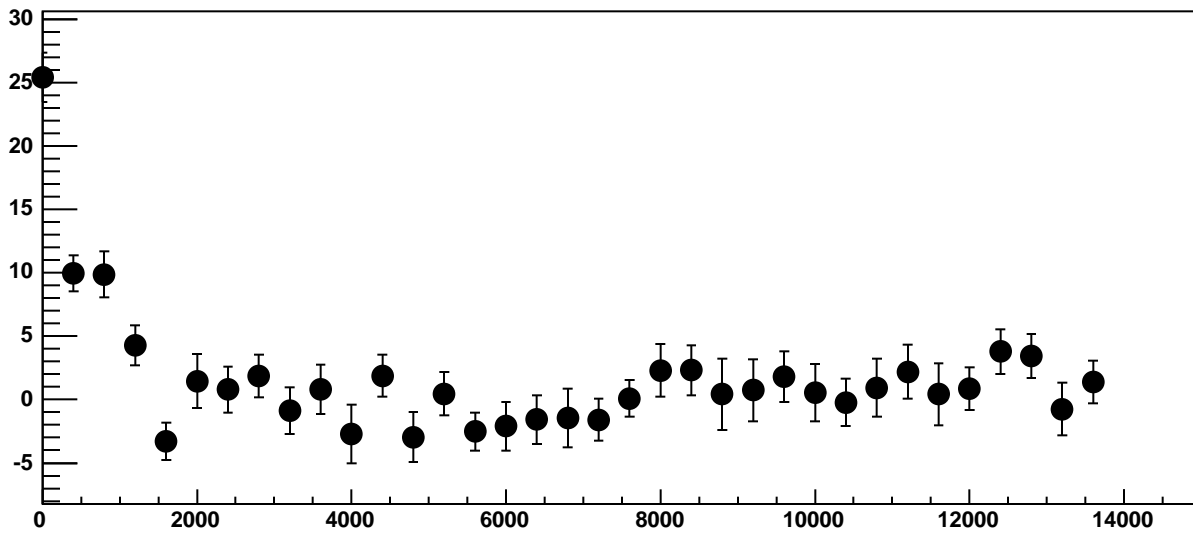


$\chi^2 / \text{ndf}$  29.39 / 23  
p0 36.57 ± 0.8258  
p1 0.02117 ± 0.0001318

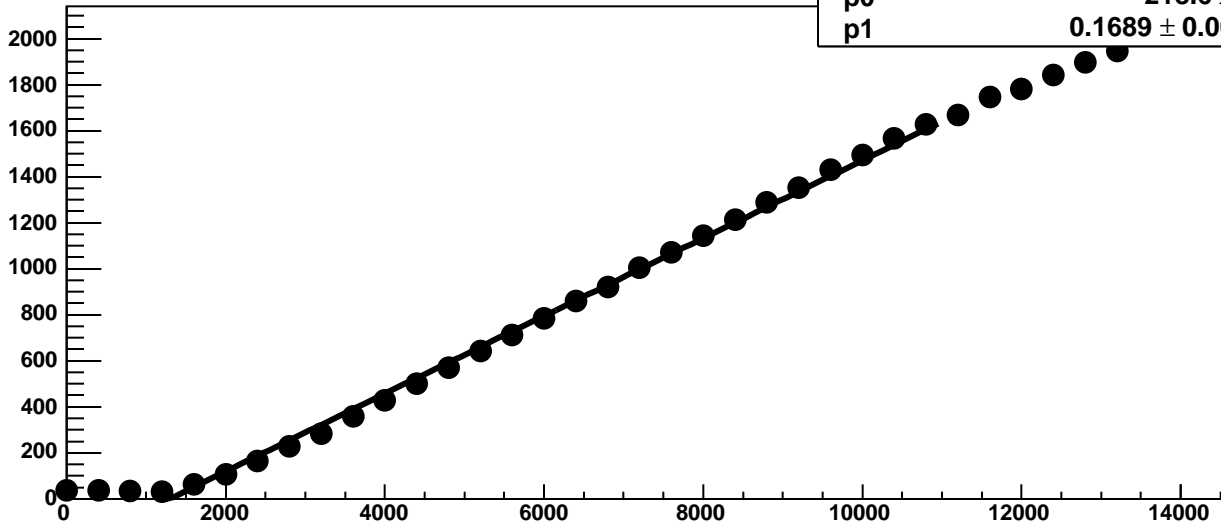
Chip 10, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



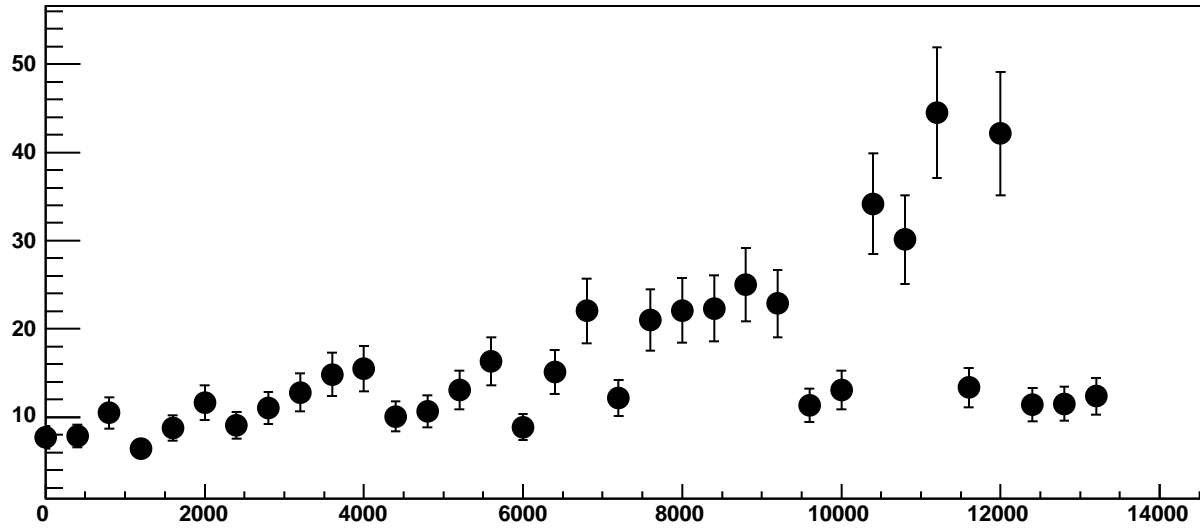
Chip 10, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



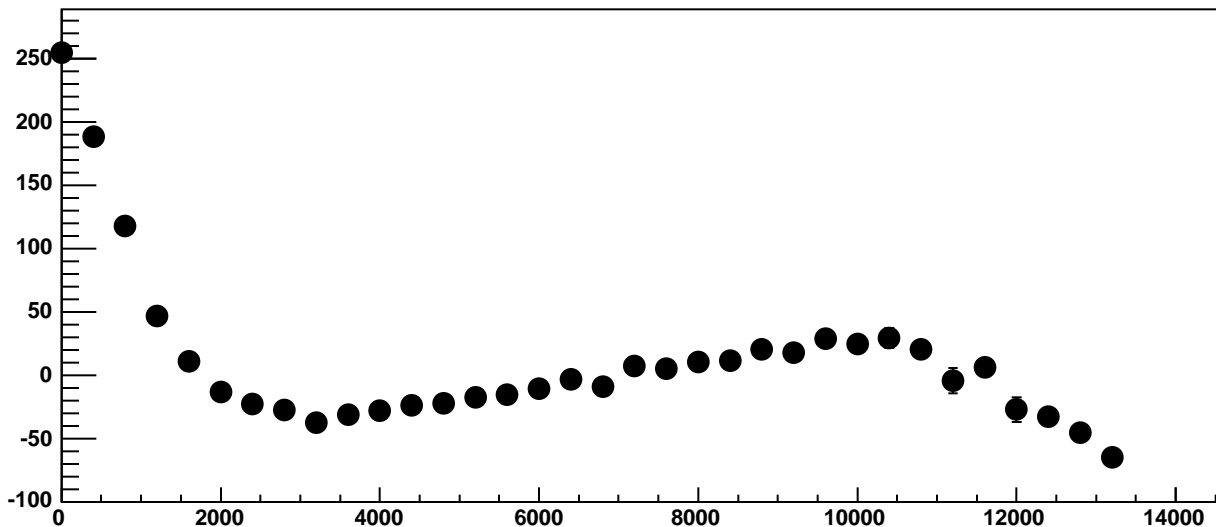
Chip 10, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



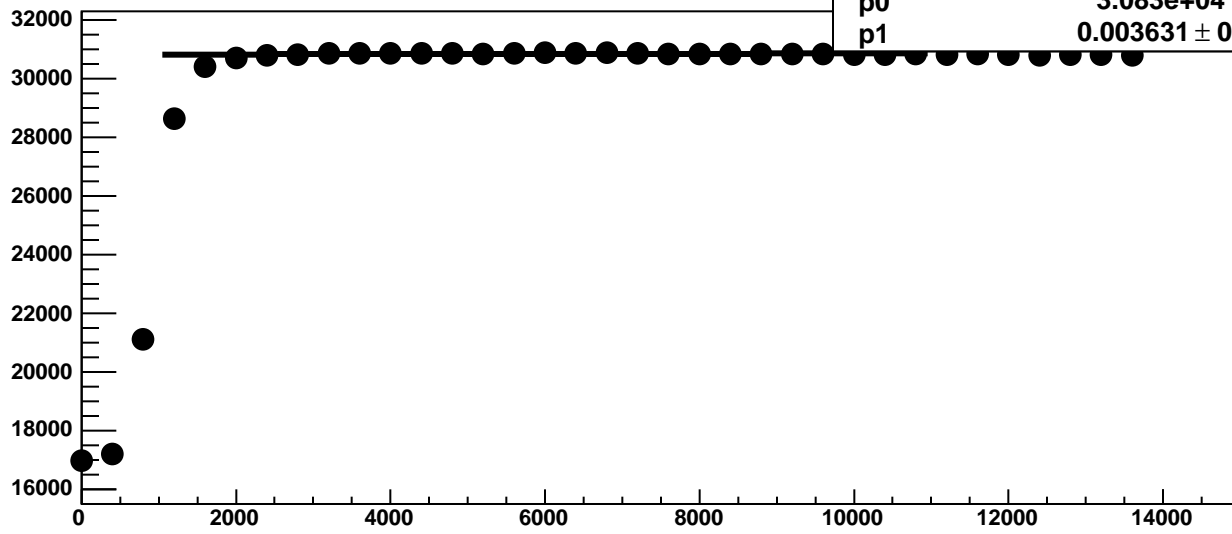
Chip 10, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



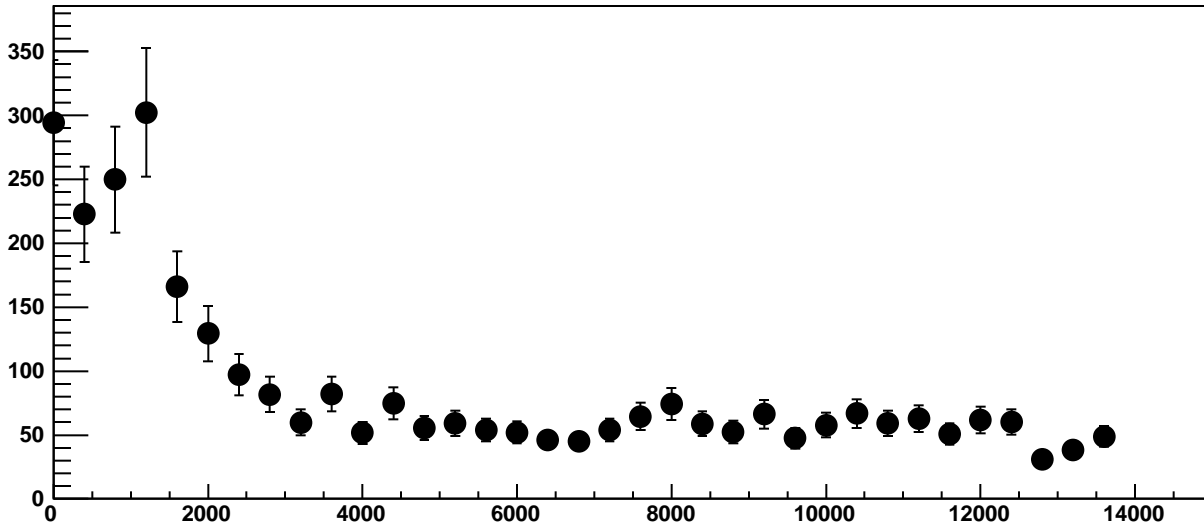
Chip 10, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



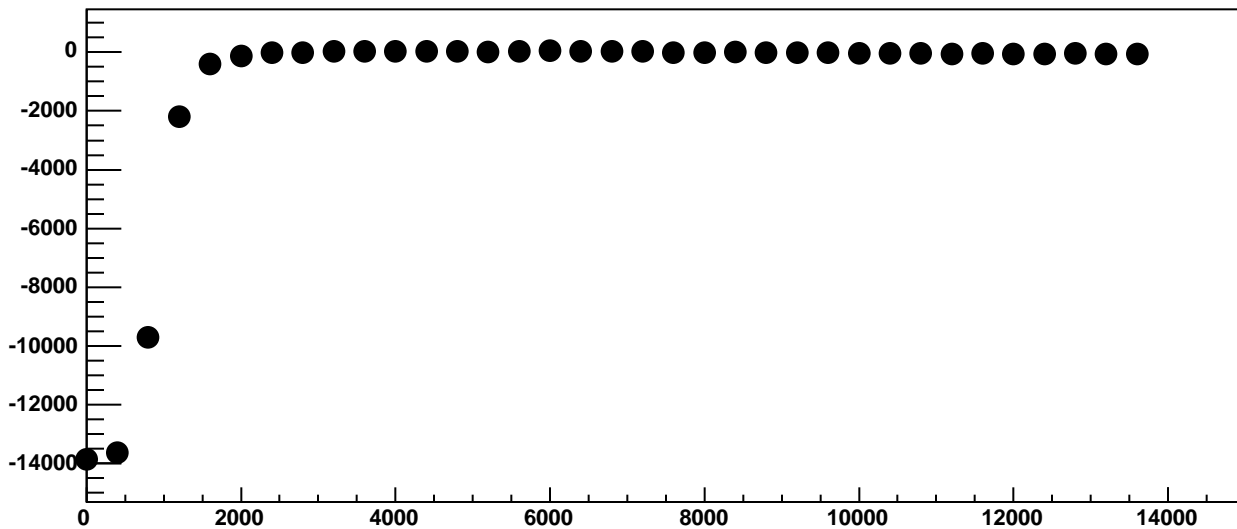
Chip 10, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC



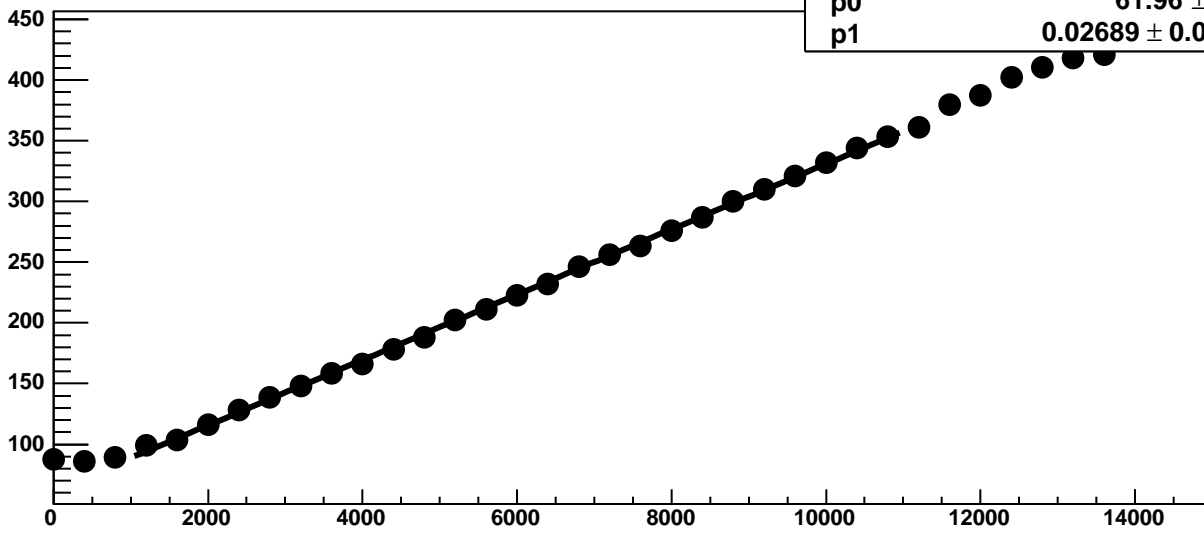
Chip 10, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC

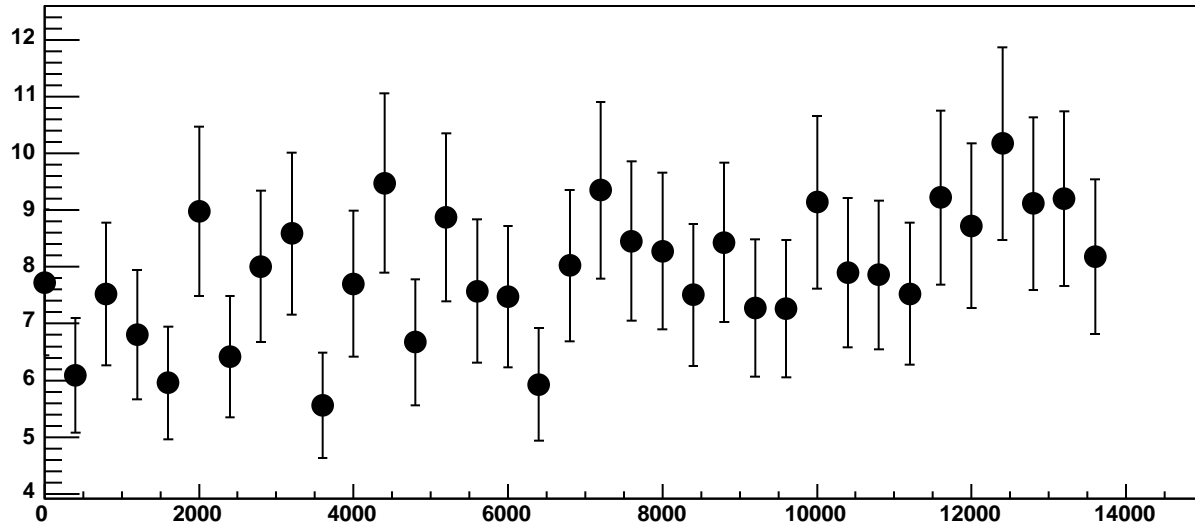


Chip 10, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

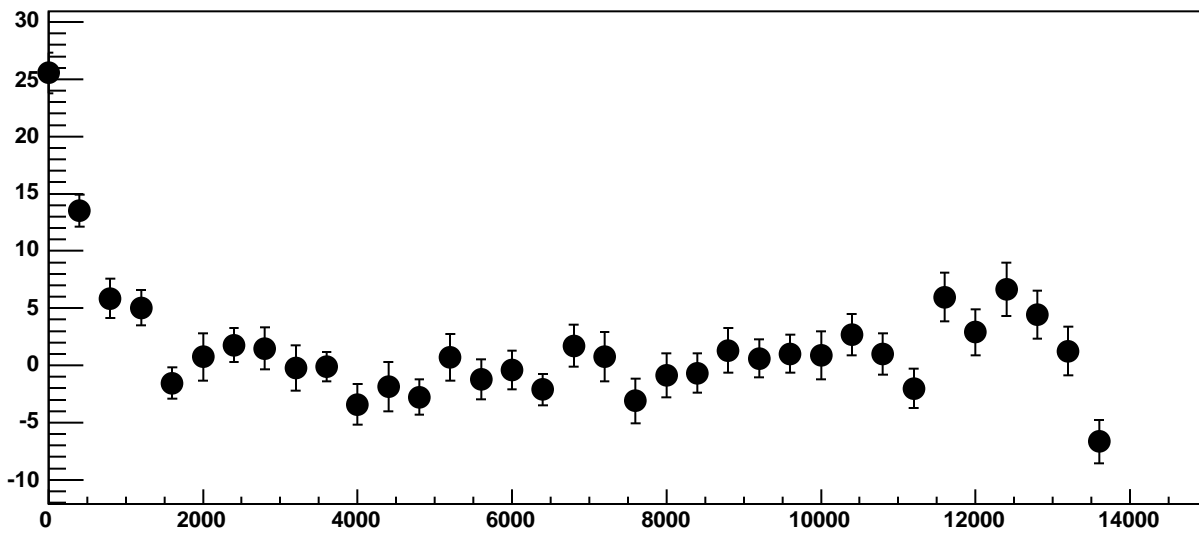


$\chi^2 / \text{ndf}$  32.33 / 23  
p0  $61.96 \pm 0.7578$   
p1  $0.02689 \pm 0.0001183$

Chip 10, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

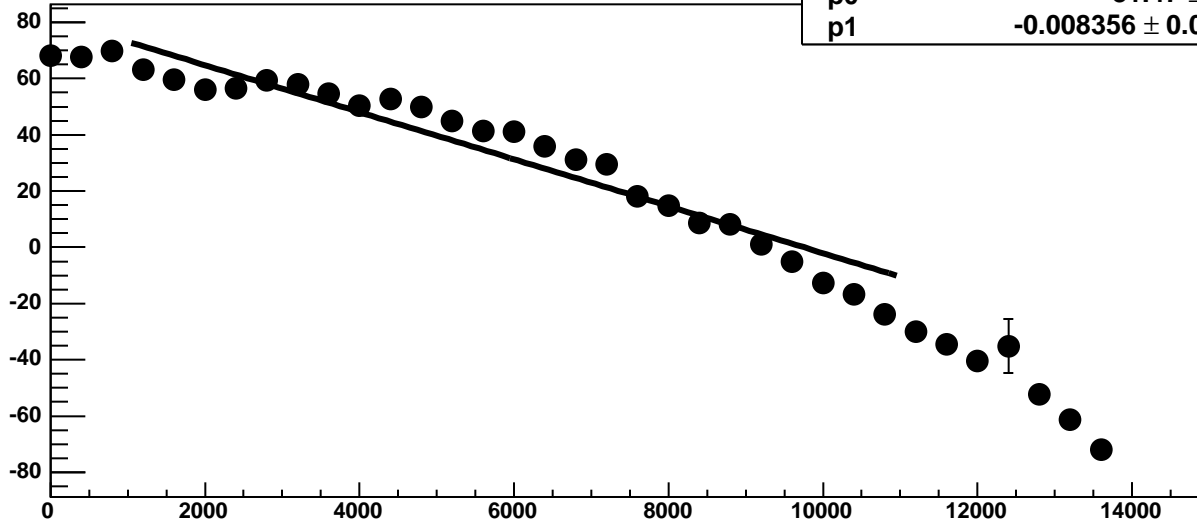
308 / 23

p0

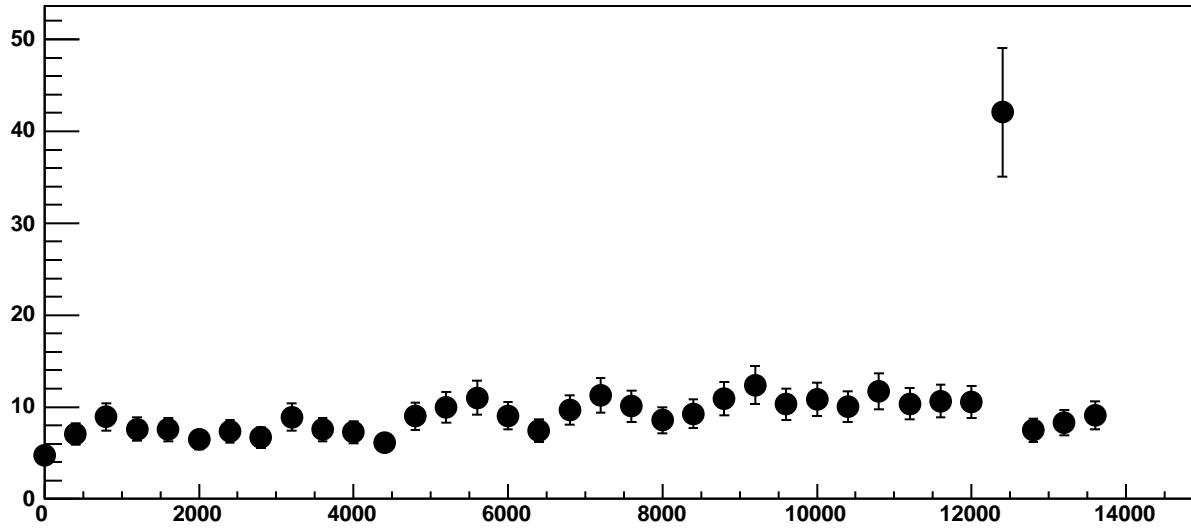
$81.47 \pm 0.8275$

p1

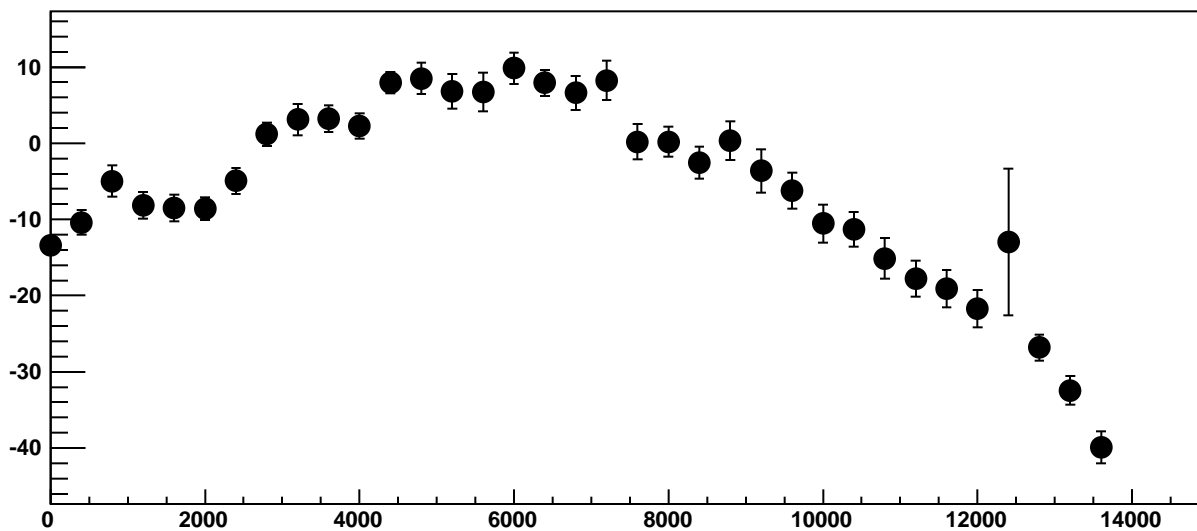
$-0.008356 \pm 0.0001416$



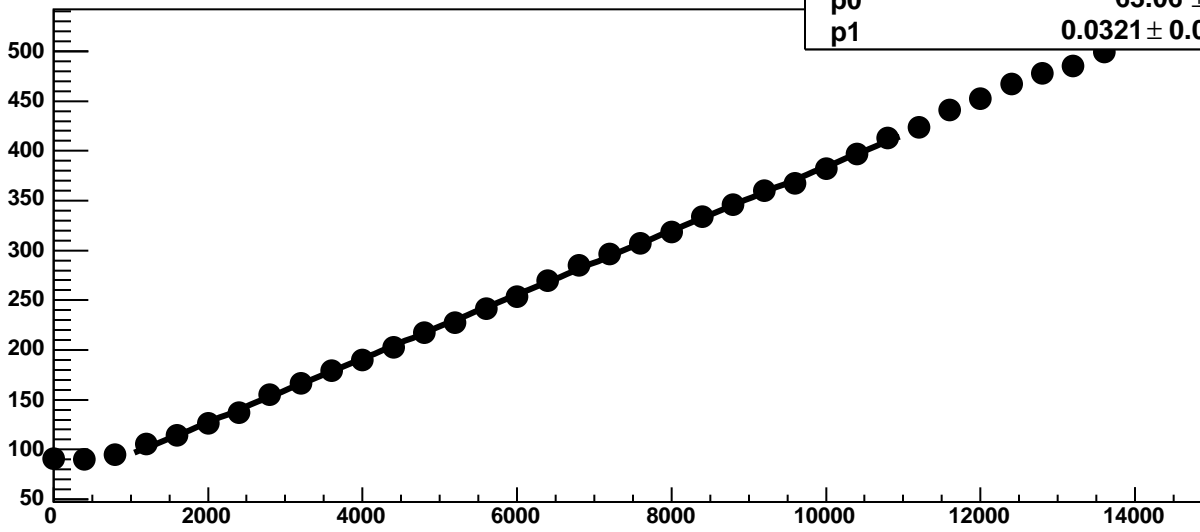
Chip 10, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

30.36 / 23

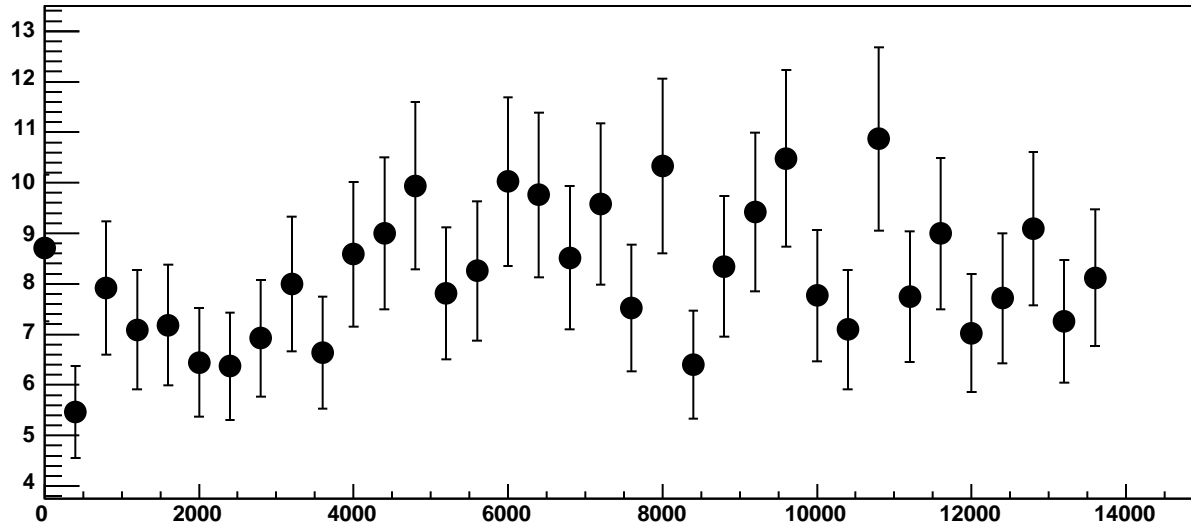
p0

$63.06 \pm 0.7759$

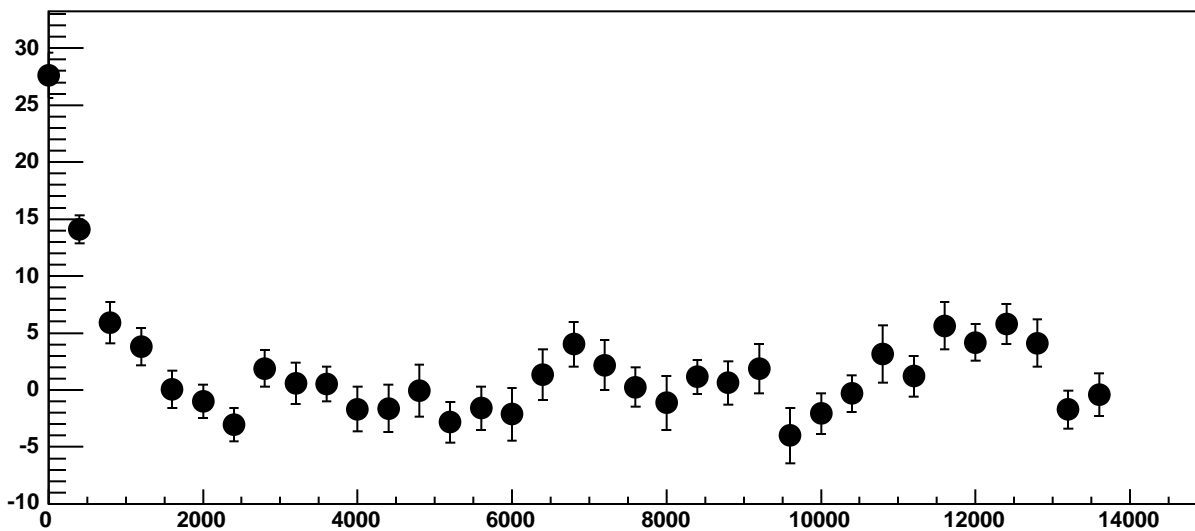
p1

$0.0321 \pm 0.0001229$

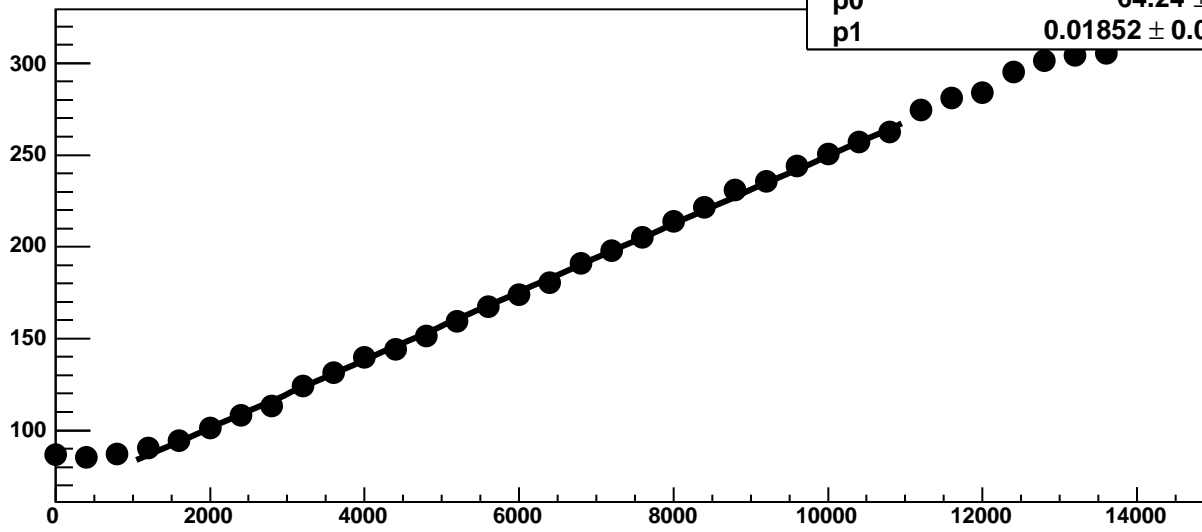
Chip 10, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



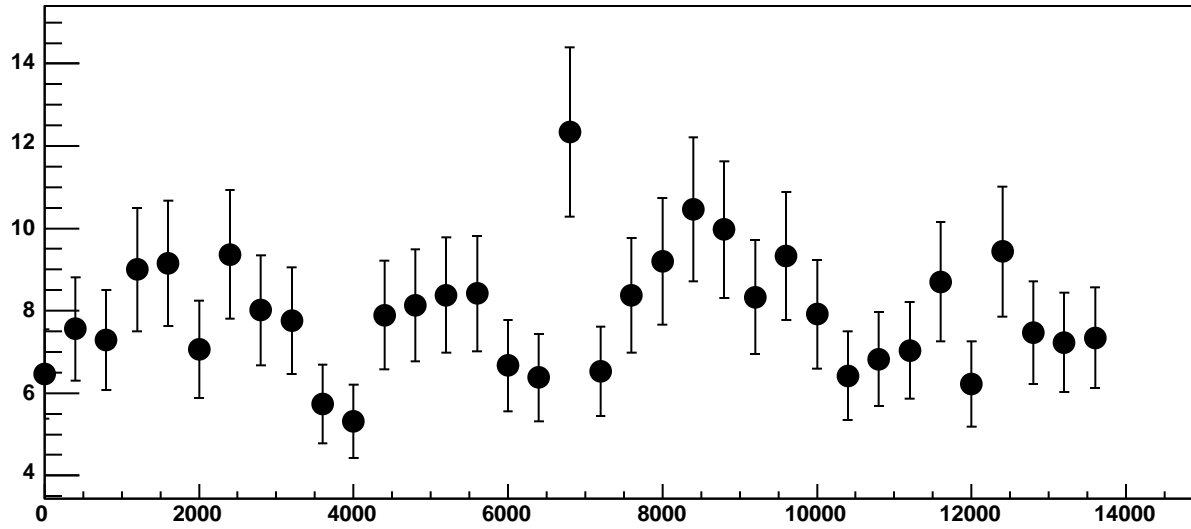
Chip 10, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



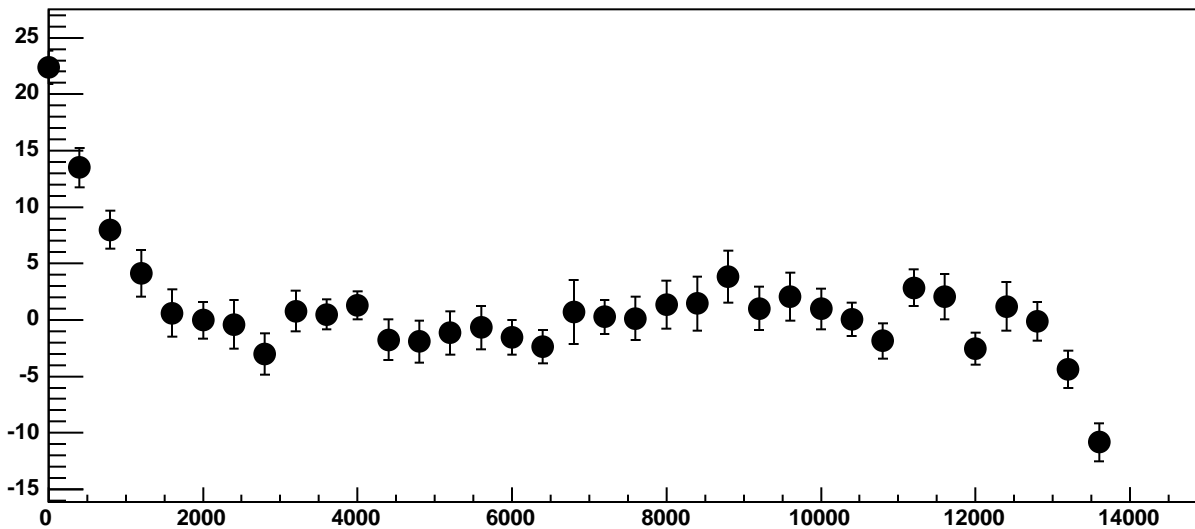
Chip 10, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



Chip 10, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

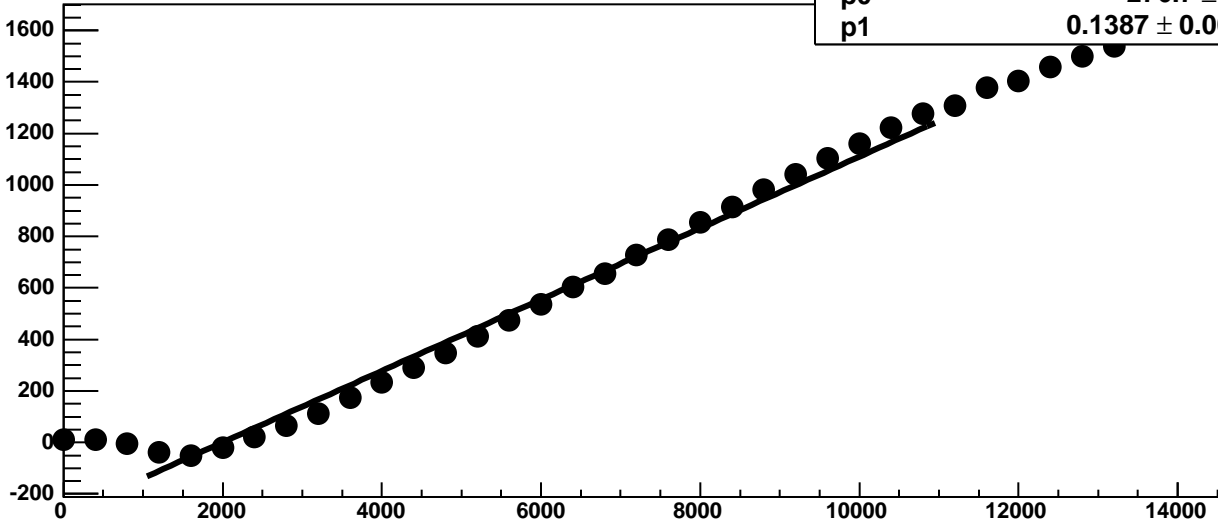


Chip 10, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

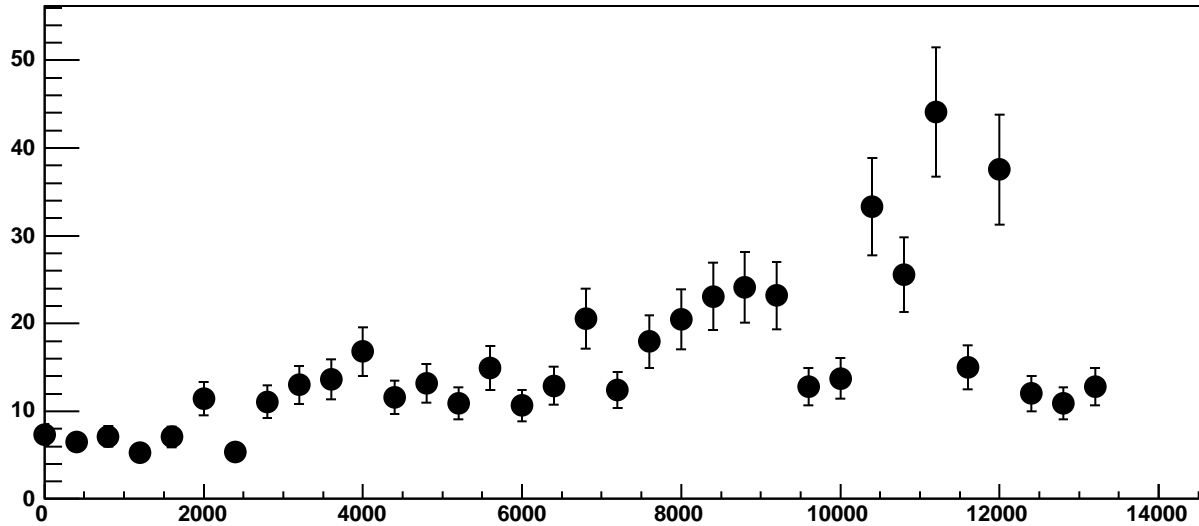




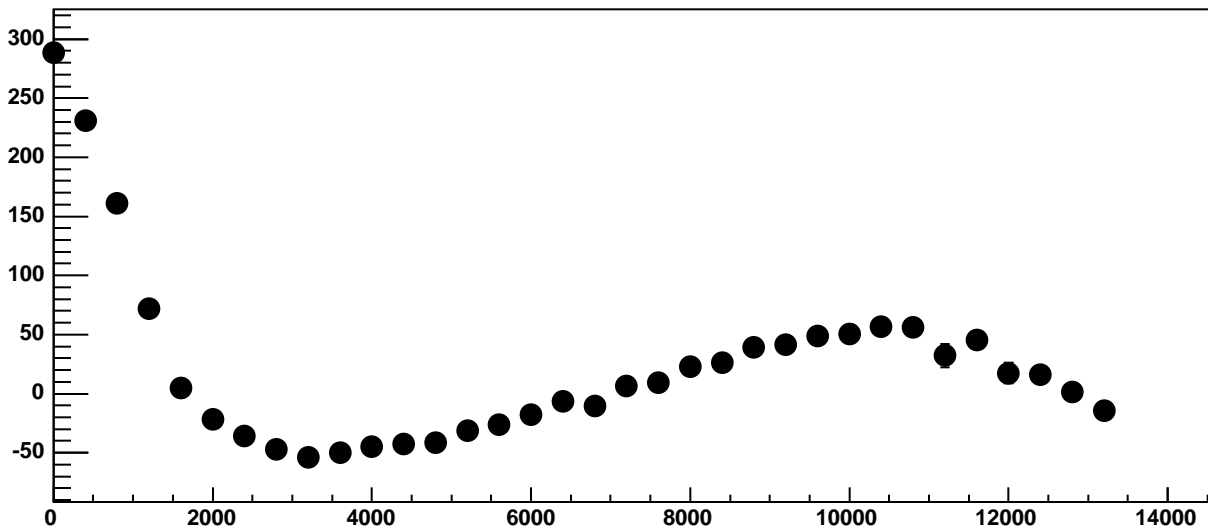
Chip 10, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

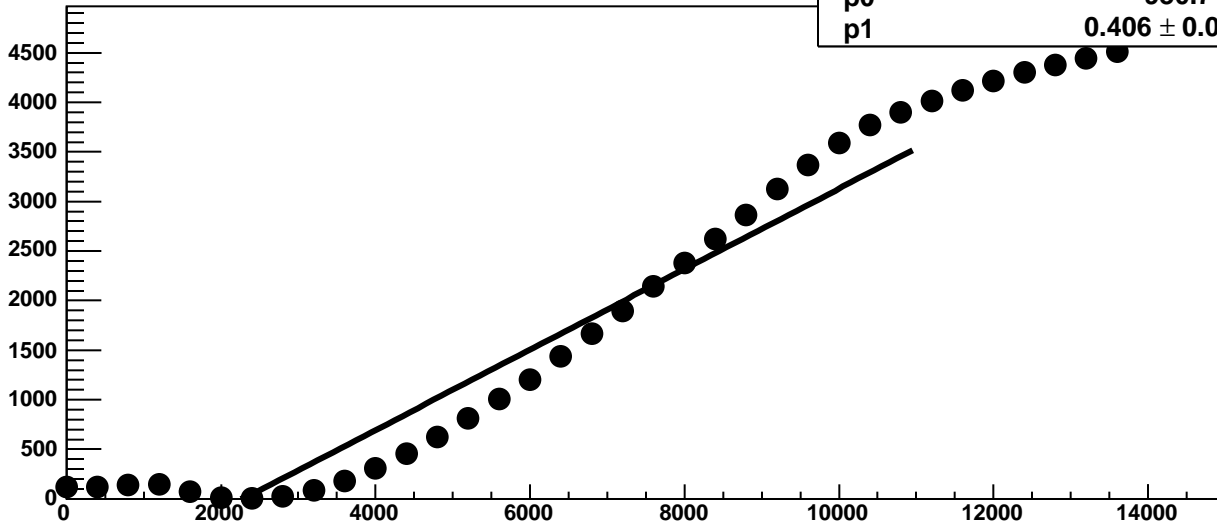


Chip 10, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

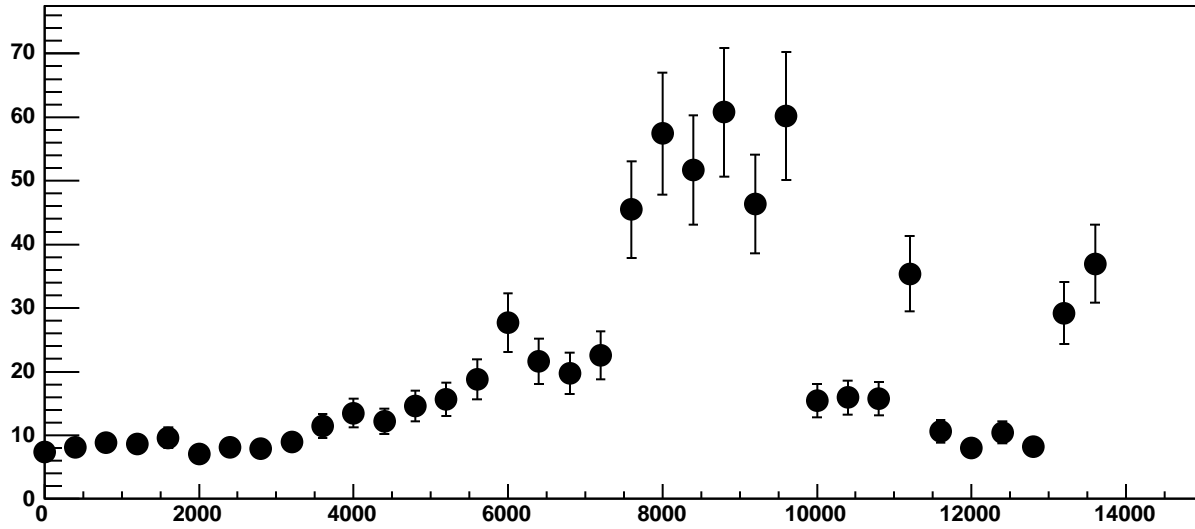


Chip 10, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

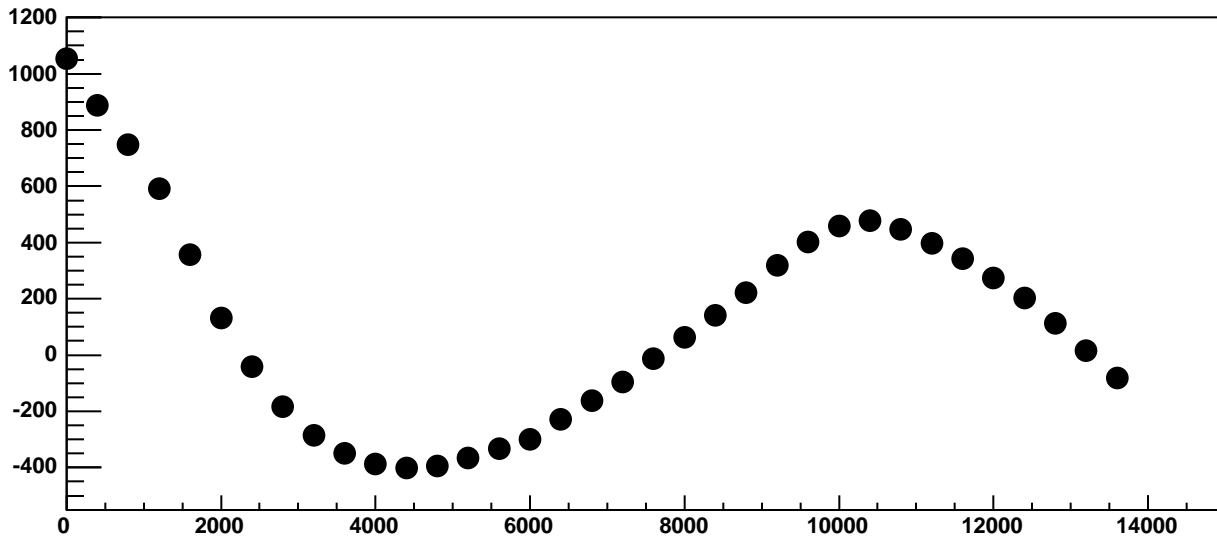
$\chi^2 / \text{ndf}$  2.933e+05 / 23  
p0 -930.7 ± 1.079  
p1 0.406 ± 0.0002378



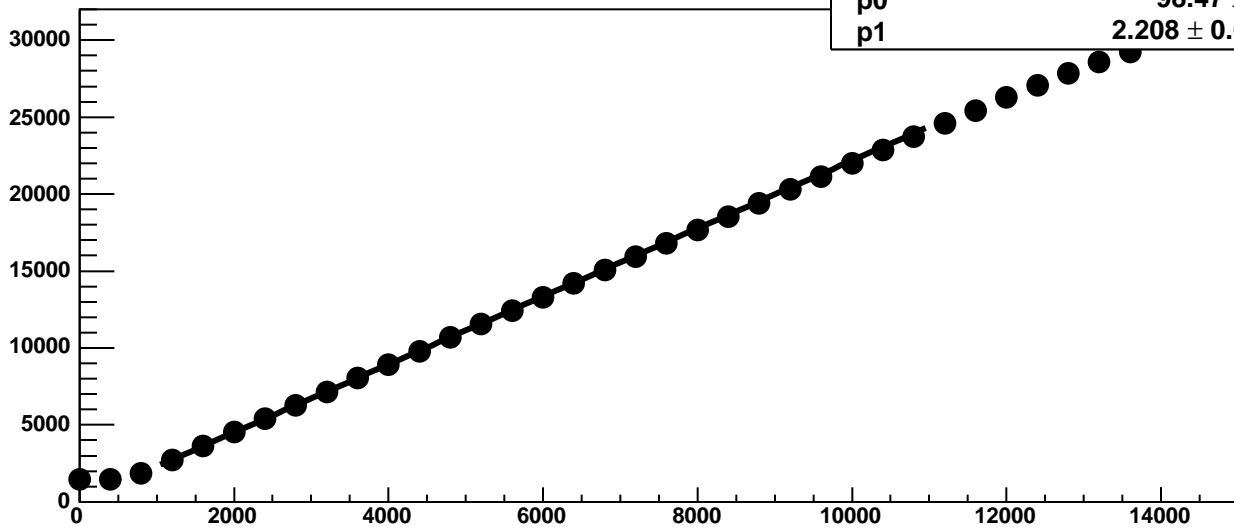
Chip 10, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



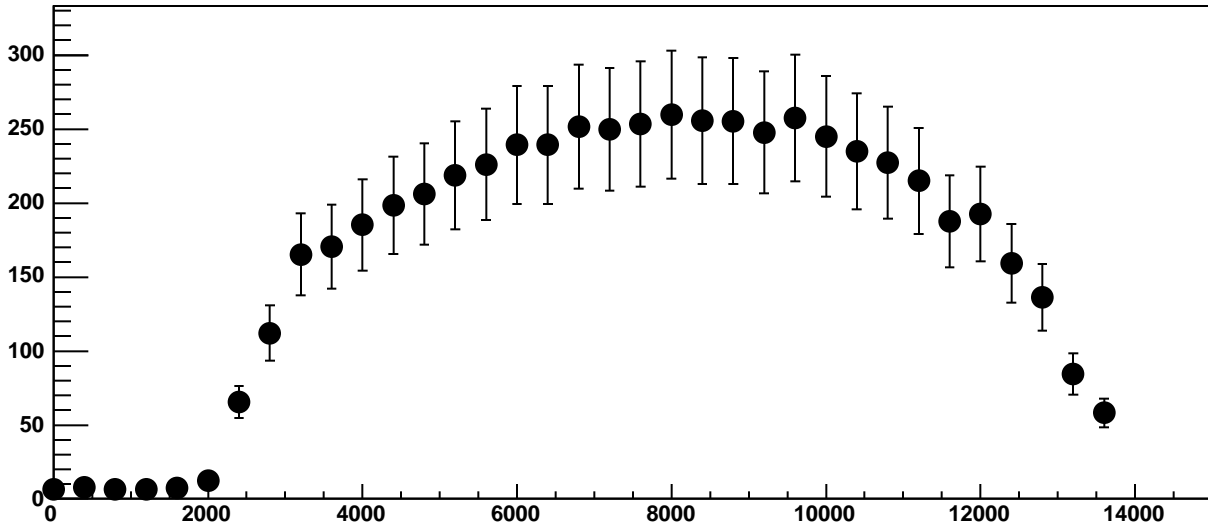
Chip 10, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC



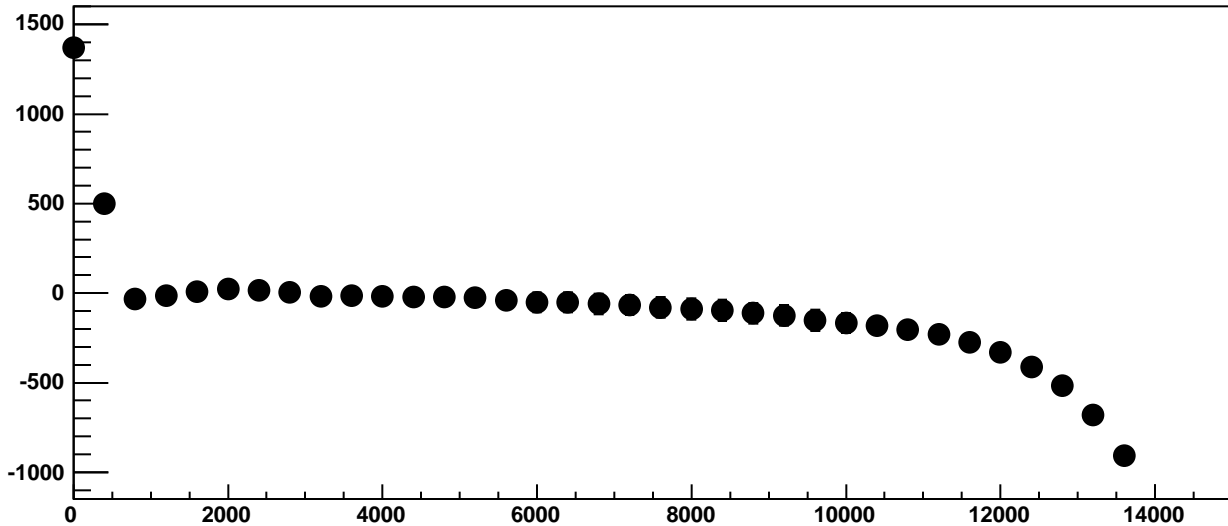
Chip 10, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 10, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

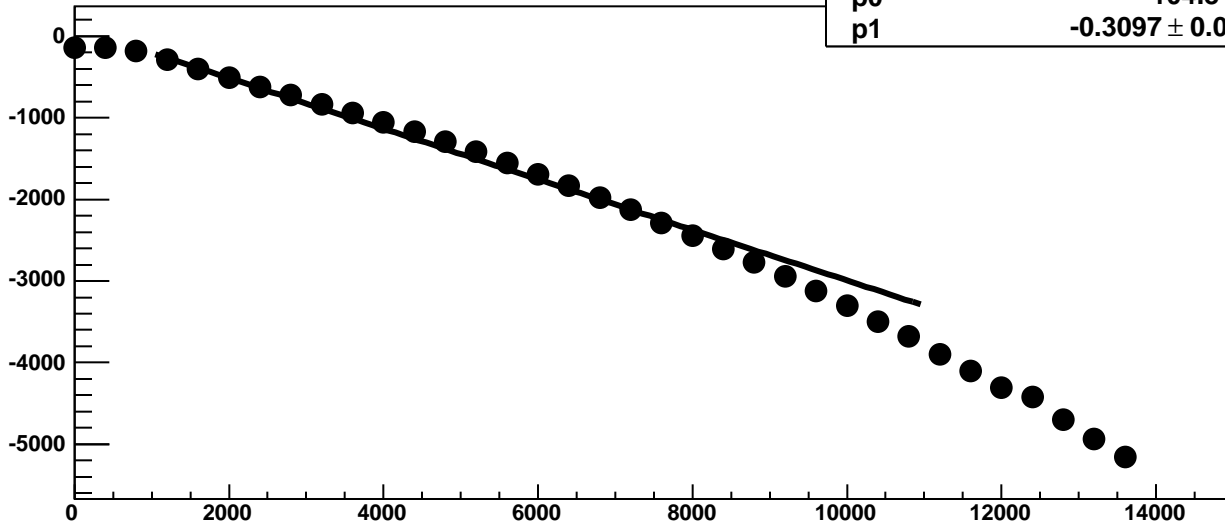
4535 / 23

p0

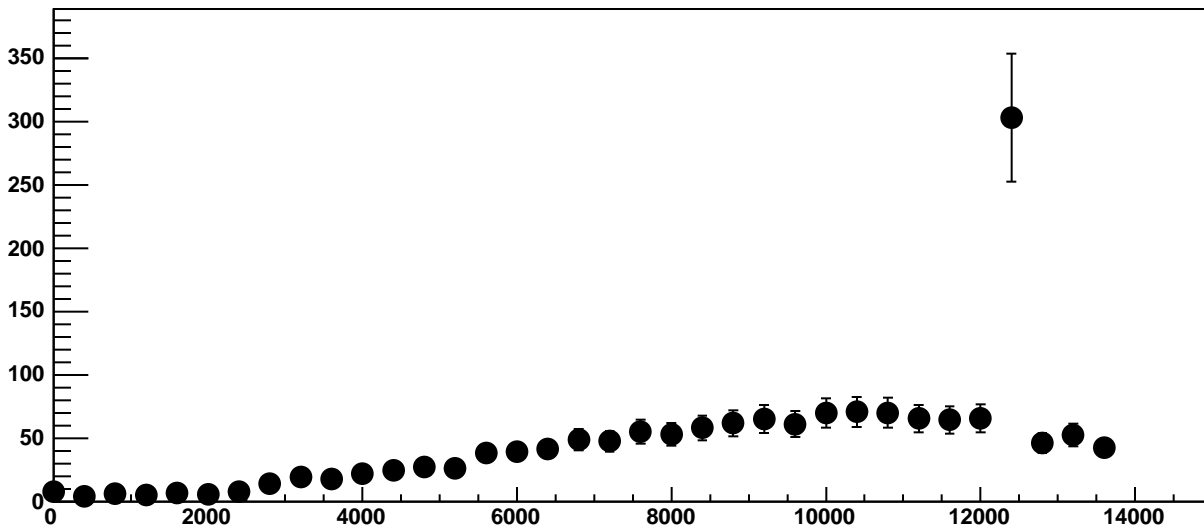
$104.8 \pm 1.204$

p1

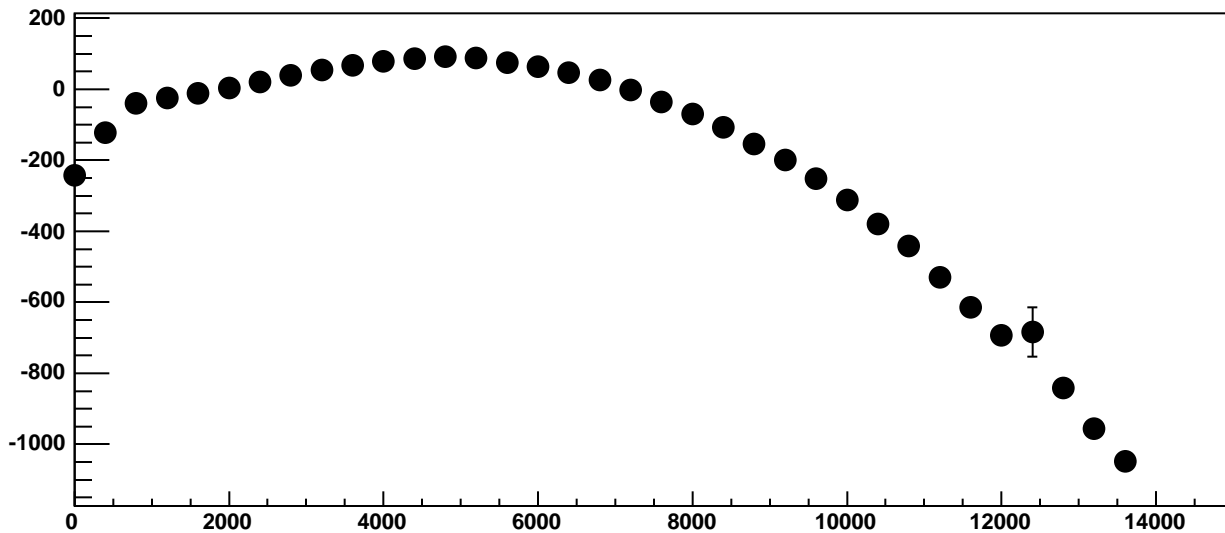
$-0.3097 \pm 0.0004603$



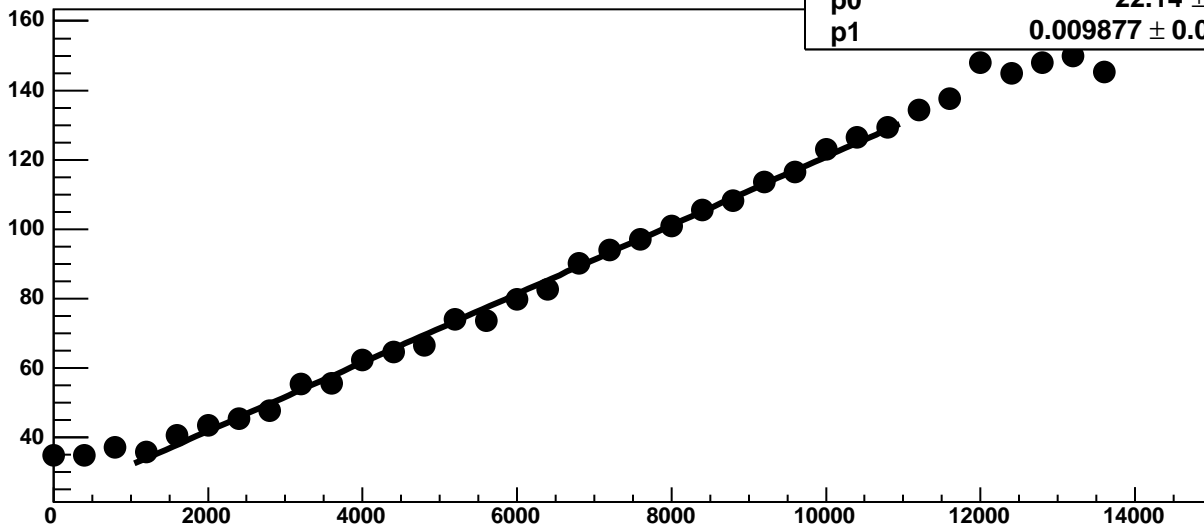
Chip 10, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

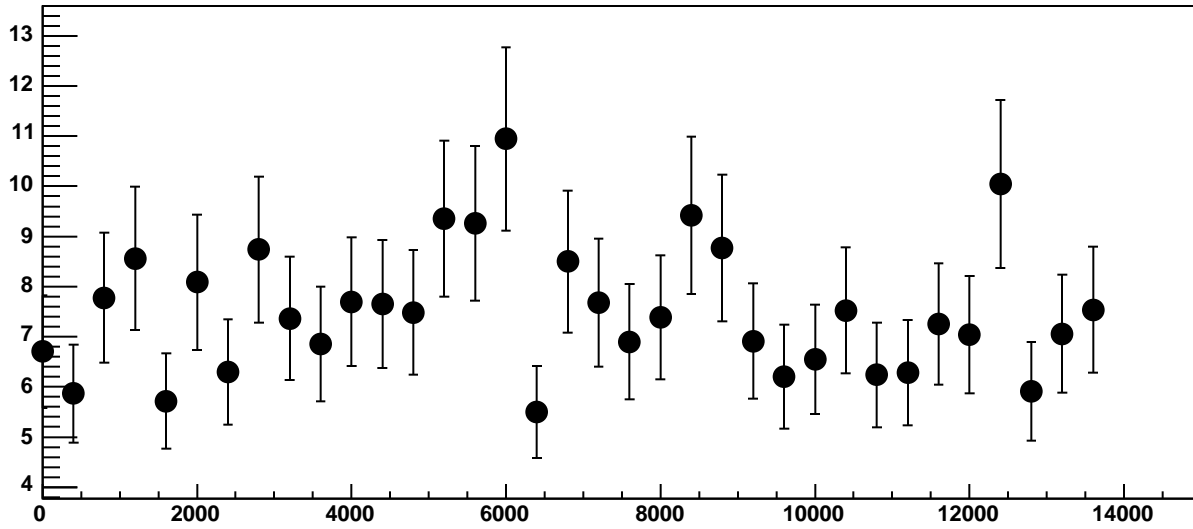


Chip 10, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

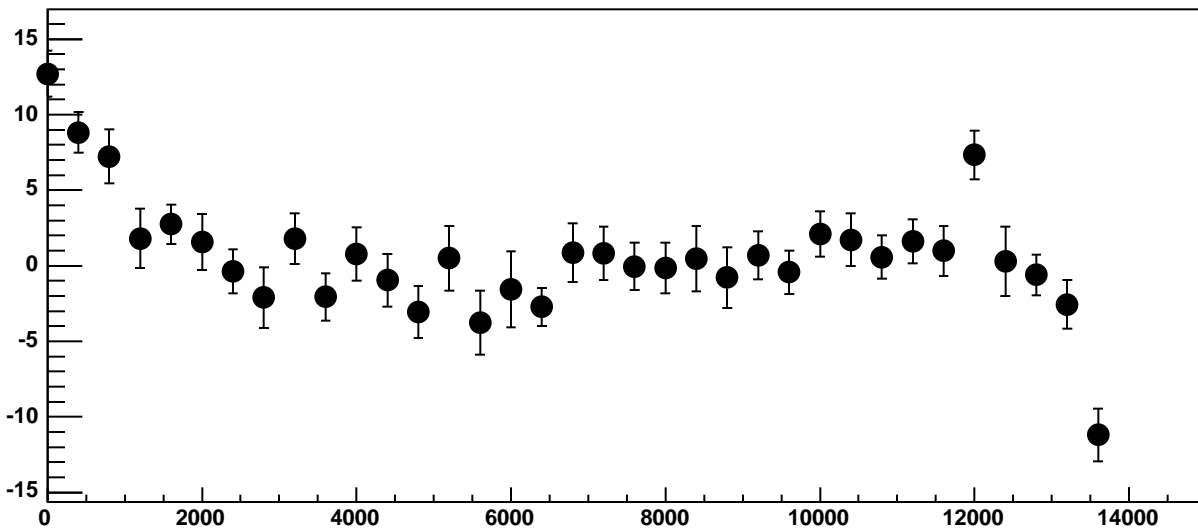


$\chi^2 / \text{ndf}$  25.84 / 23  
p0 22.14 ± 0.7596  
p1 0.009877 ± 0.0001118

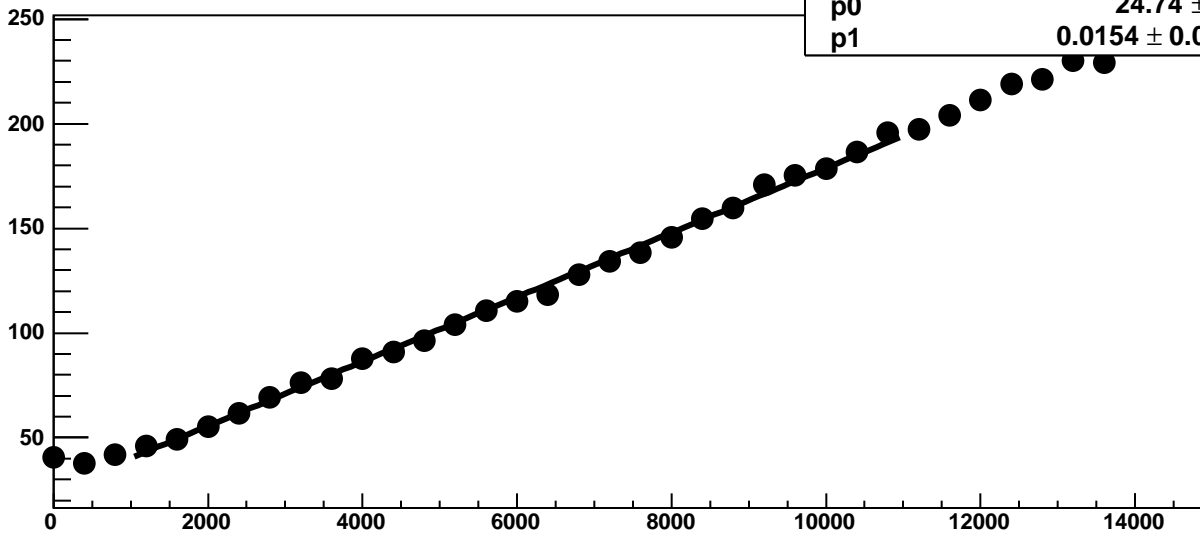
Chip 10, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



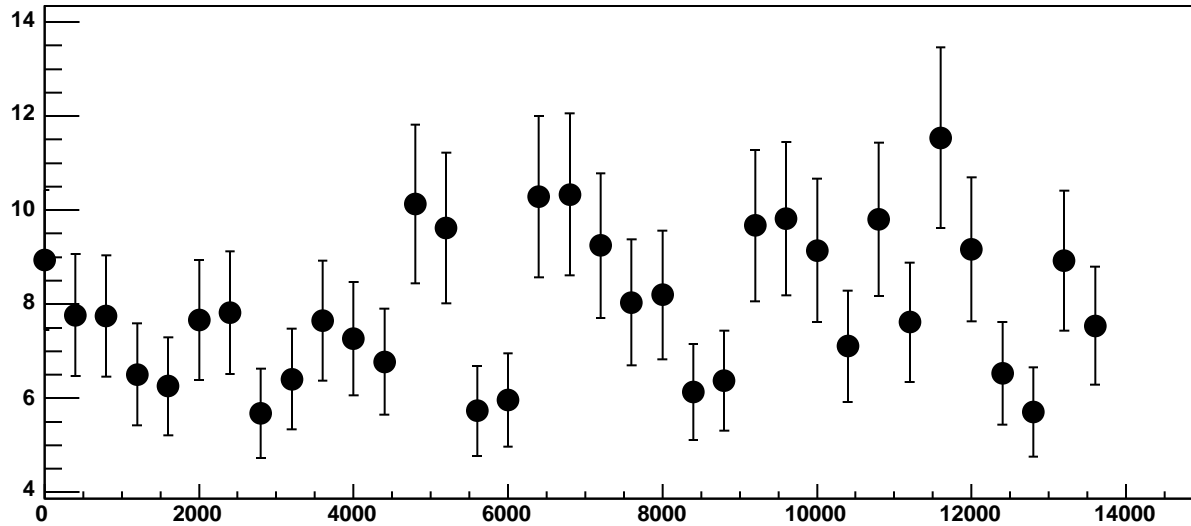
Chip 10, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



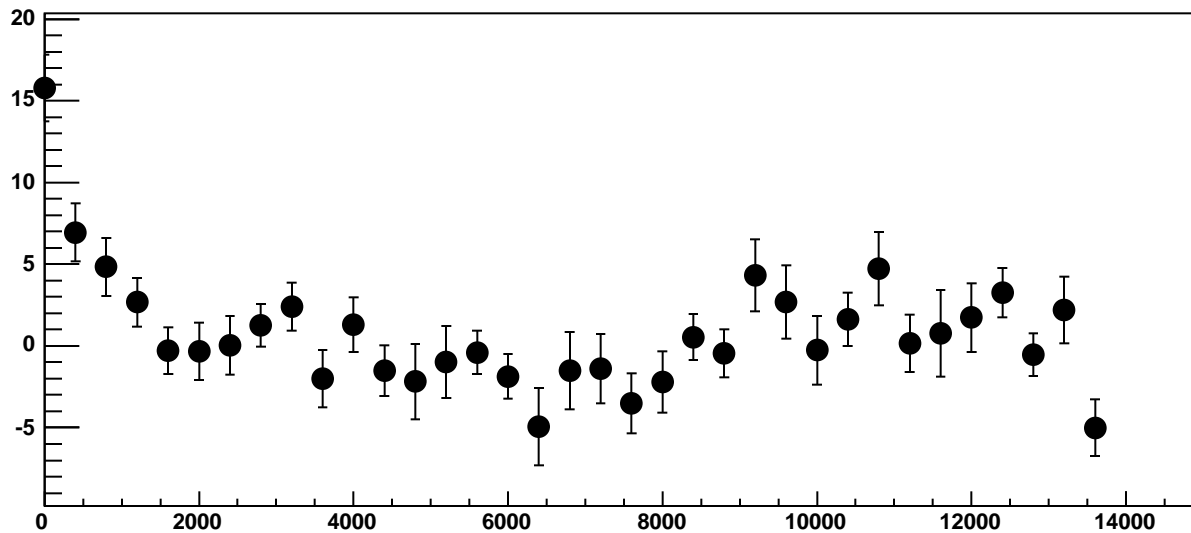
Chip 10, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



Chip 10, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

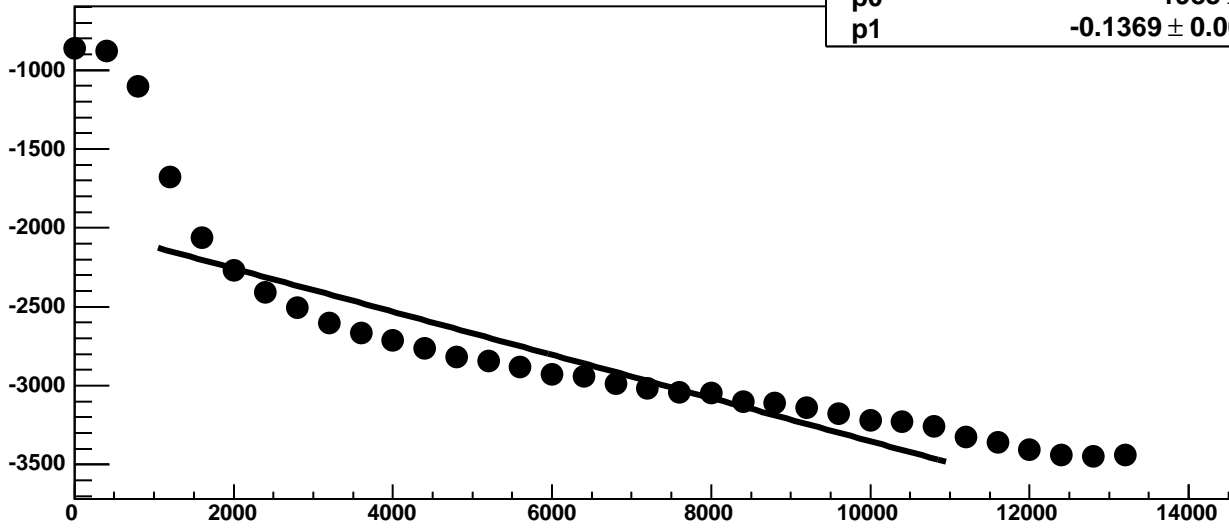
9339 / 23

p0

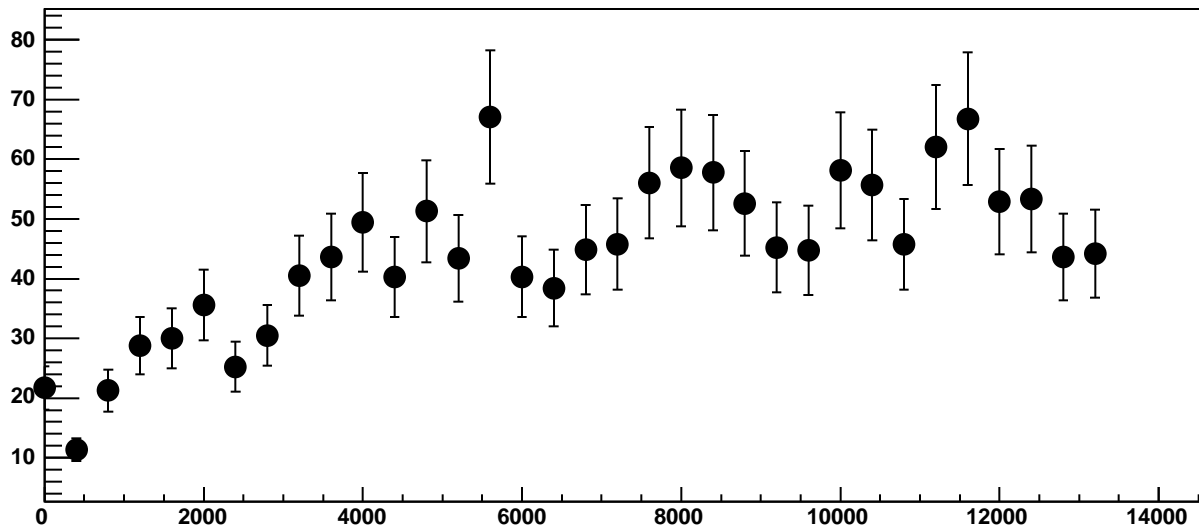
$-1983 \pm 3.659$

p1

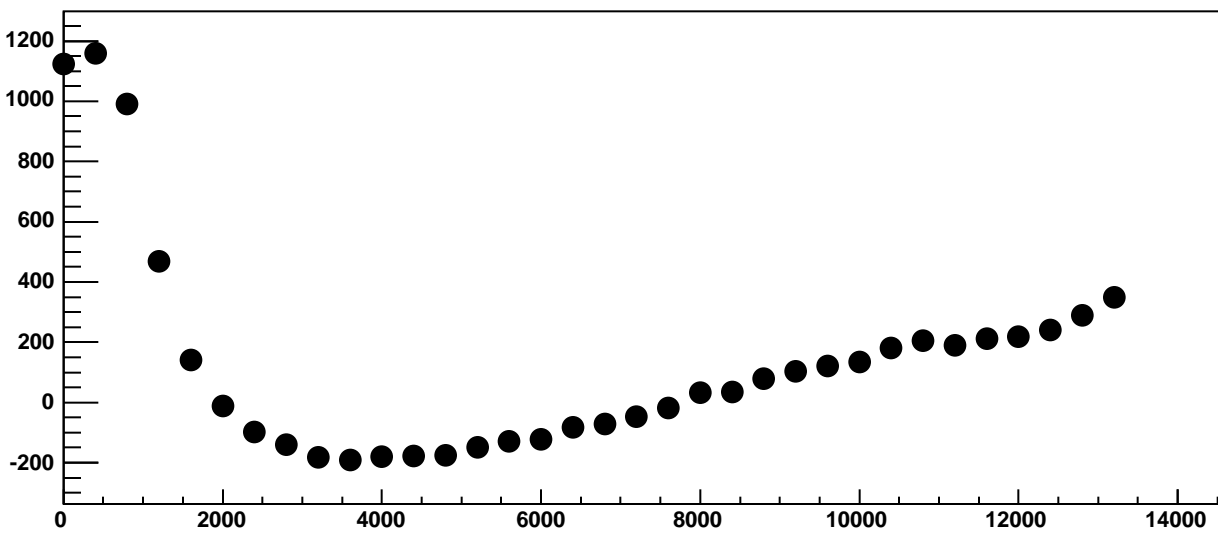
$-0.1369 \pm 0.0006408$



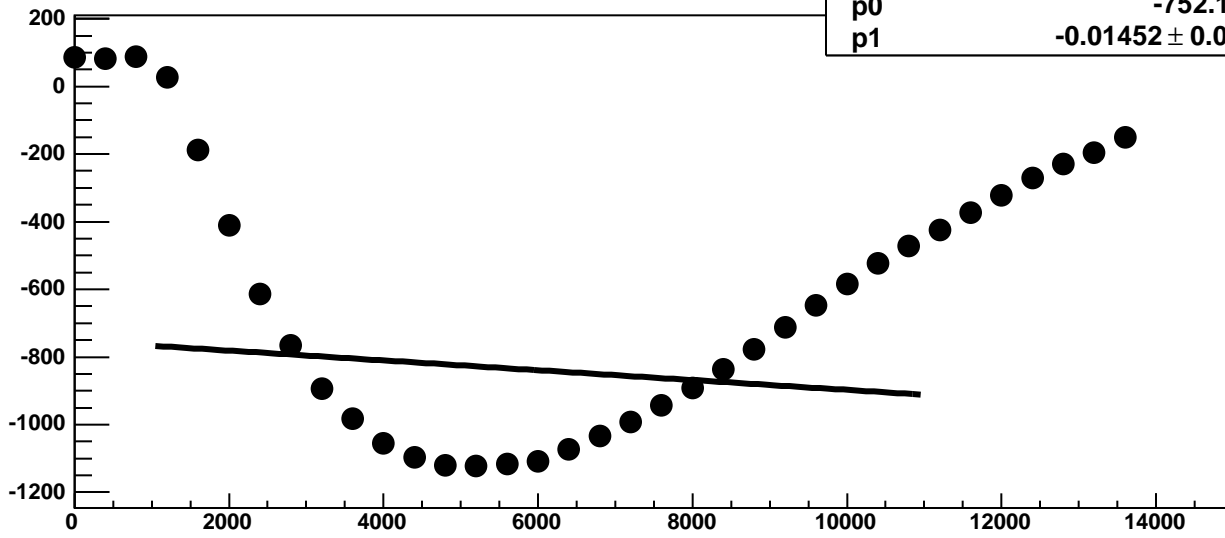
Chip 10, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

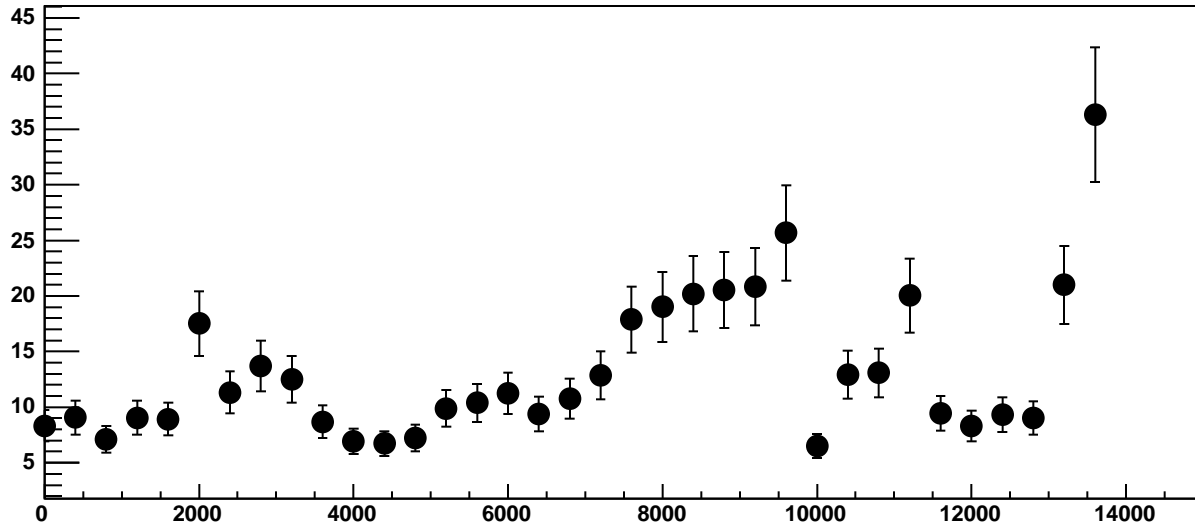


Chip 10, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

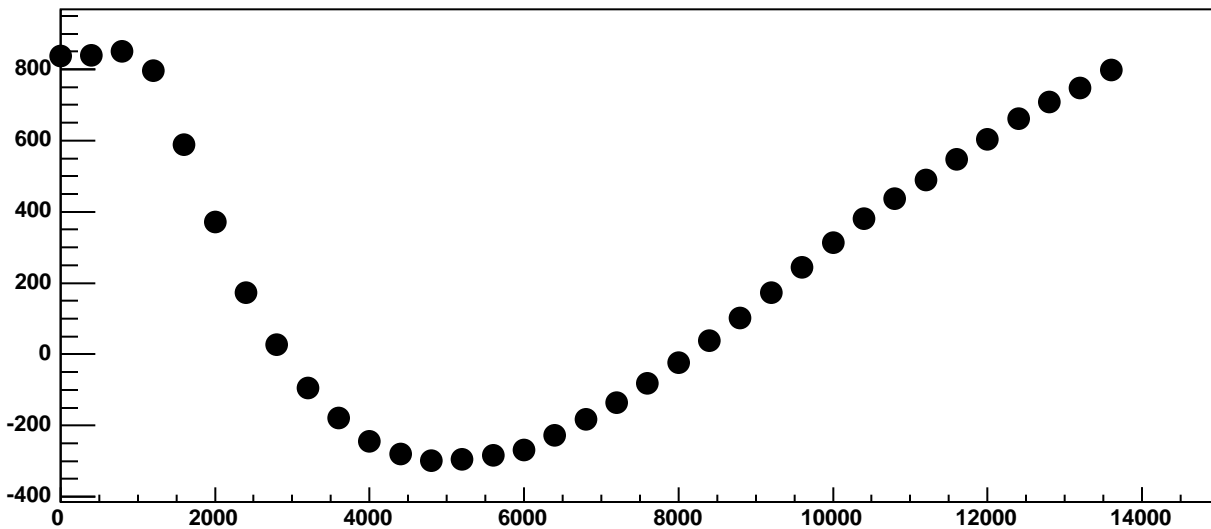


$\chi^2 / \text{ndf}$  4.86e+05 / 23  
p0 -752.1 ± 1.07  
p1 -0.01452 ± 0.0001762

Chip 10, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

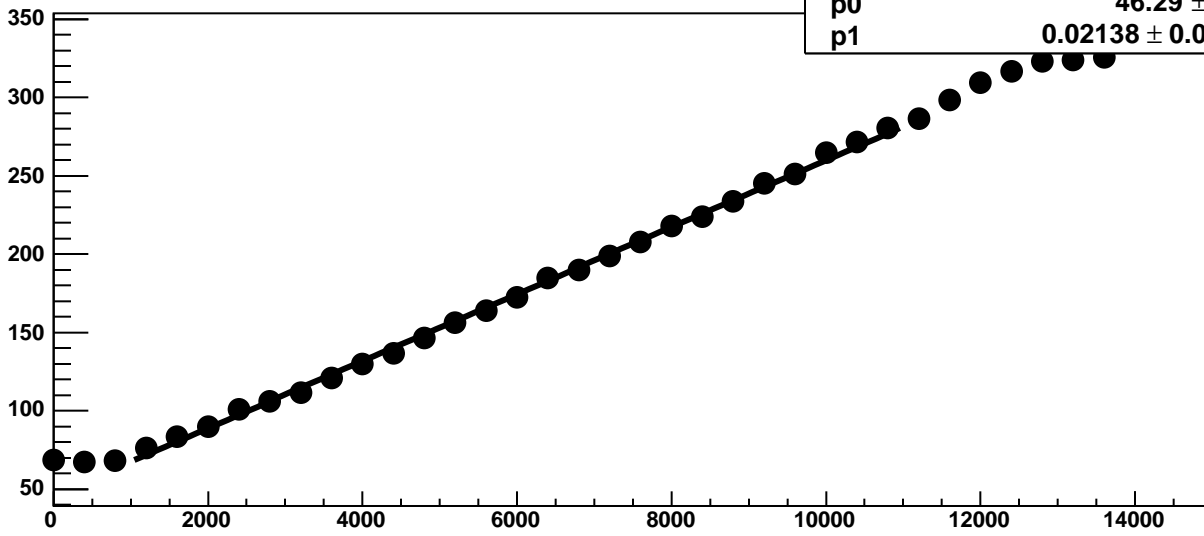


Chip 10, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

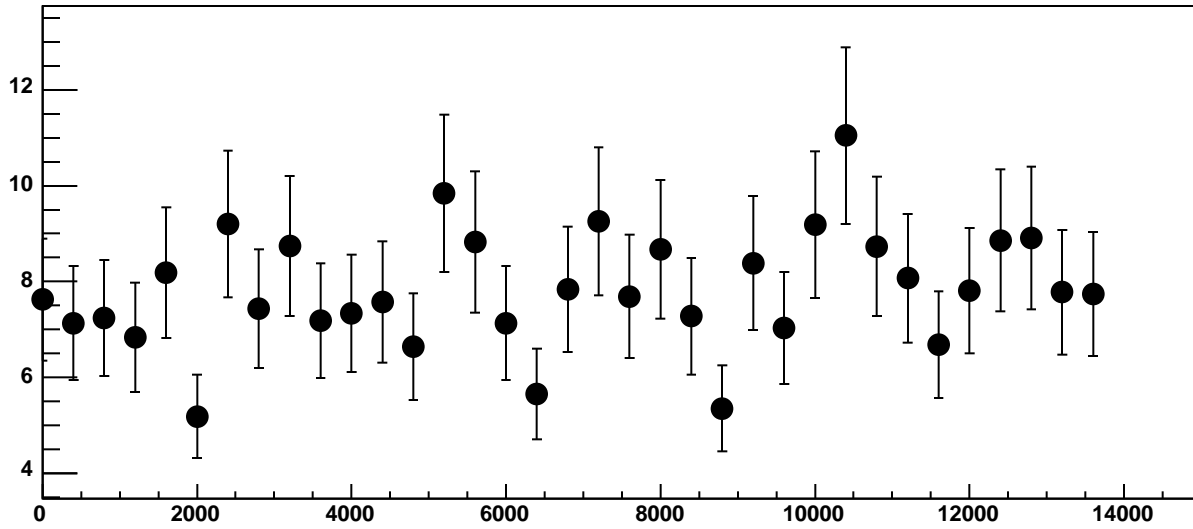




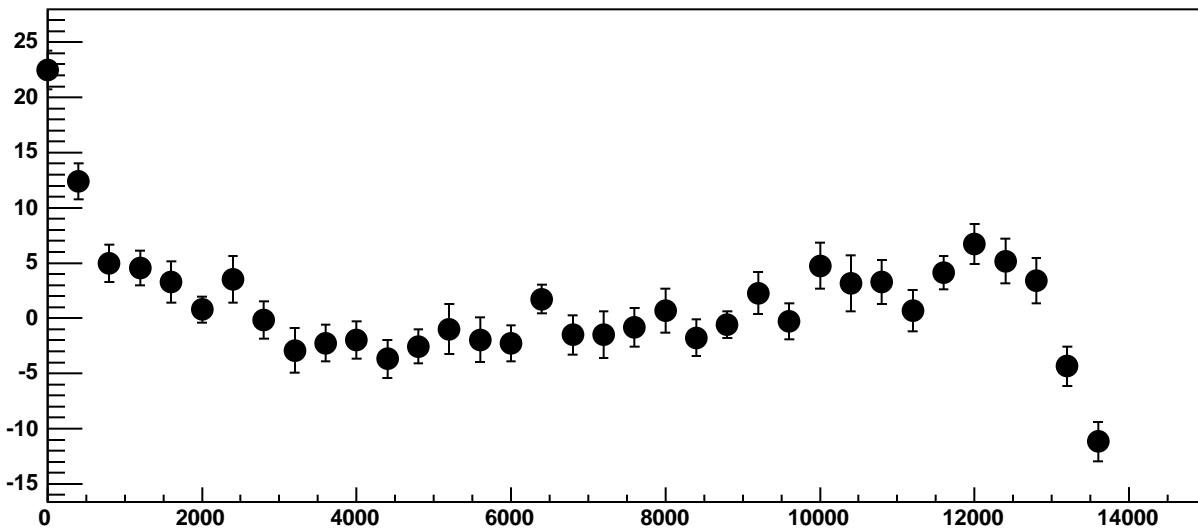
Chip 10, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC



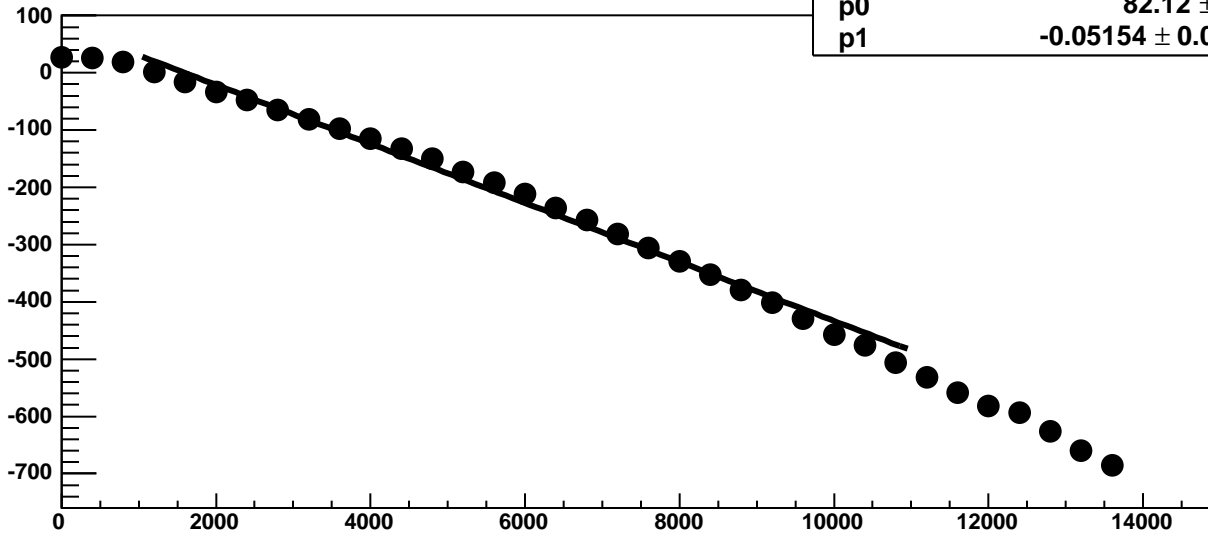
Chip 10, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

997.3 / 23

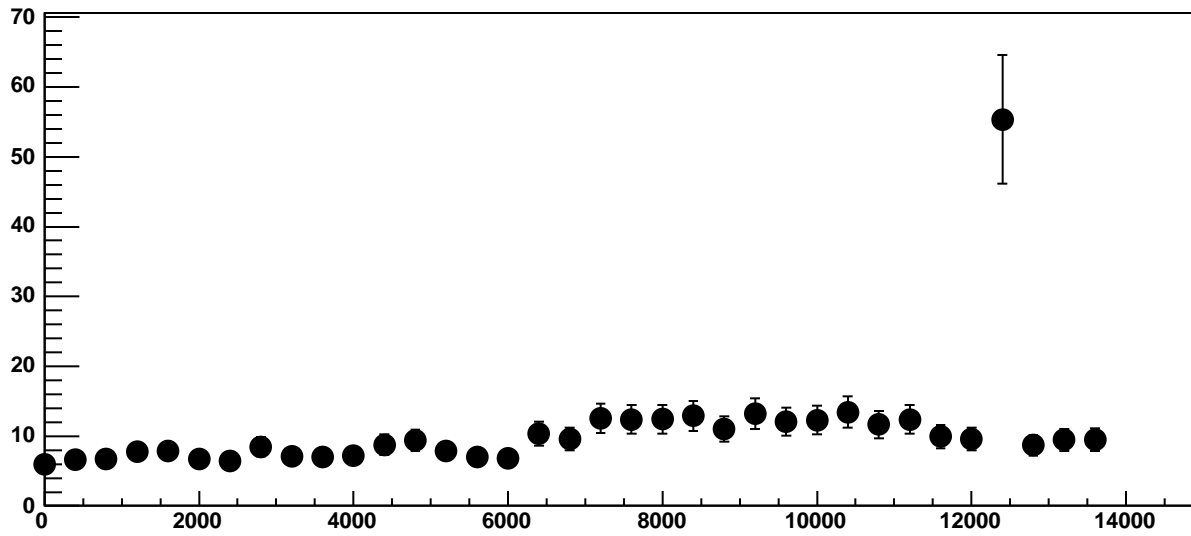
p0

$82.12 \pm 0.8567$

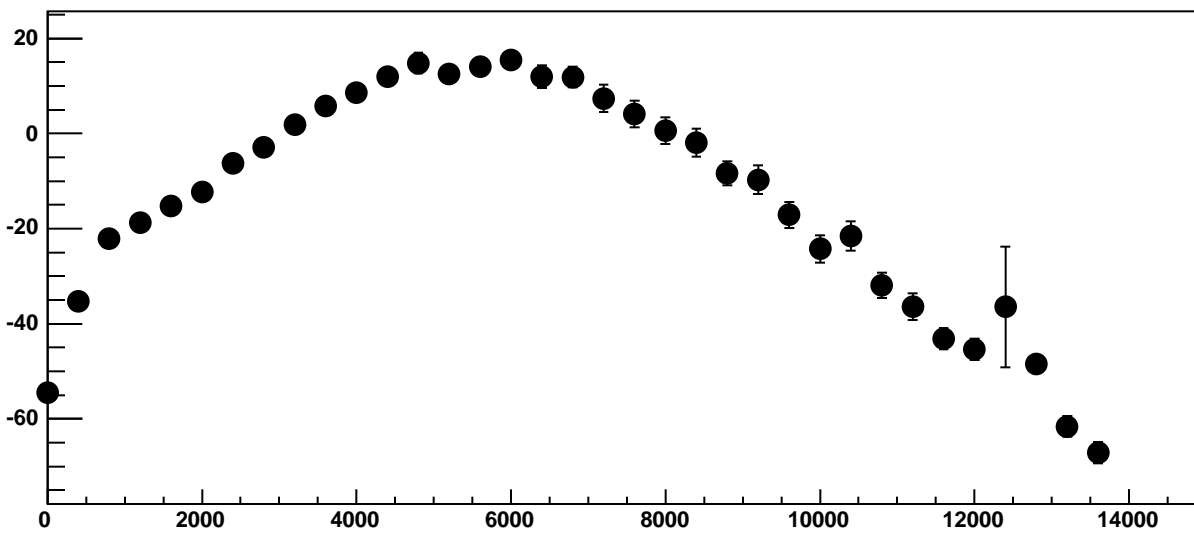
p1

$-0.05154 \pm 0.0001546$

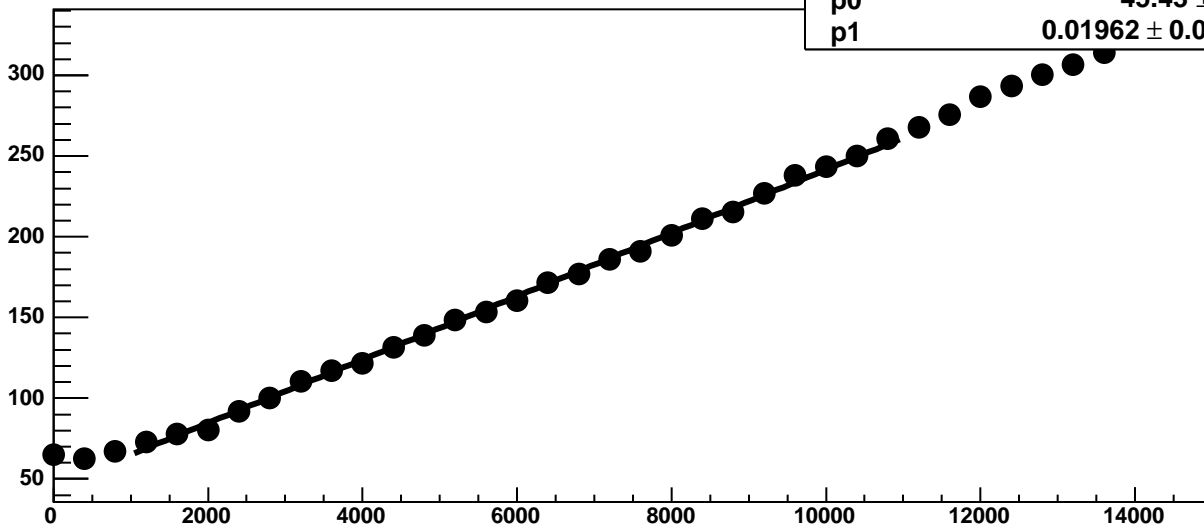
Chip 10, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

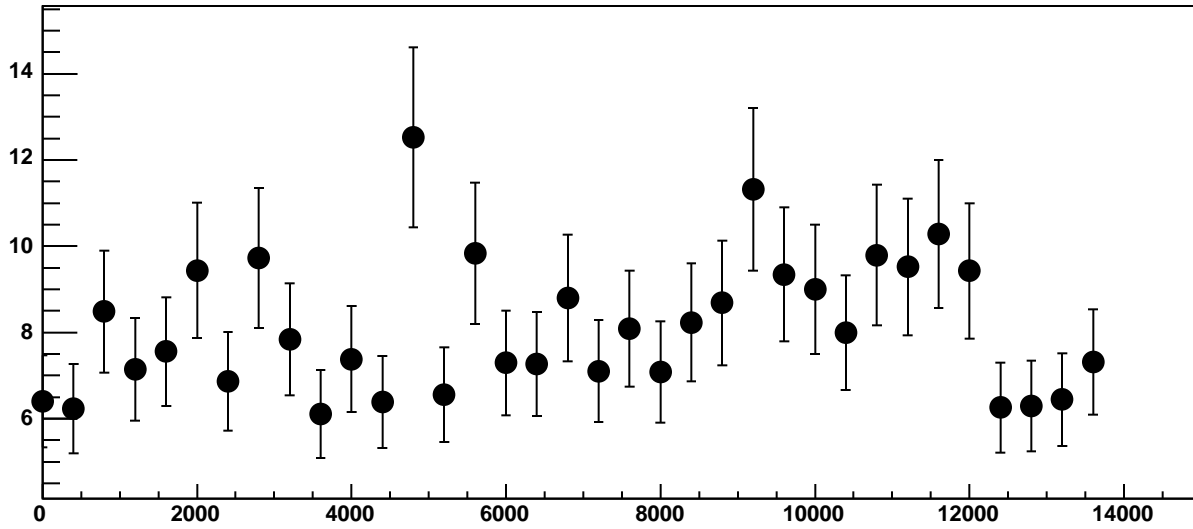


Chip 10, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

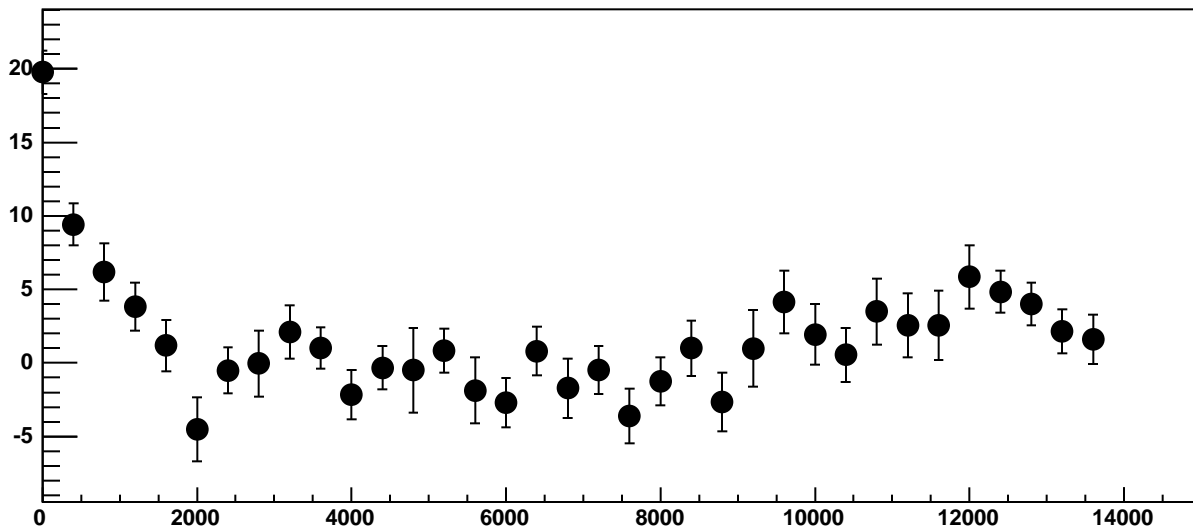


$\chi^2 / \text{ndf}$  32.29 / 23  
p0 45.43 ± 0.8271  
p1 0.01962 ± 0.0001309

Chip 10, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

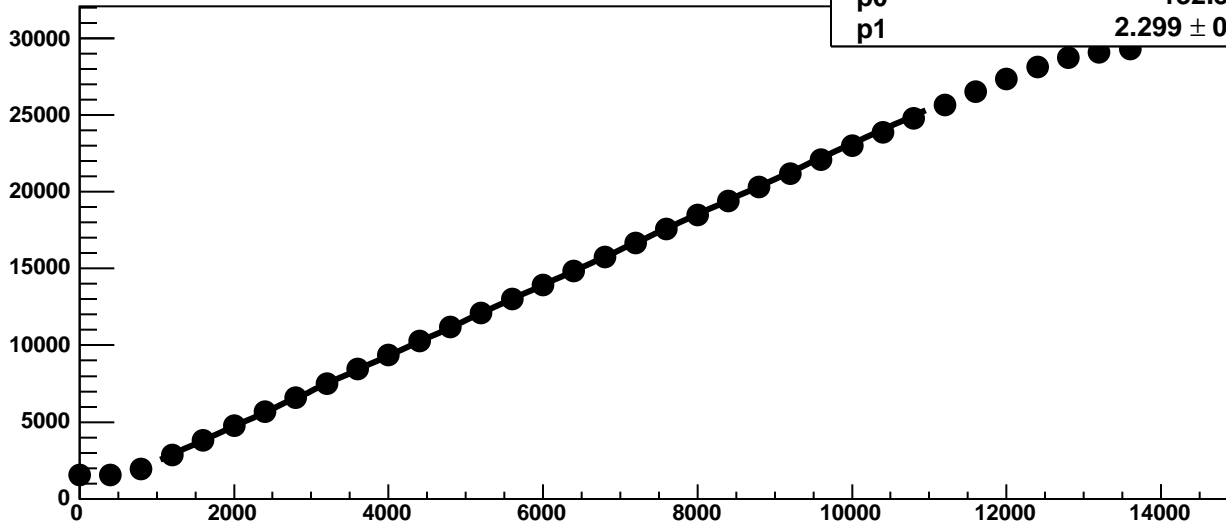


Chip 10, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

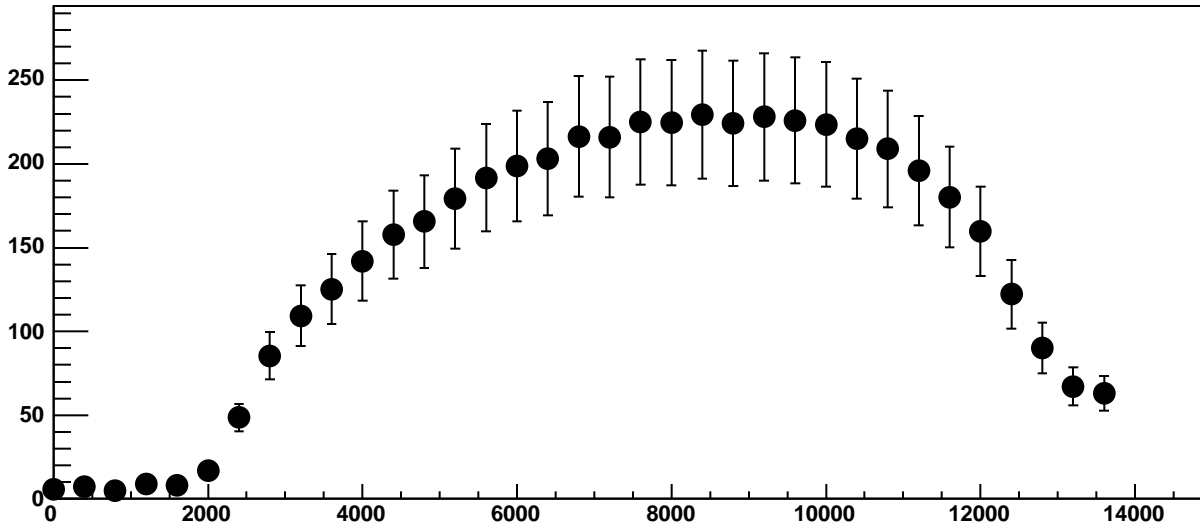


Chip 10, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

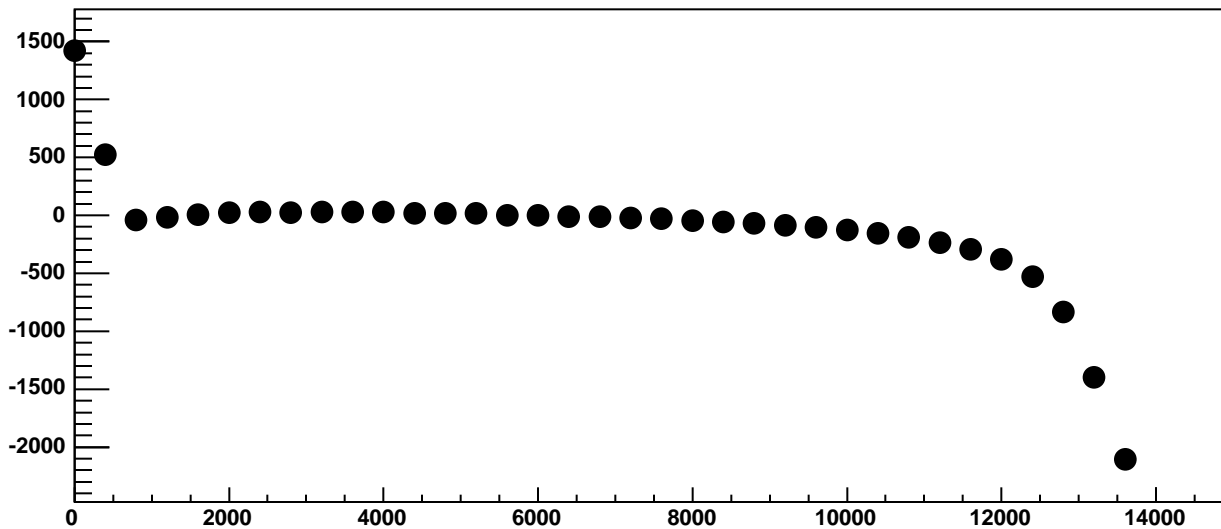
$\chi^2 / \text{ndf}$  182.5 / 23  
p0 132.3  $\pm$  2.971  
p1 2.299  $\pm$  0.001691



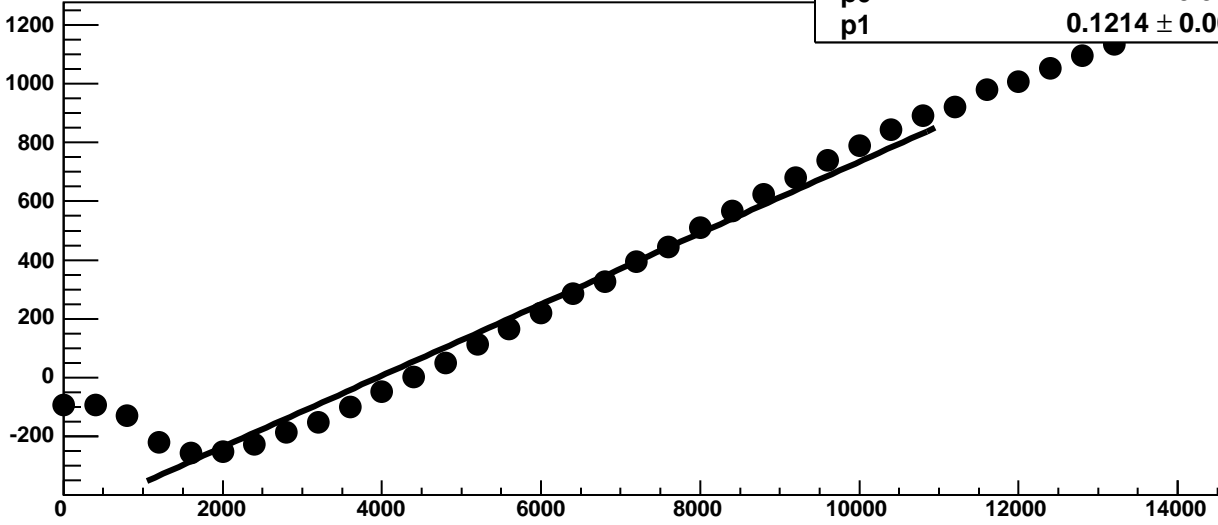
Chip 10, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC



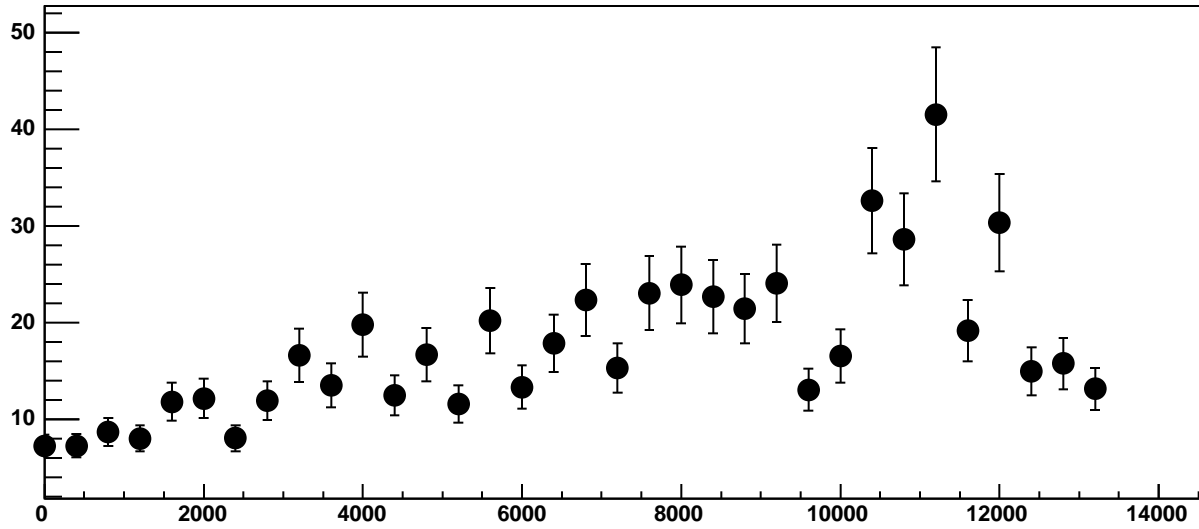
Chip 10, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC



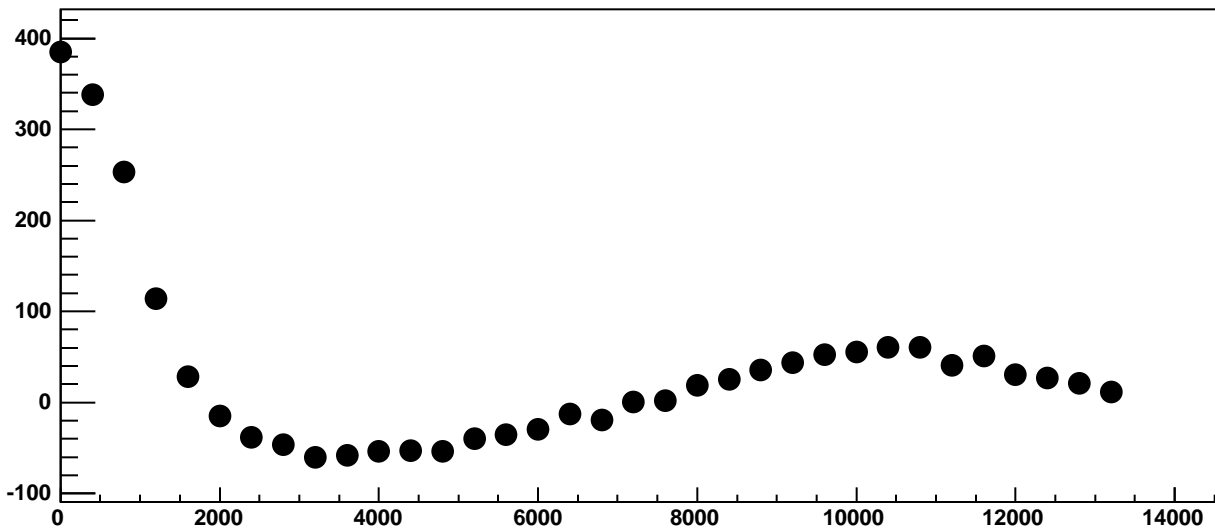
Chip 10, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

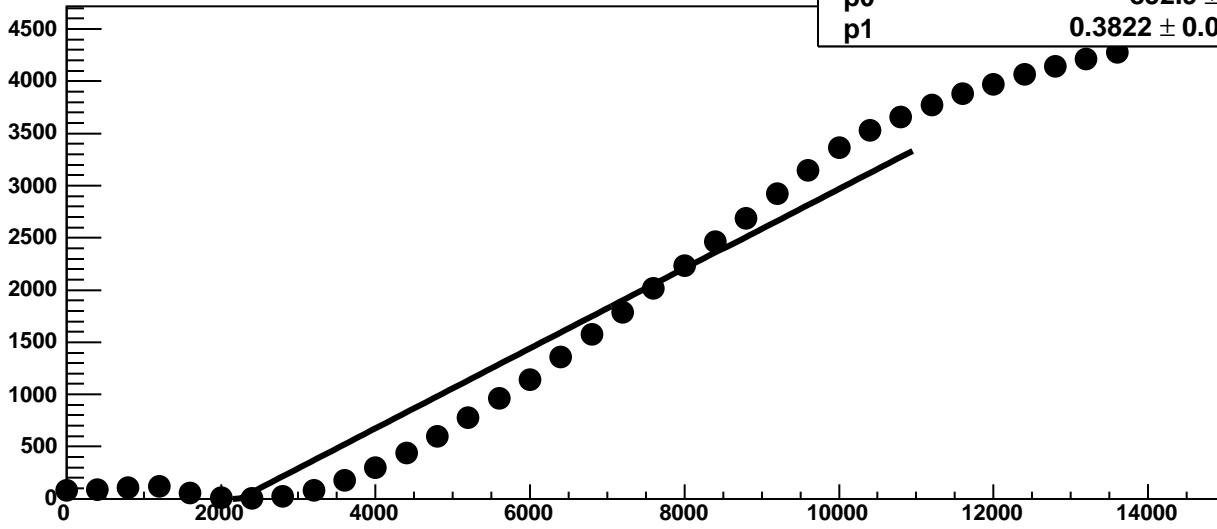


Chip 10, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

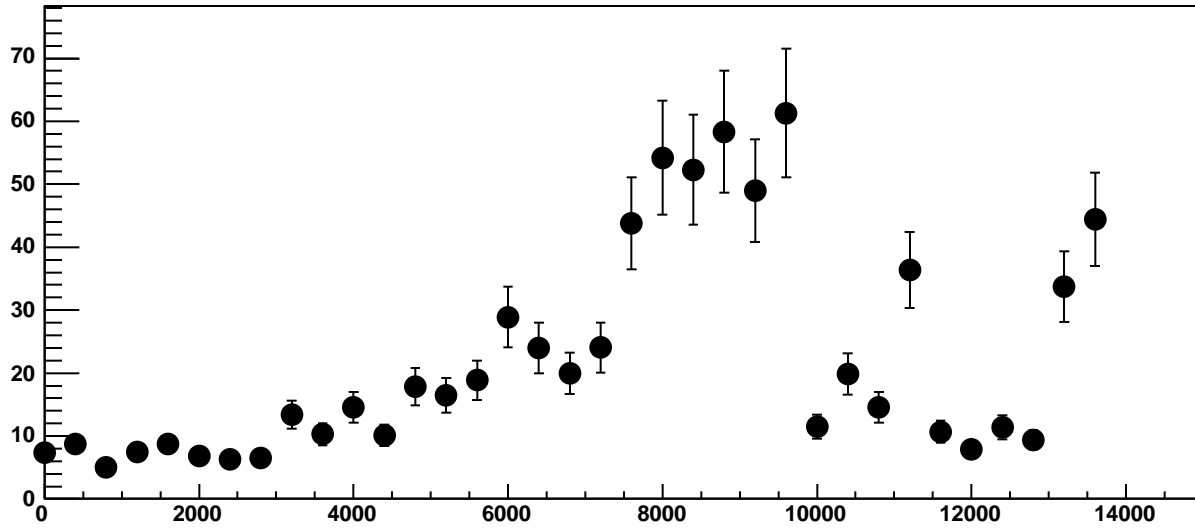


Chip 10, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

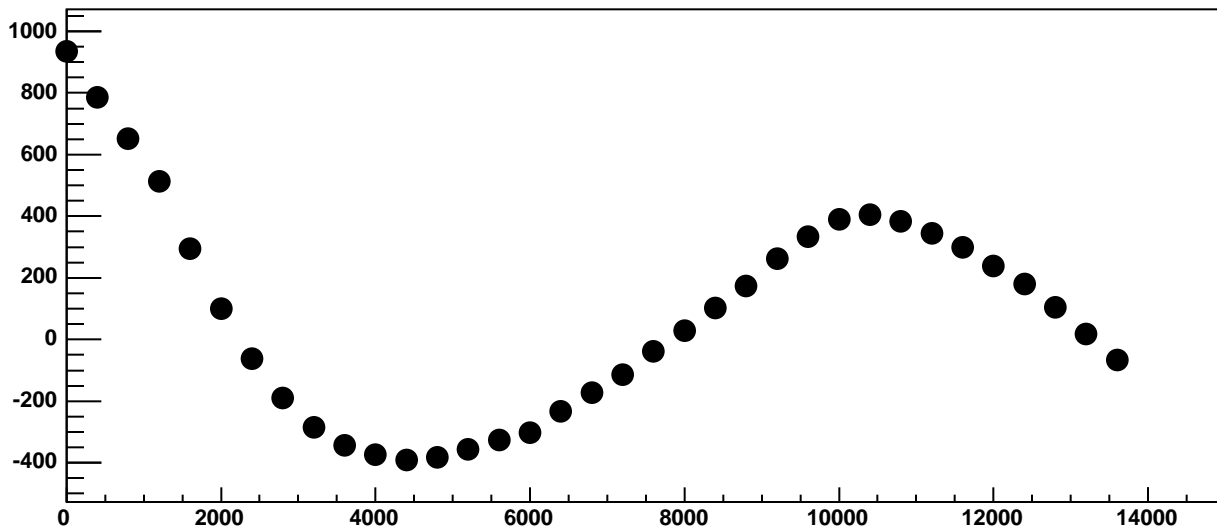
$\chi^2 / \text{ndf}$  2.789e+05 / 23  
p0 -852.9 ± 0.9742  
p1 0.3822 ± 0.0002196



Chip 10, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

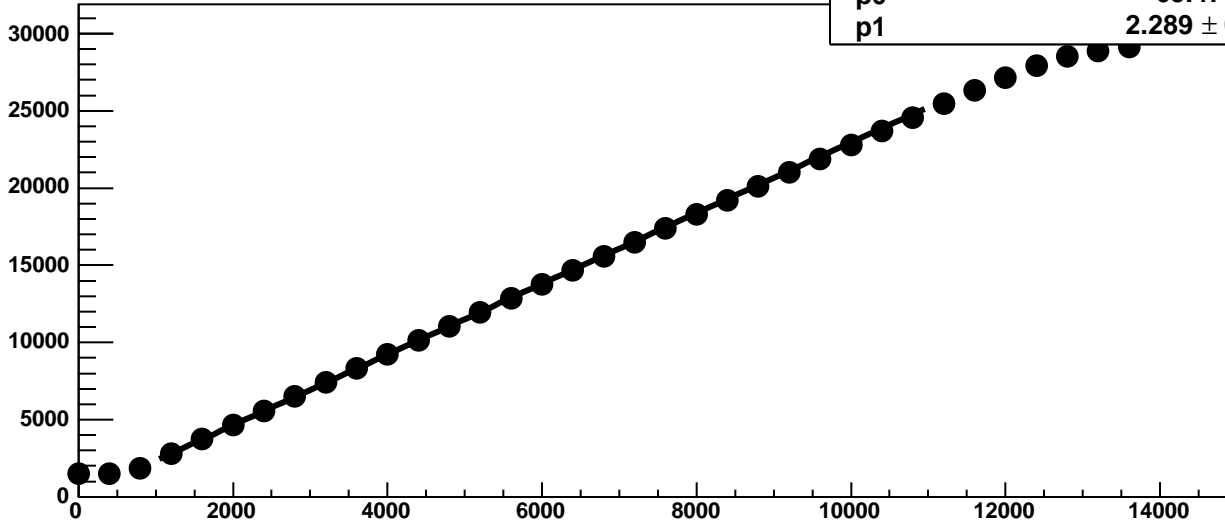


Chip 10, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

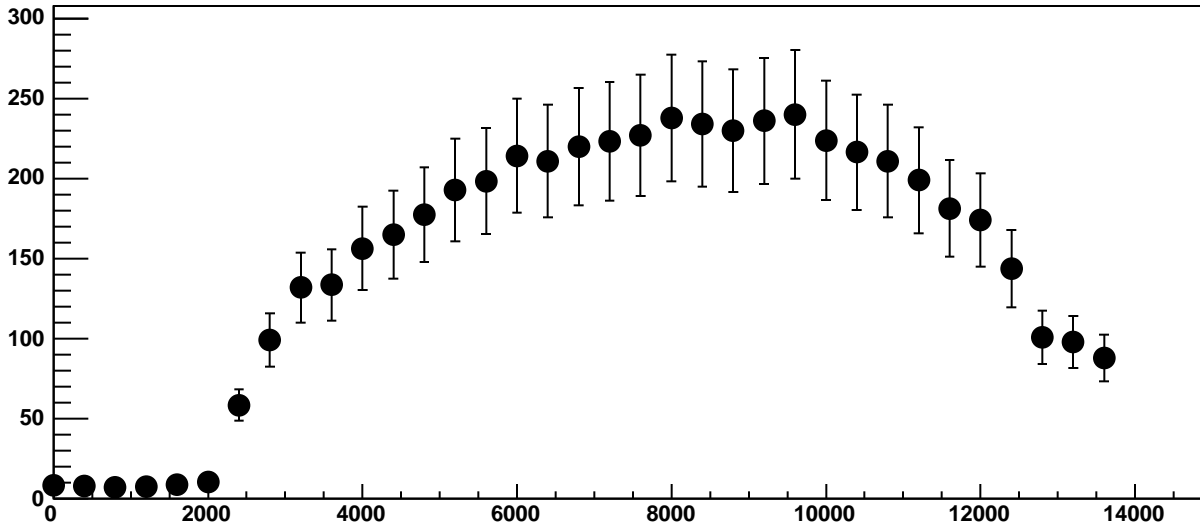


Chip 10, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC

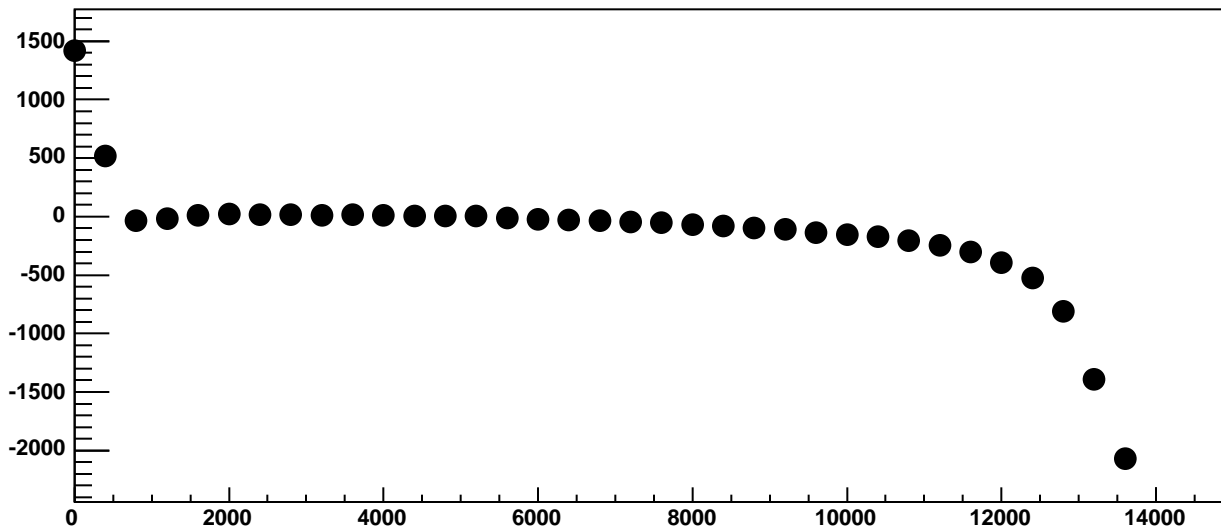
$\chi^2 / \text{ndf}$  225.6 / 23  
p0  $65.47 \pm 2.892$   
p1  $2.289 \pm 0.00167$



Chip 10, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC

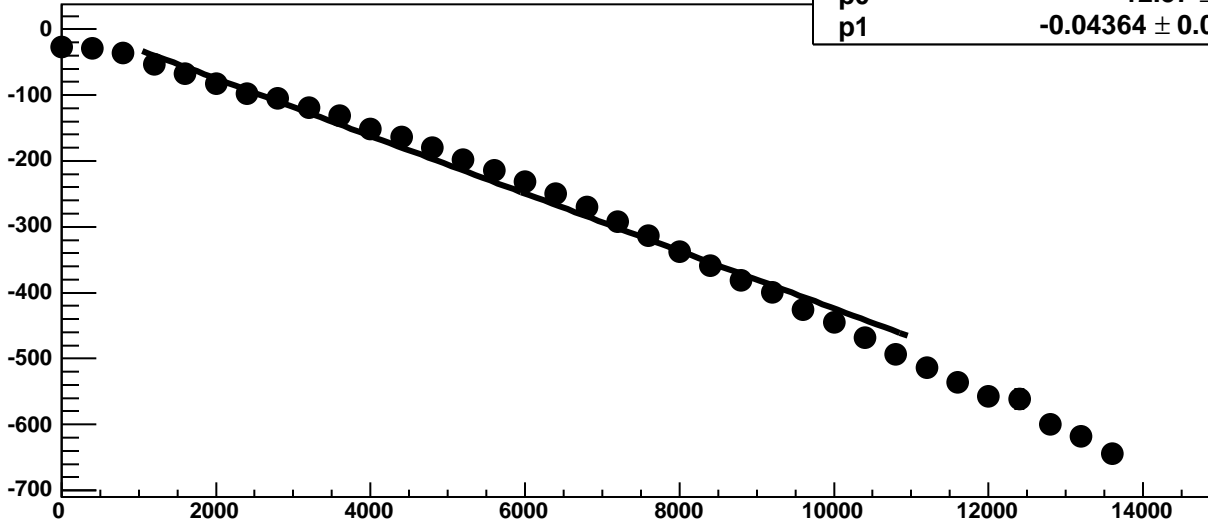


Chip 10, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC

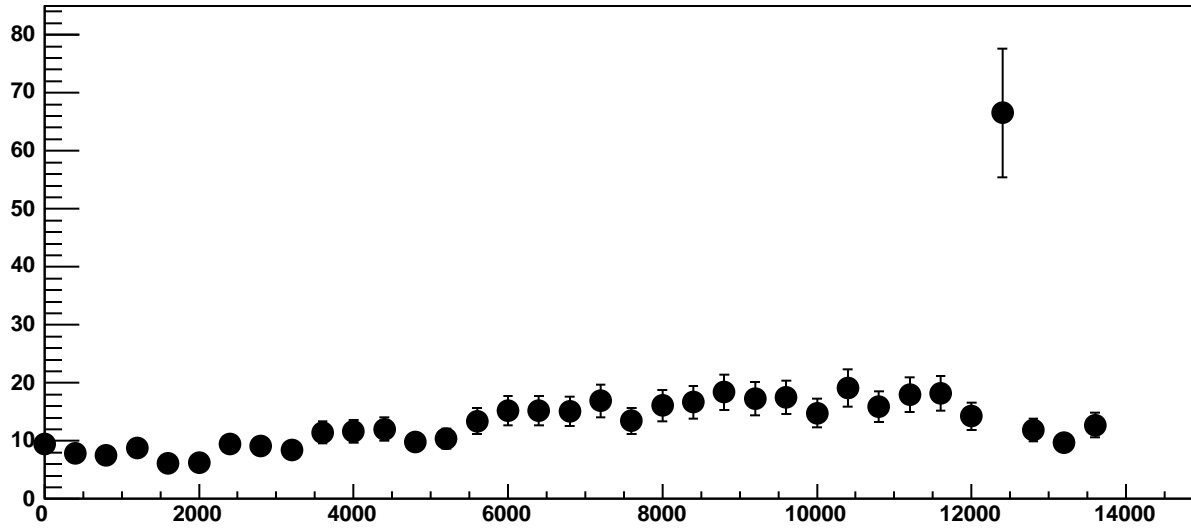


Chip 10, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

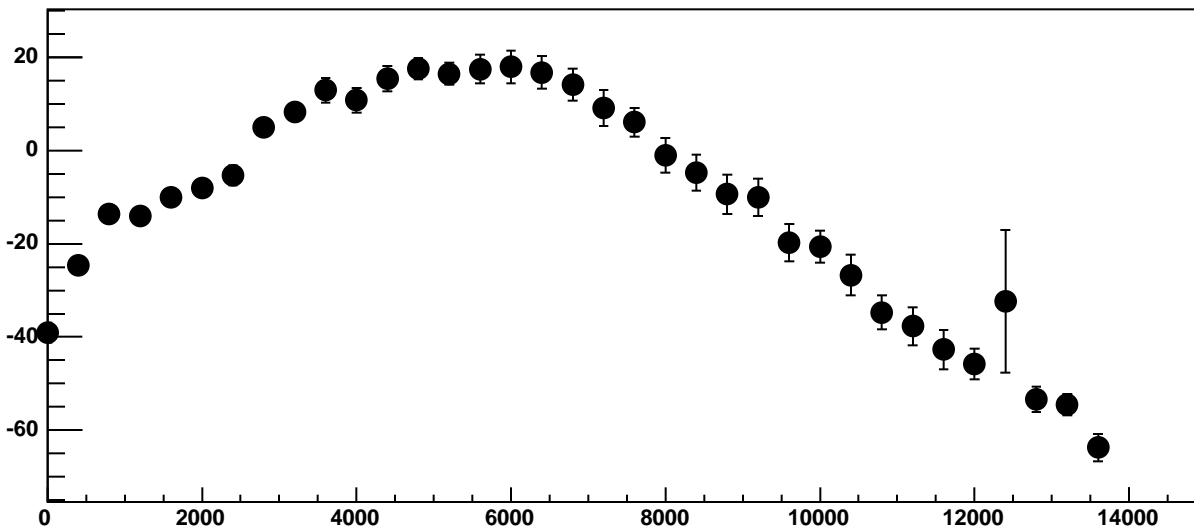
$\chi^2 / \text{ndf}$  651.2 / 23  
p0  $12.57 \pm 0.9505$   
p1  $-0.04364 \pm 0.0001889$



Chip 10, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

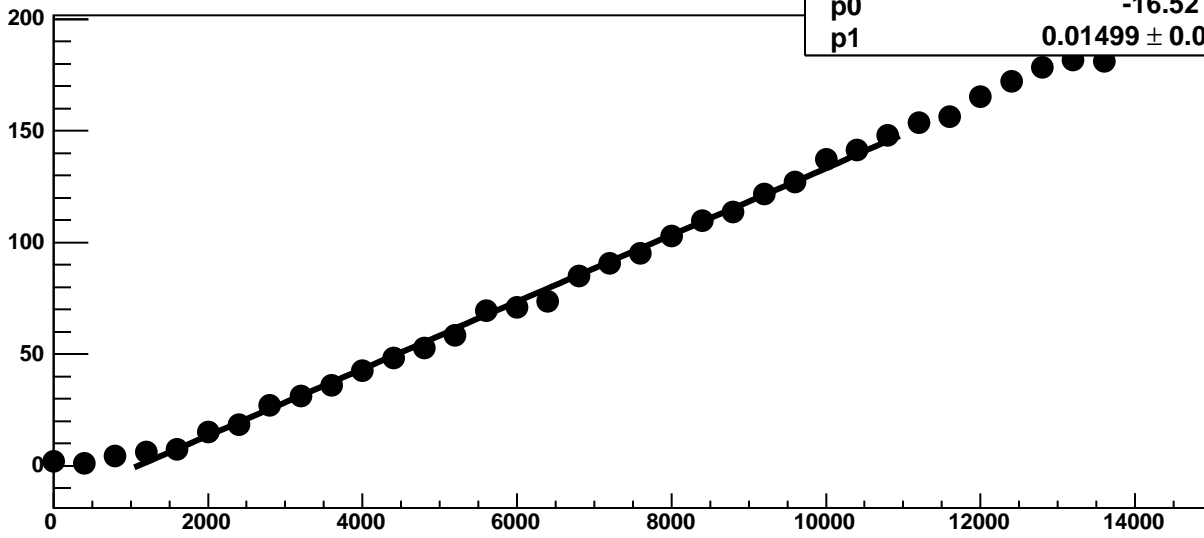


Chip 10, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



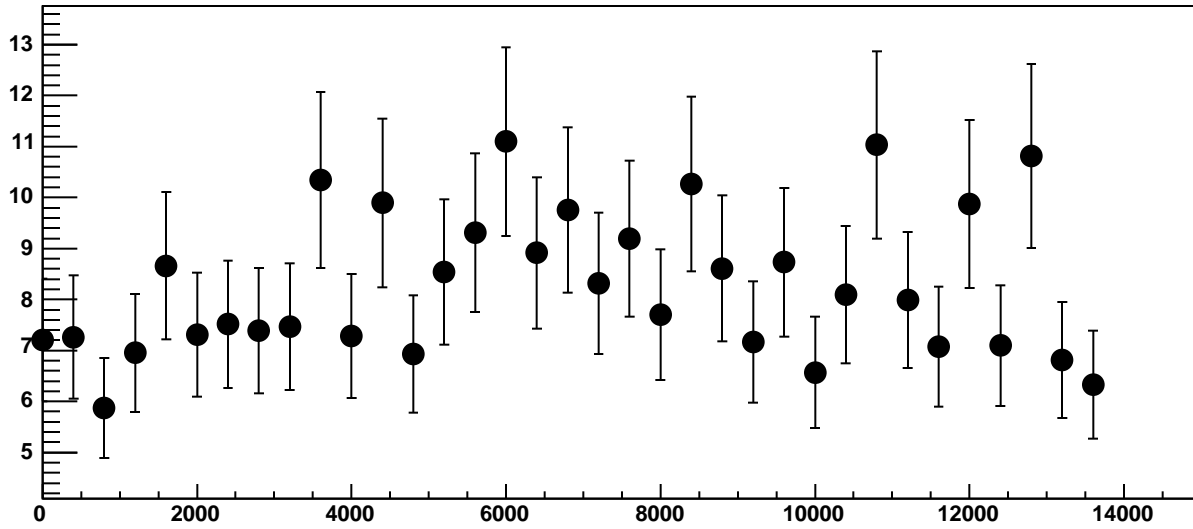


Chip 10, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

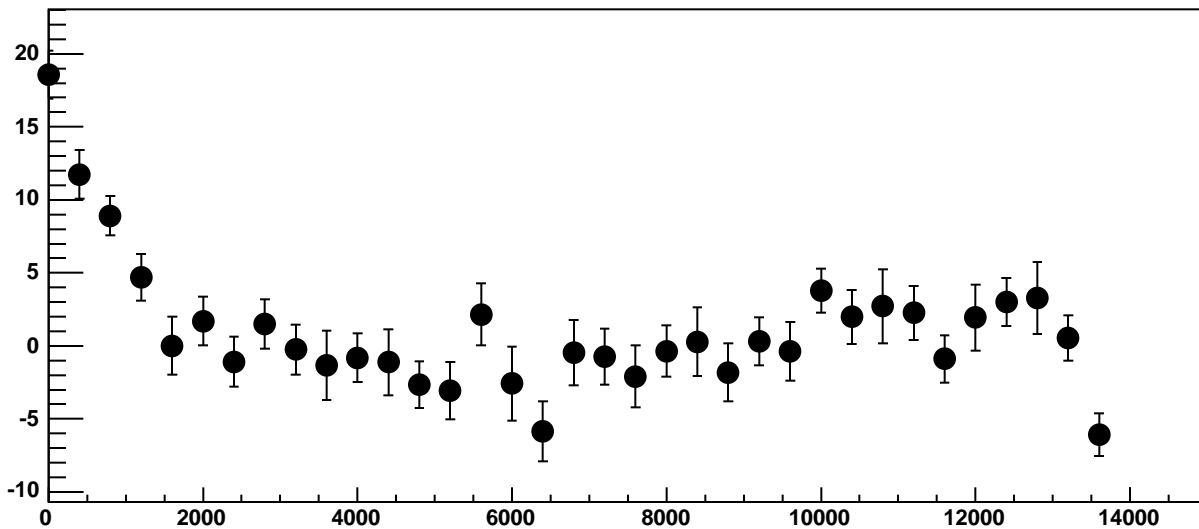


$\chi^2 / \text{ndf}$  37.78 / 23  
p0 -16.52 ± 0.827  
p1 0.01499 ± 0.0001266

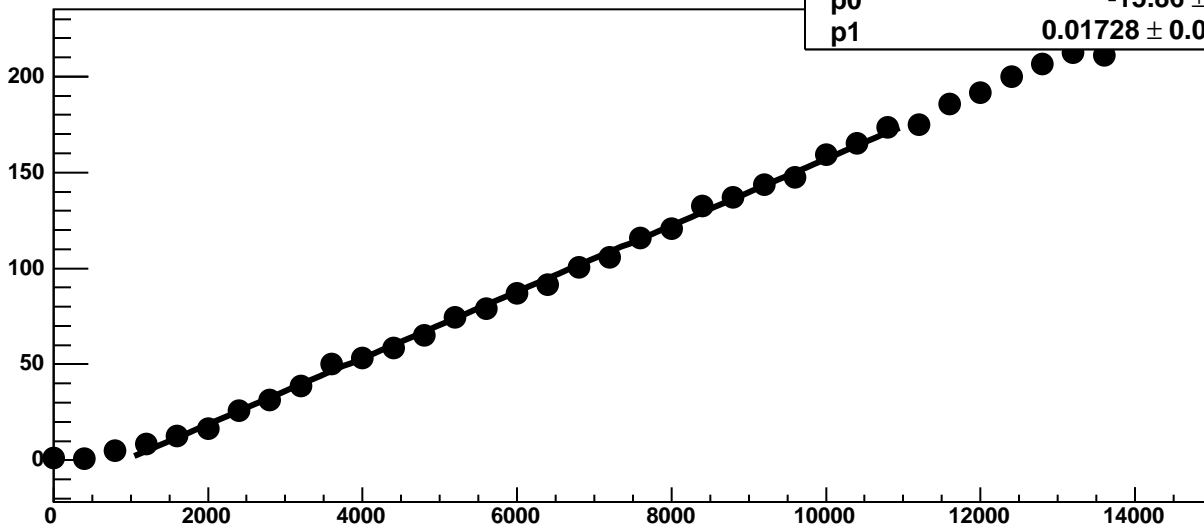
Chip 10, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

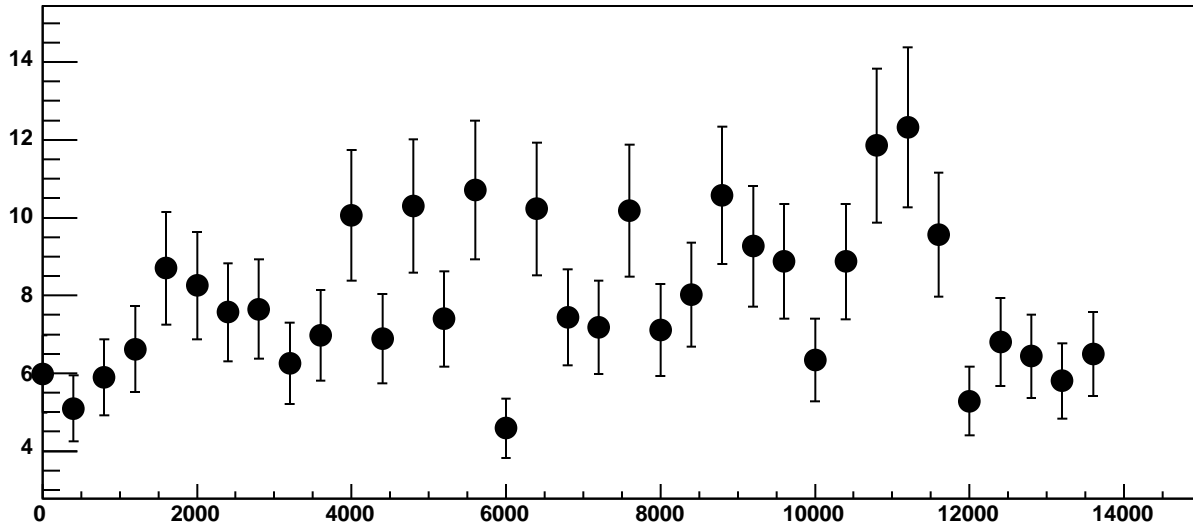


Chip 10, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

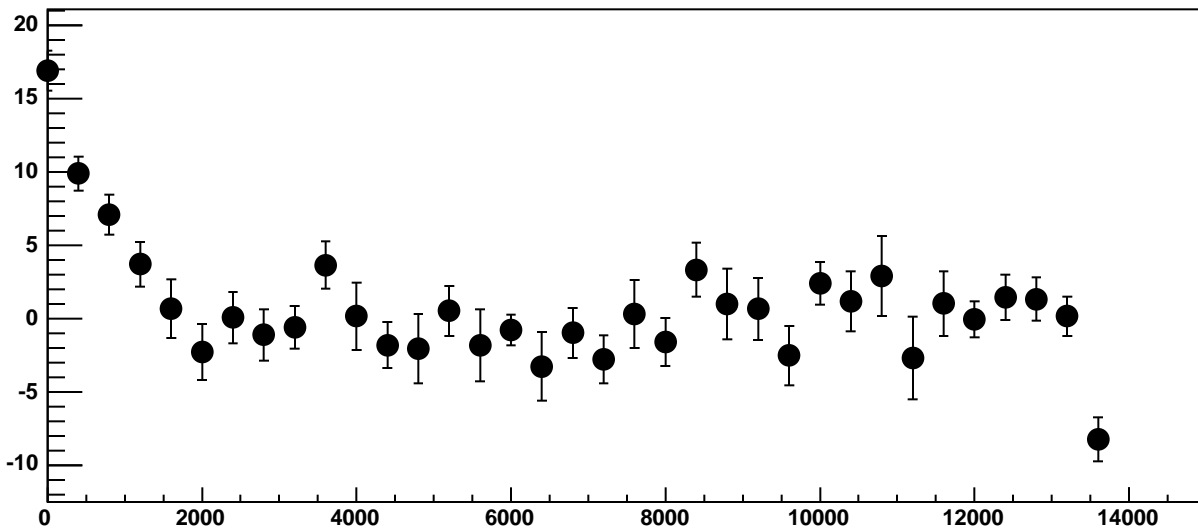


$\chi^2 / \text{ndf}$  31.89 / 23  
p0 -15.86 ± 0.8133  
p1 0.01728 ± 0.0001279

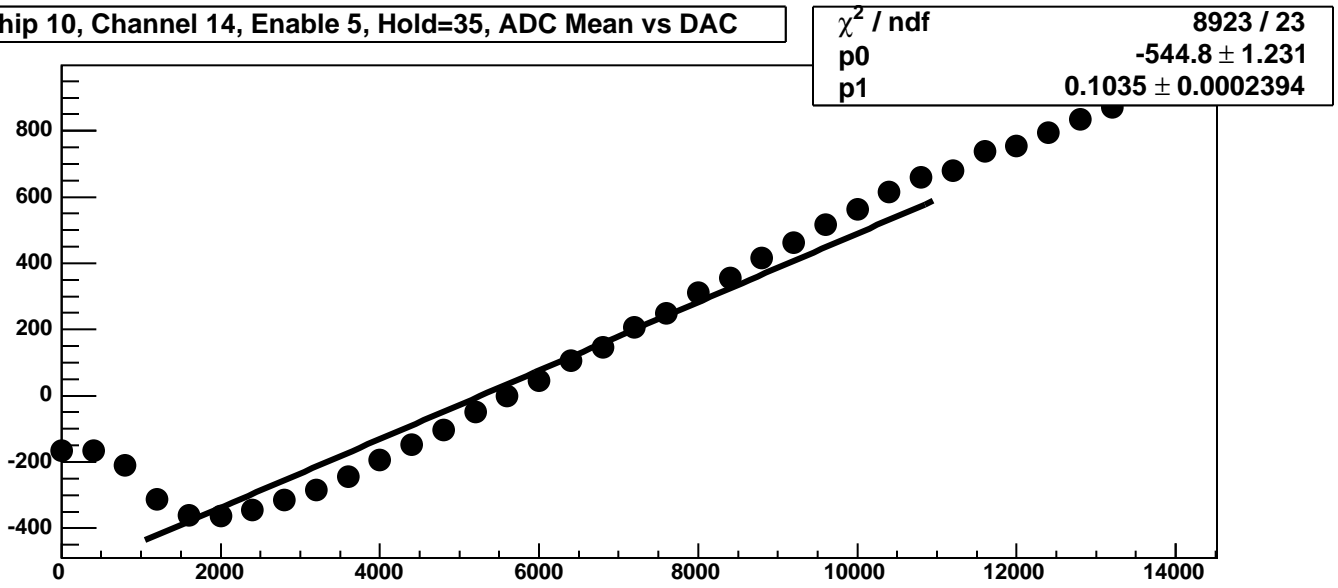
Chip 10, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



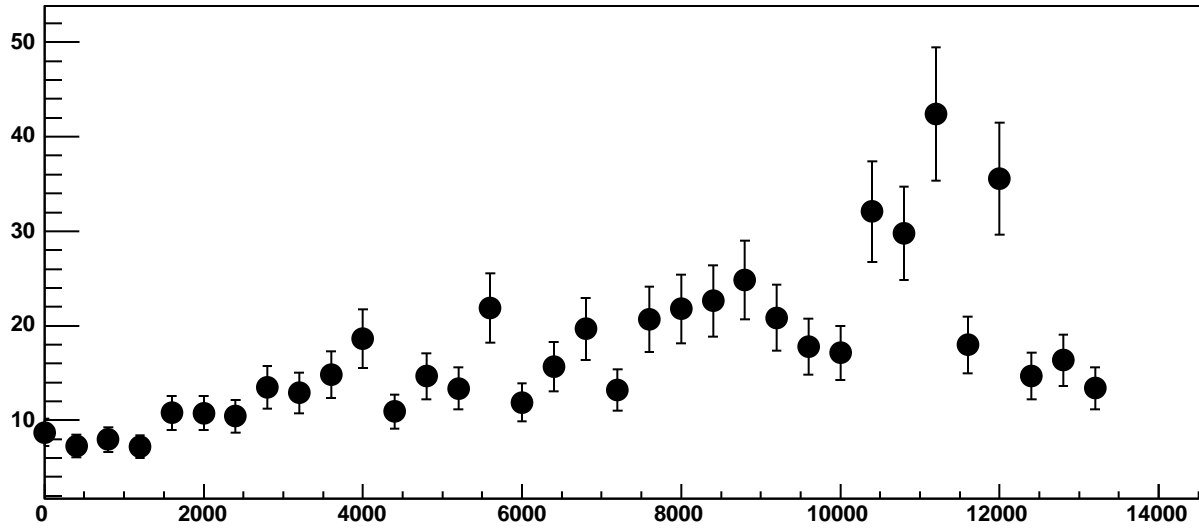
Chip 10, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



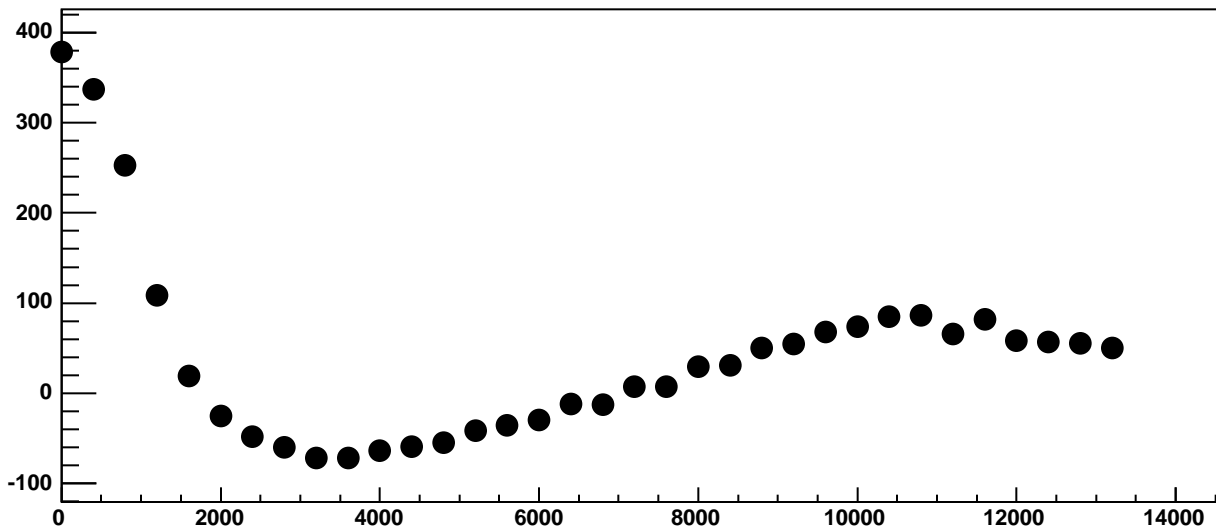
Chip 10, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



Chip 10, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

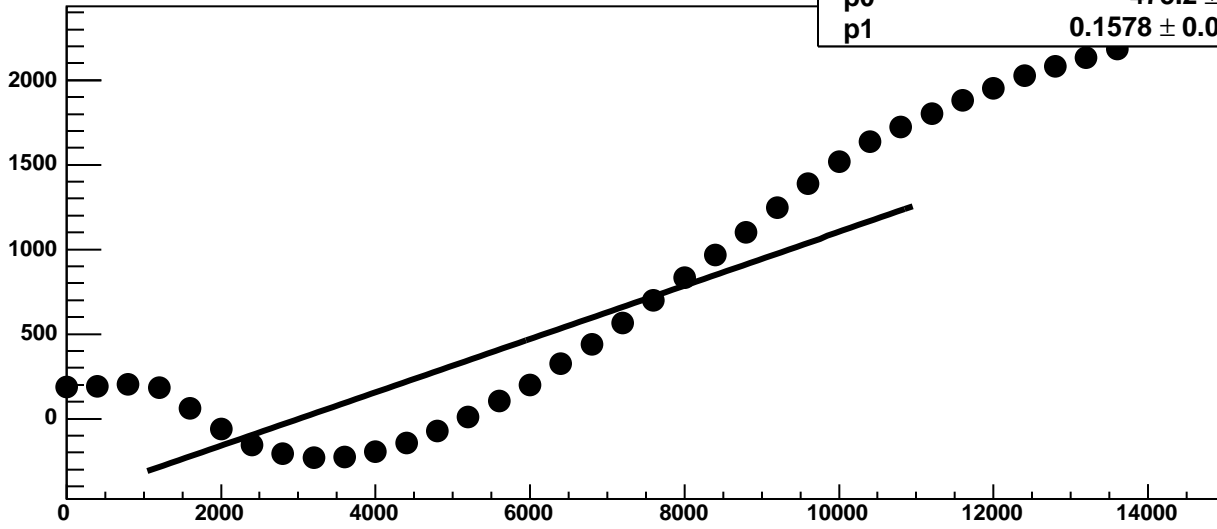


Chip 10, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

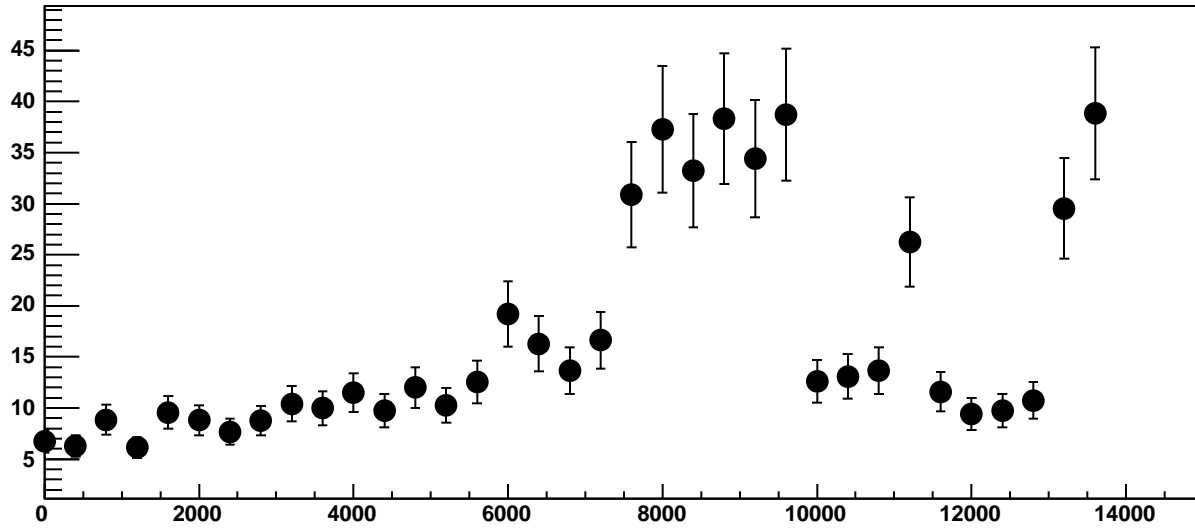


Chip 10, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

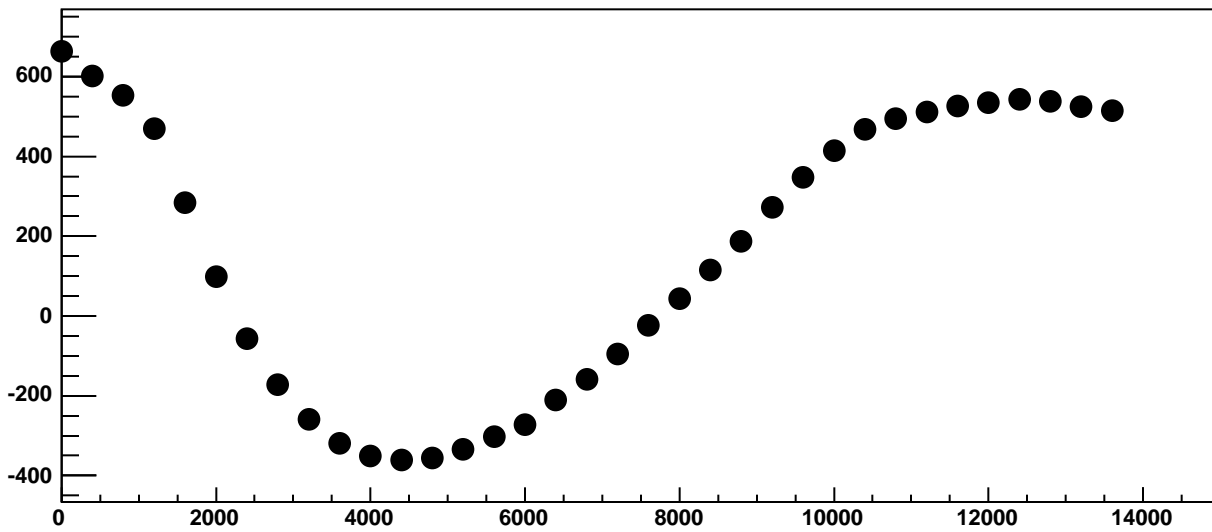
$\chi^2 / \text{ndf}$  3.451e+05 / 23  
p0 -475.2 ± 0.9815  
p1 0.1578 ± 0.0001974



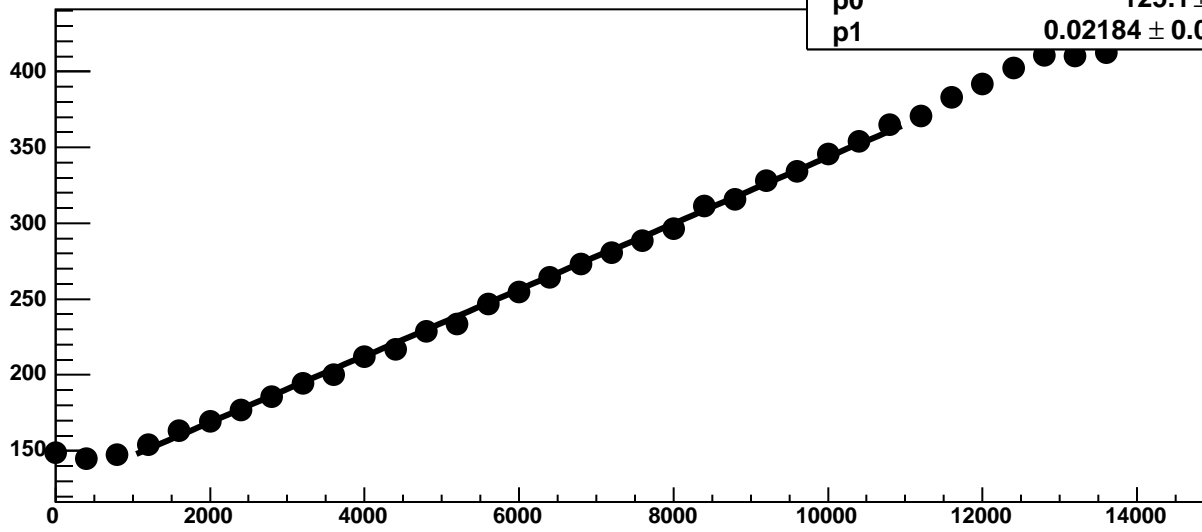
Chip 10, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

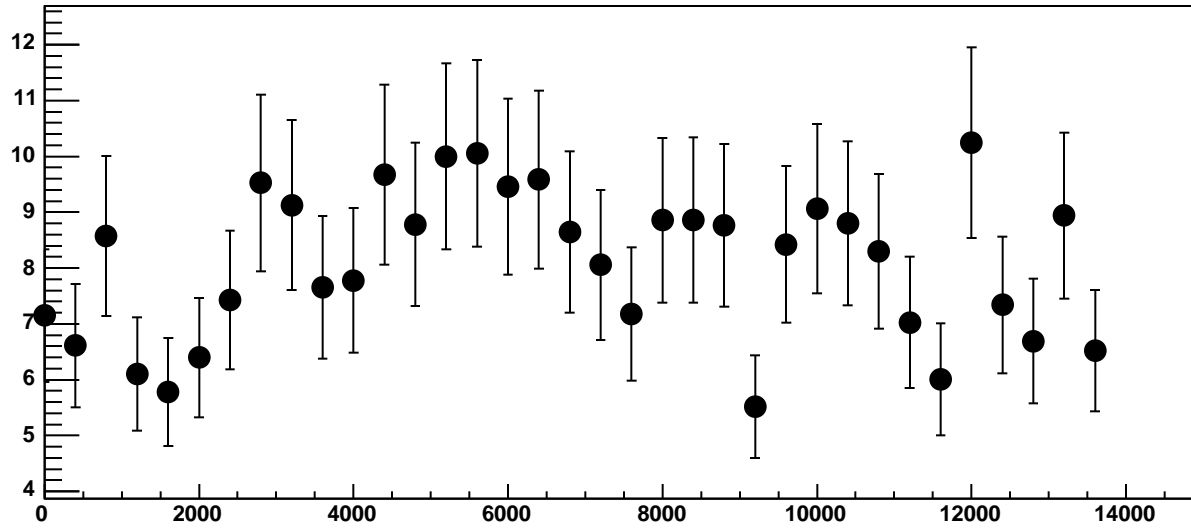


Chip 10, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

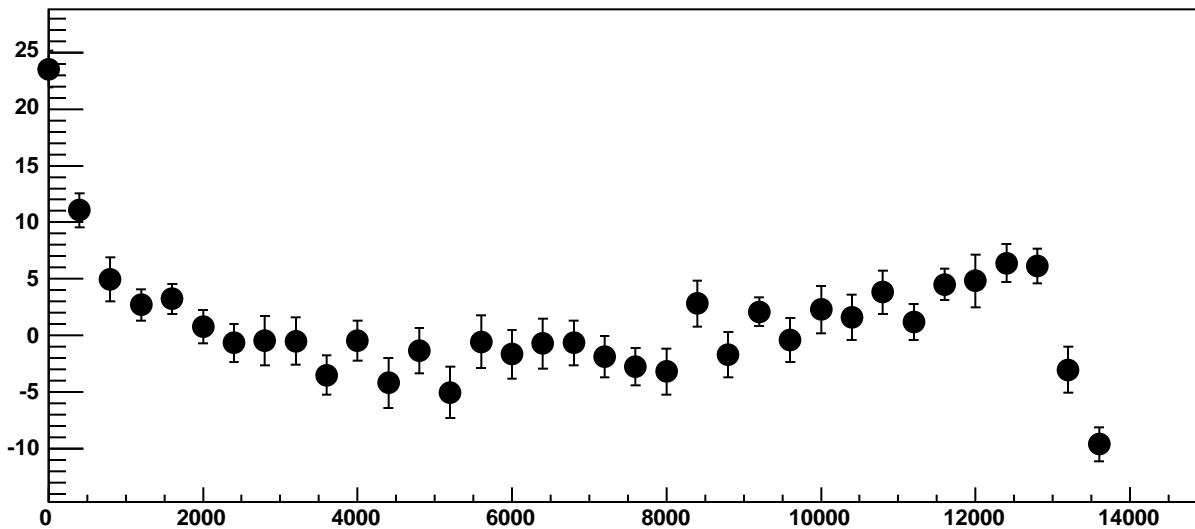


$\chi^2 / \text{ndf}$  41.44 / 23  
p0 125.1 ± 0.7614  
p1 0.02184 ± 0.0001174

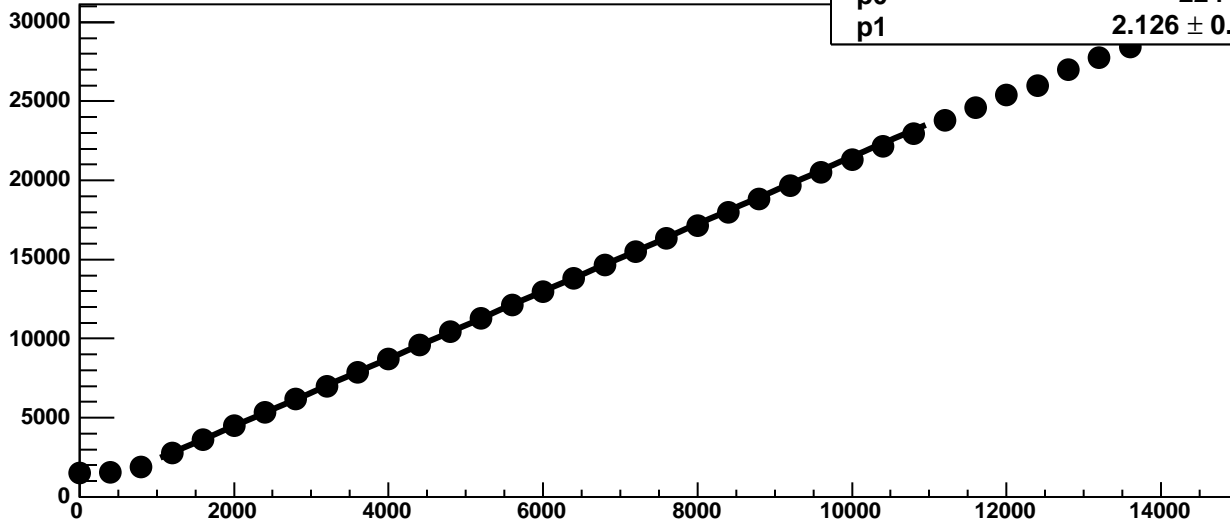
Chip 10, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



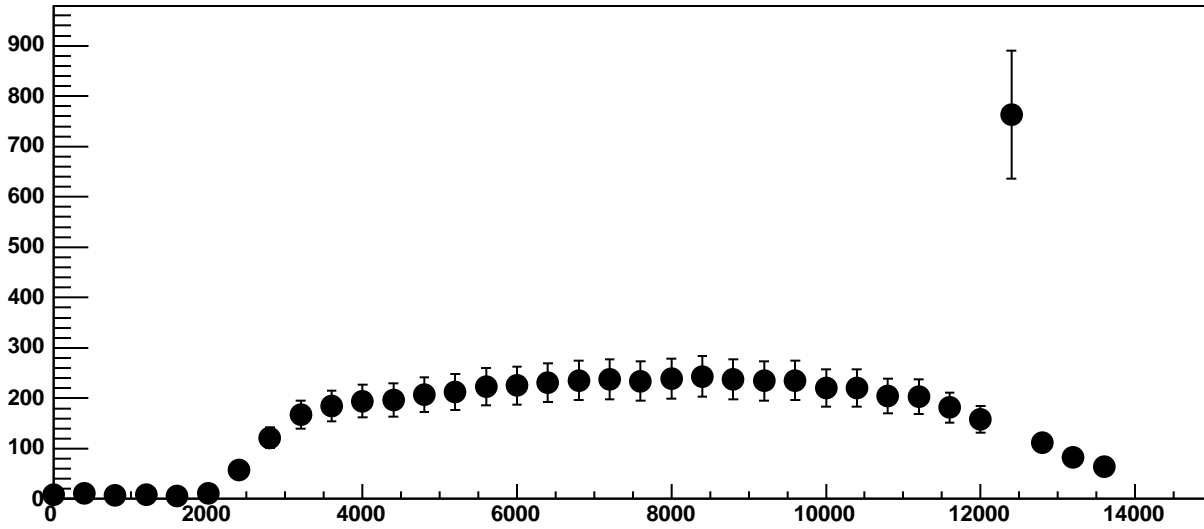
Chip 10, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



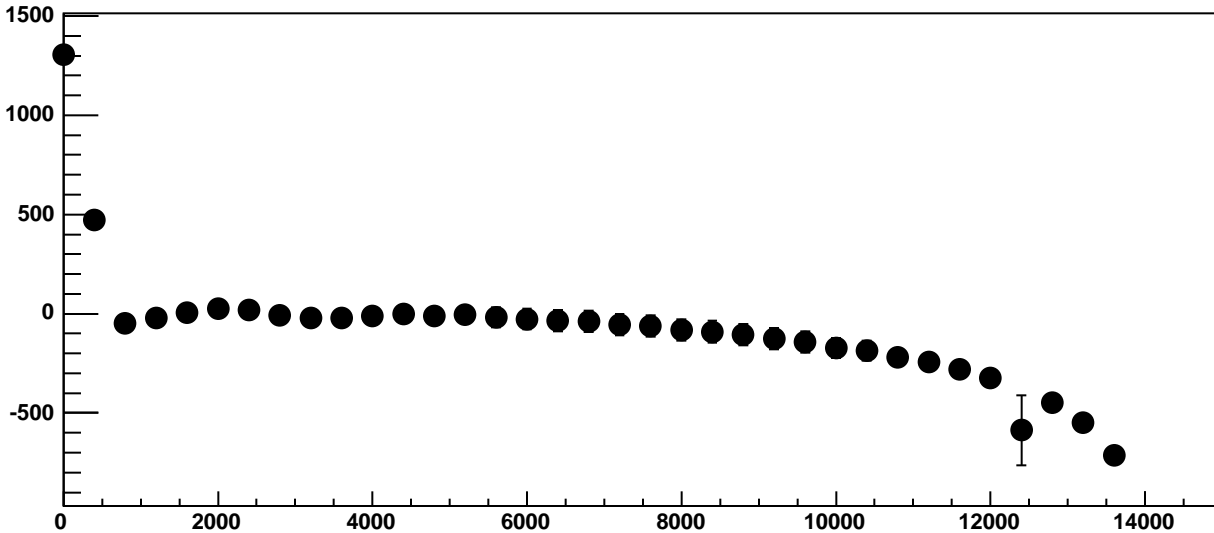
Chip 10, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC



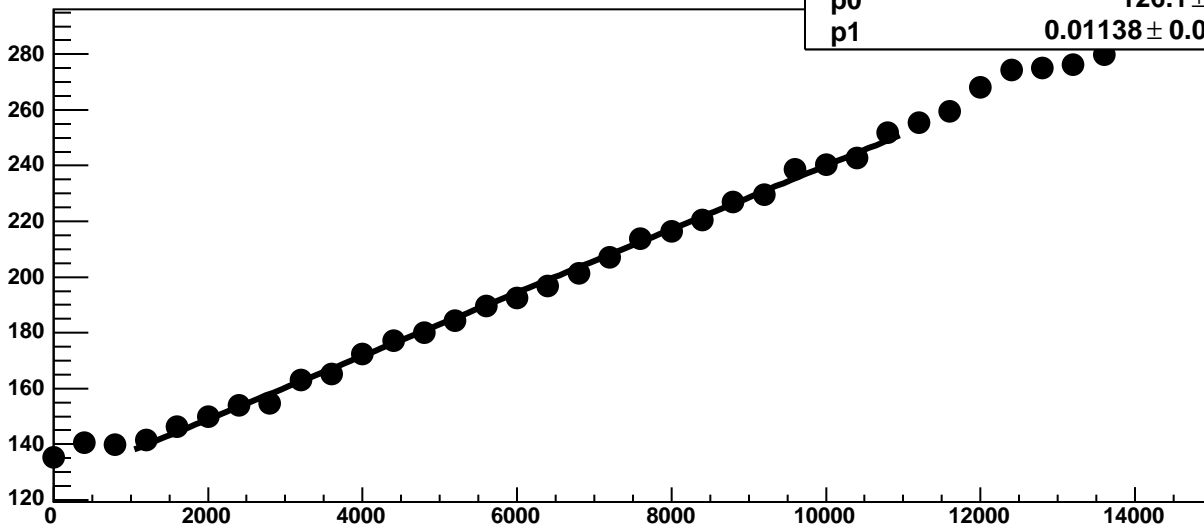
Chip 10, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

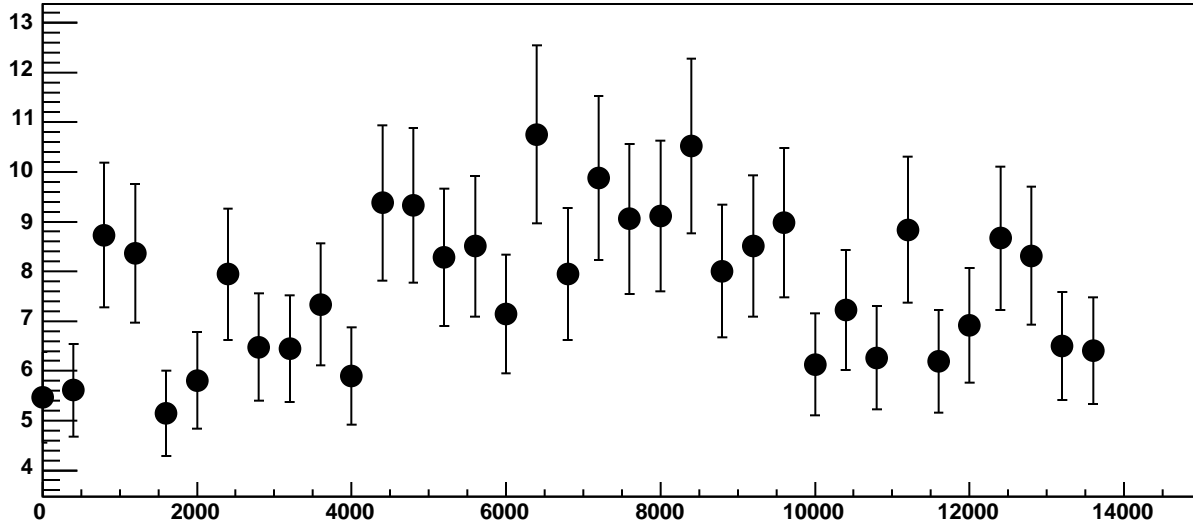


Chip 10, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

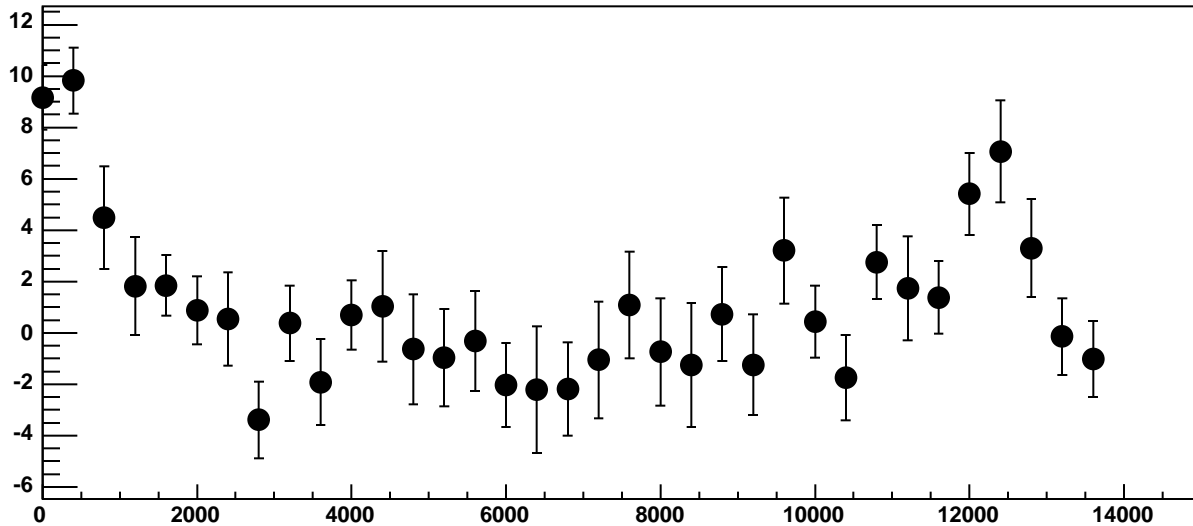


$\chi^2 / \text{ndf}$  23.81 / 23  
p0  $126.1 \pm 0.7077$   
p1  $0.01138 \pm 0.0001099$

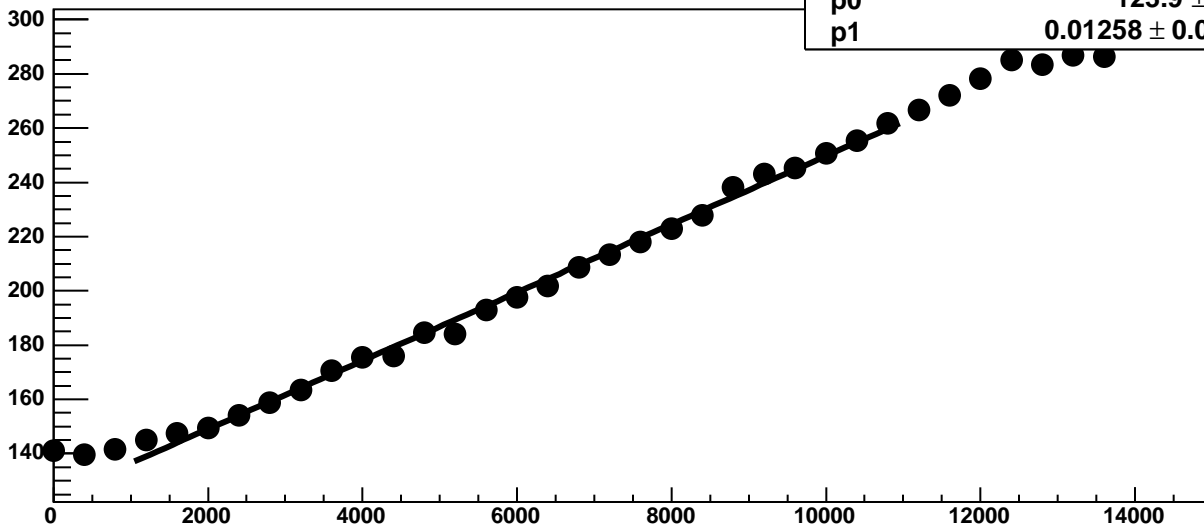
Chip 10, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



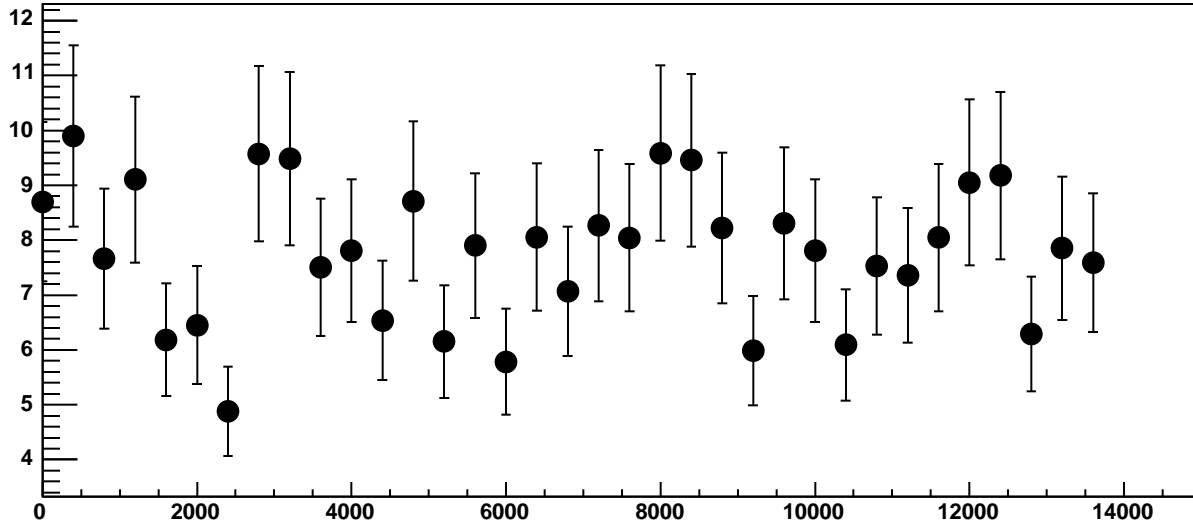
Chip 10, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



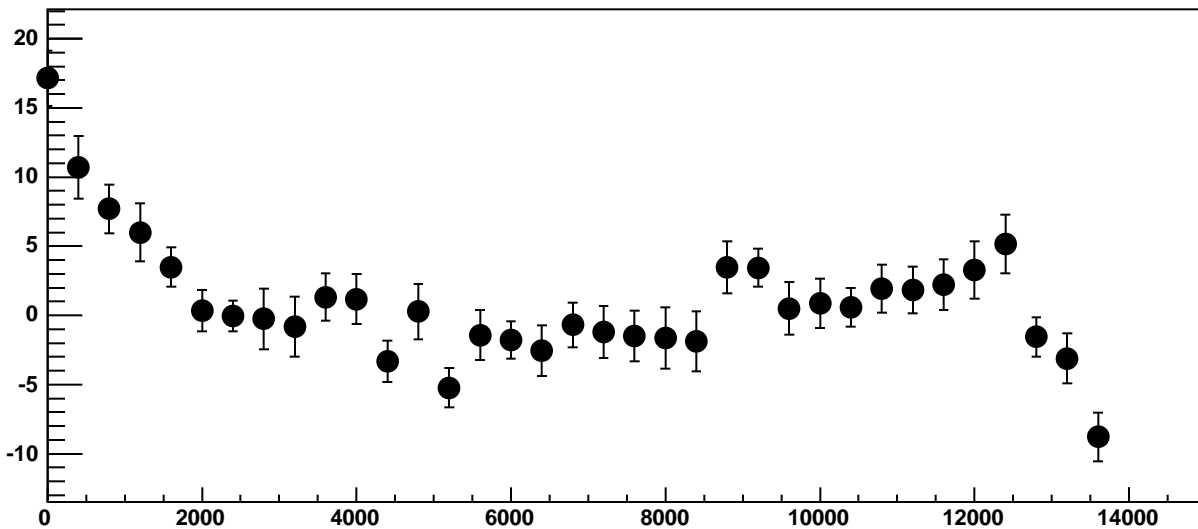
Chip 10, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC



Chip 10, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

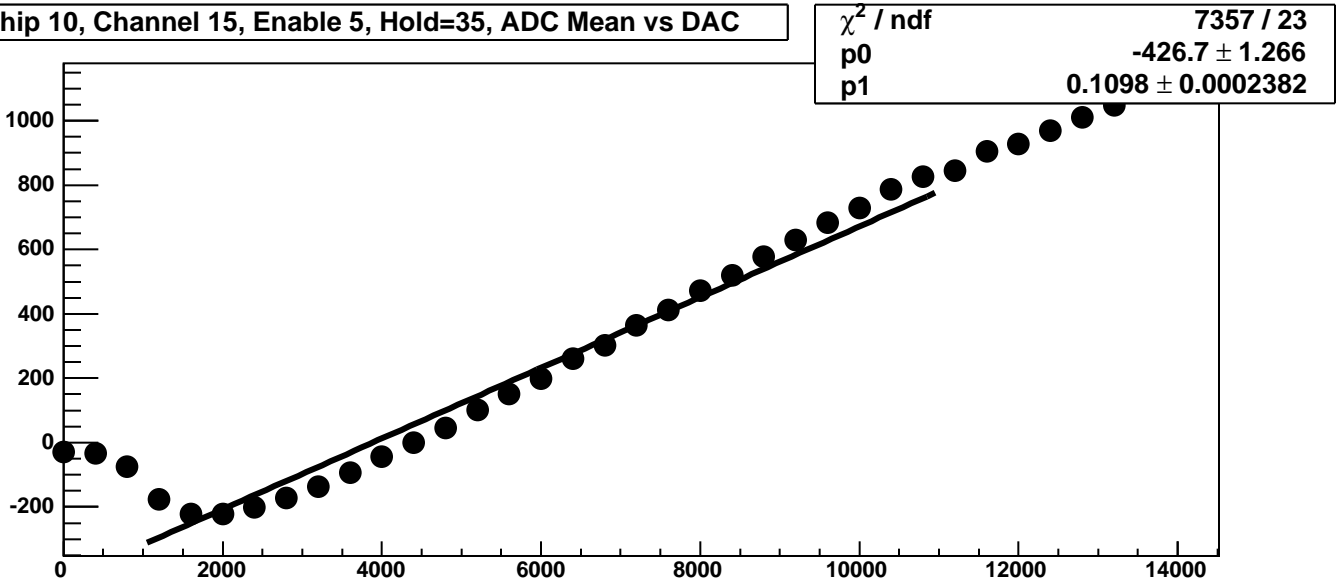


Chip 10, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

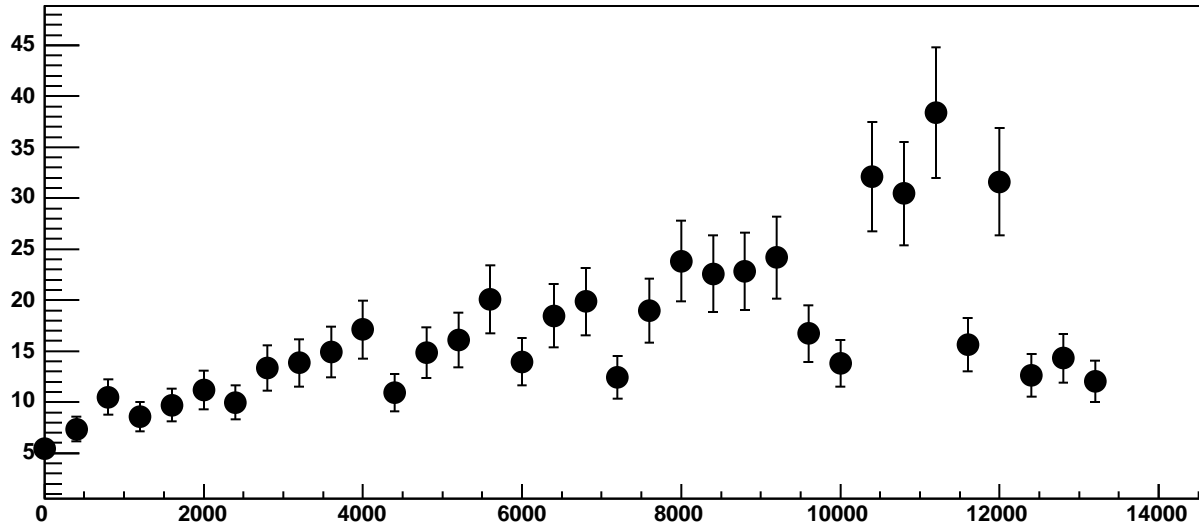




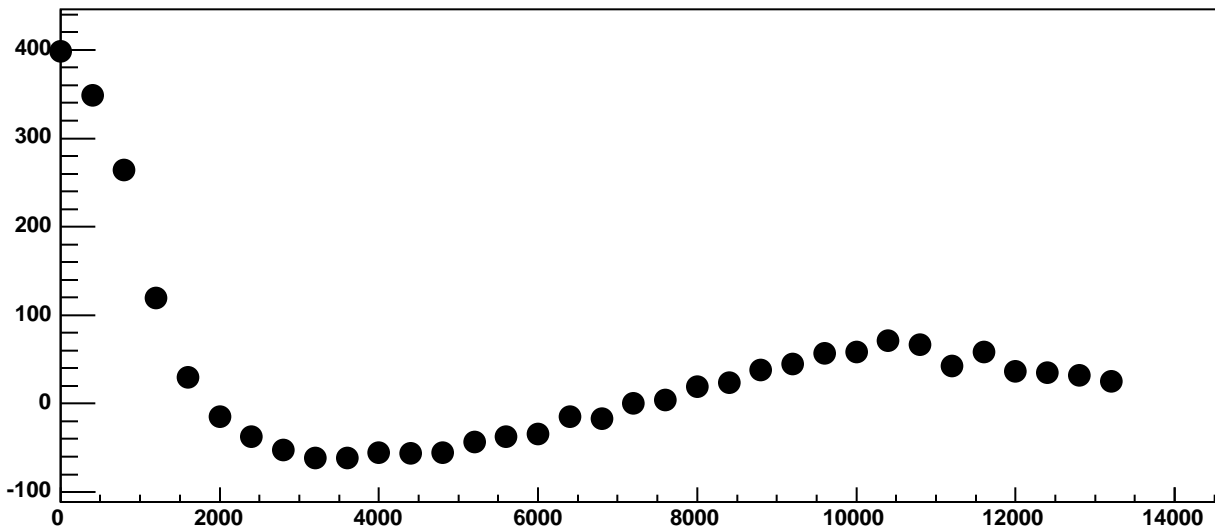
Chip 10, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



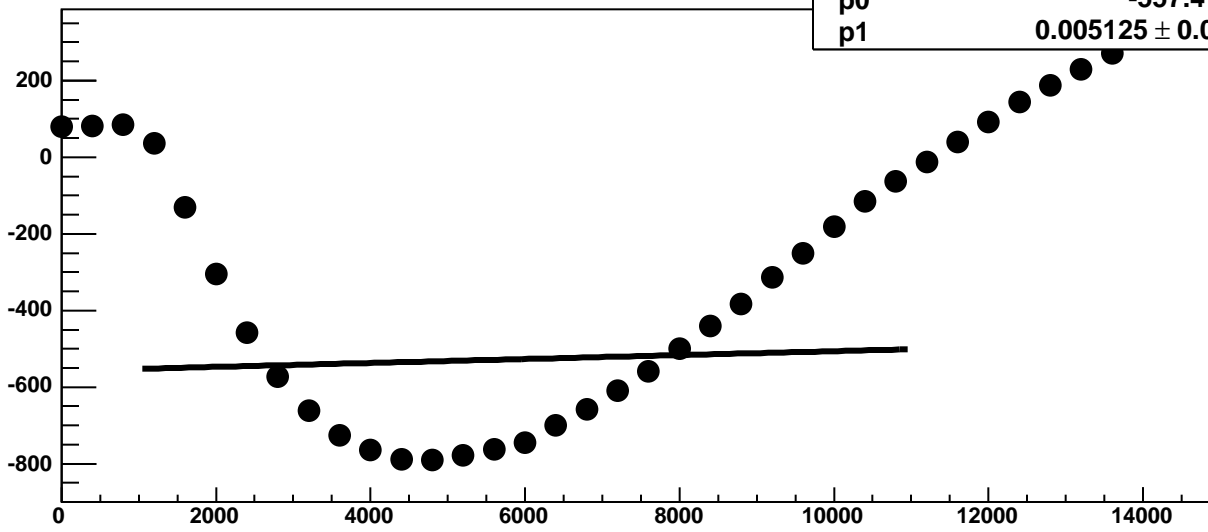
Chip 10, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



Chip 10, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

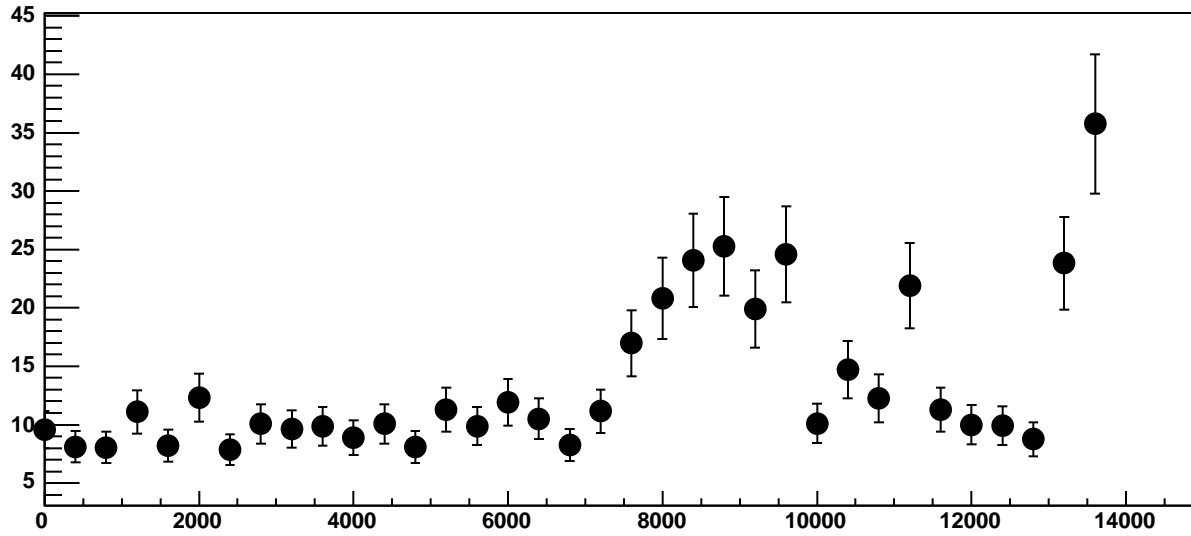


Chip 10, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

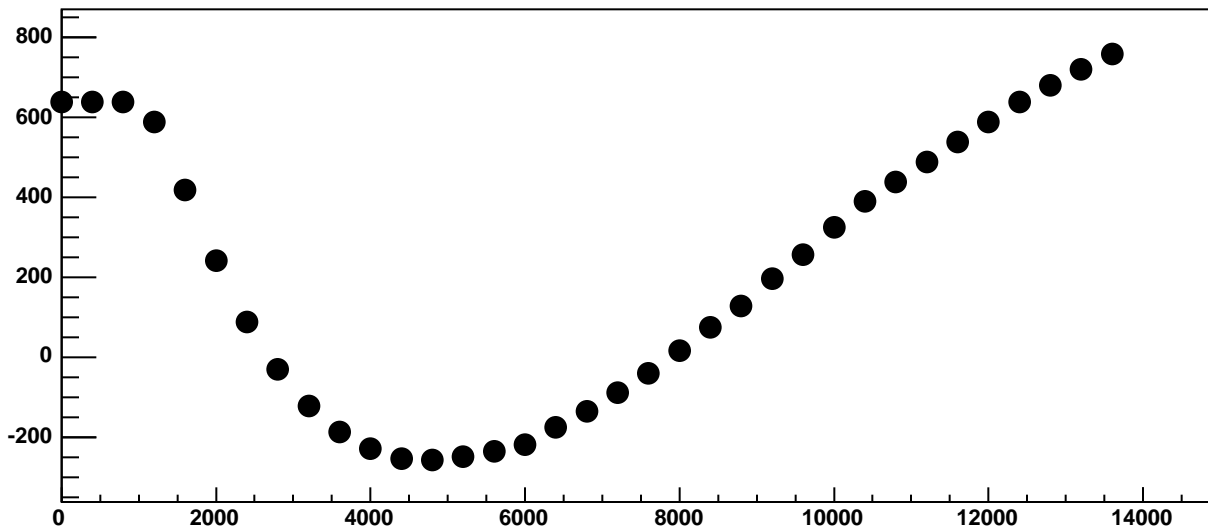


$\chi^2 / \text{ndf}$  2.657e+05 / 23  
p0 -557.4 ± 1.083  
p1 0.005125 ± 0.0001898

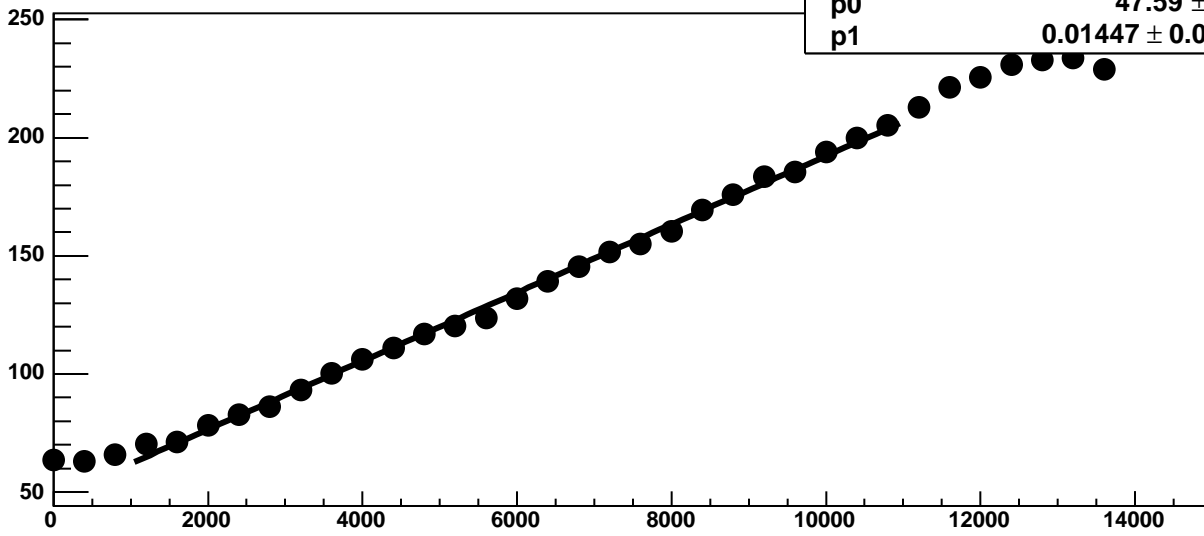
Chip 10, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



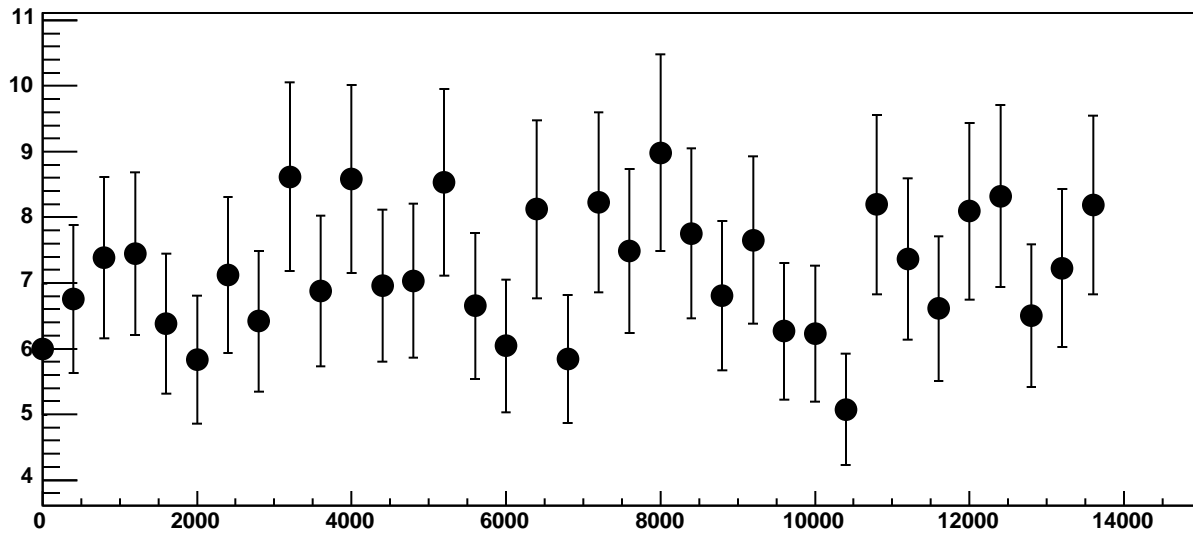
Chip 10, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



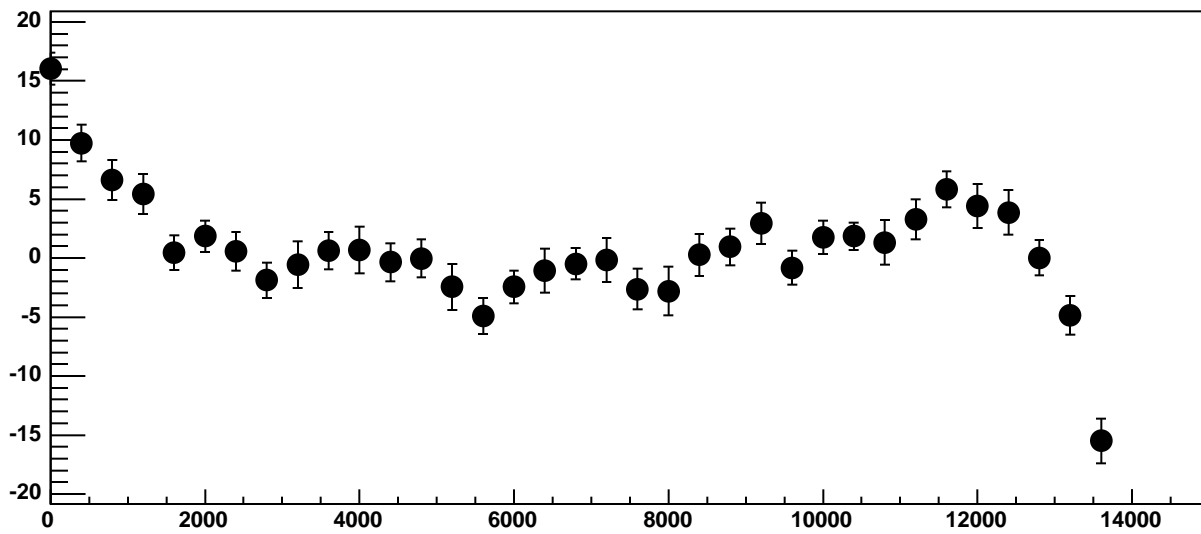
Chip 10, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



Chip 10, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

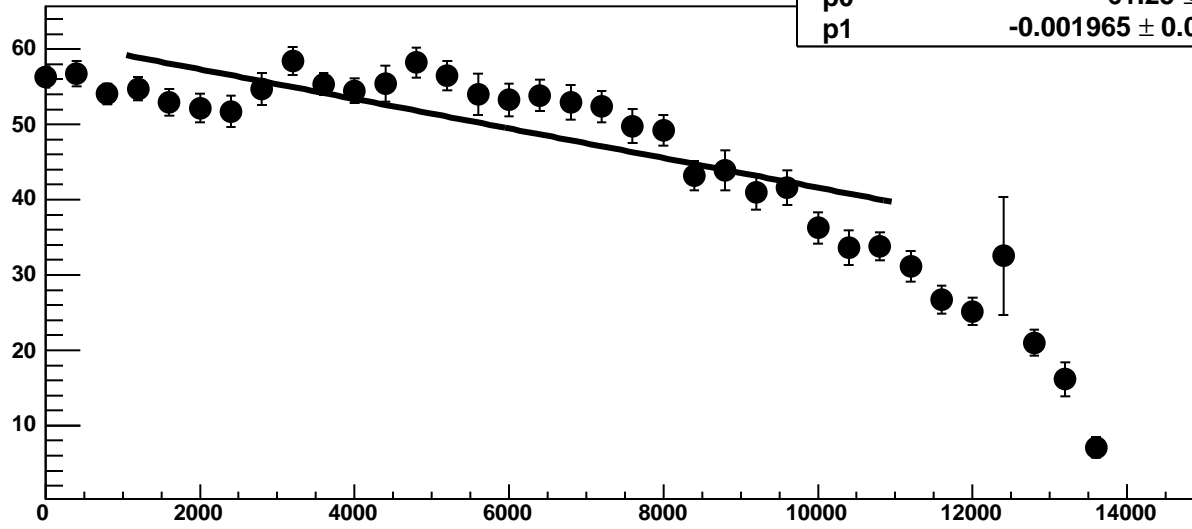
109.9 / 23

p0

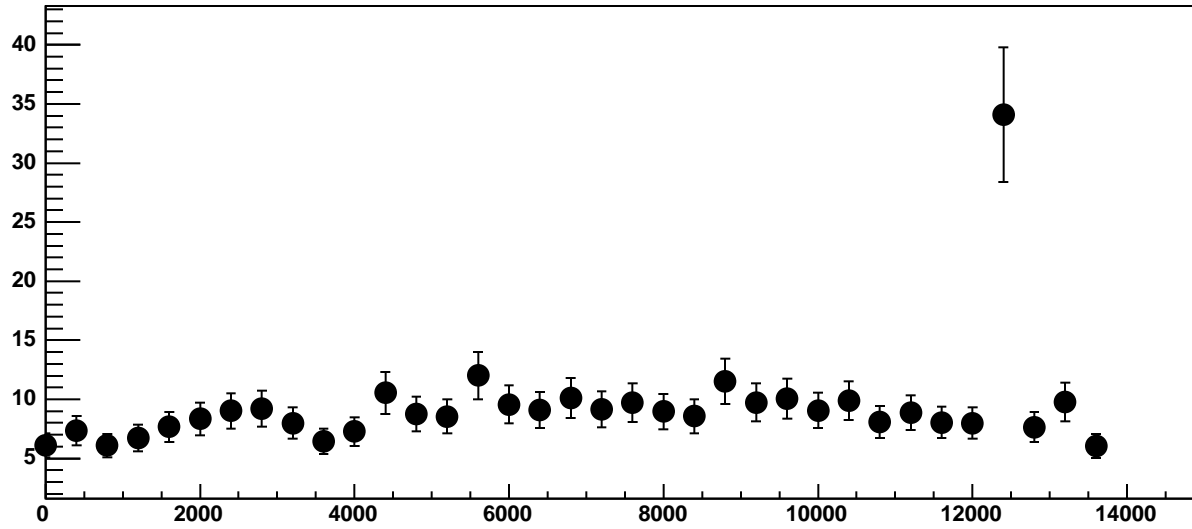
$61.25 \pm 0.8493$

p1

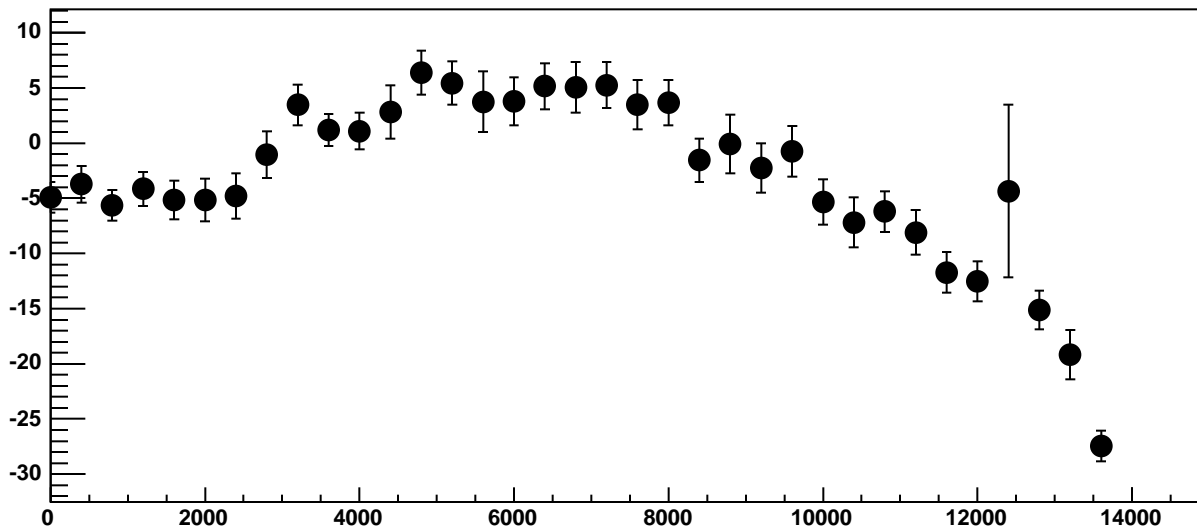
$-0.001965 \pm 0.0001347$



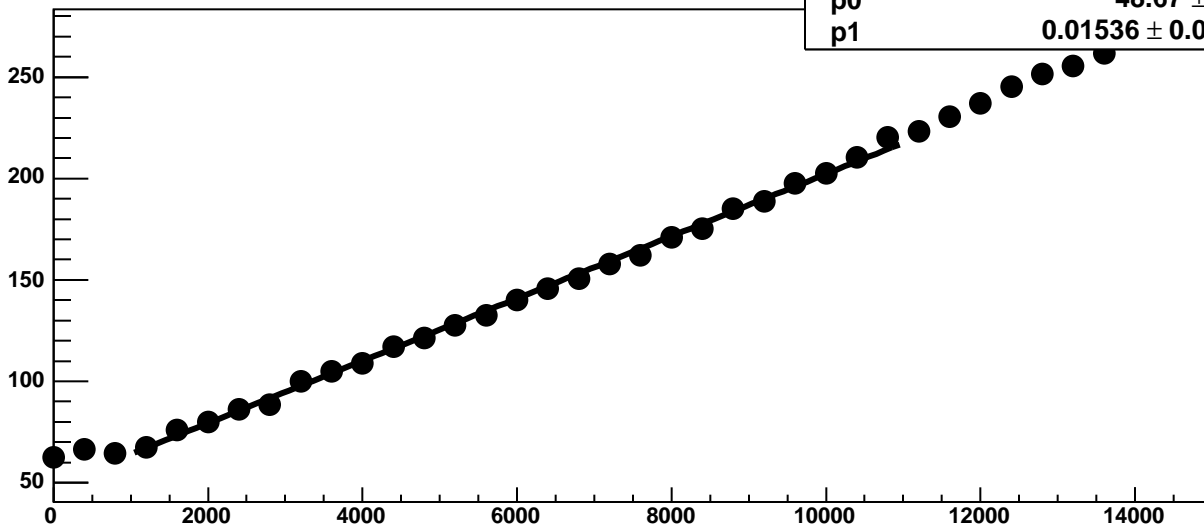
Chip 10, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

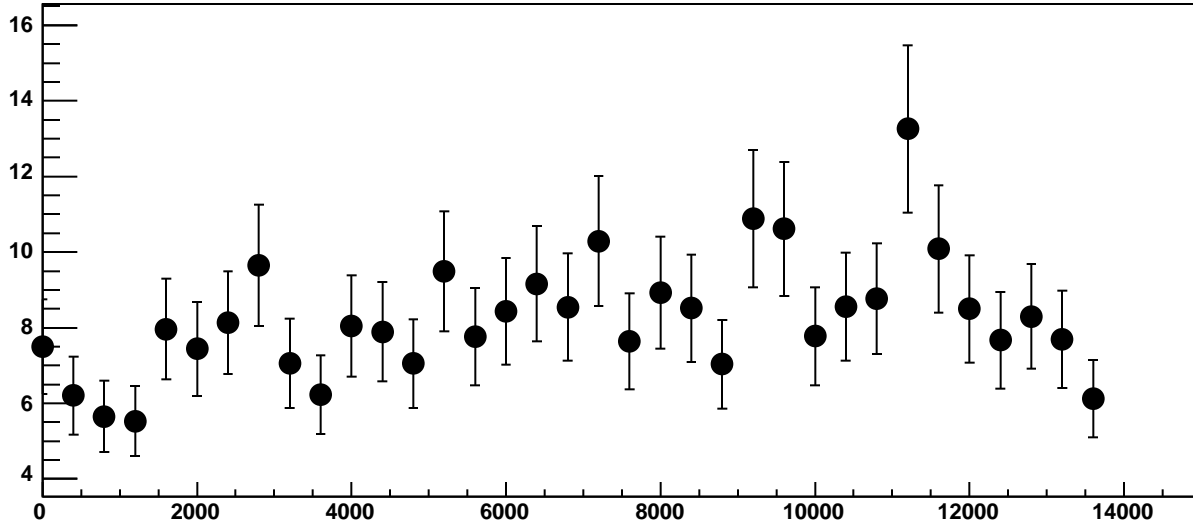


Chip 10, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

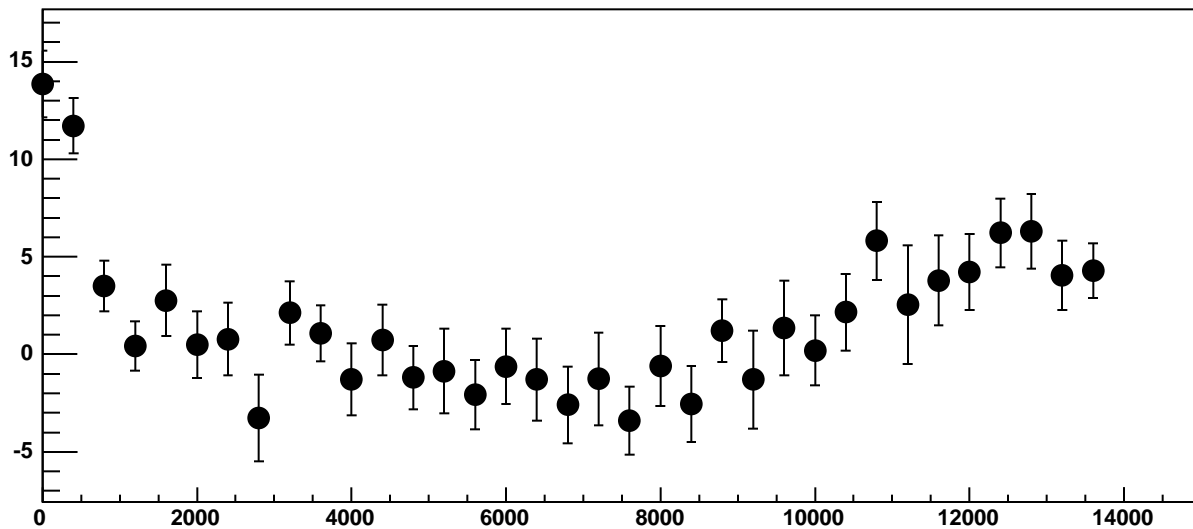


$\chi^2 / \text{ndf}$  28.54 / 23  
p0 48.67 ± 0.7796  
p1 0.01536 ± 0.0001244

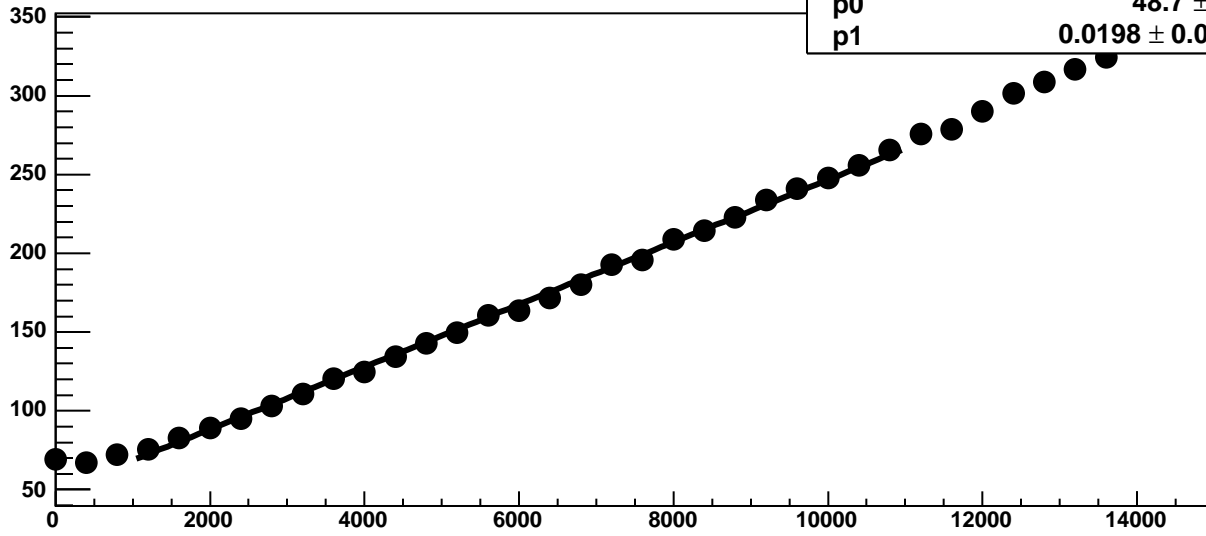
Chip 10, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

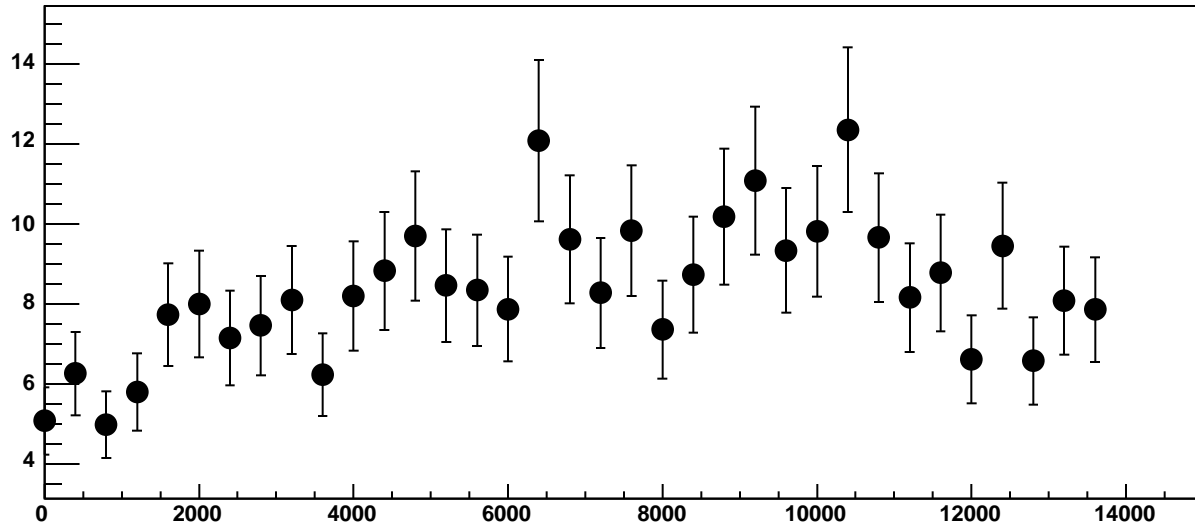


Chip 10, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

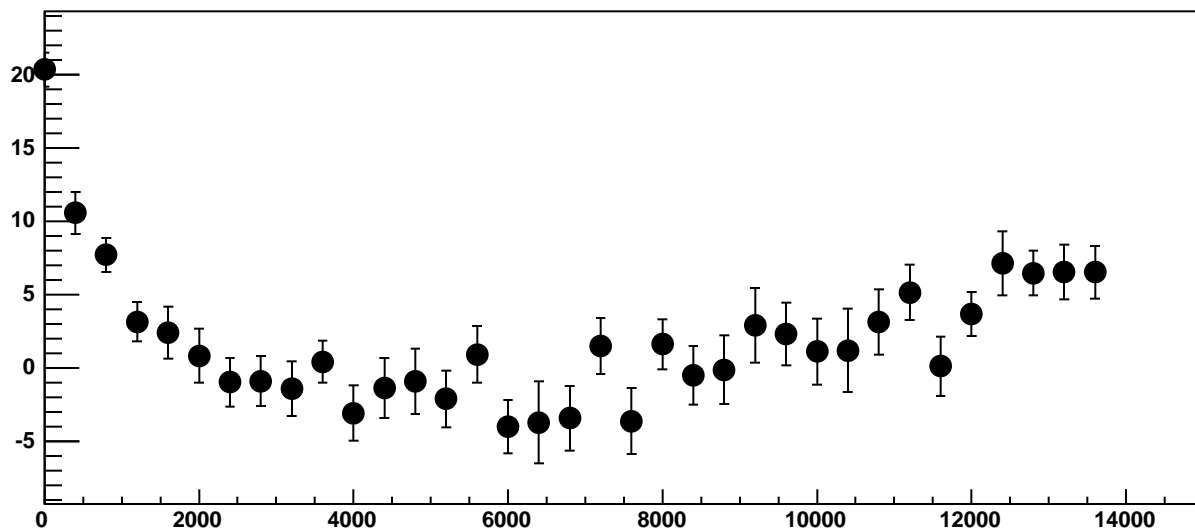


$\chi^2 / \text{ndf}$  31.77 / 23  
p0  $48.7 \pm 0.8023$   
p1  $0.0198 \pm 0.0001337$

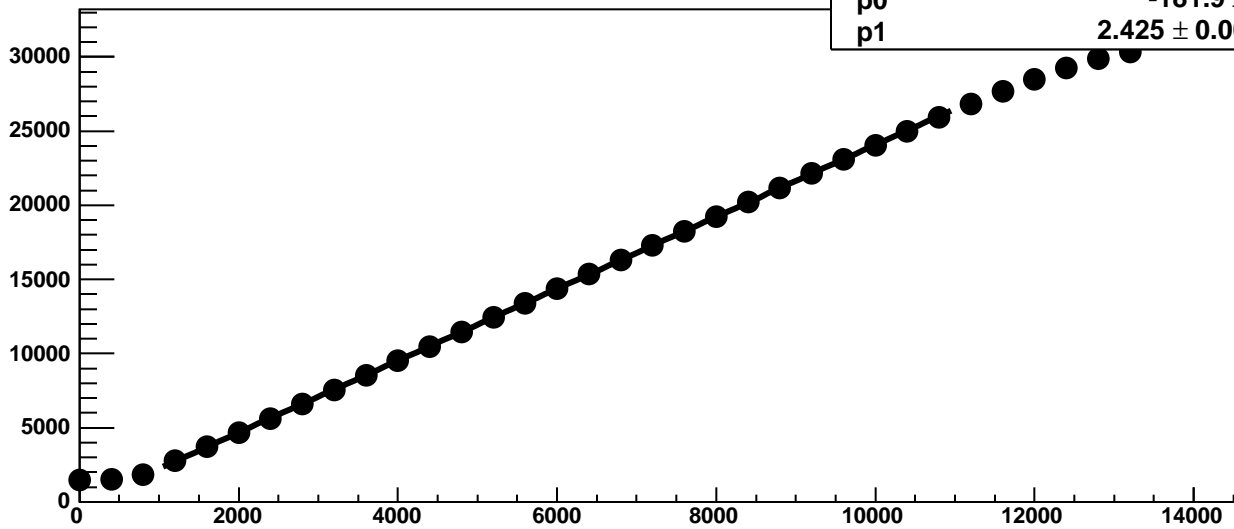
Chip 10, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



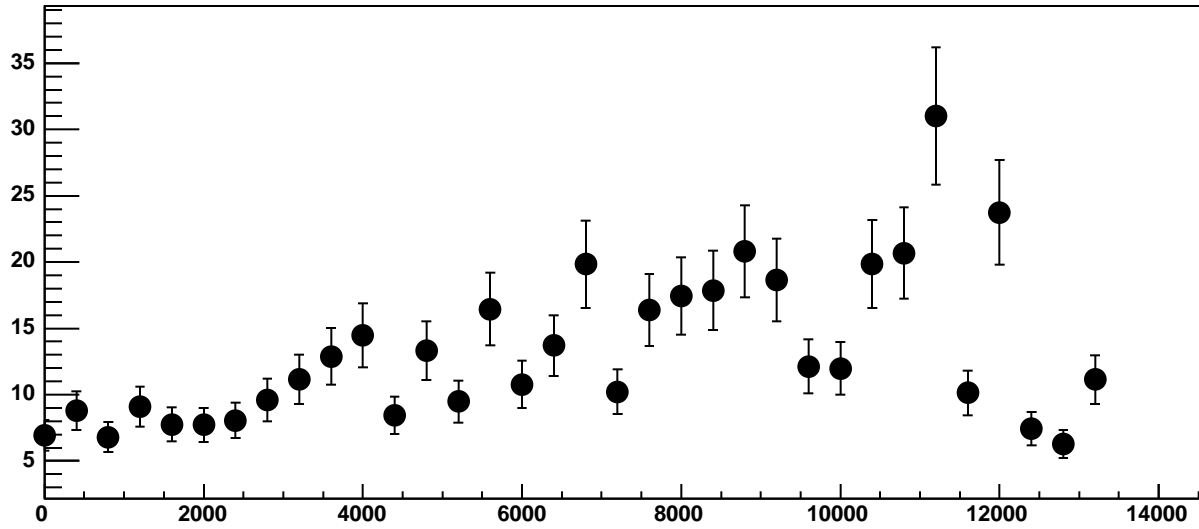
Chip 10, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



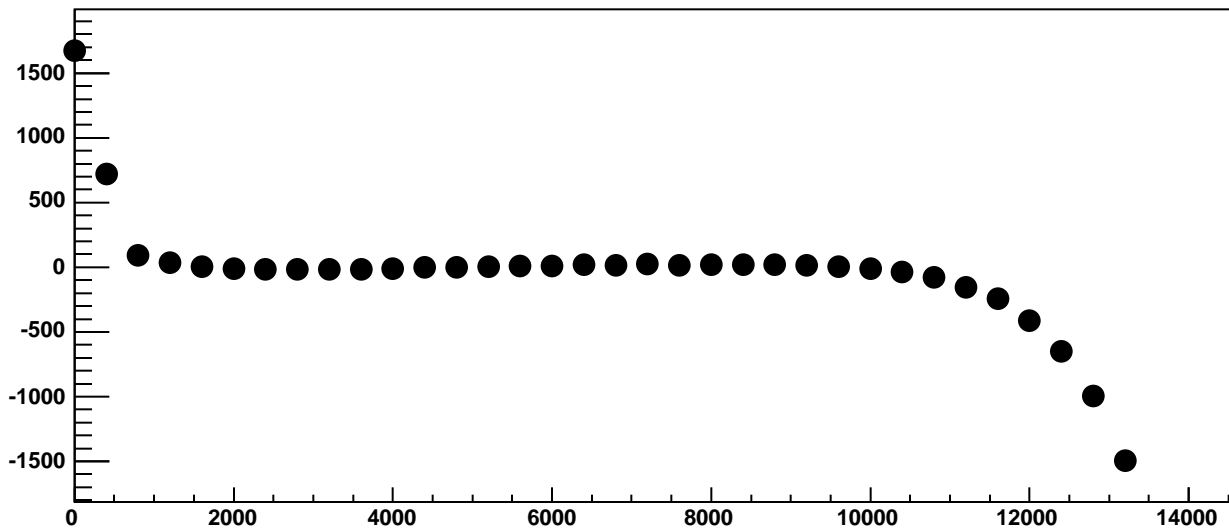
Chip 10, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 10, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC

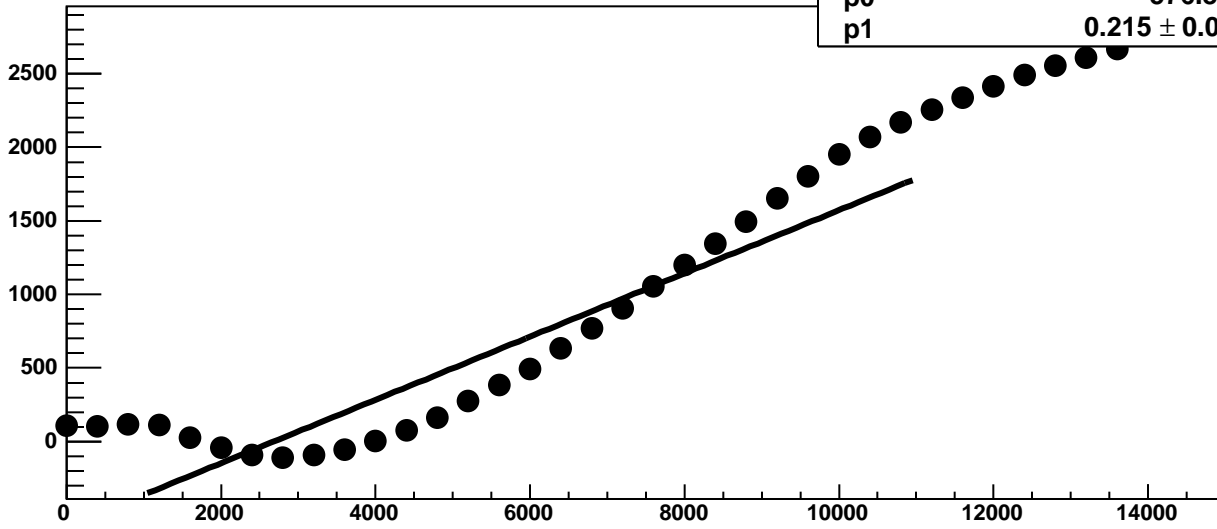


Chip 10, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

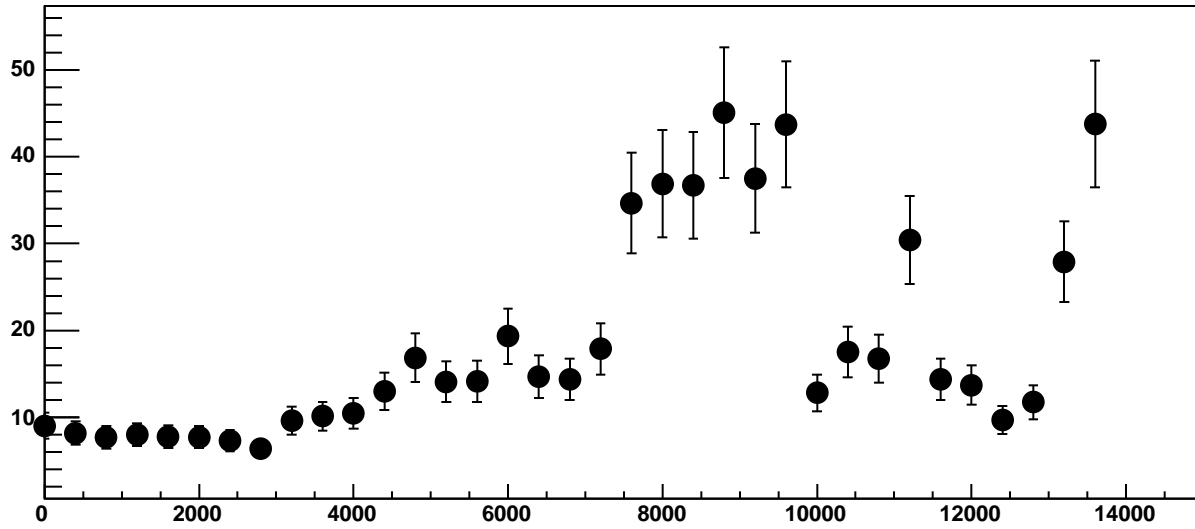


Chip 10, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

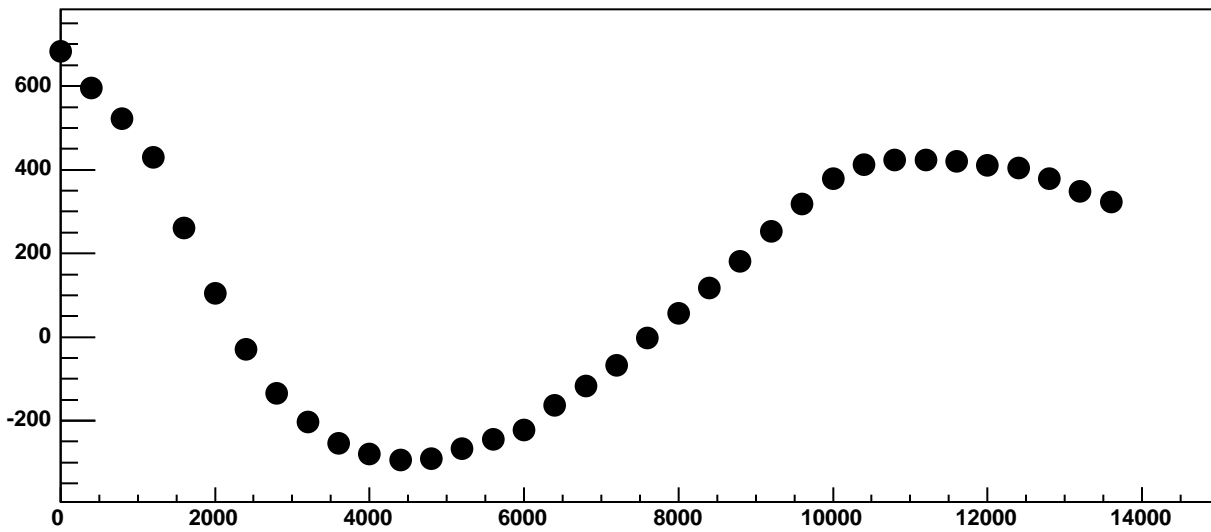
$\chi^2 / \text{ndf}$  1.985e+05 / 23  
p0 -576.8 ± 1.001  
p1 0.215 ± 0.0002184



Chip 10, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

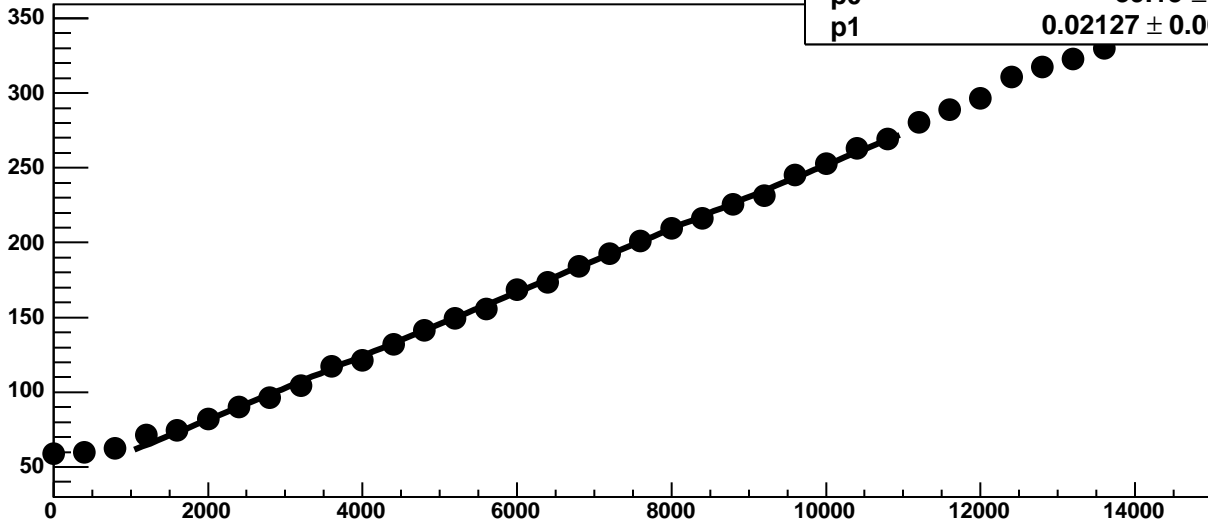


Chip 10, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



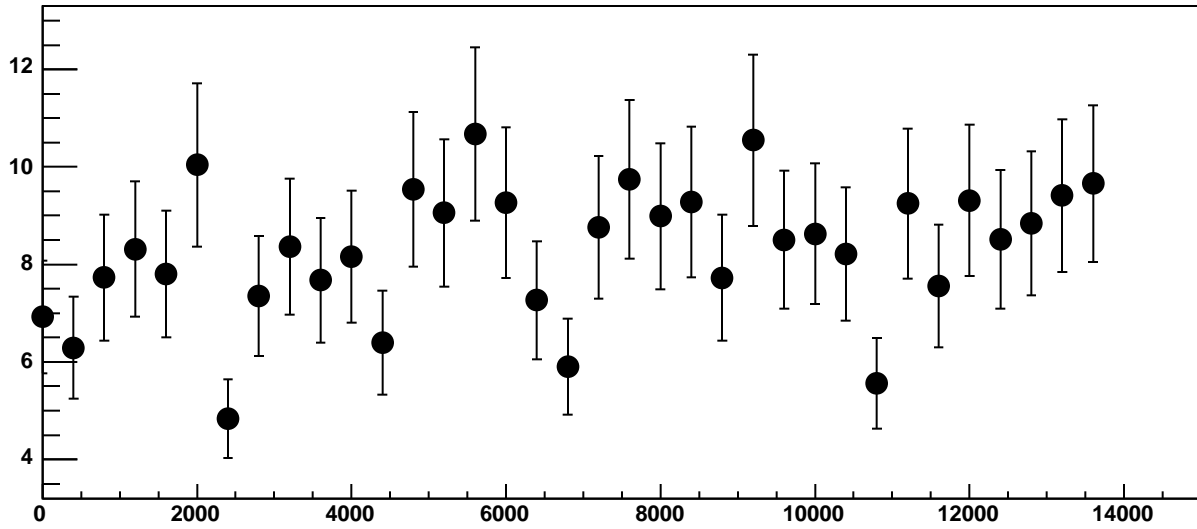


Chip 10, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

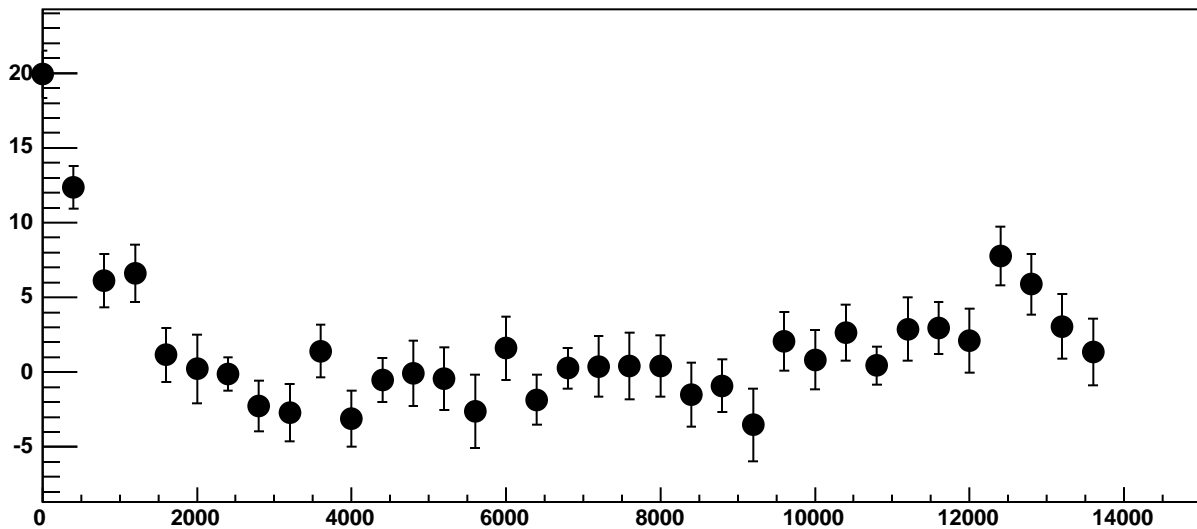


$\chi^2 / \text{ndf}$  29.21 / 23  
p0  $39.13 \pm 0.7824$   
p1  $0.02127 \pm 0.0001187$

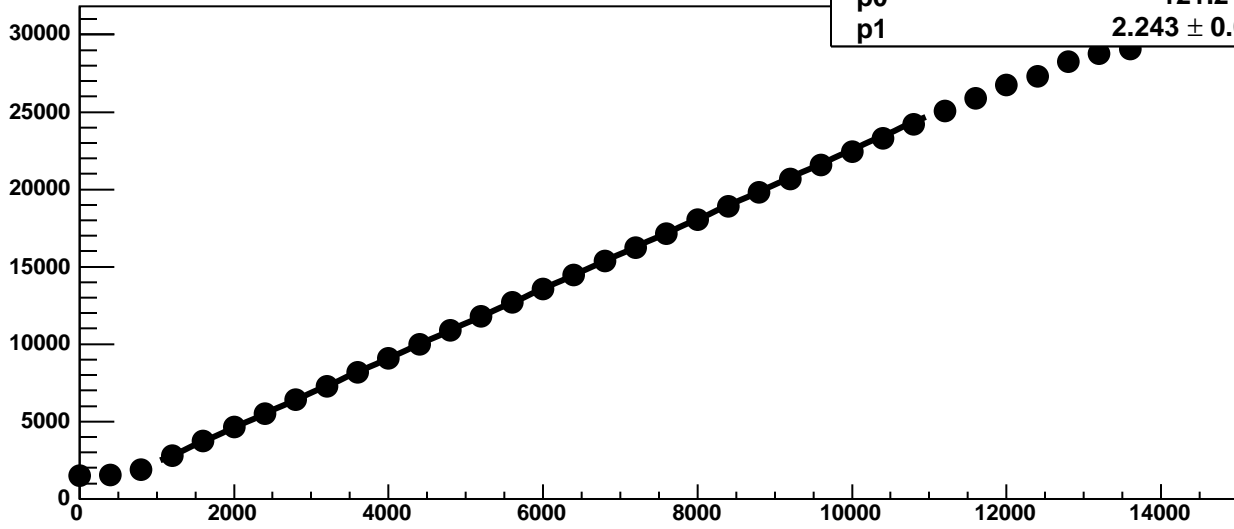
Chip 10, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 10, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

162.8 / 23

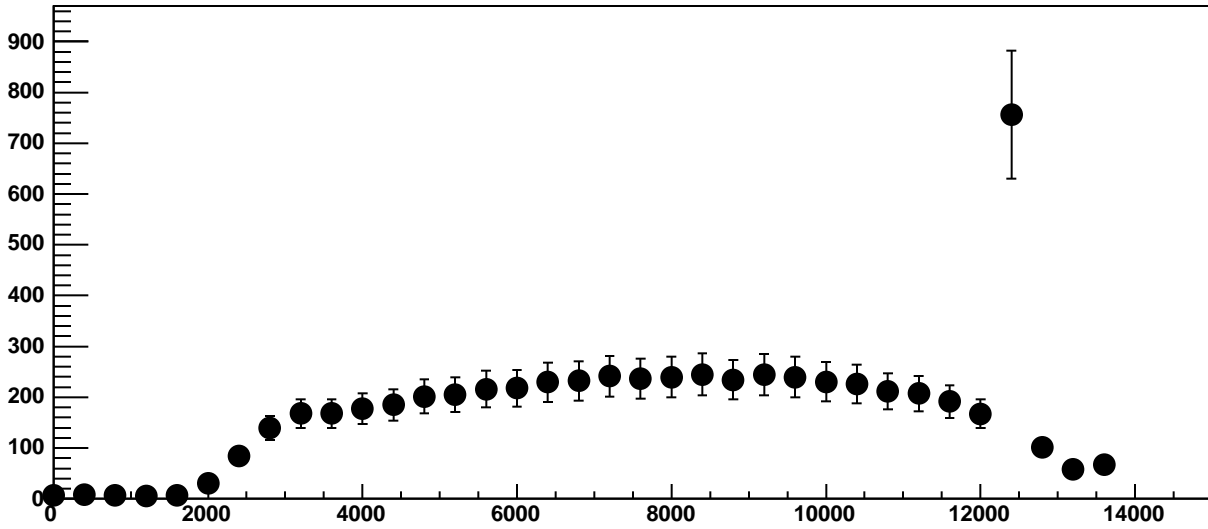
p0

$121.2 \pm 2.781$

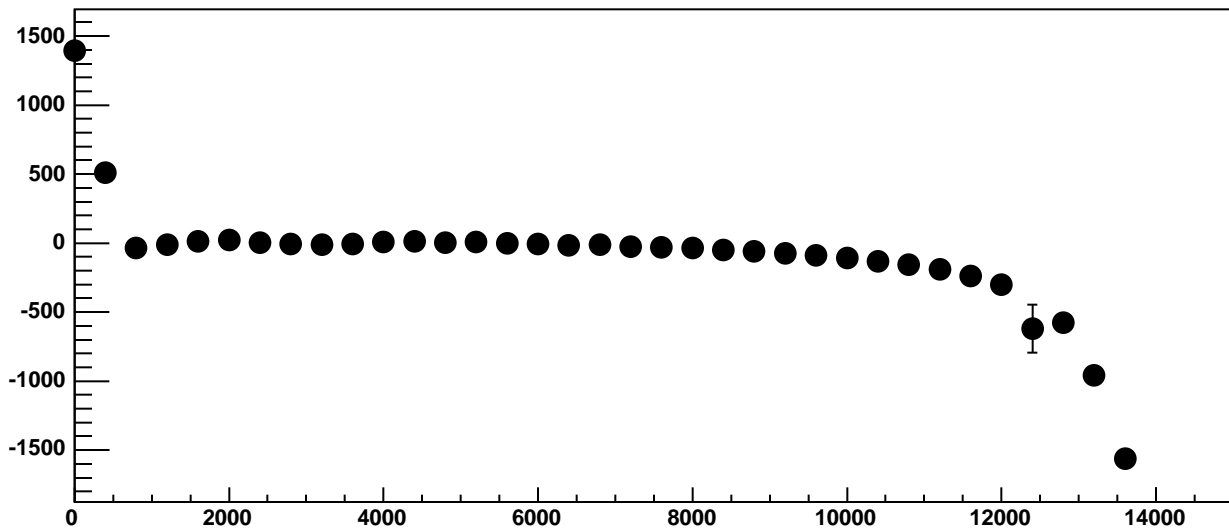
p1

$2.243 \pm 0.001785$

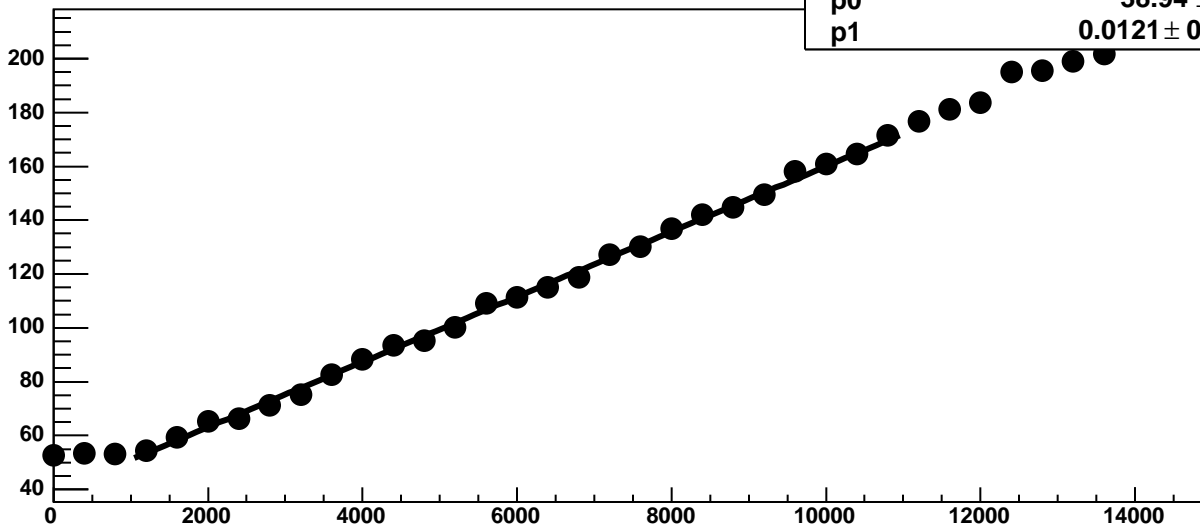
Chip 10, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC

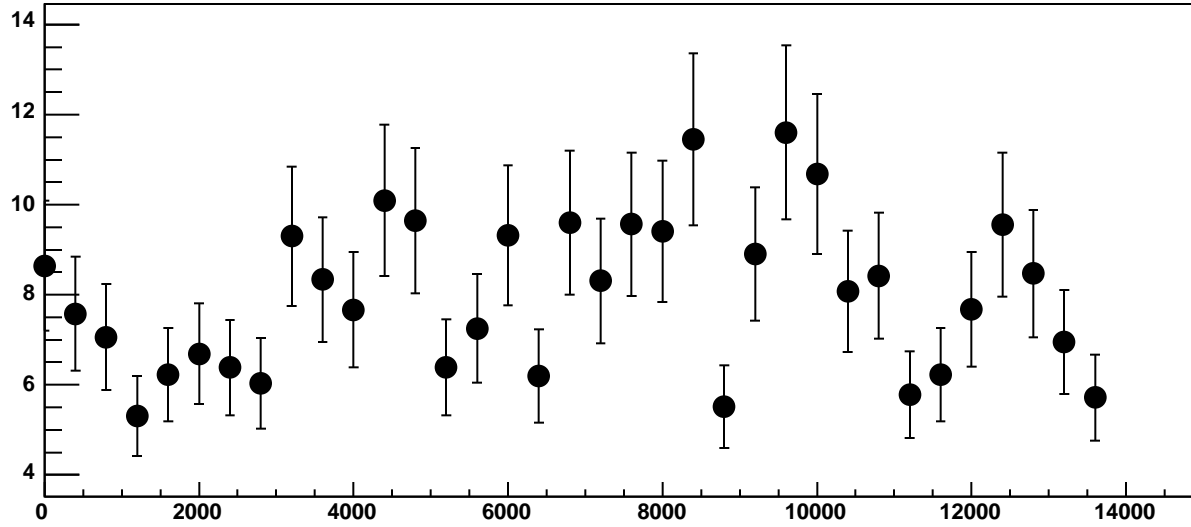


Chip 10, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

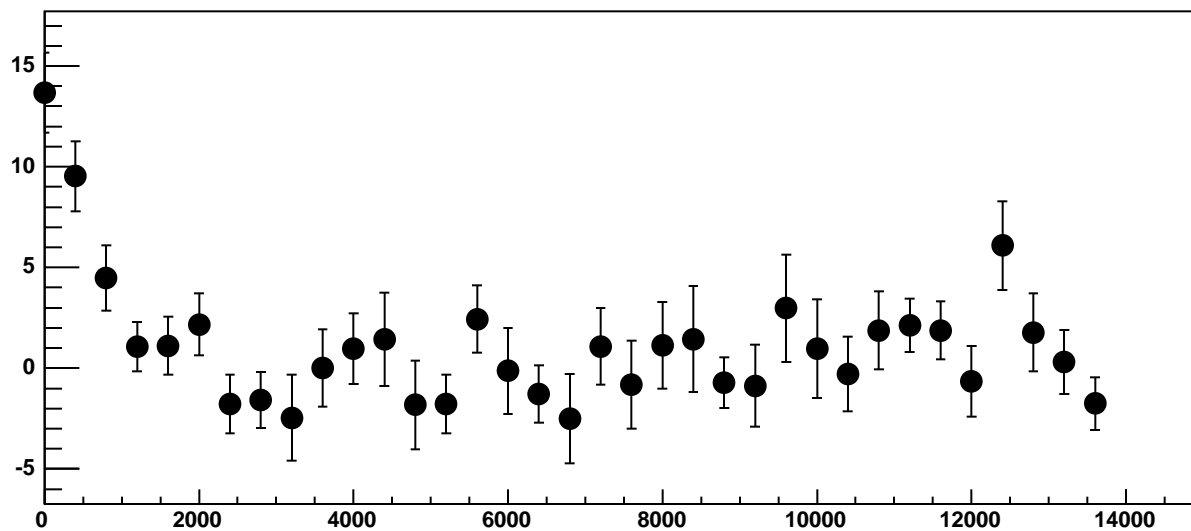


$\chi^2 / \text{ndf}$  18.45 / 23  
p0  $38.94 \pm 0.7211$   
p1  $0.0121 \pm 0.000118$

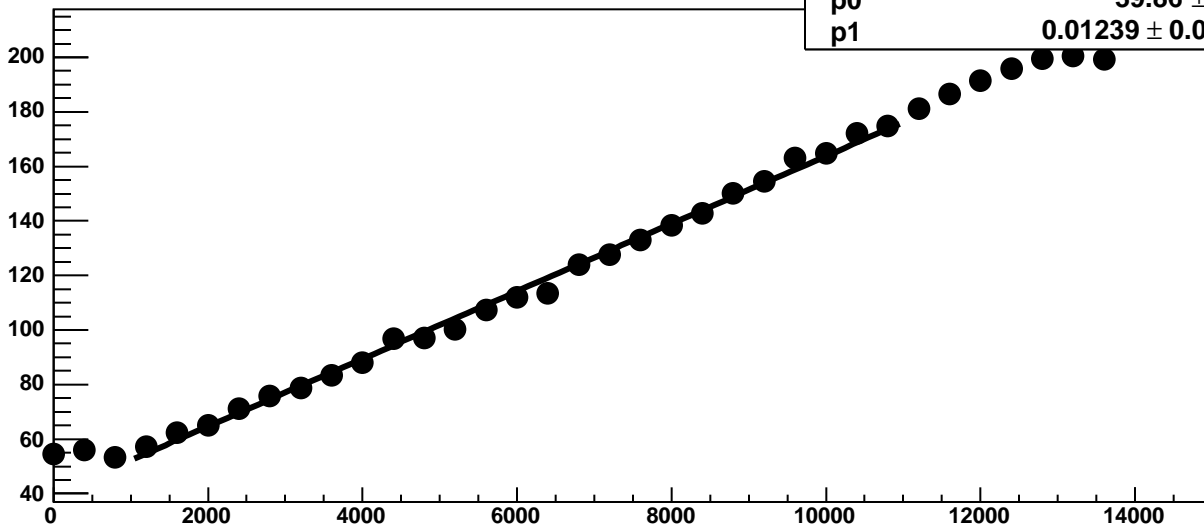
Chip 10, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

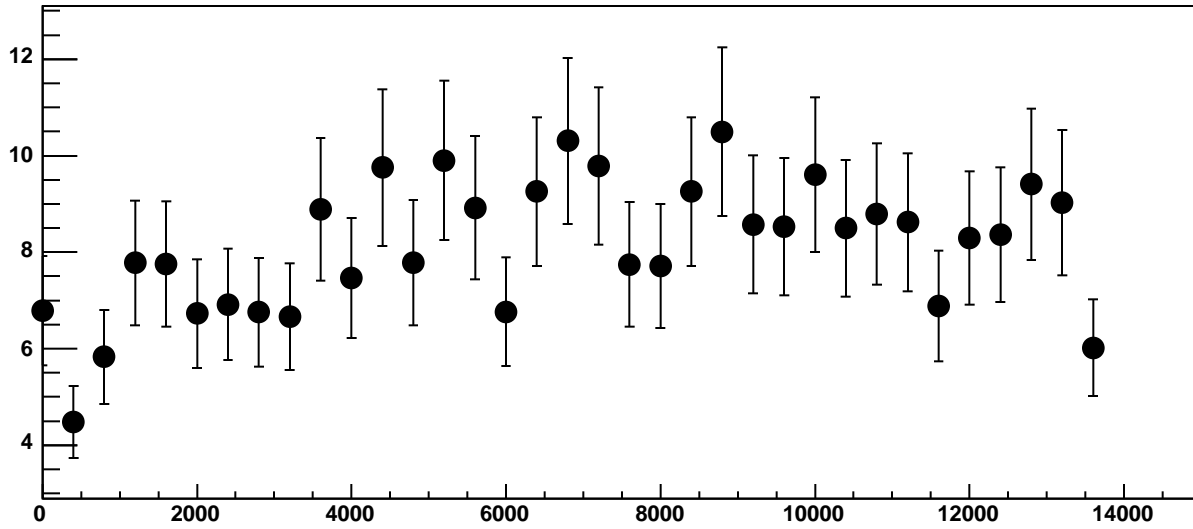


Chip 10, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

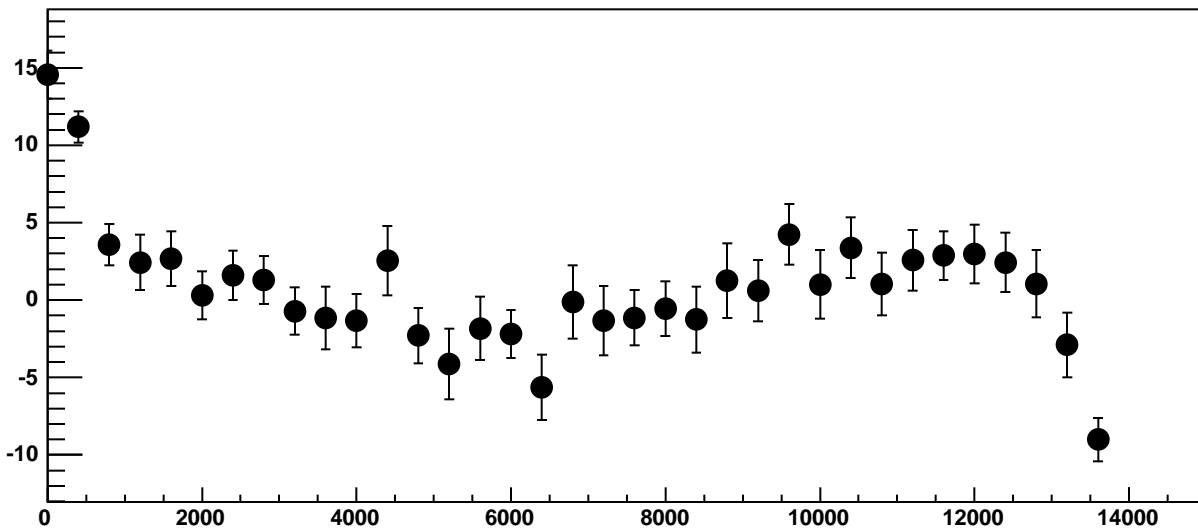


$\chi^2 / \text{ndf}$  32.92 / 23  
p0  $39.86 \pm 0.8063$   
p1  $0.01239 \pm 0.0001285$

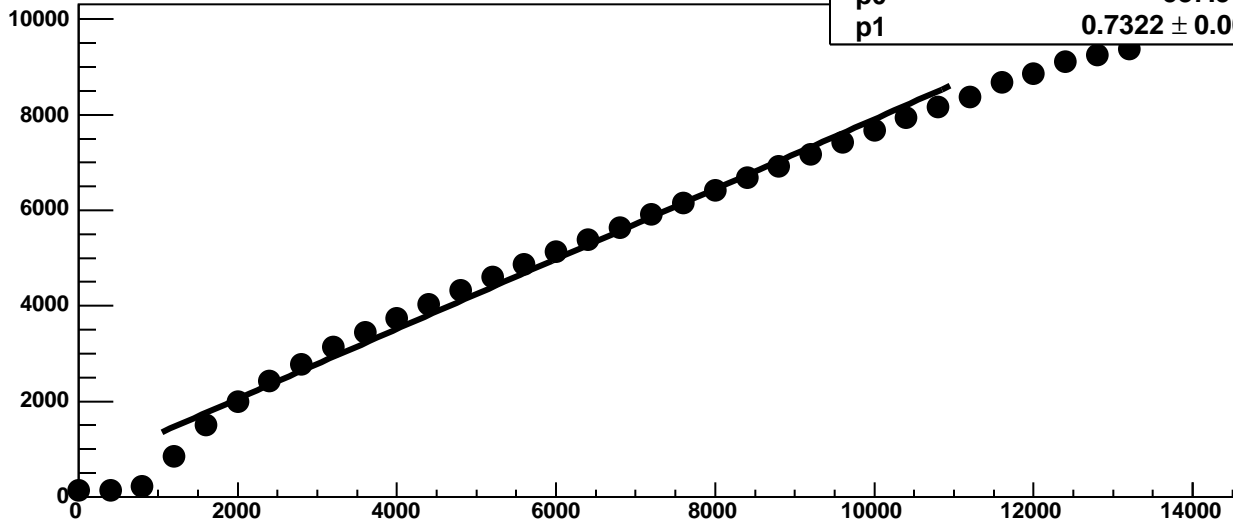
Chip 10, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 10, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

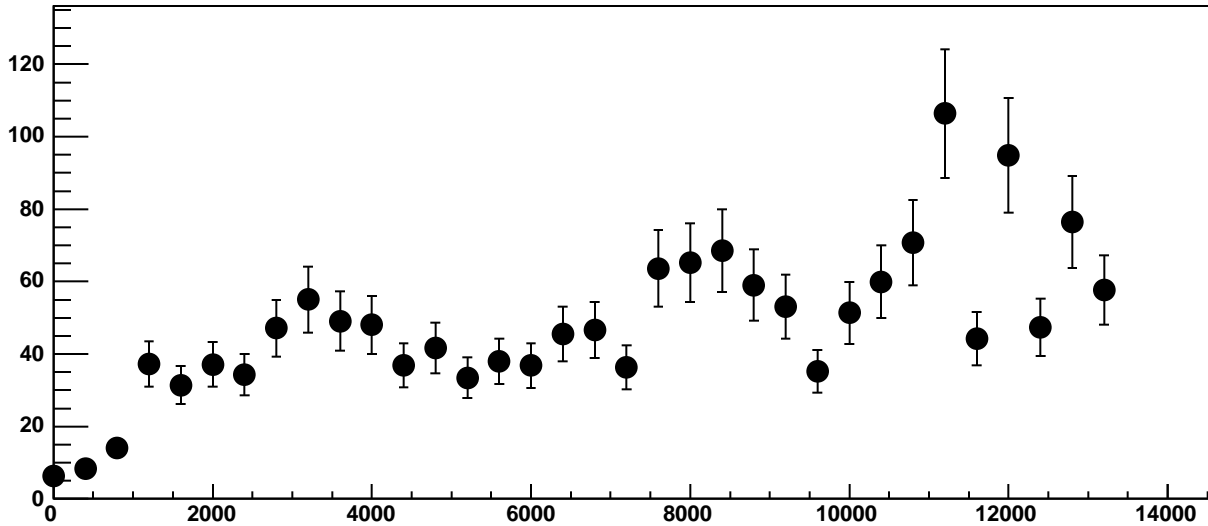


Chip 10, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC

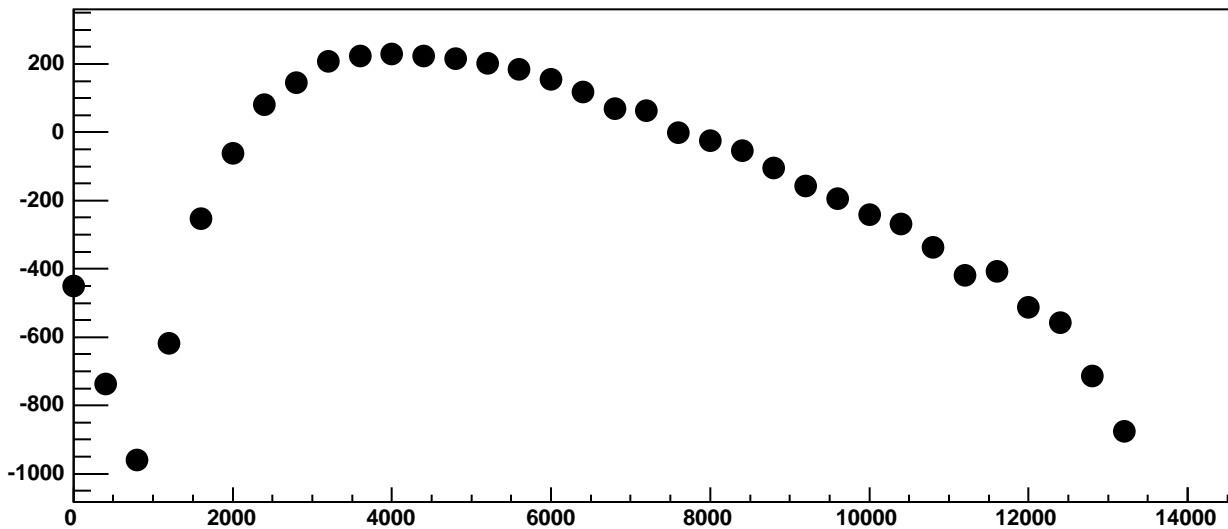


$\chi^2 / \text{ndf}$  1.287e+04 / 23  
p0 587.5 ± 4.26  
p1 0.7322 ± 0.0007137

Chip 10, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC

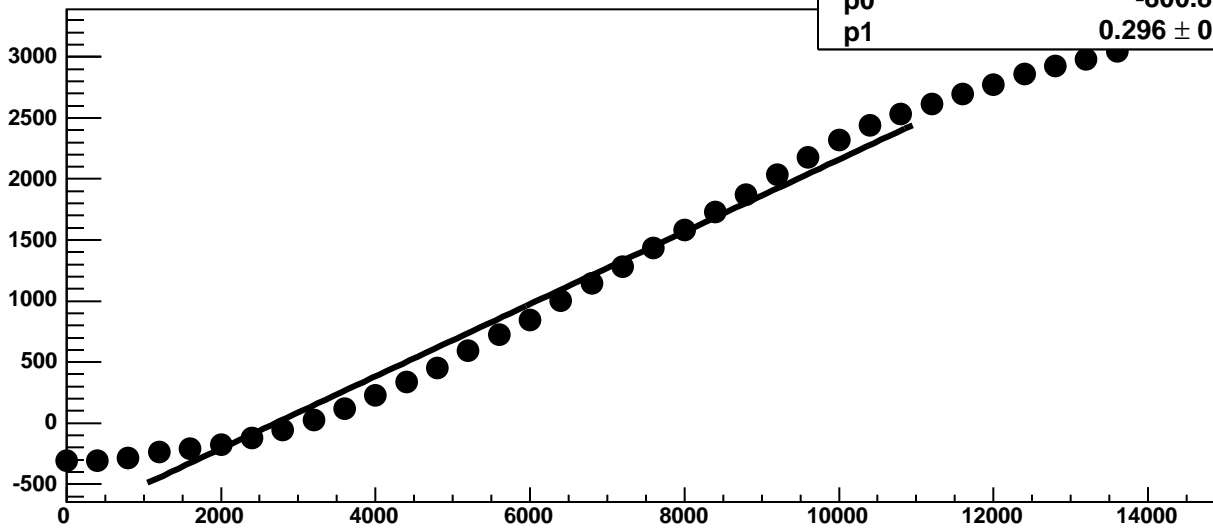


Chip 10, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC

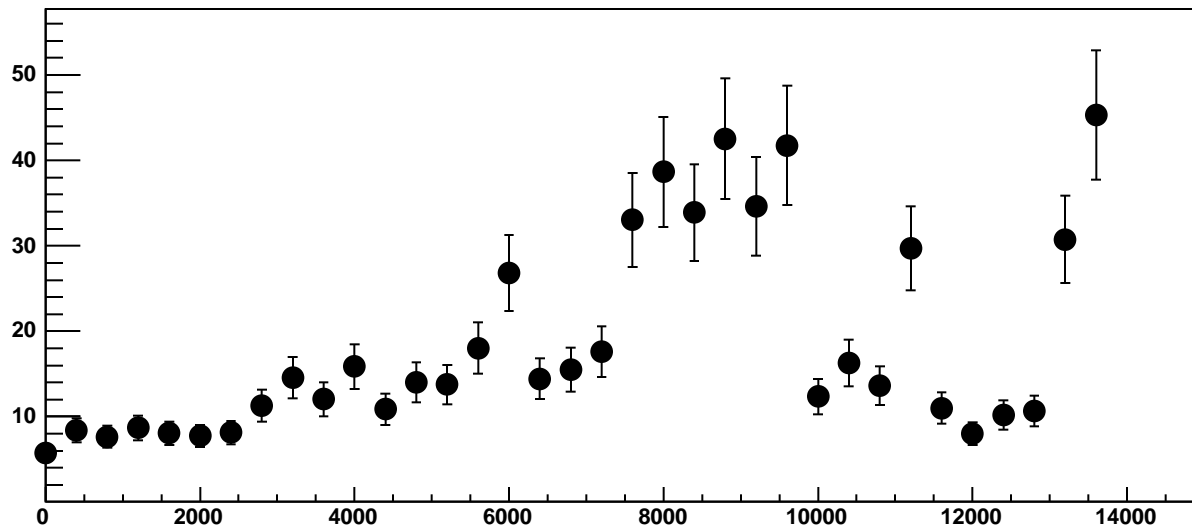


Chip 11, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

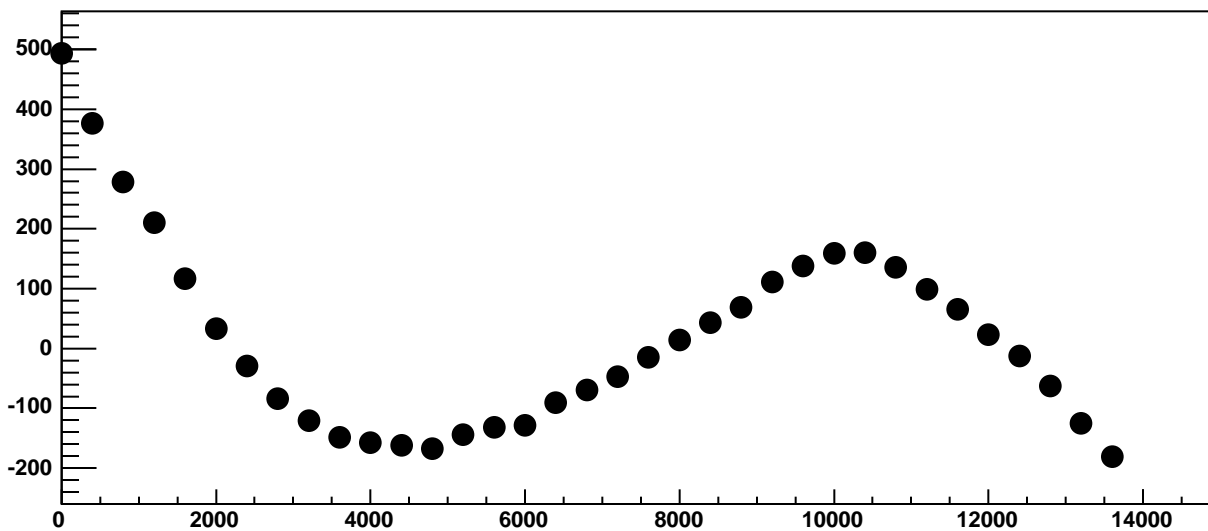
$\chi^2 / \text{ndf}$  4.2e+04 / 23  
p0 -800.8 ± 1.085  
p1 0.296 ± 0.000213



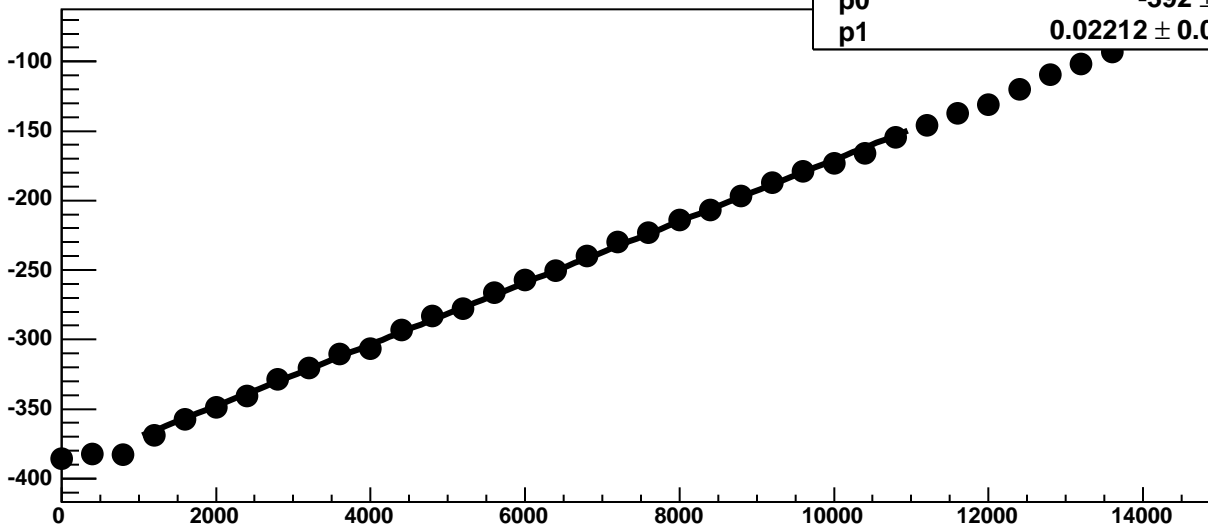
Chip 11, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

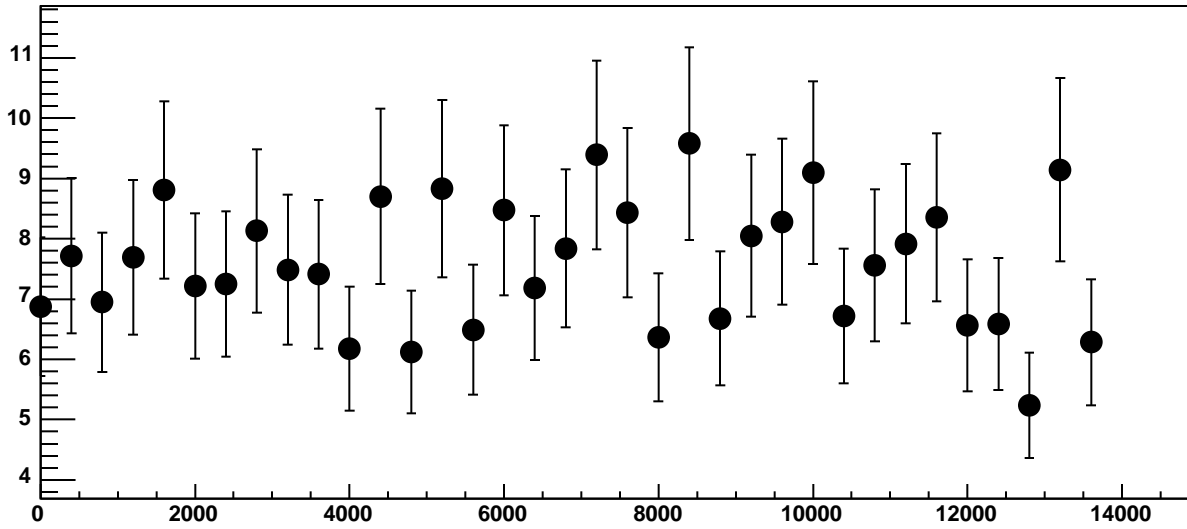


Chip 11, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

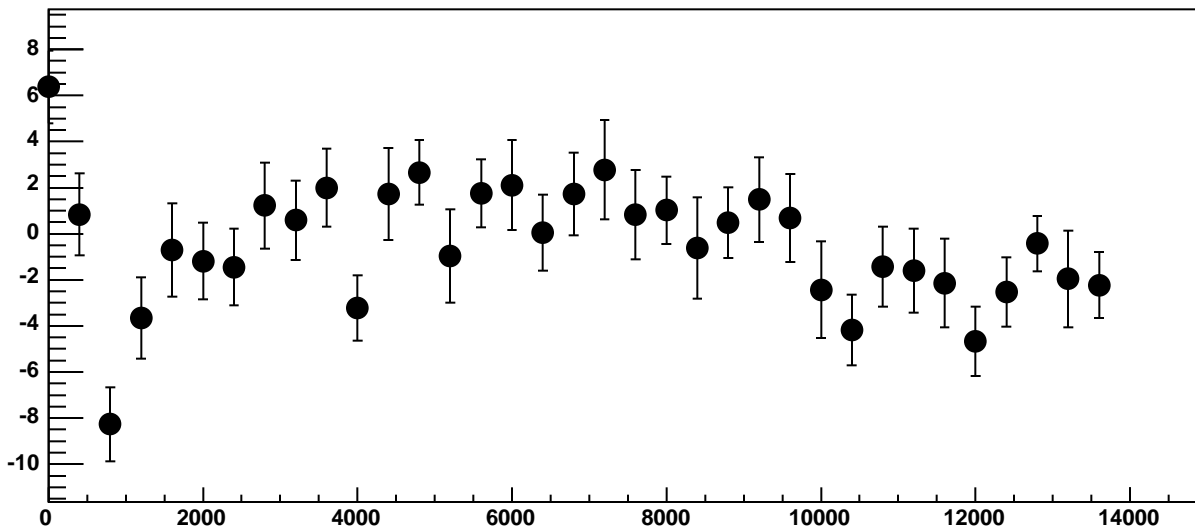


$\chi^2 / \text{ndf}$  33.49 / 23  
p0  $-392 \pm 0.8024$   
p1  $0.02212 \pm 0.0001217$

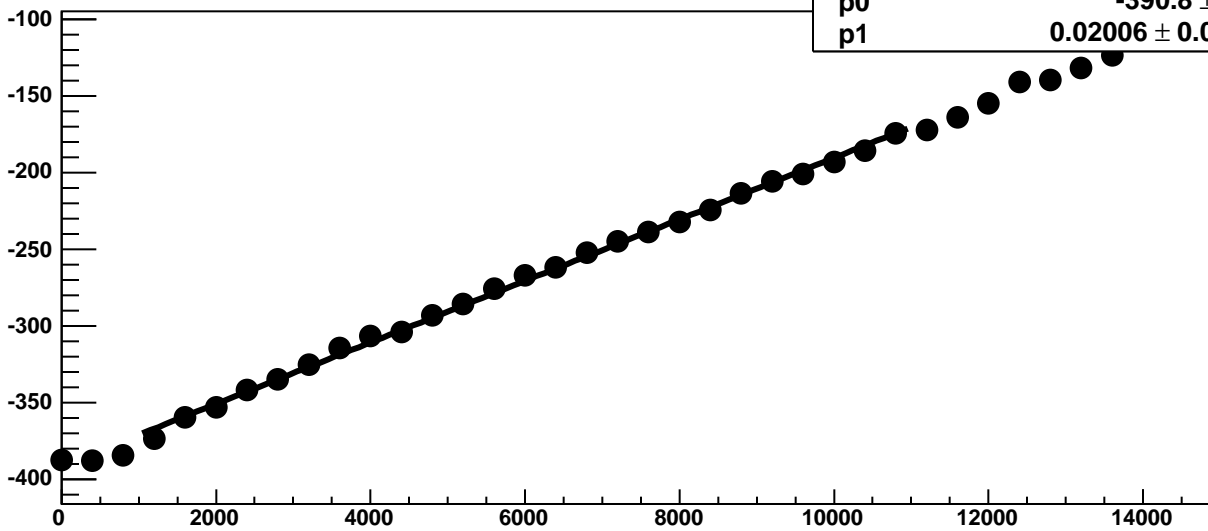
Chip 11, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC



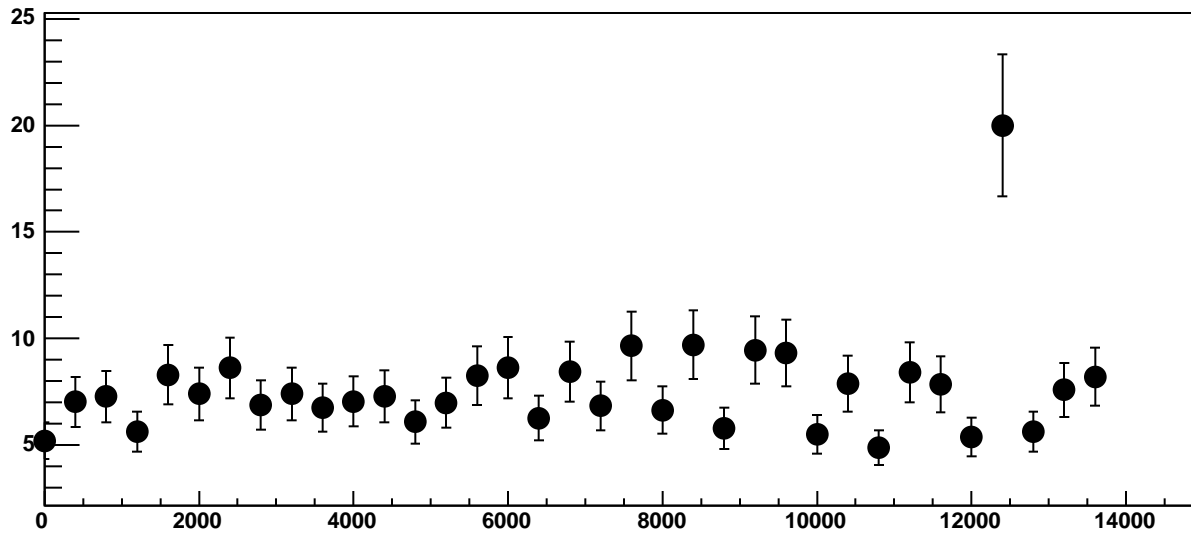
Chip 11, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC



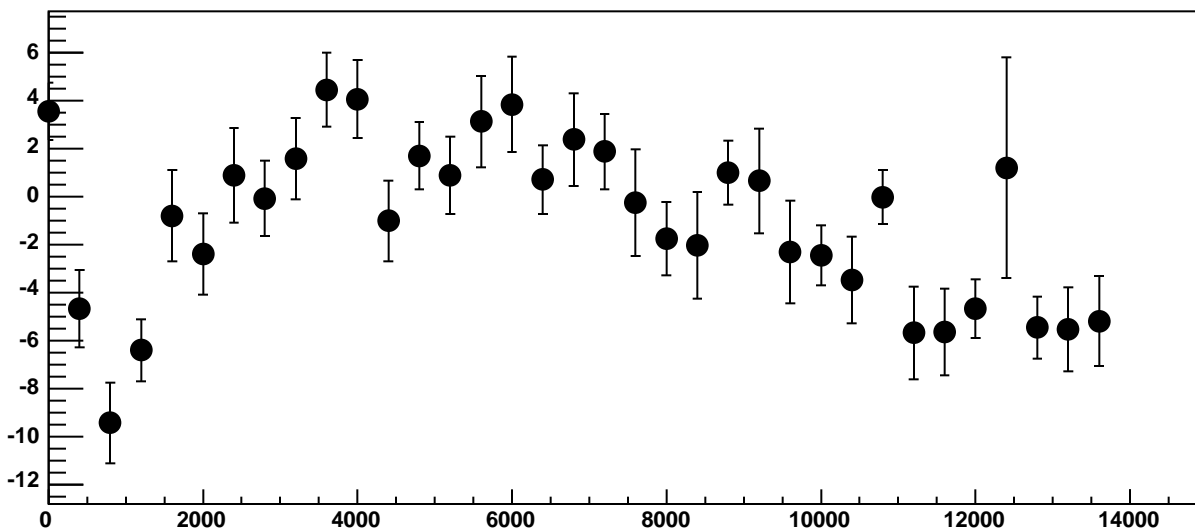
Chip 11, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC



Chip 11, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

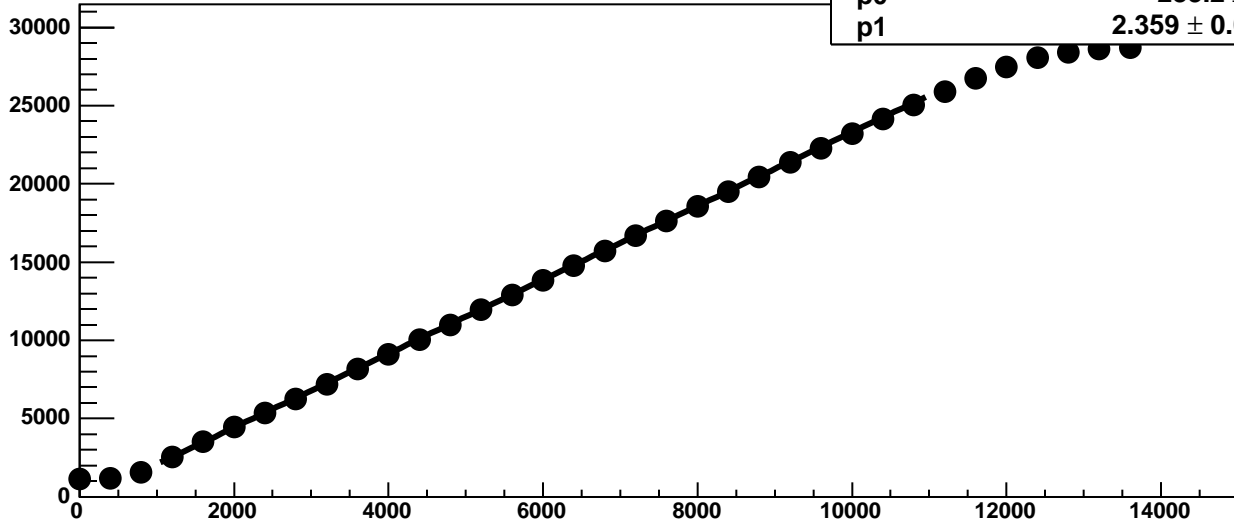


Chip 11, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

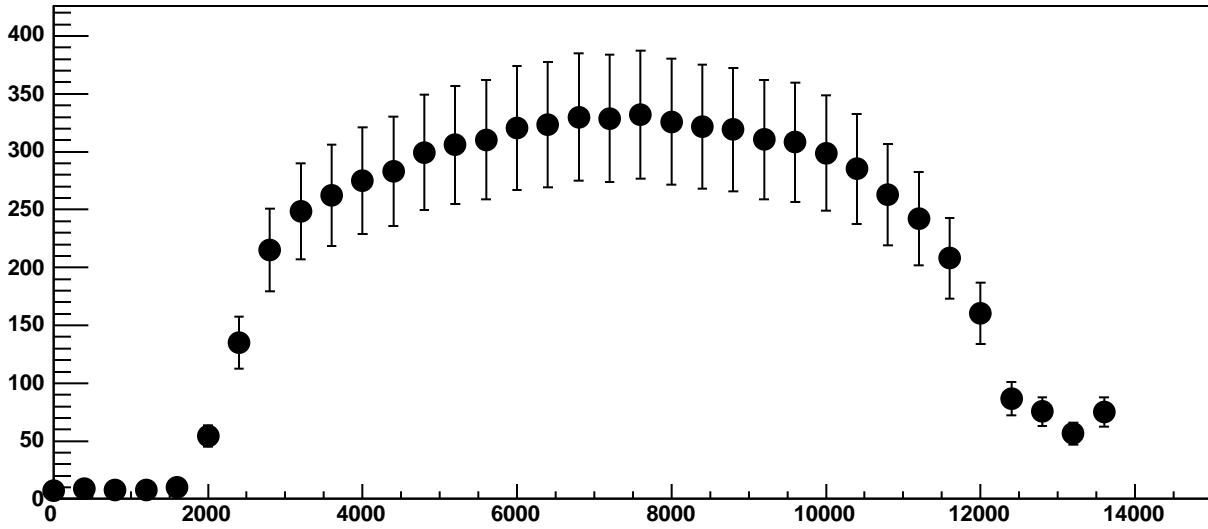




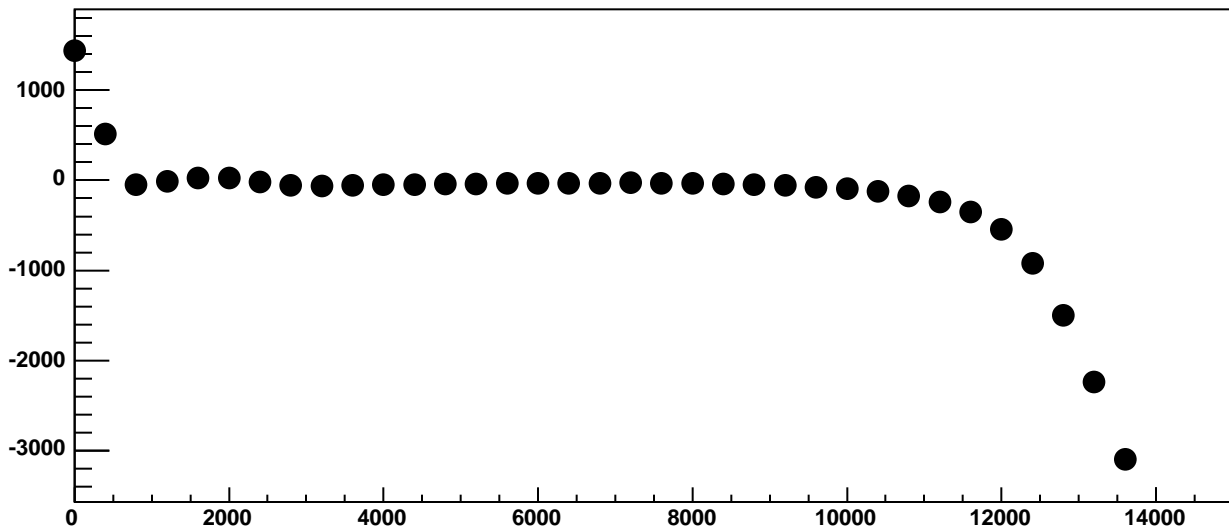
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC



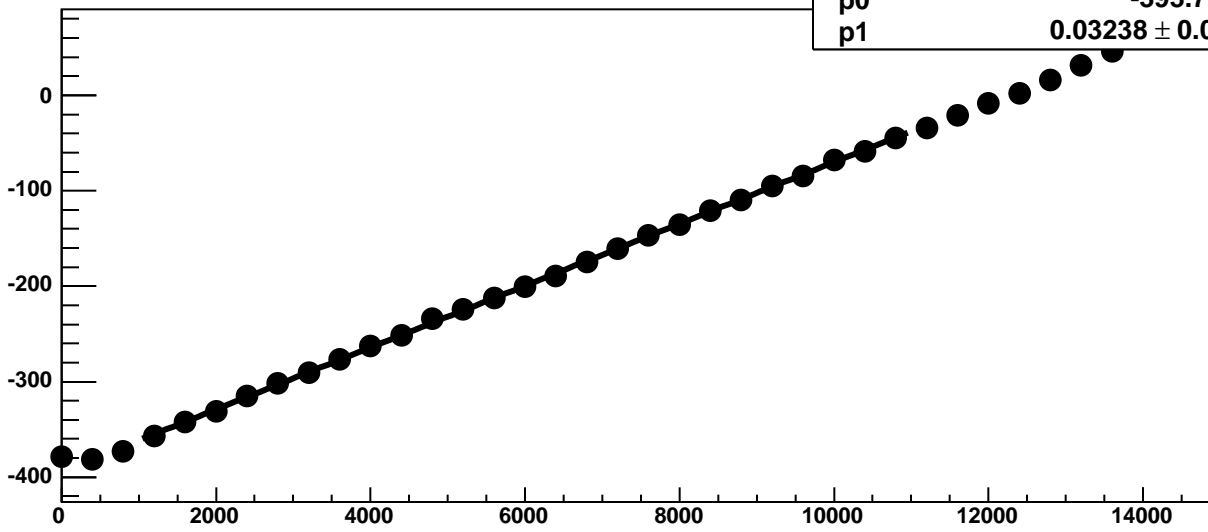
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC



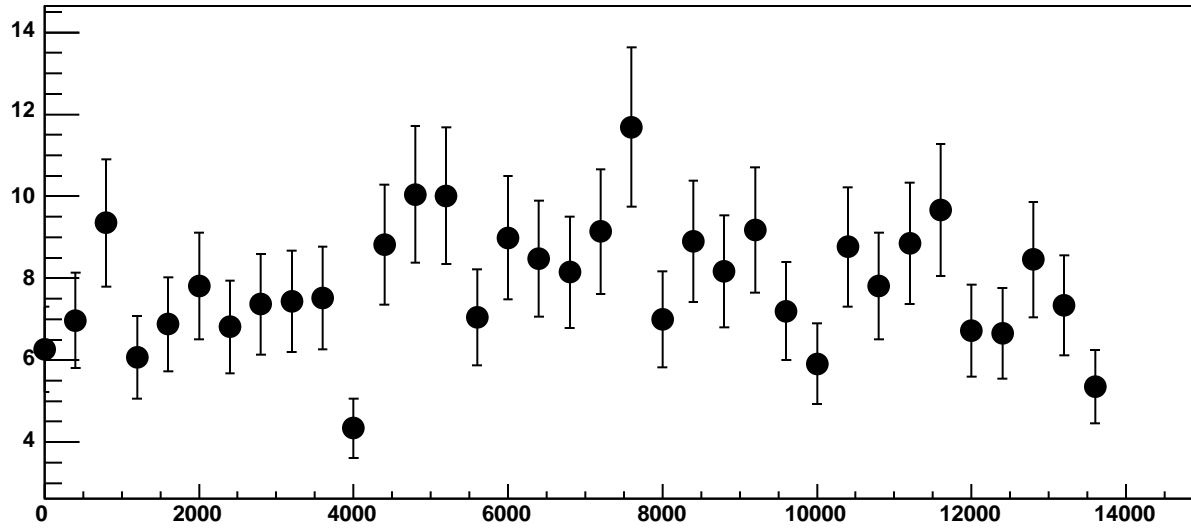
Chip 11, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC



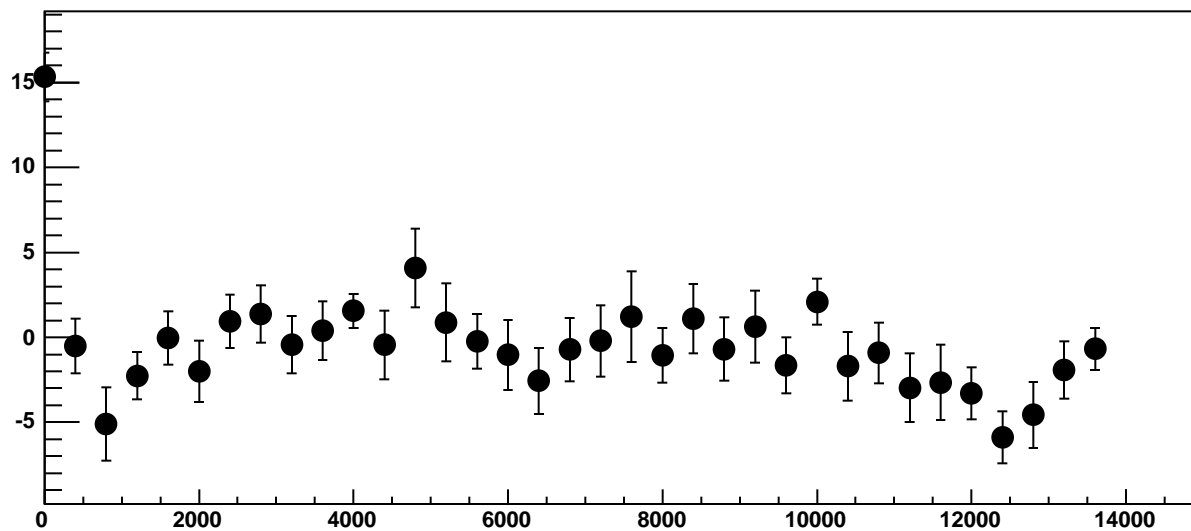
Chip 11, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



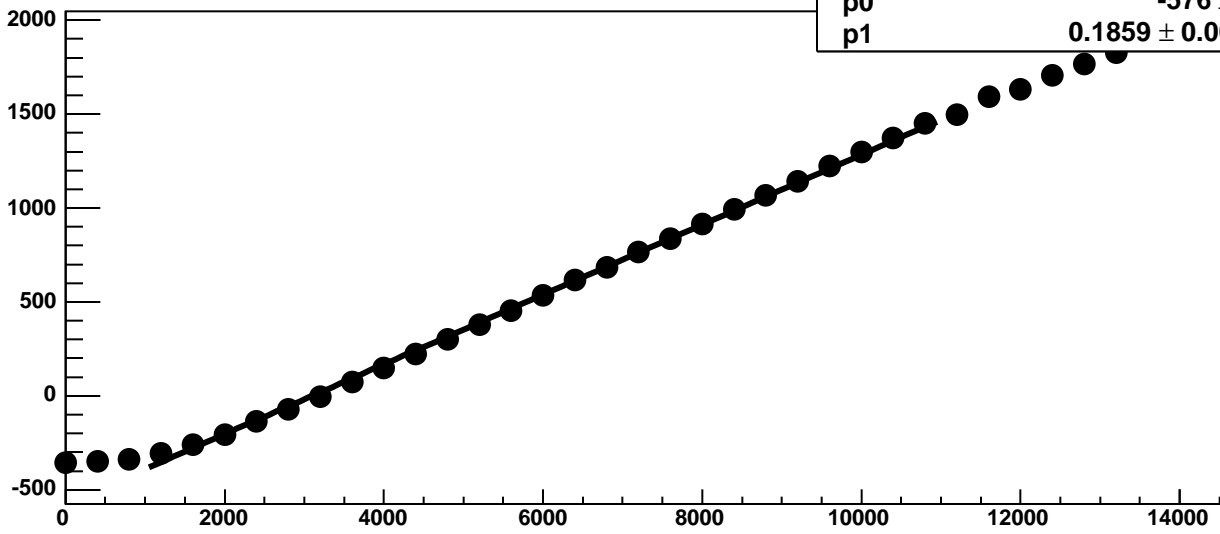
Chip 11, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



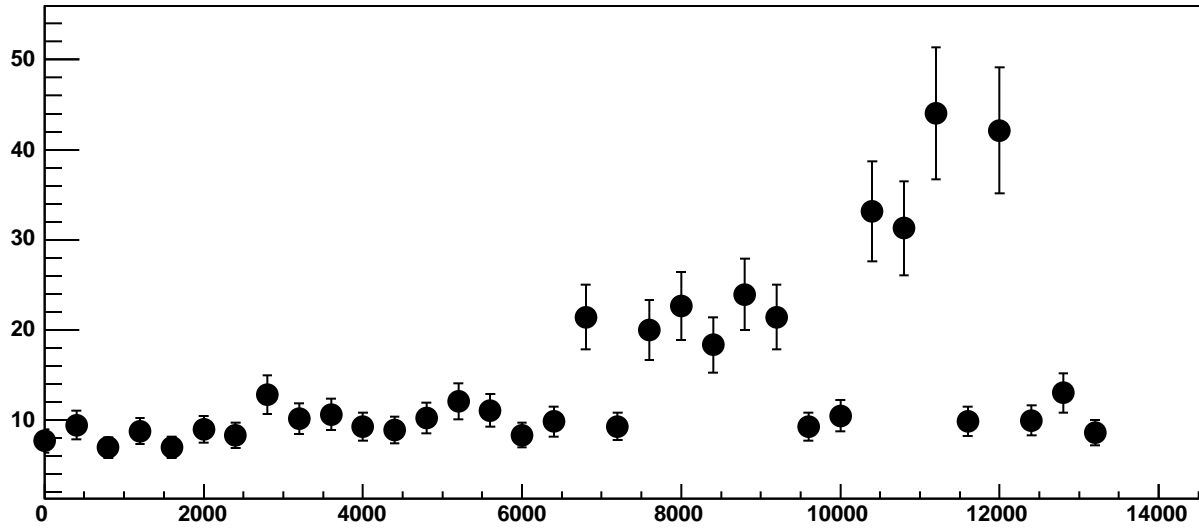
Chip 11, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



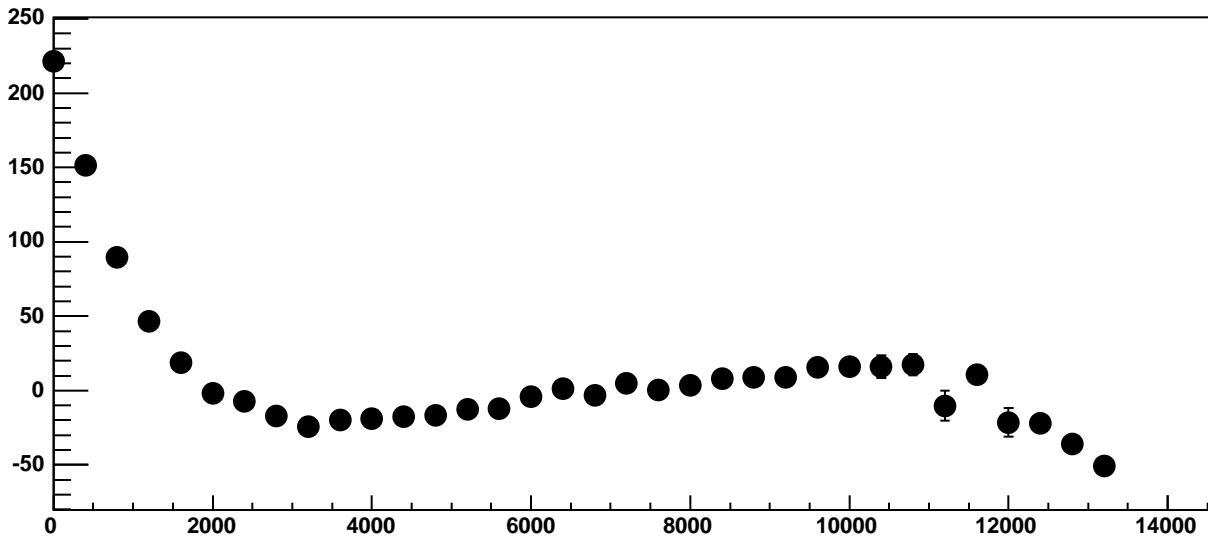
Chip 11, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

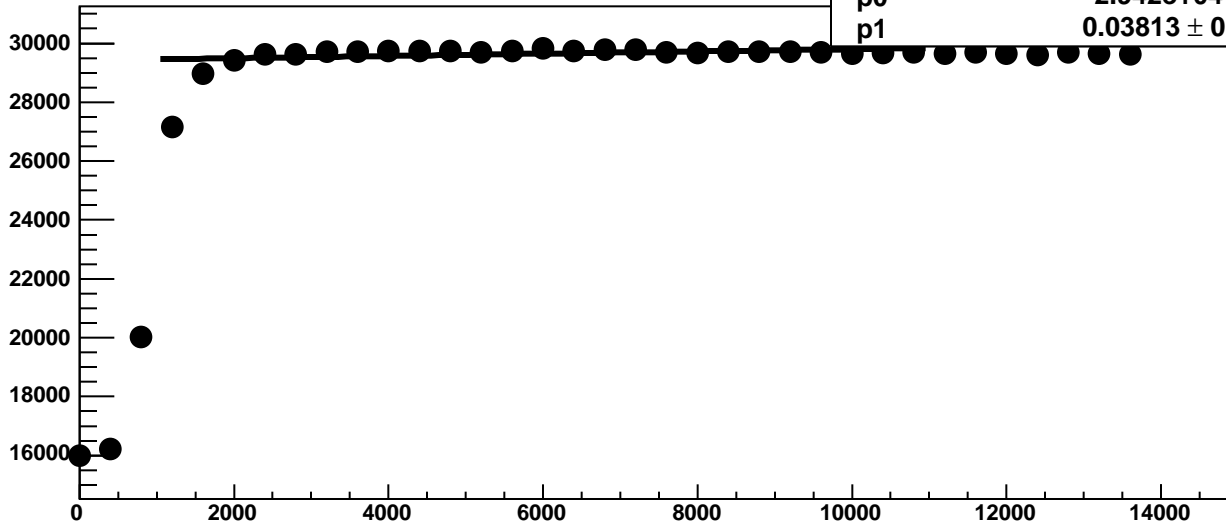
1330 / 23

p0

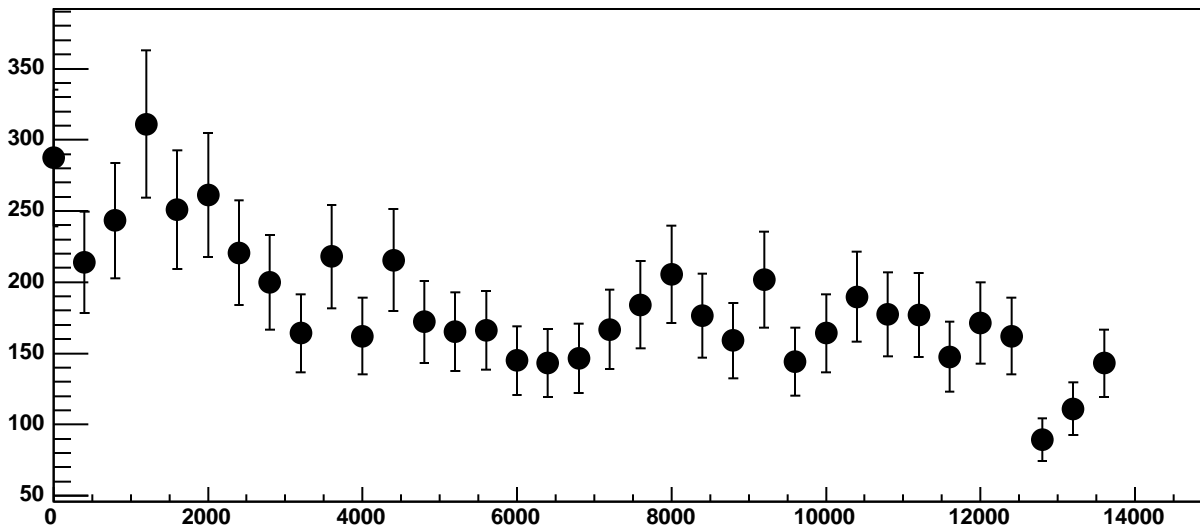
$2.942\text{e}+04 \pm 21.98$

p1

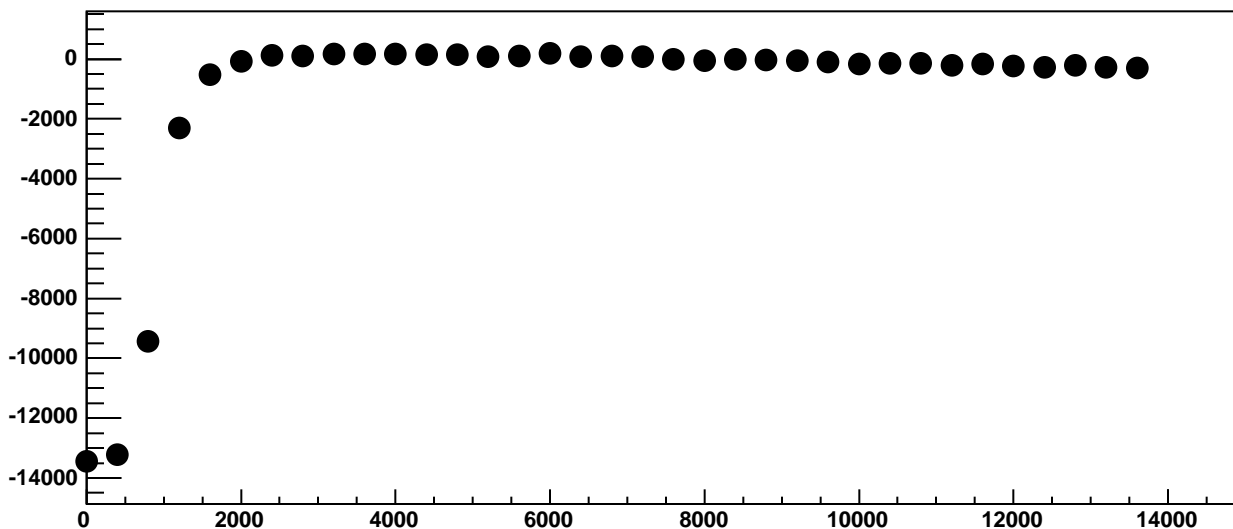
$0.03813 \pm 0.003148$



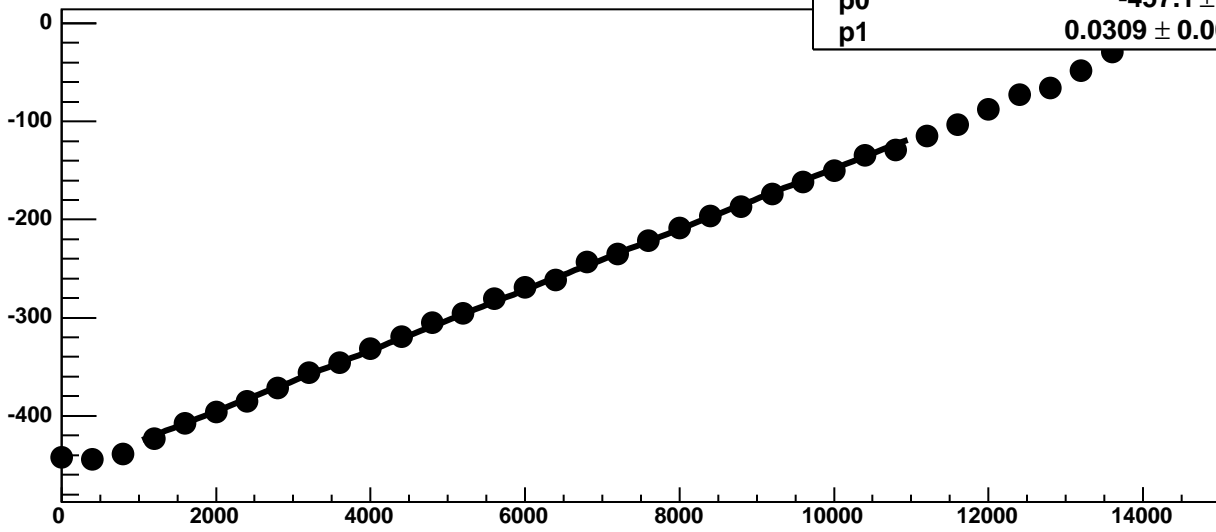
Chip 11, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

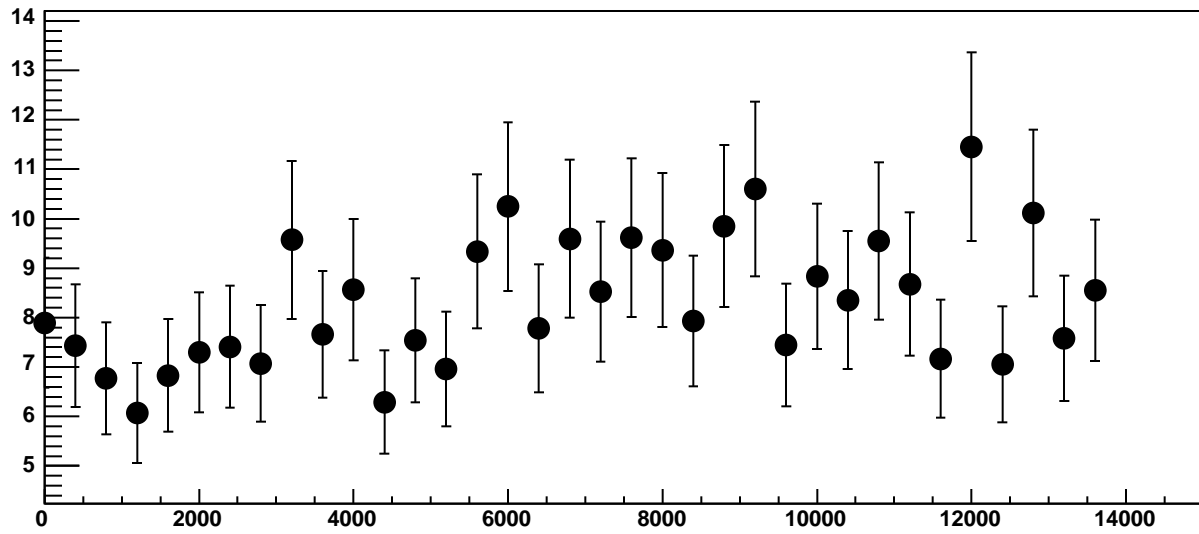


Chip 11, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

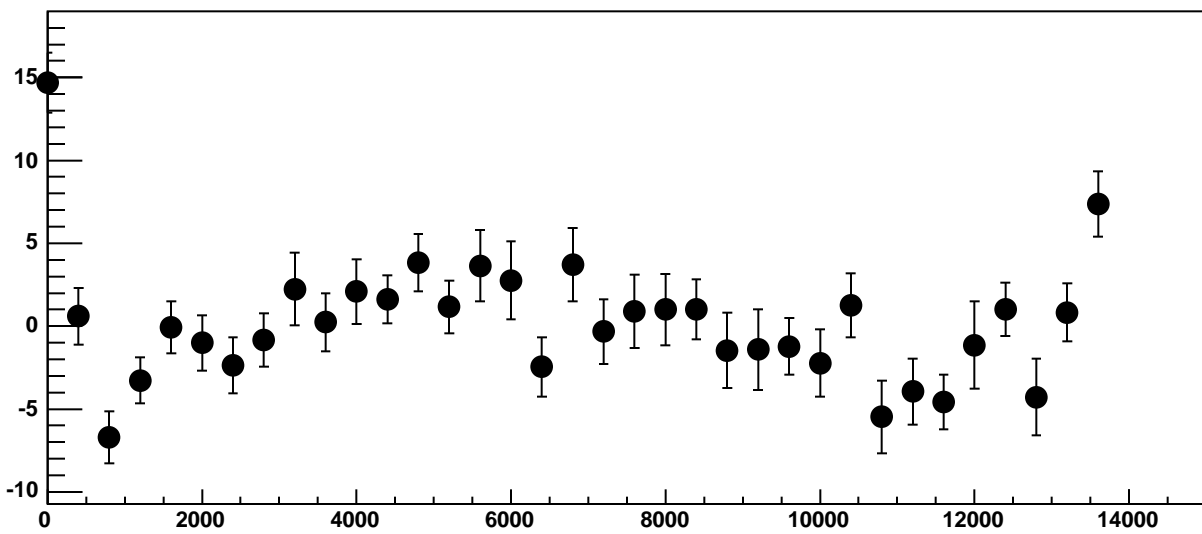


$\chi^2 / \text{ndf}$  35.84 / 23  
p0 -457.1 ± 0.7774  
p1 0.0309 ± 0.0001255

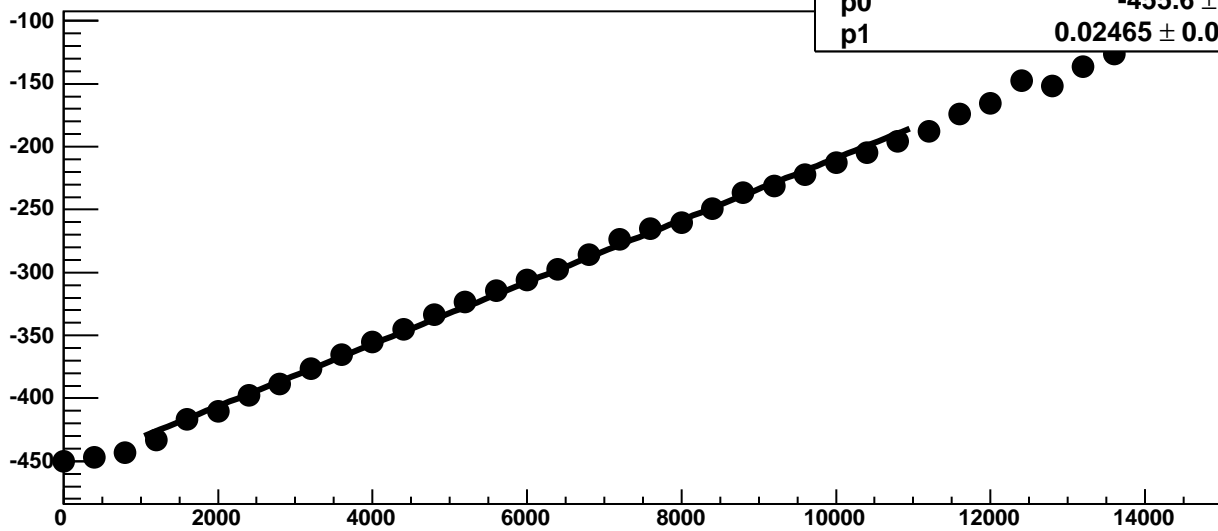
Chip 11, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



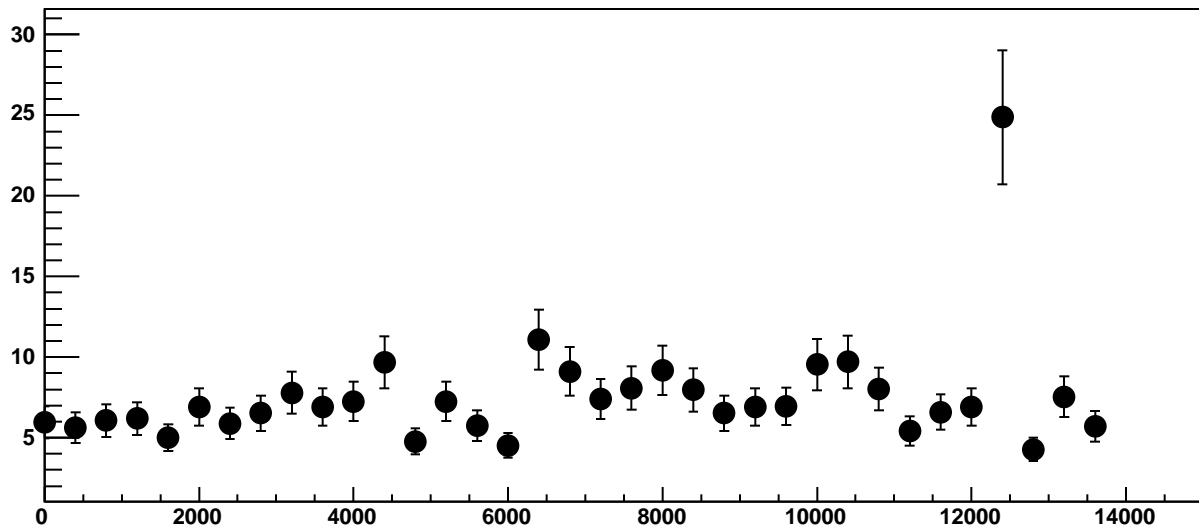
Chip 11, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



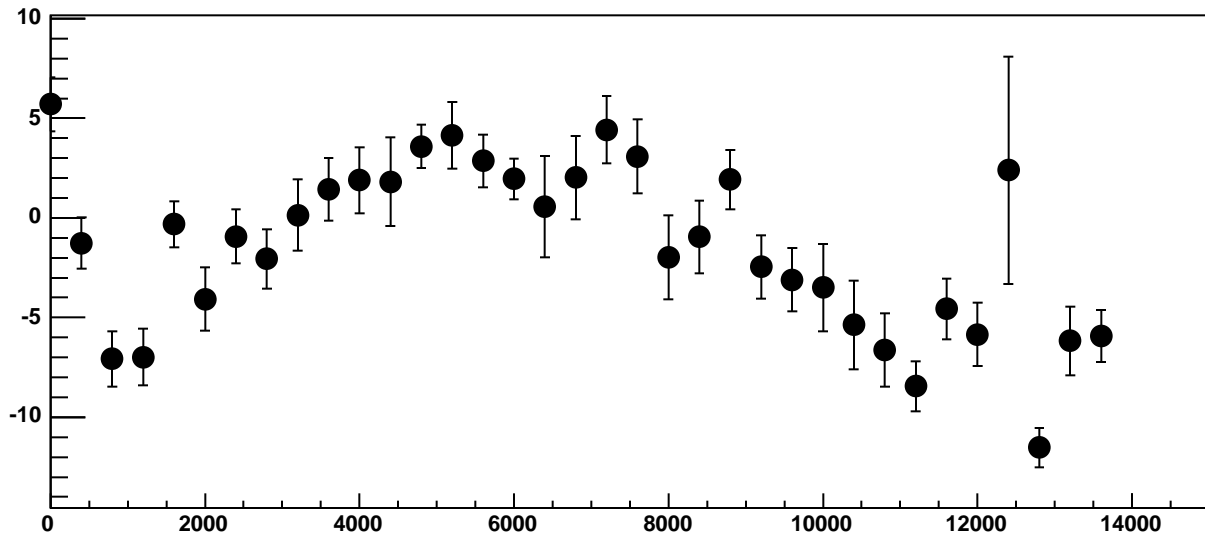
Chip 11, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



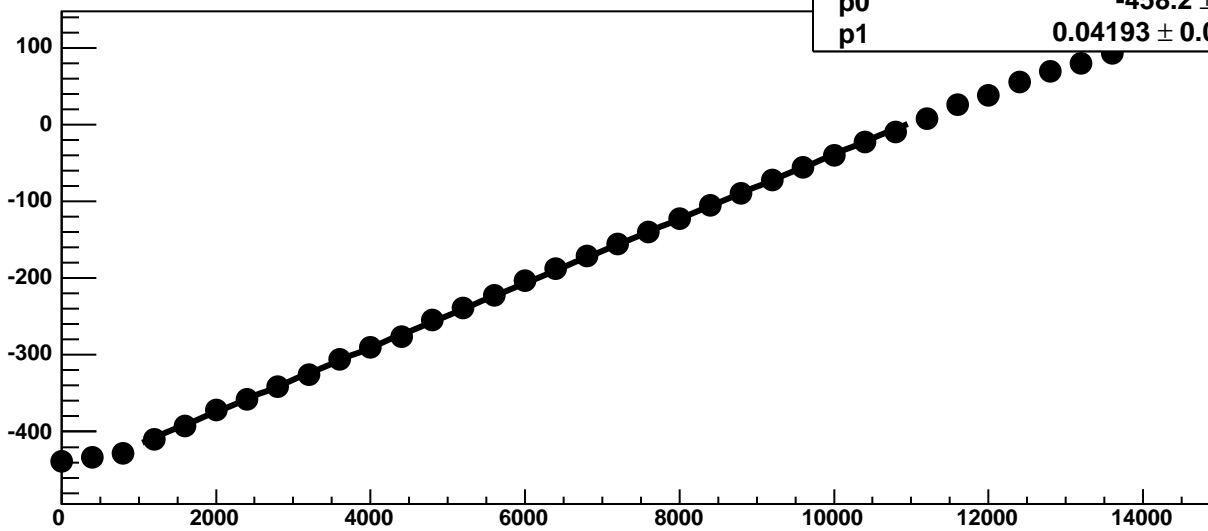
Chip 11, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

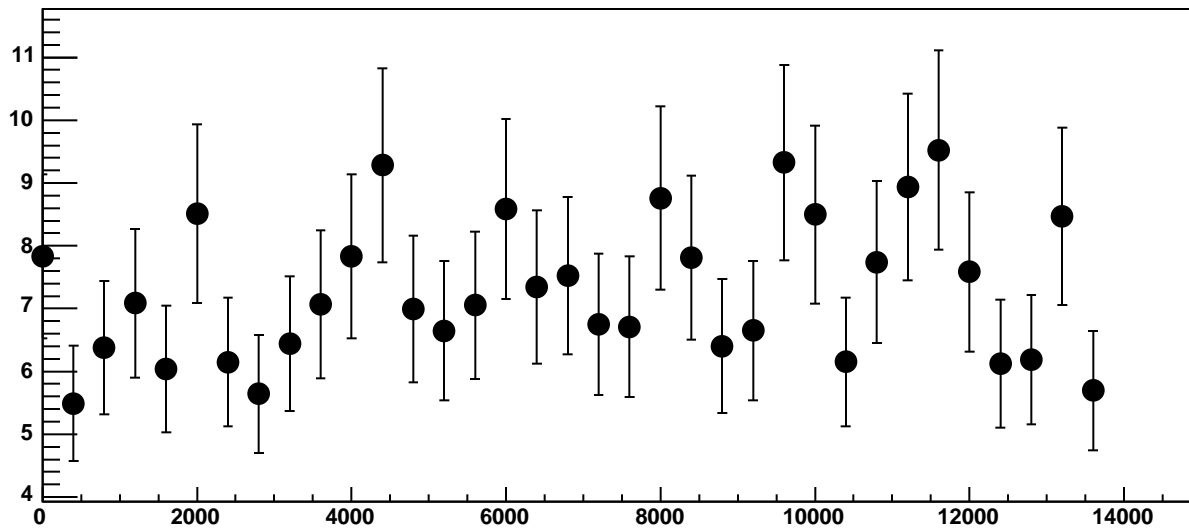


Chip 11, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

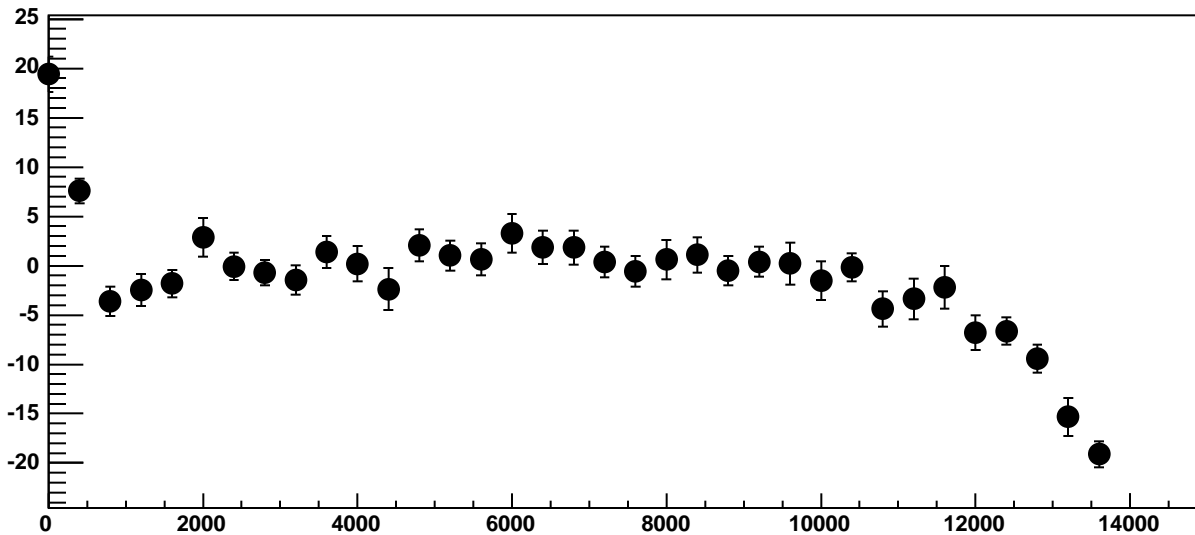


$\chi^2 / \text{ndf}$  24.28 / 23  
p0  $-458.2 \pm 0.7235$   
p1  $0.04193 \pm 0.0001114$

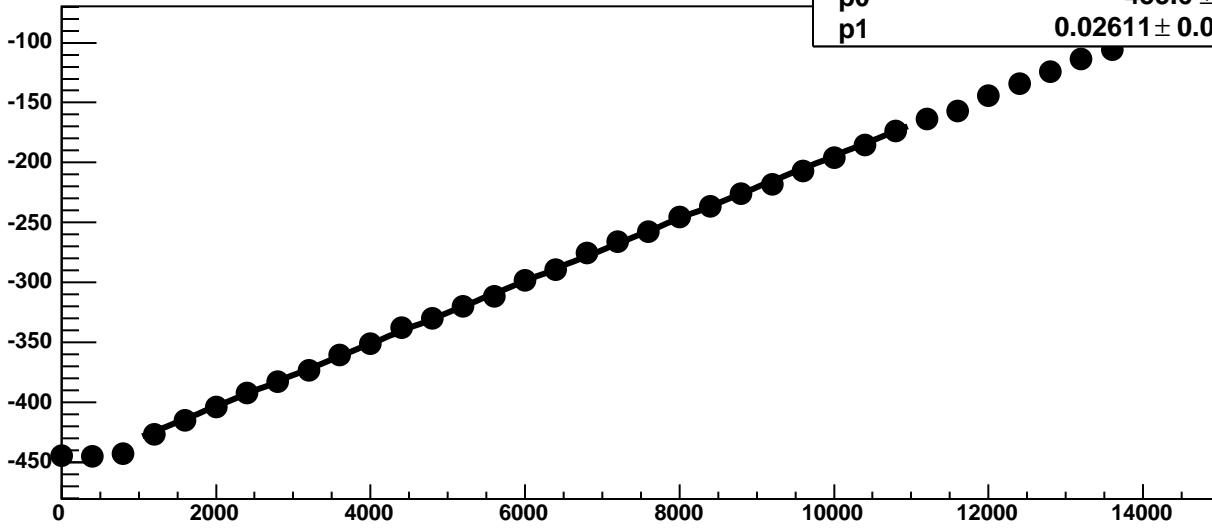
Chip 11, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

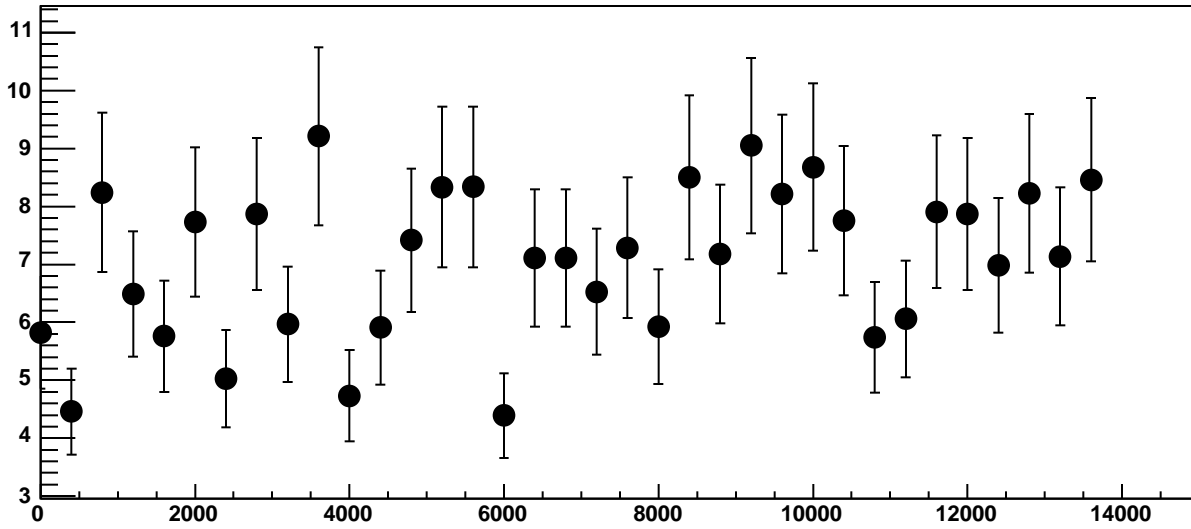


Chip 11, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

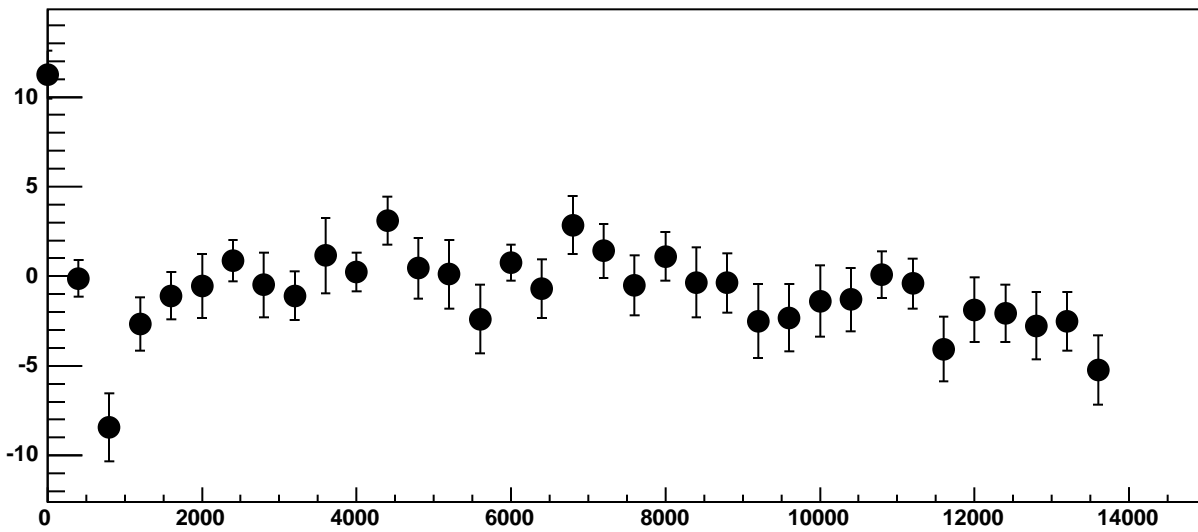


$\chi^2 / \text{ndf}$  21.92 / 23  
p0  $-455.6 \pm 0.6779$   
p1  $0.02611 \pm 0.0001072$

Chip 11, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

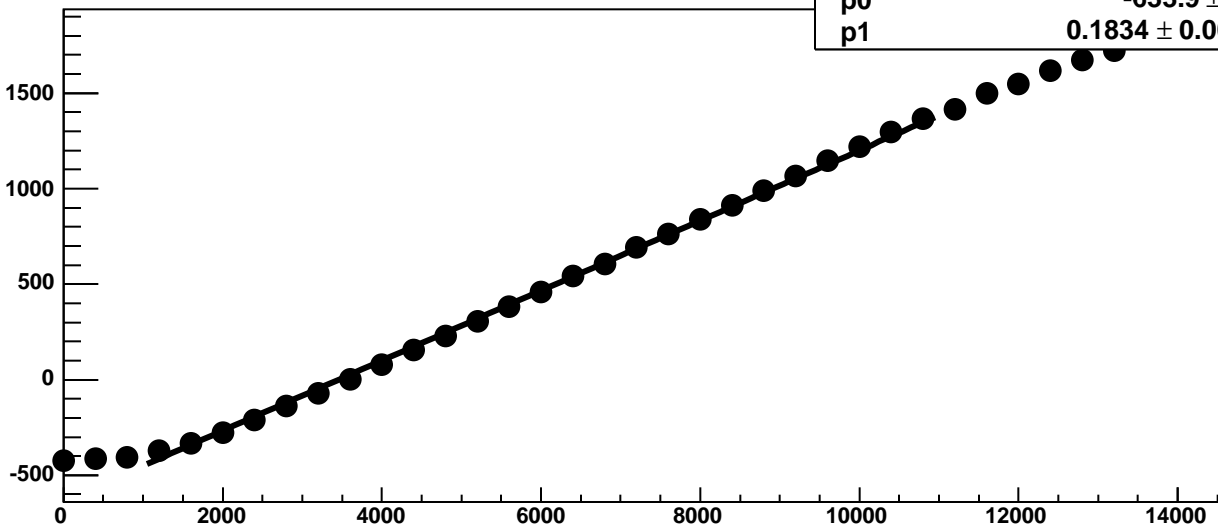


Chip 11, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

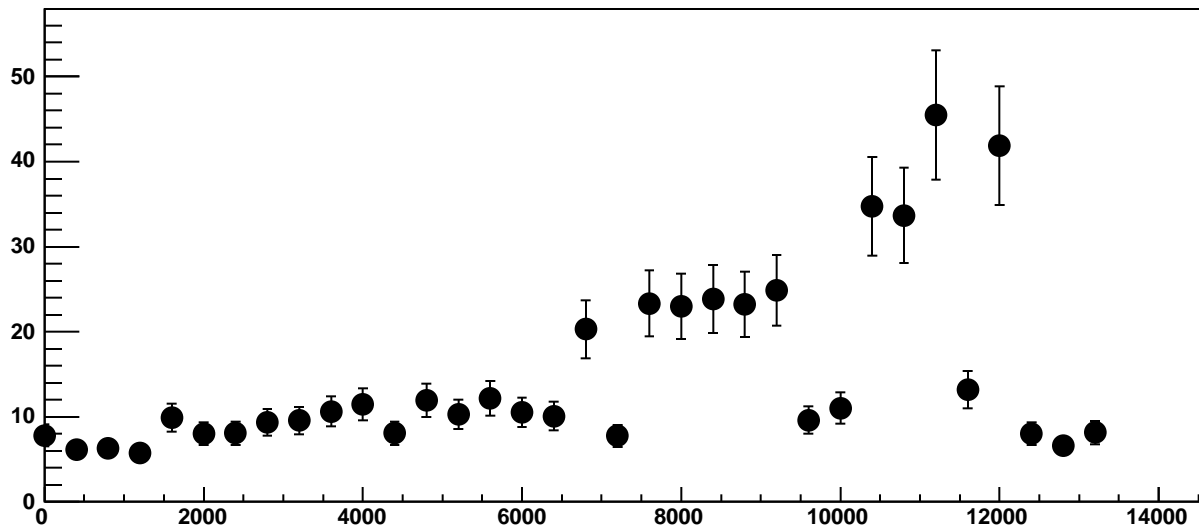




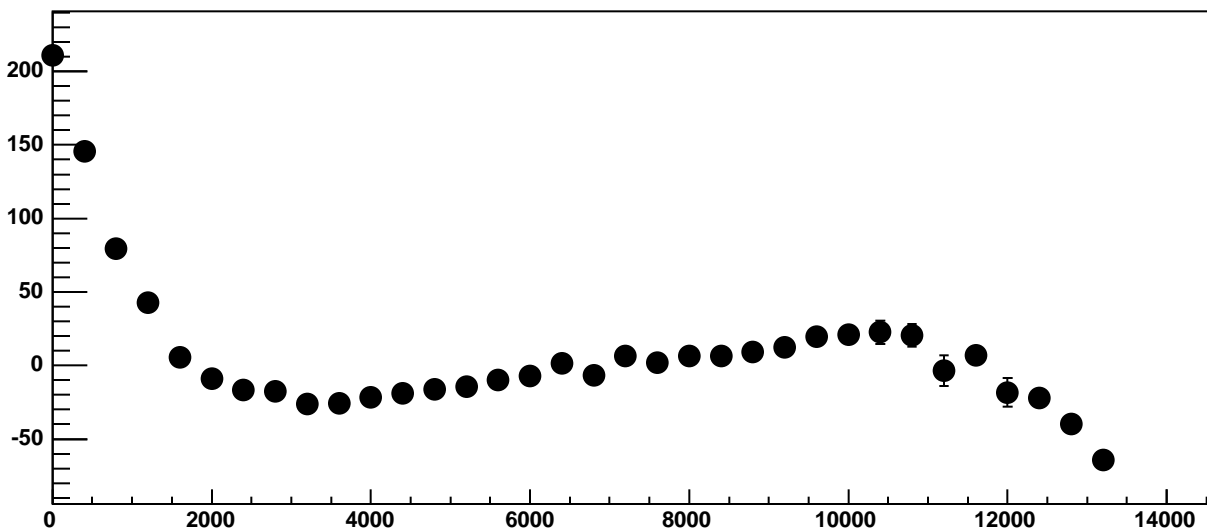
Chip 11, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



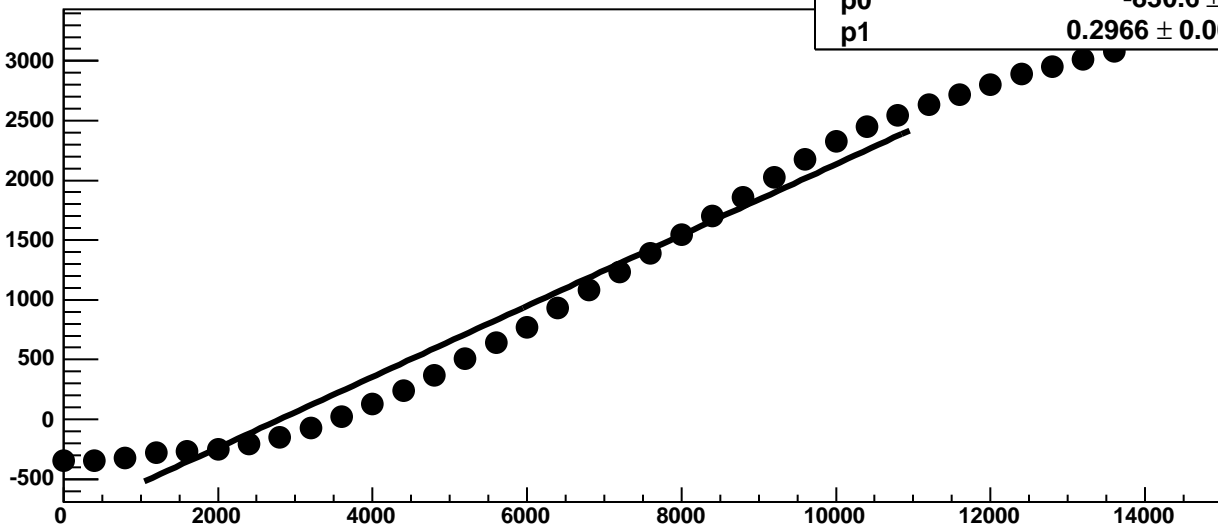
Chip 11, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



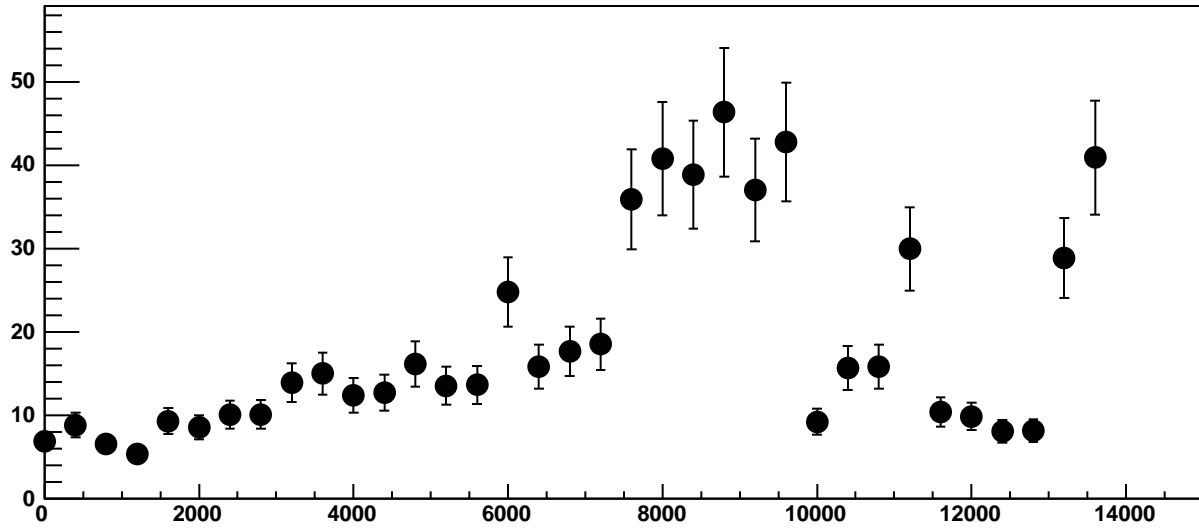
Chip 11, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



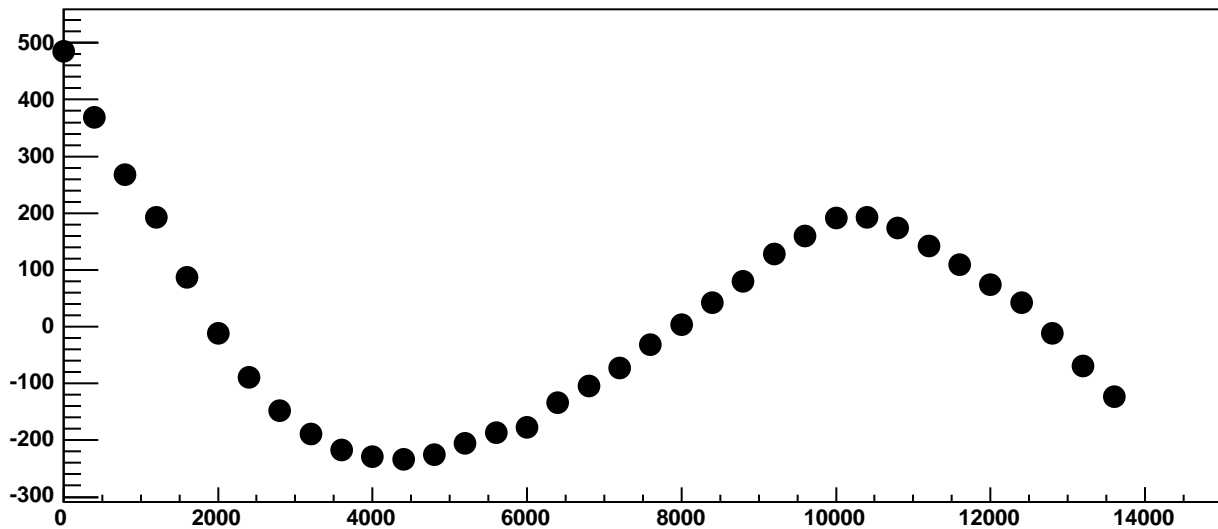
Chip 11, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



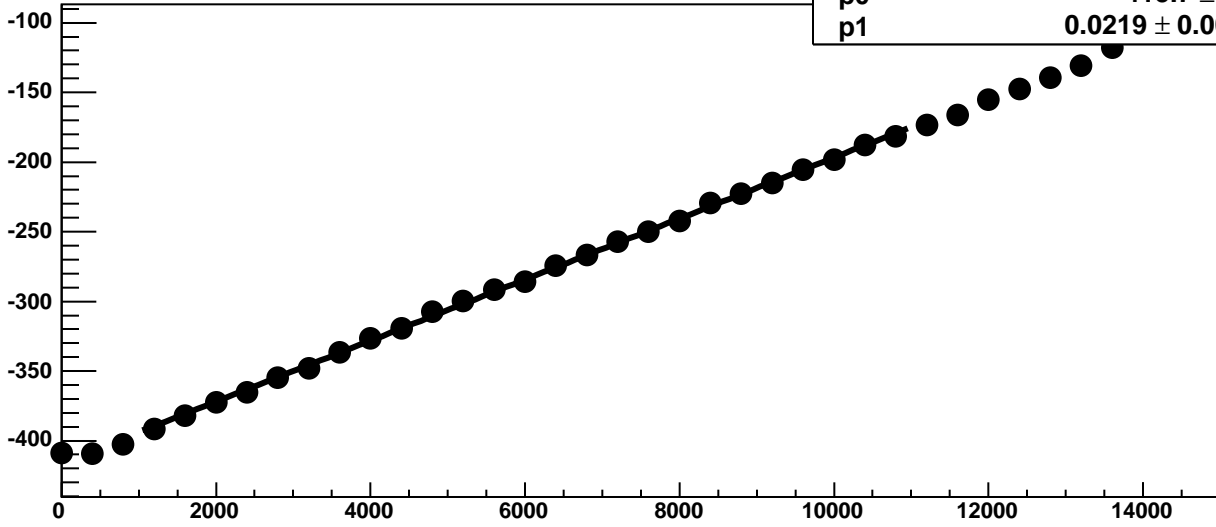
Chip 11, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



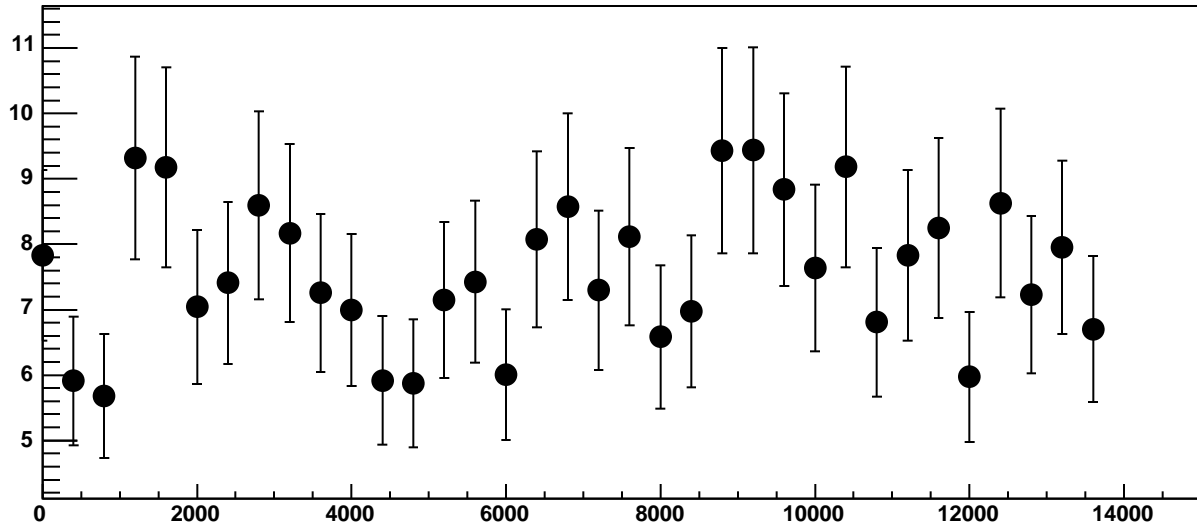
Chip 11, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



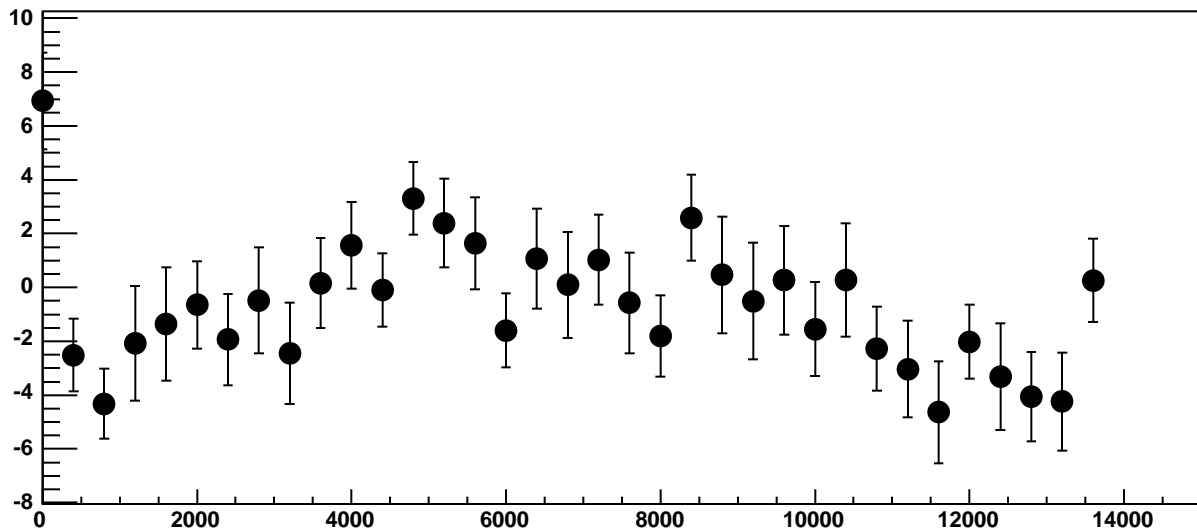
Chip 11, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



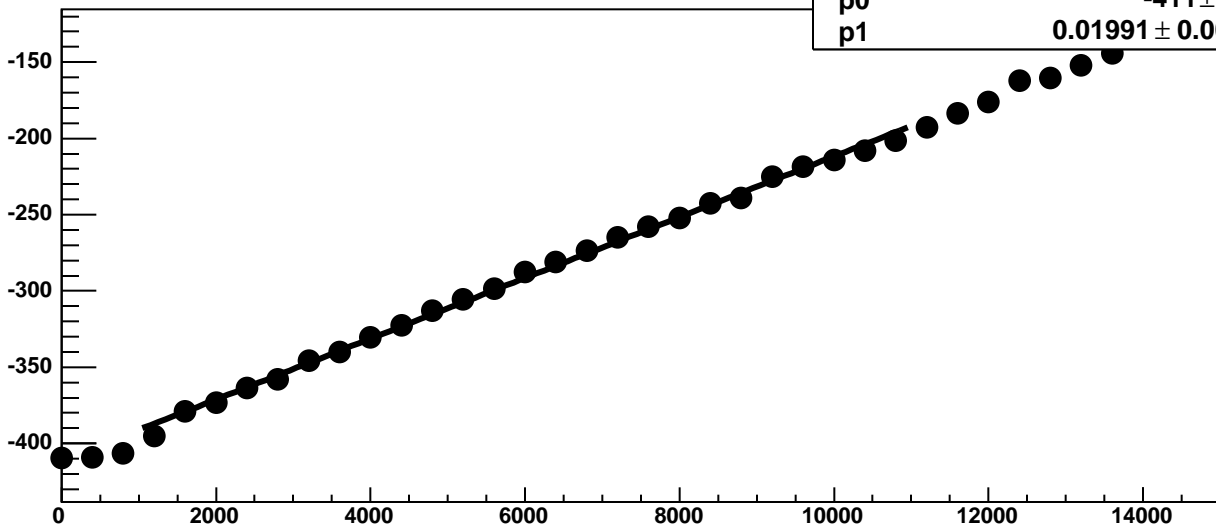
Chip 11, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



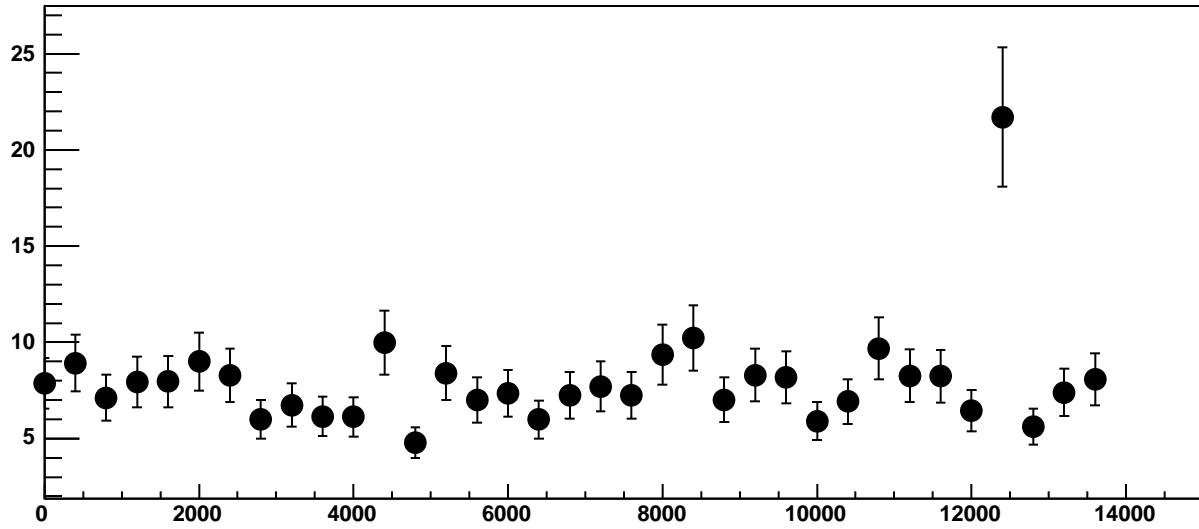
Chip 11, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



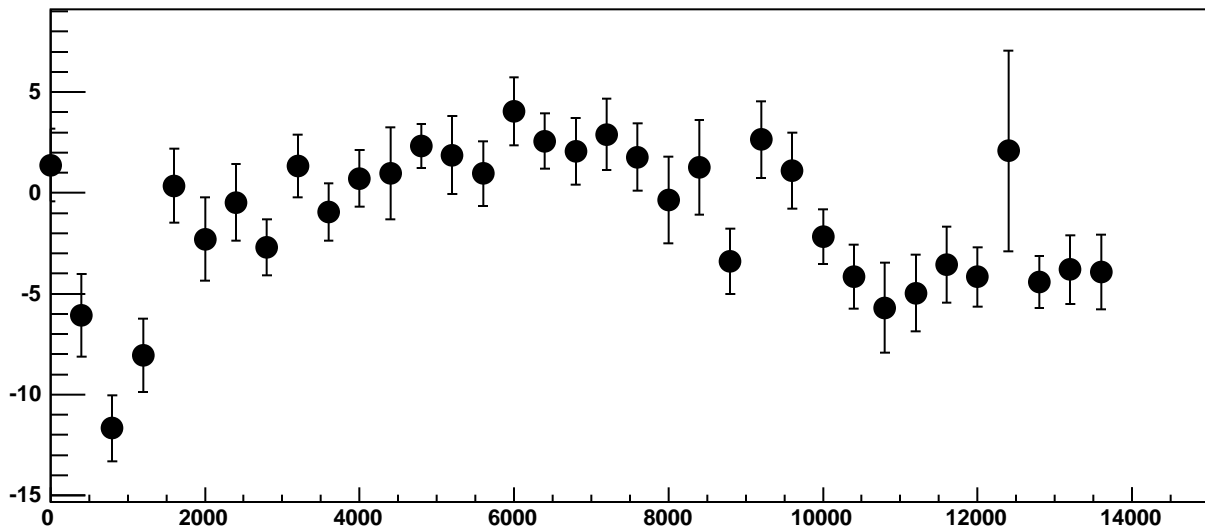
Chip 11, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



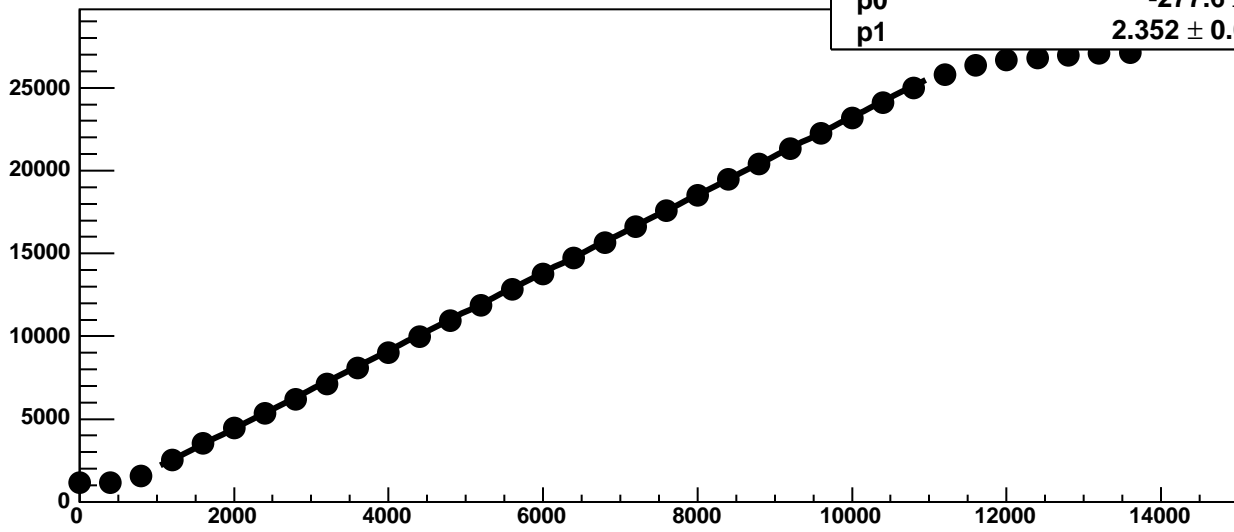
Chip 11, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



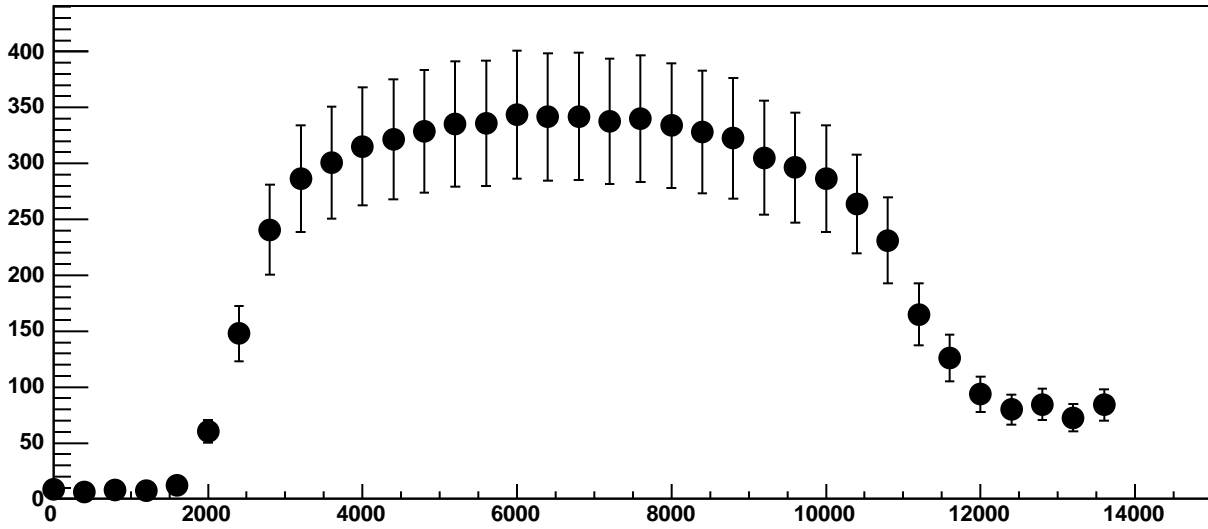
Chip 11, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



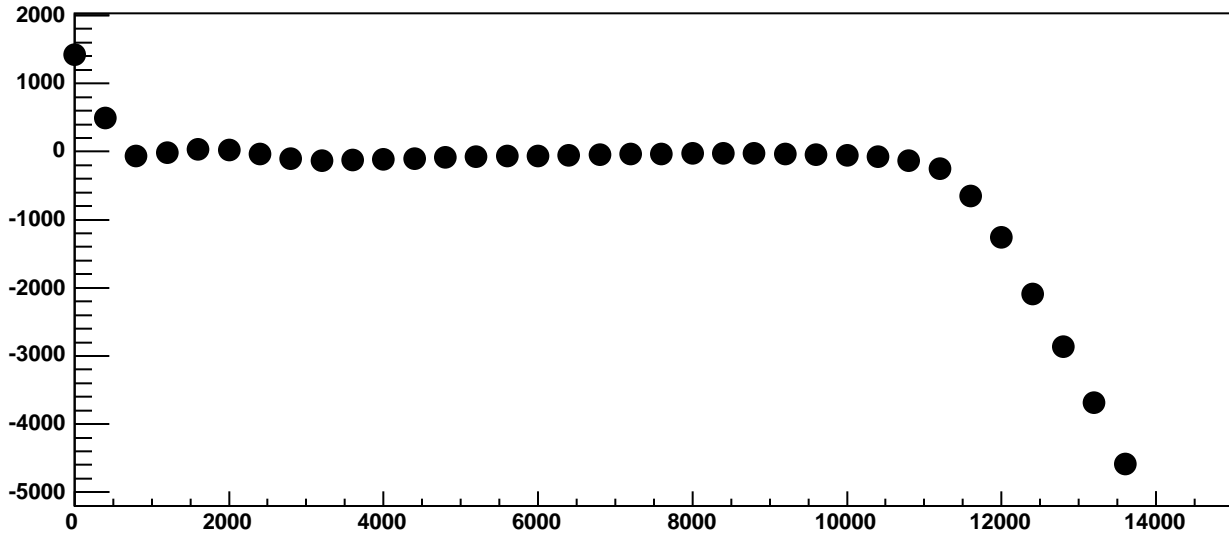
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC



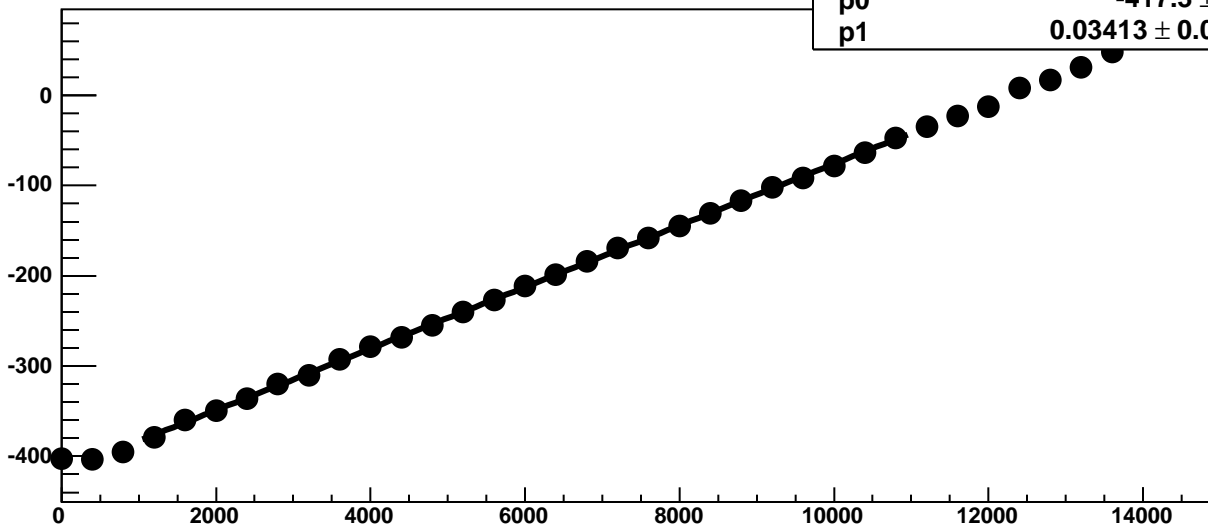
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC



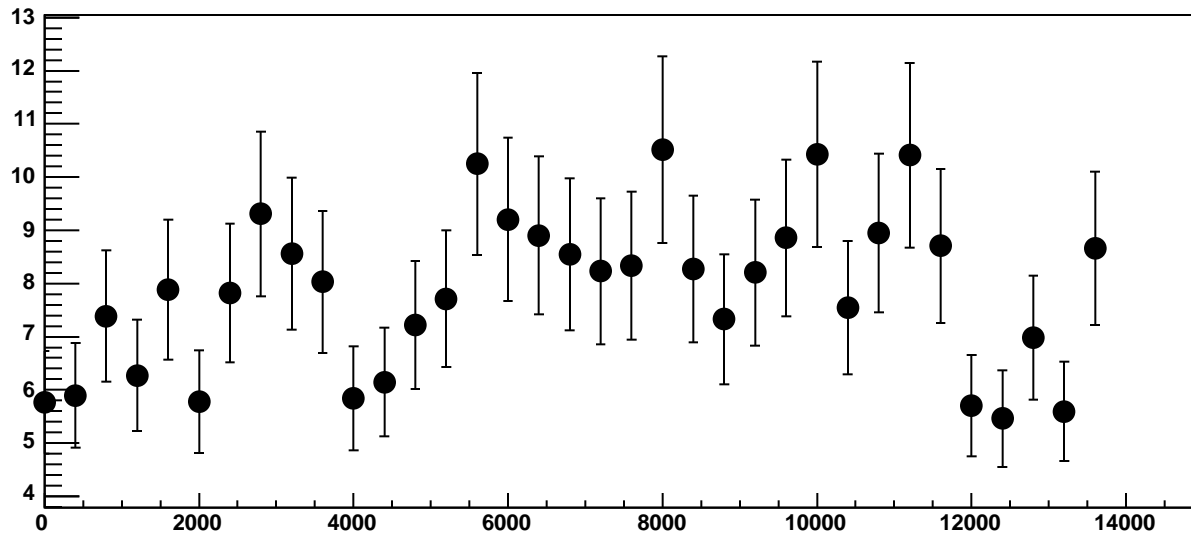
Chip 11, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC



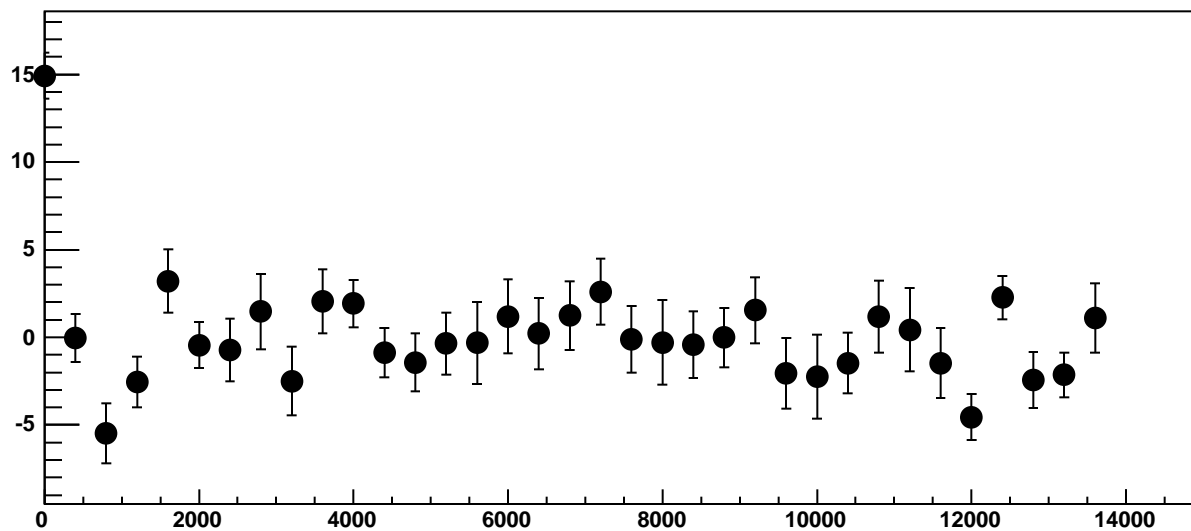
Chip 11, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



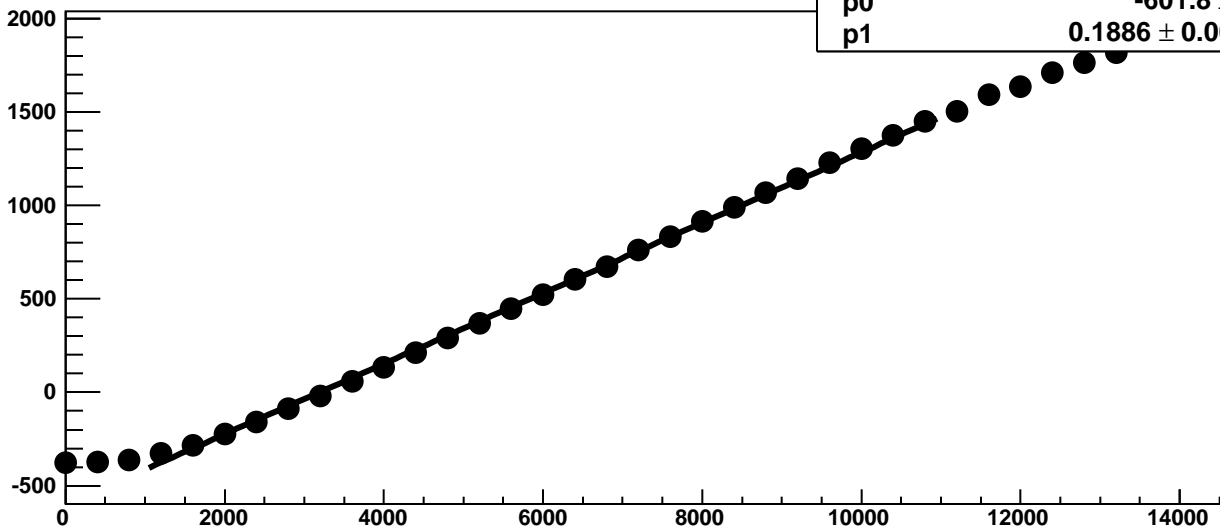
Chip 11, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

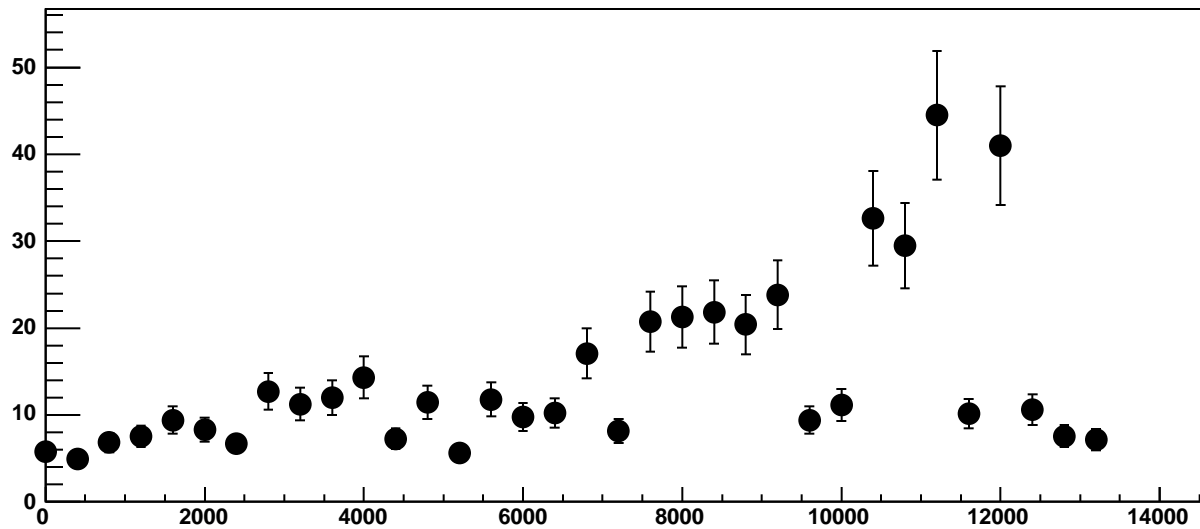


Chip 11, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

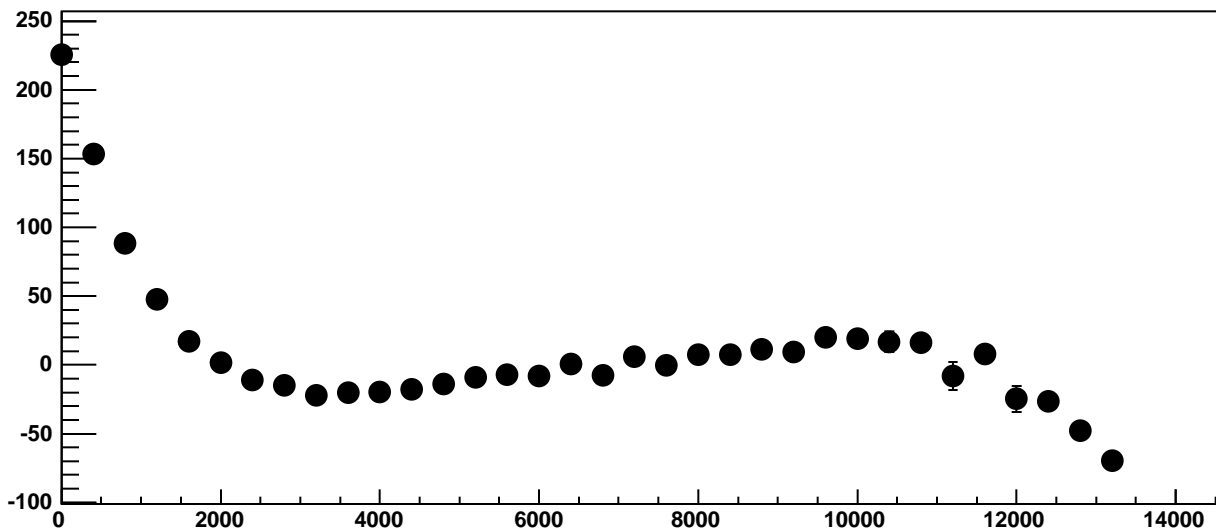


$\chi^2 / \text{ndf}$  1439 / 23  
p0  $-601.8 \pm 1.024$   
p1  $0.1886 \pm 0.0001886$

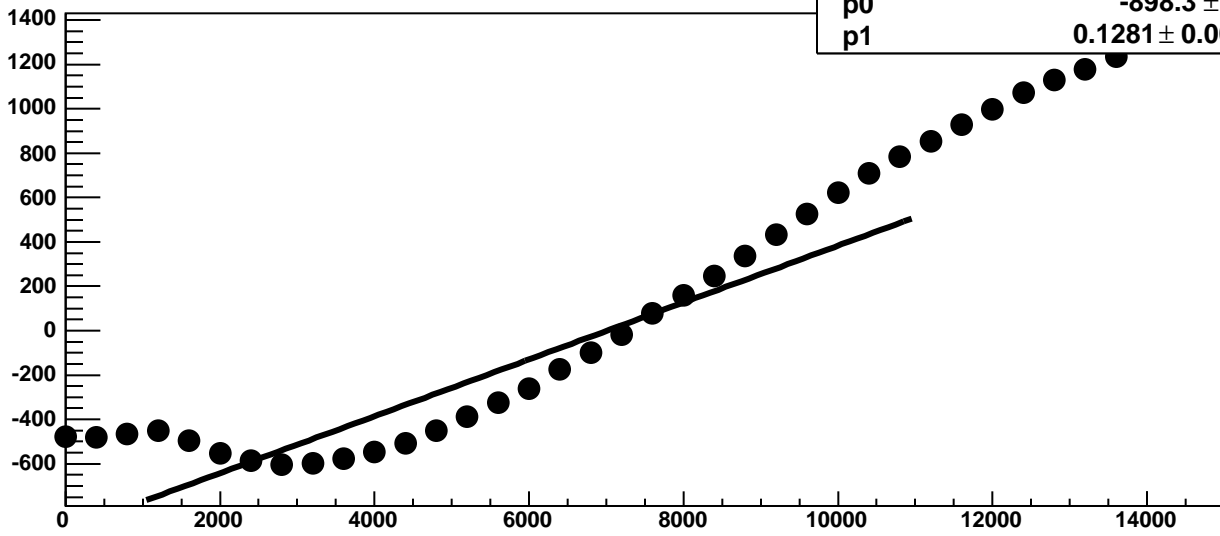
Chip 11, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

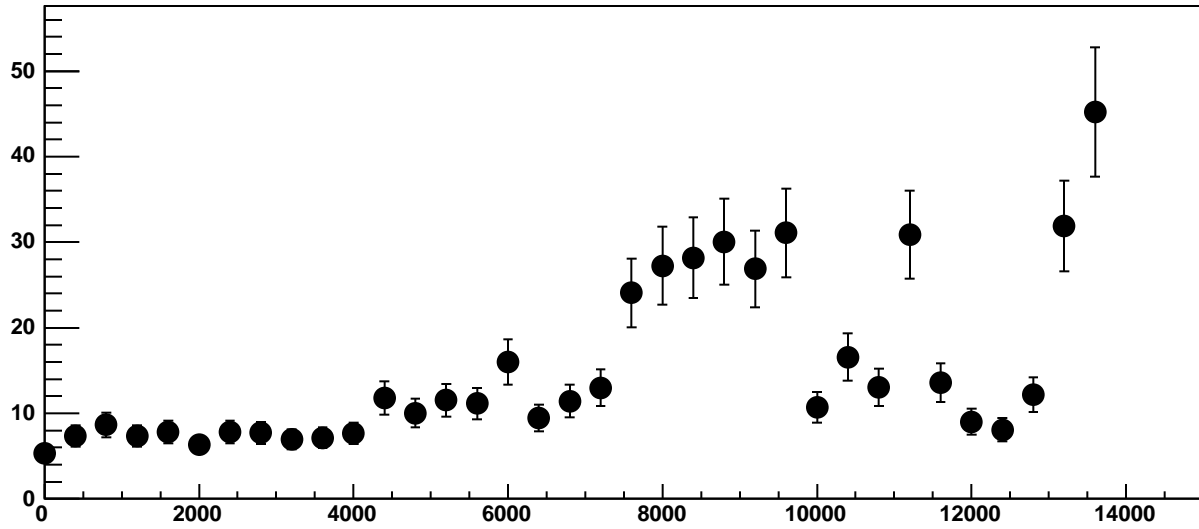


Chip 11, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

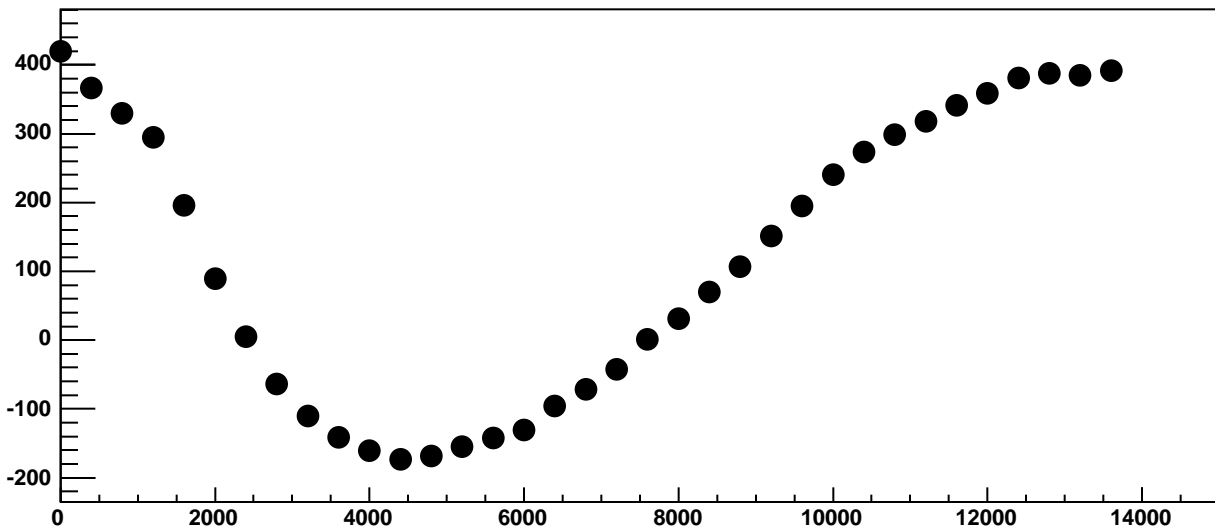


$\chi^2 / \text{ndf}$  1.146e+05 / 23  
p0 -898.3 ± 0.9068  
p1 0.1281 ± 0.0001844

Chip 11, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

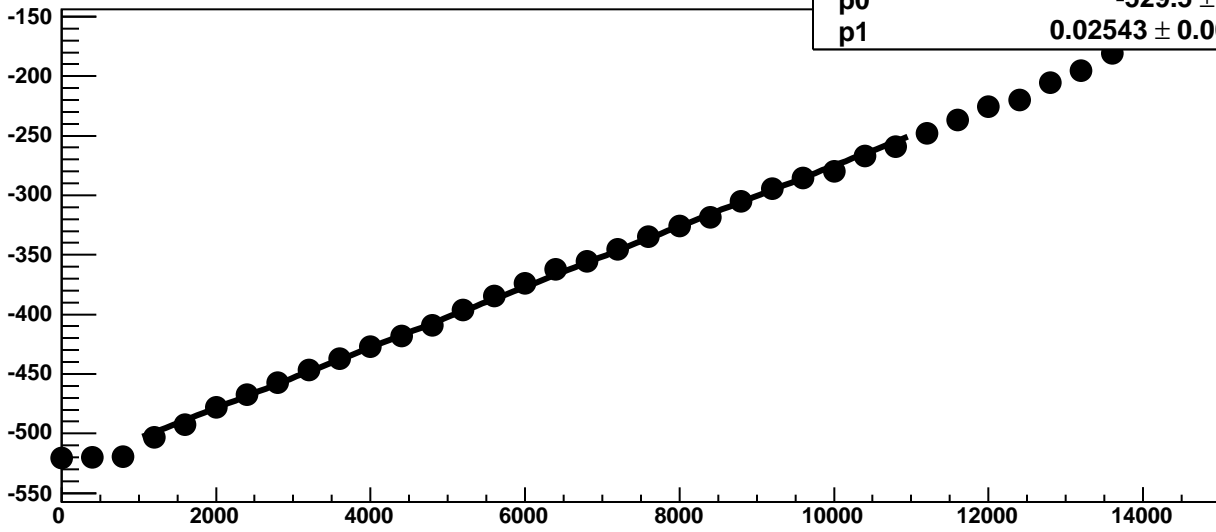


Chip 11, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



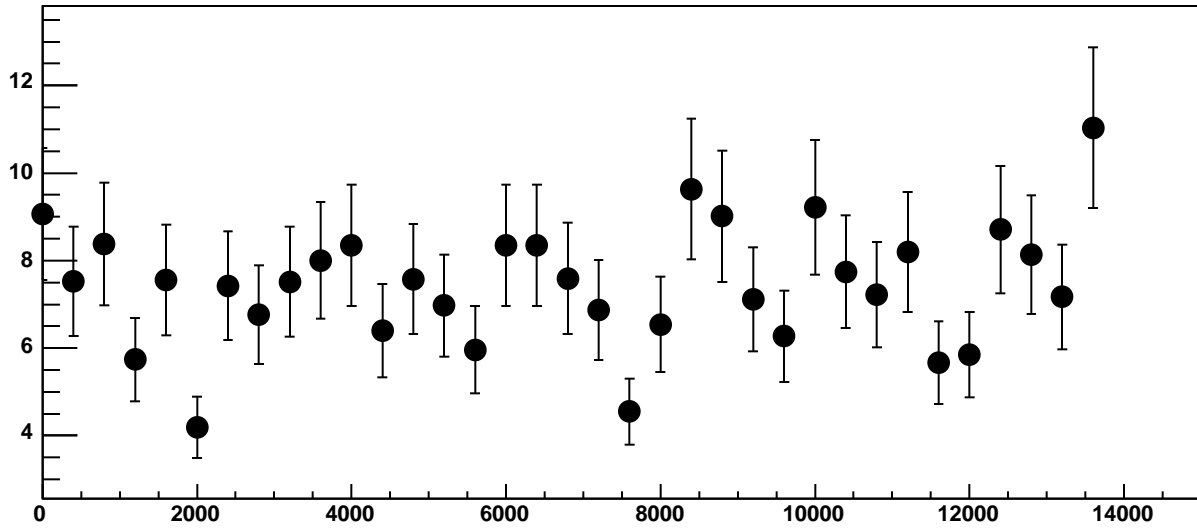


Chip 11, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

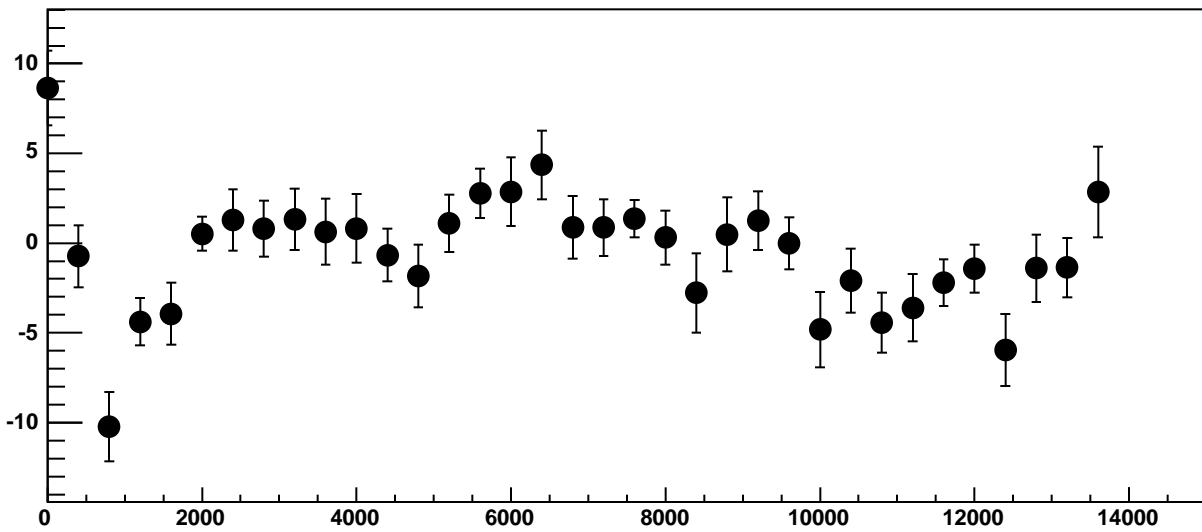


$\chi^2 / \text{ndf}$  49.88 / 23  
p0  $-529.5 \pm 0.6776$   
p1  $0.02543 \pm 0.0001066$

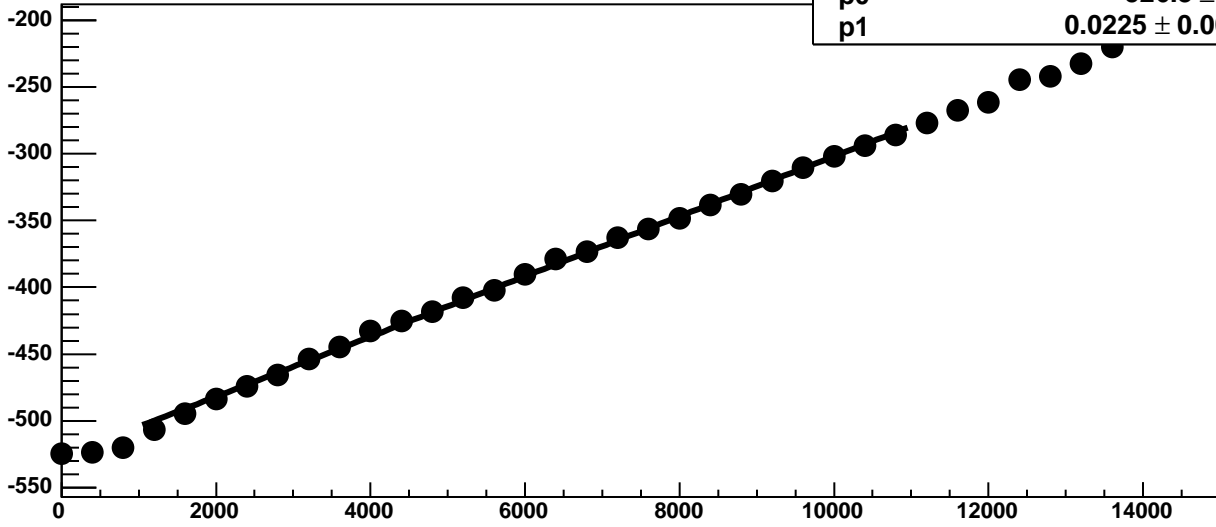
Chip 11, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



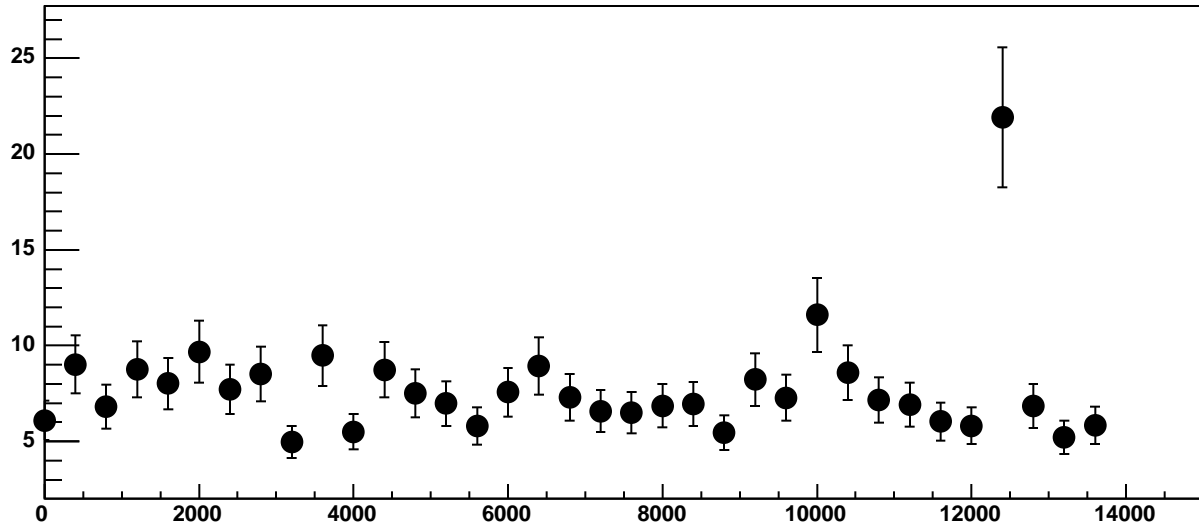
Chip 11, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



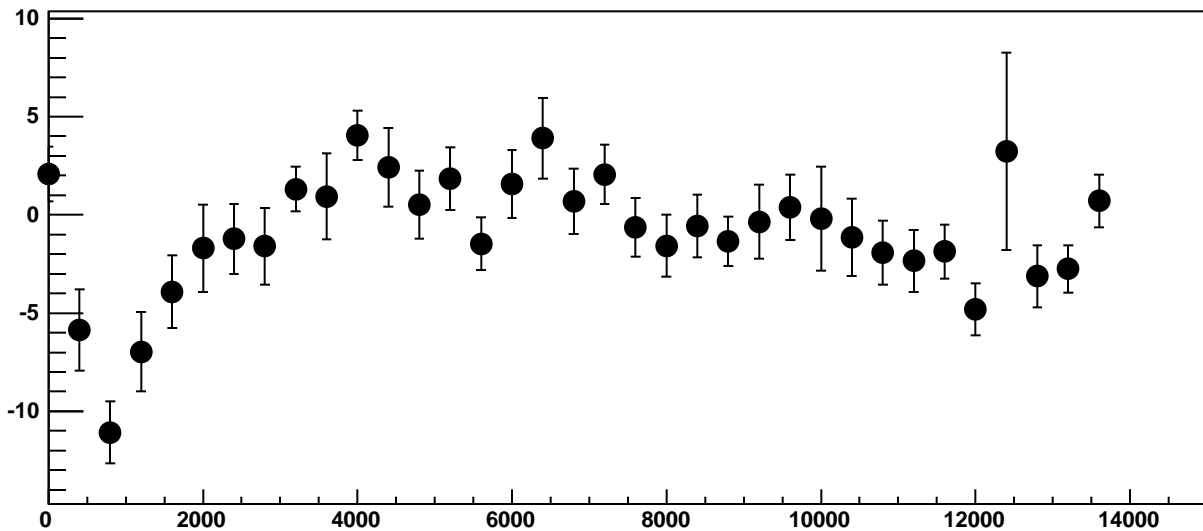
Chip 11, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



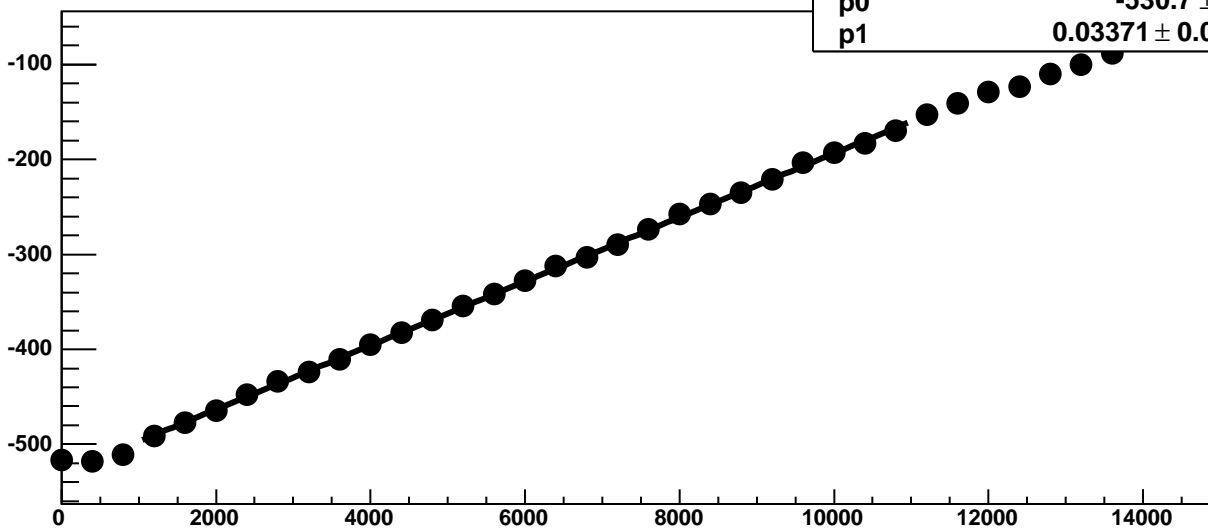
Chip 11, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

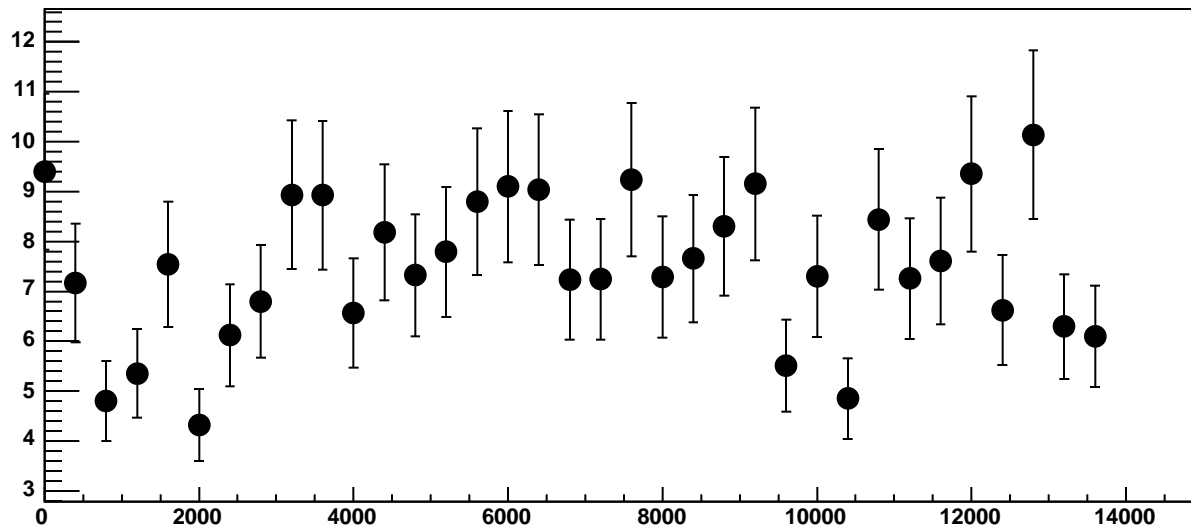


Chip 11, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

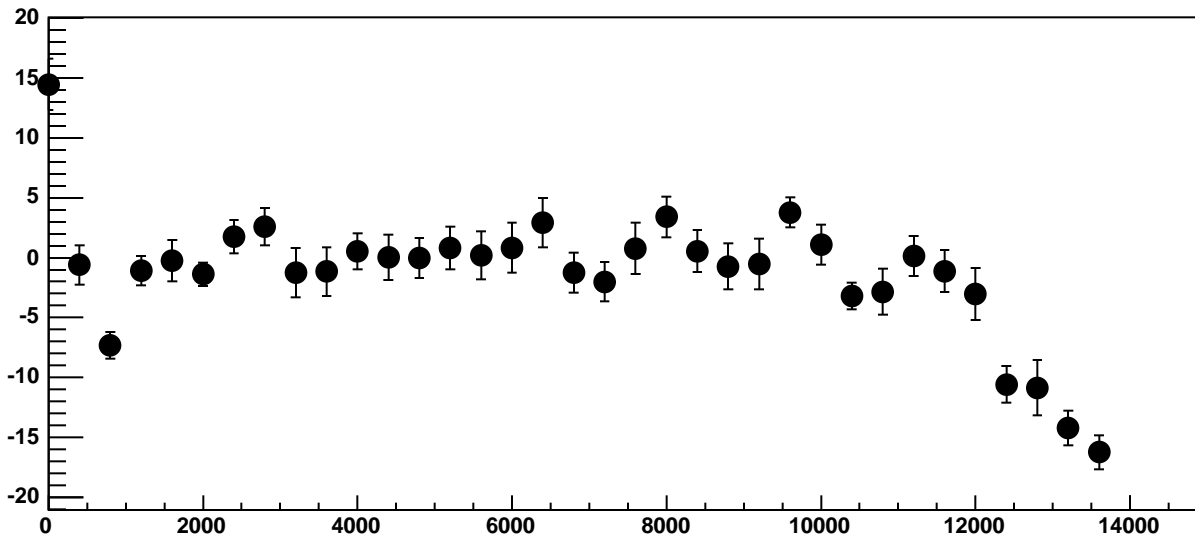


$\chi^2 / \text{ndf}$  36.57 / 23  
p0  $-530.7 \pm 0.6593$   
p1  $0.03371 \pm 0.0001003$

Chip 11, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

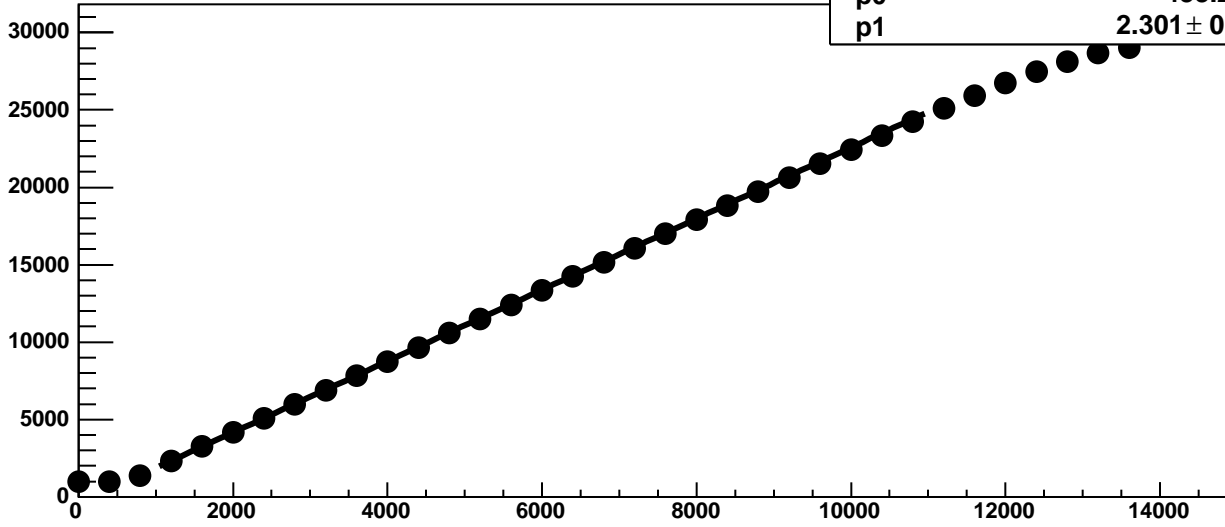


Chip 11, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

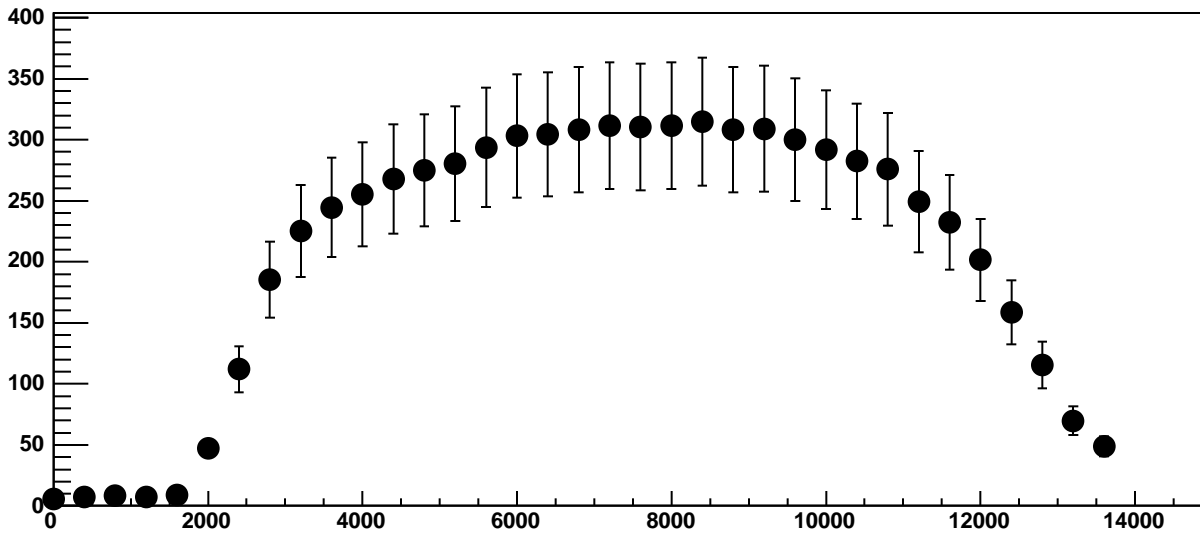


Chip 11, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

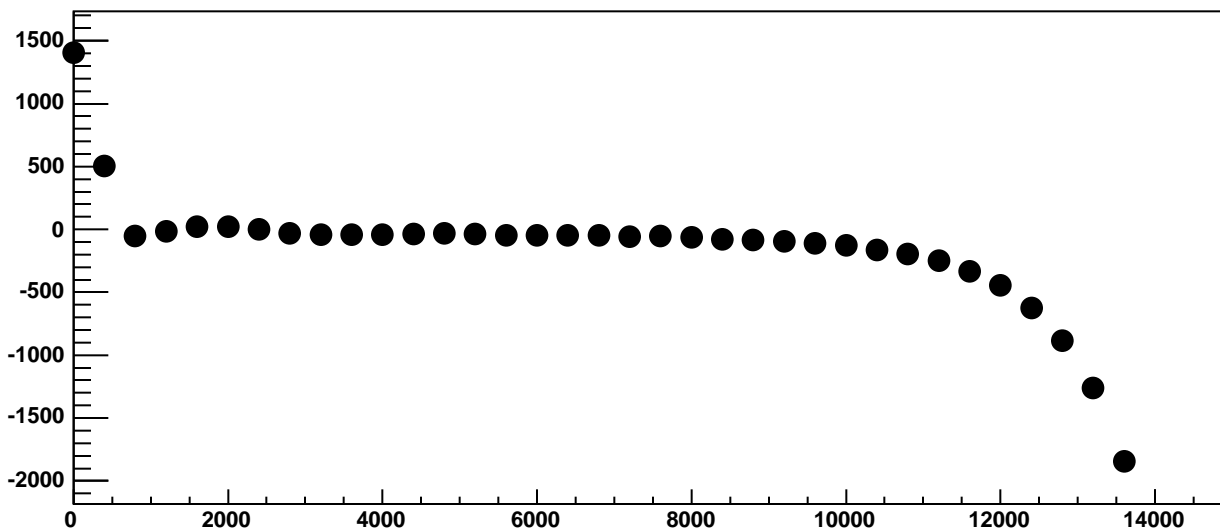
$\chi^2 / \text{ndf}$  225.8 / 23  
p0  $-433.2 \pm 3.48$   
p1  $2.301 \pm 0.002294$



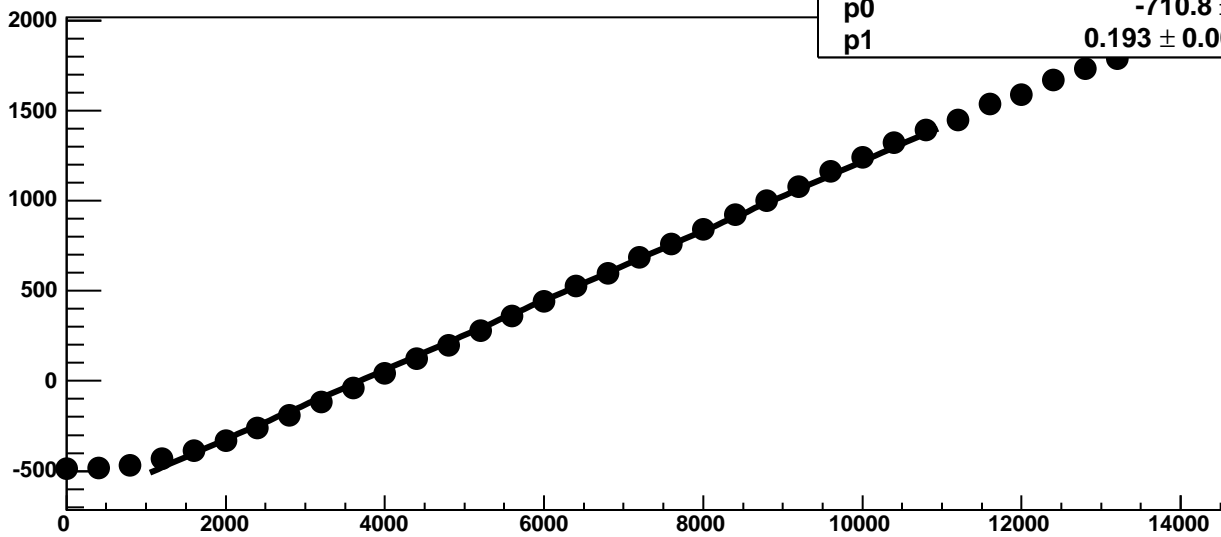
Chip 11, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

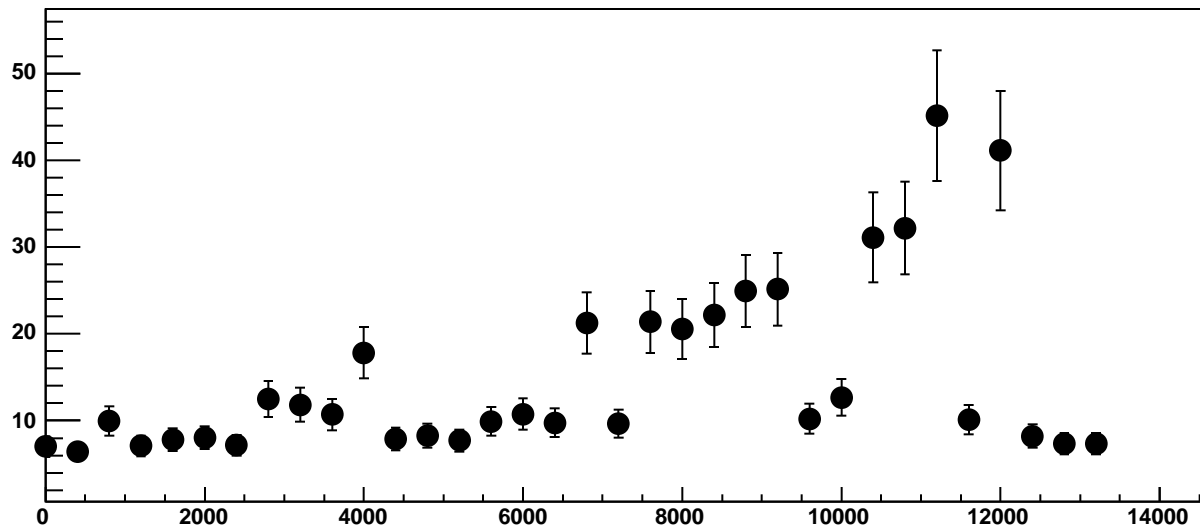


Chip 11, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

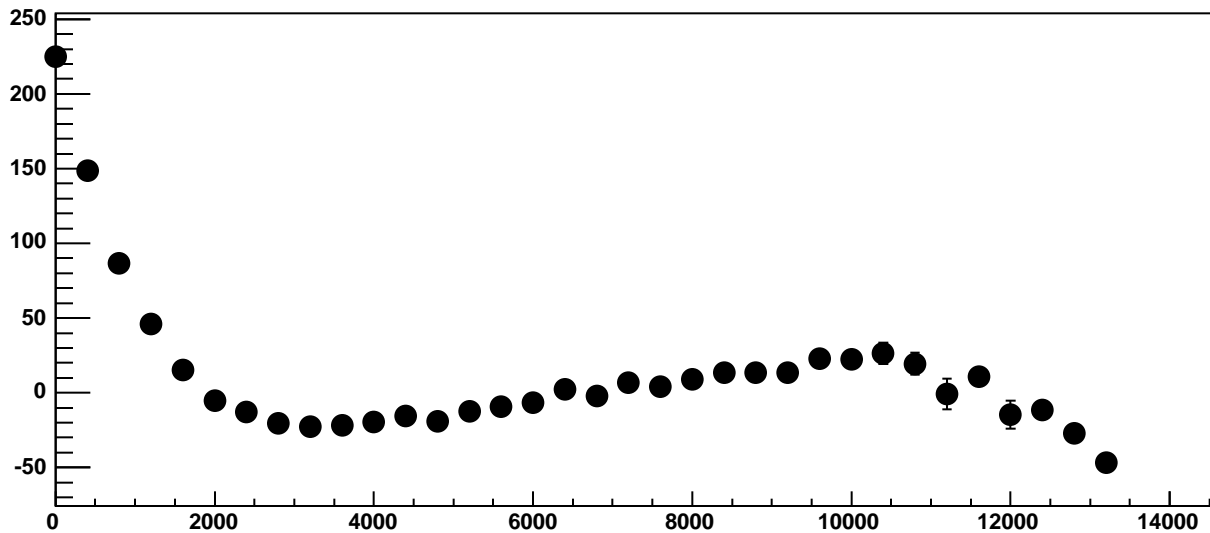


$\chi^2 / \text{ndf}$  1623 / 23  
p0  $-710.8 \pm 1.006$   
p1  $0.193 \pm 0.0001937$

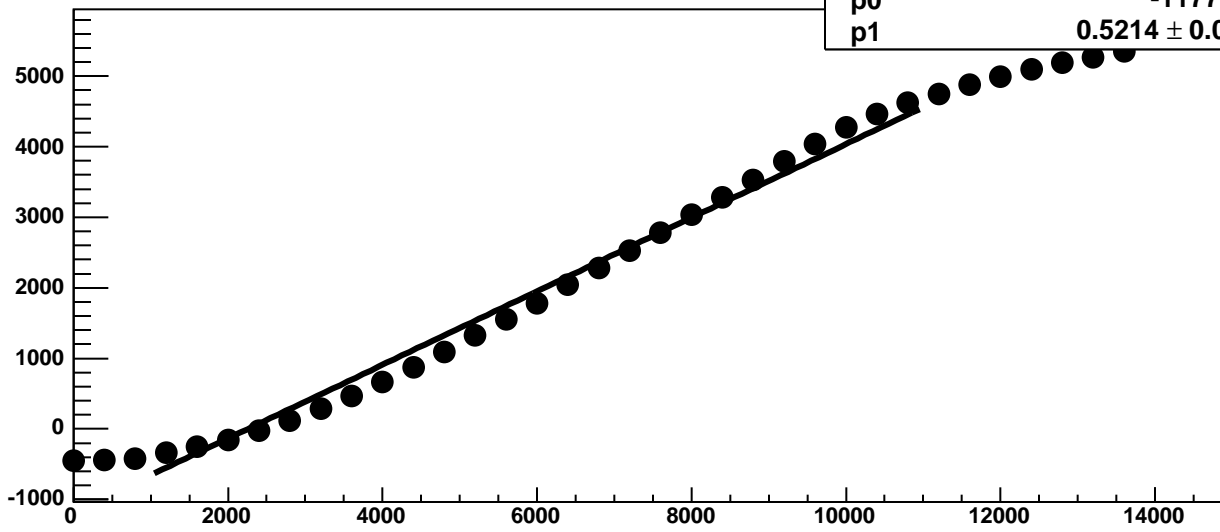
Chip 11, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



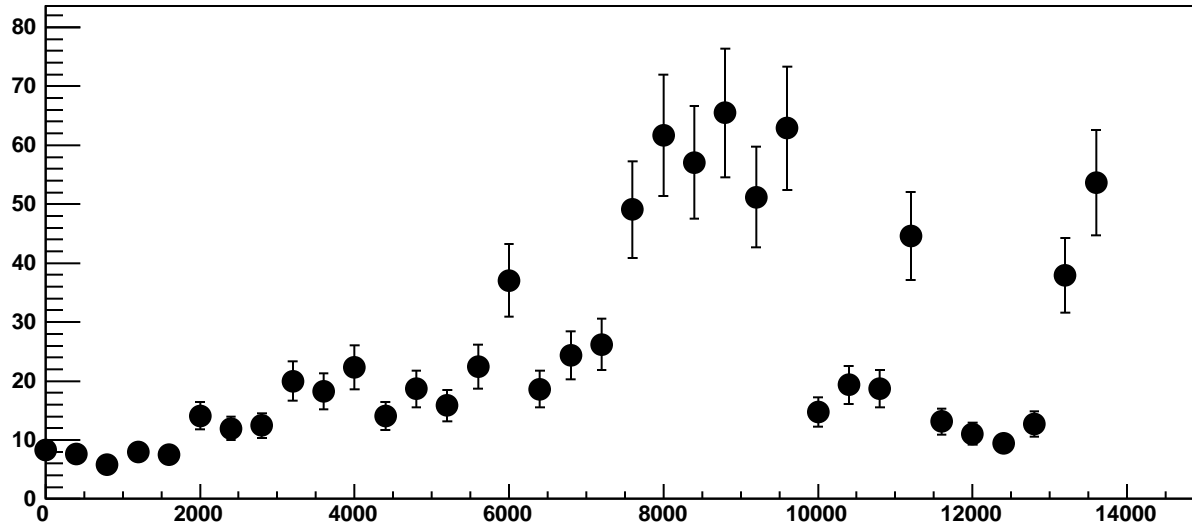
Chip 11, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



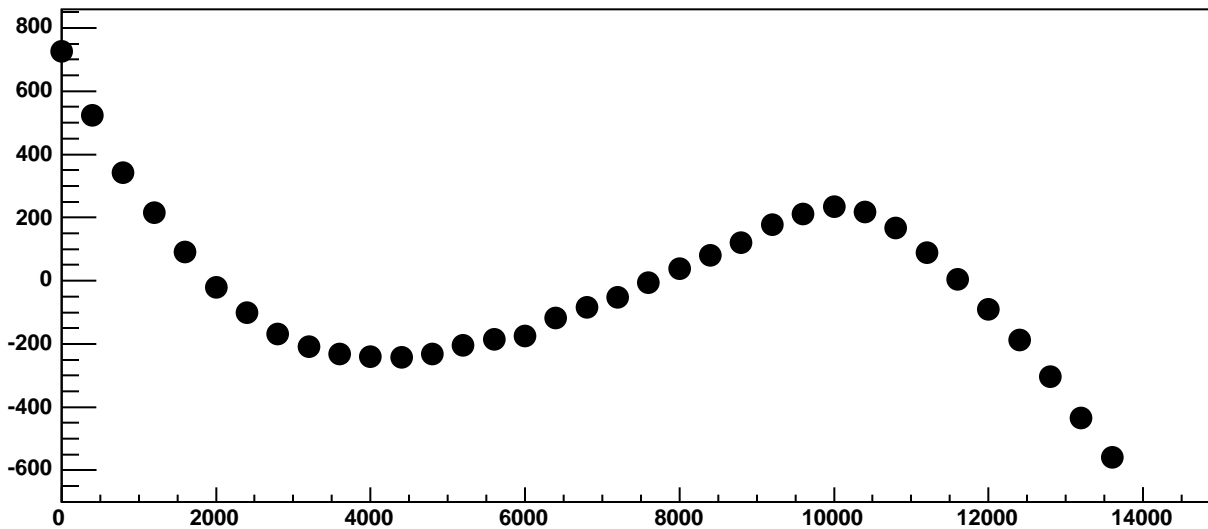
Chip 11, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



Chip 11, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

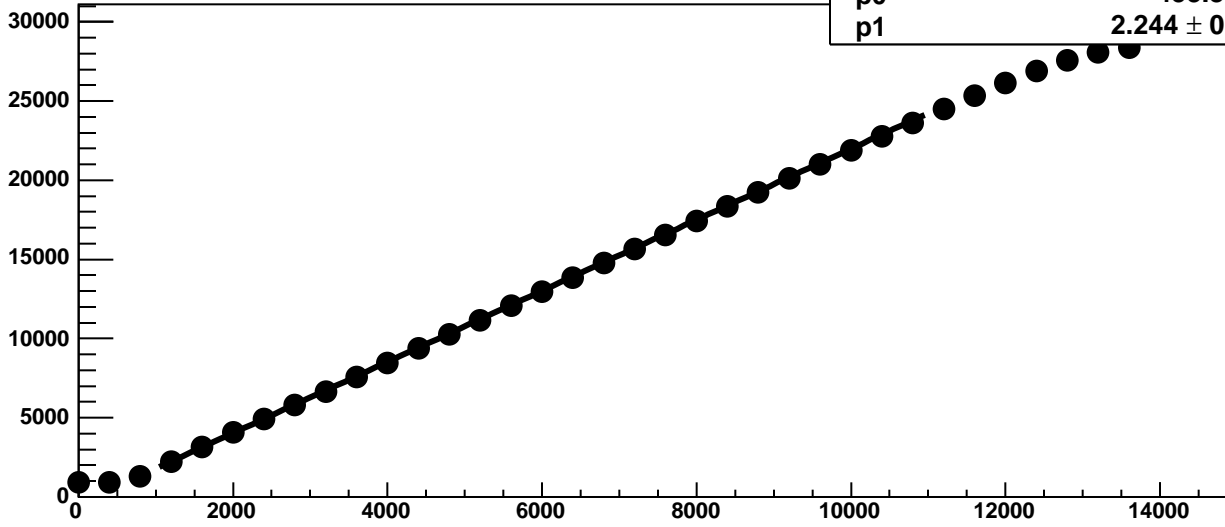


Chip 11, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

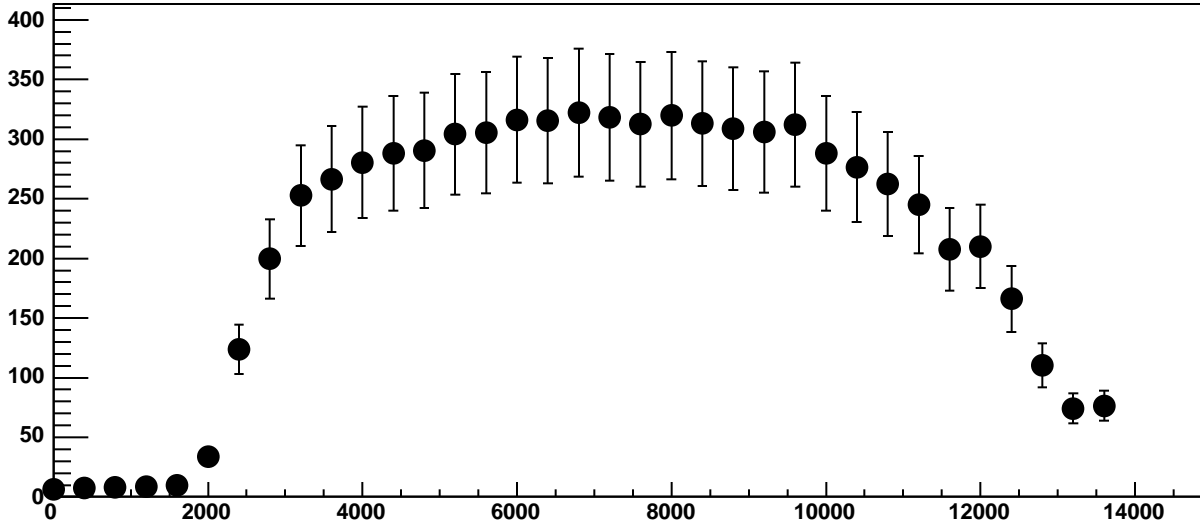


Chip 11, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC

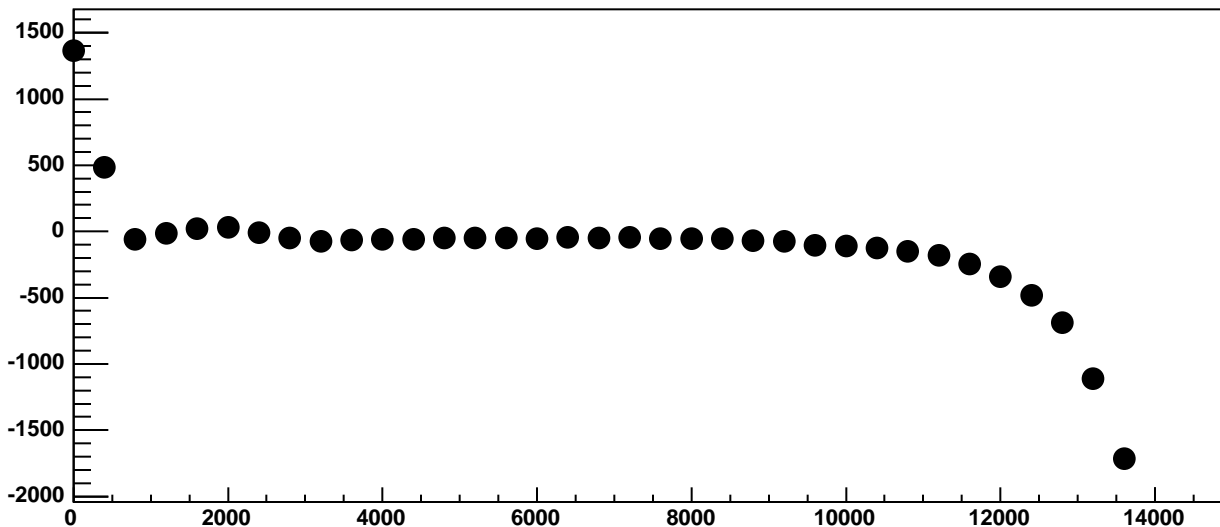
$\chi^2 / \text{ndf}$  192.2 / 23  
p0  $-453.8 \pm 3.654$   
p1  $2.244 \pm 0.002328$



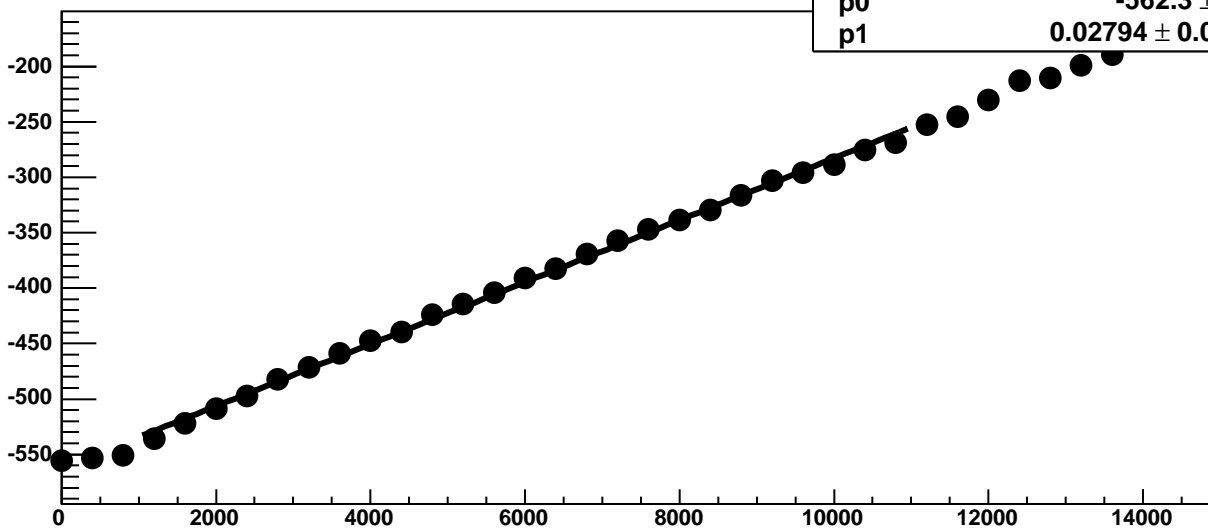
Chip 11, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC



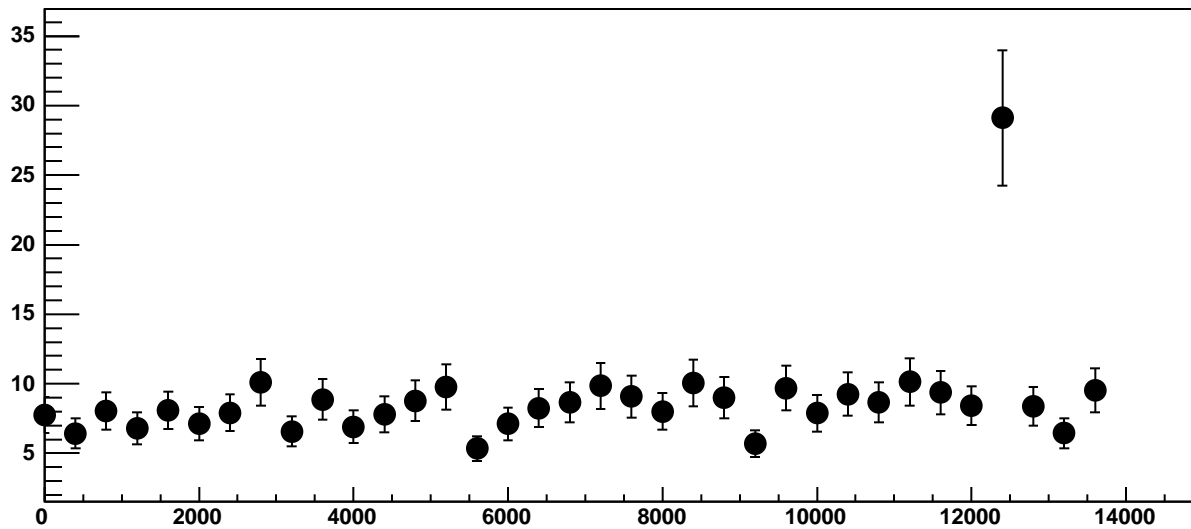
Chip 11, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC



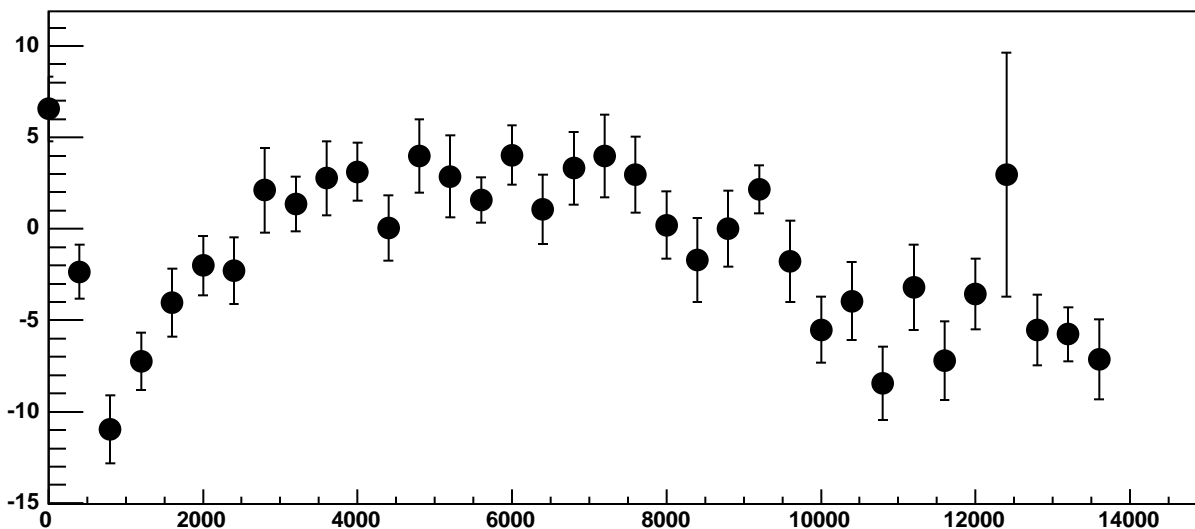
Chip 11, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



Chip 11, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

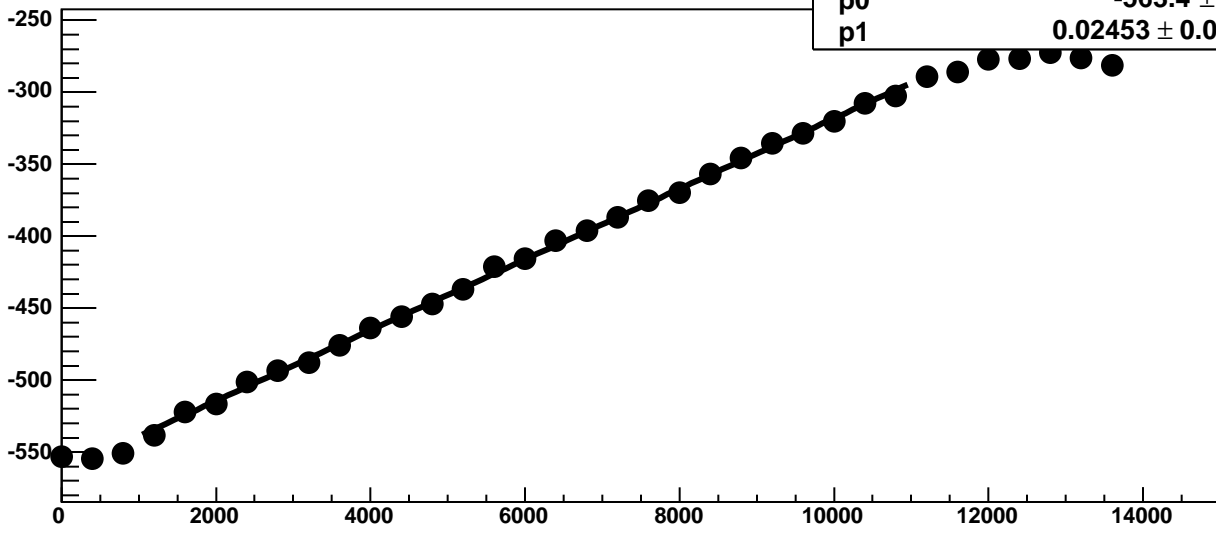


Chip 11, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

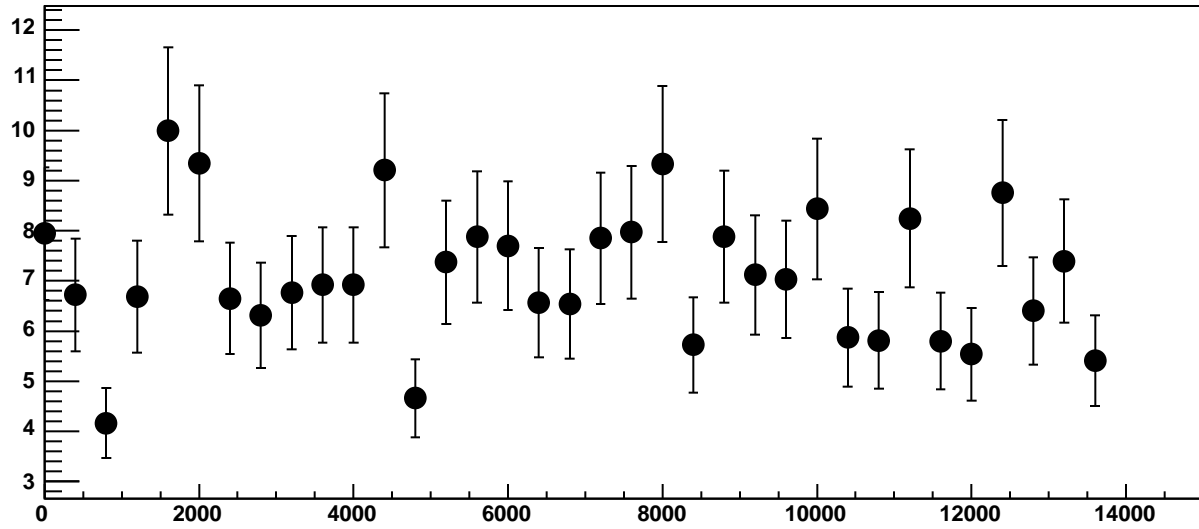




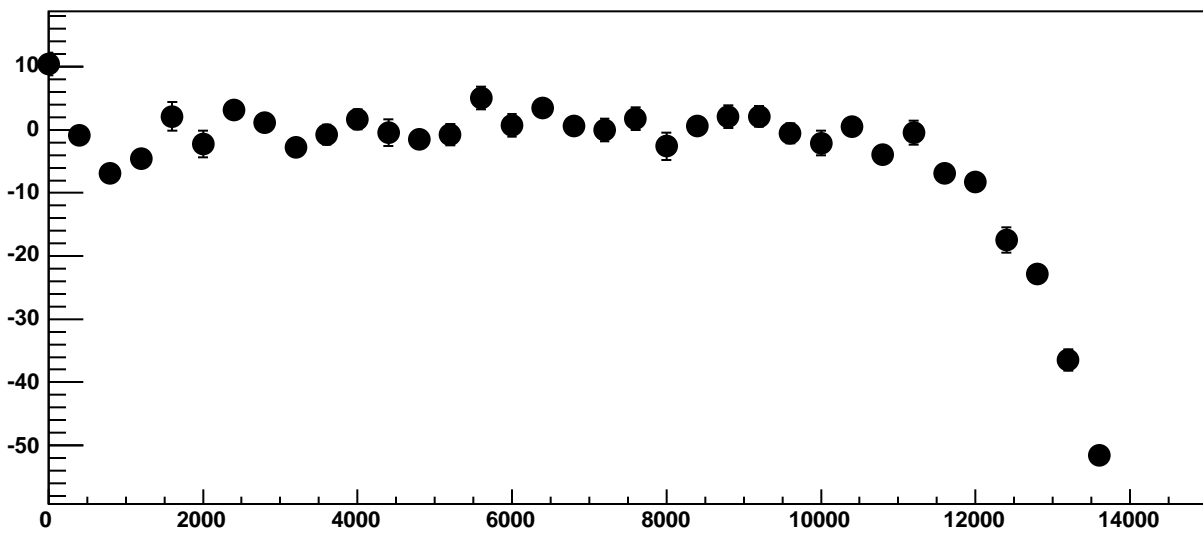
Chip 11, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



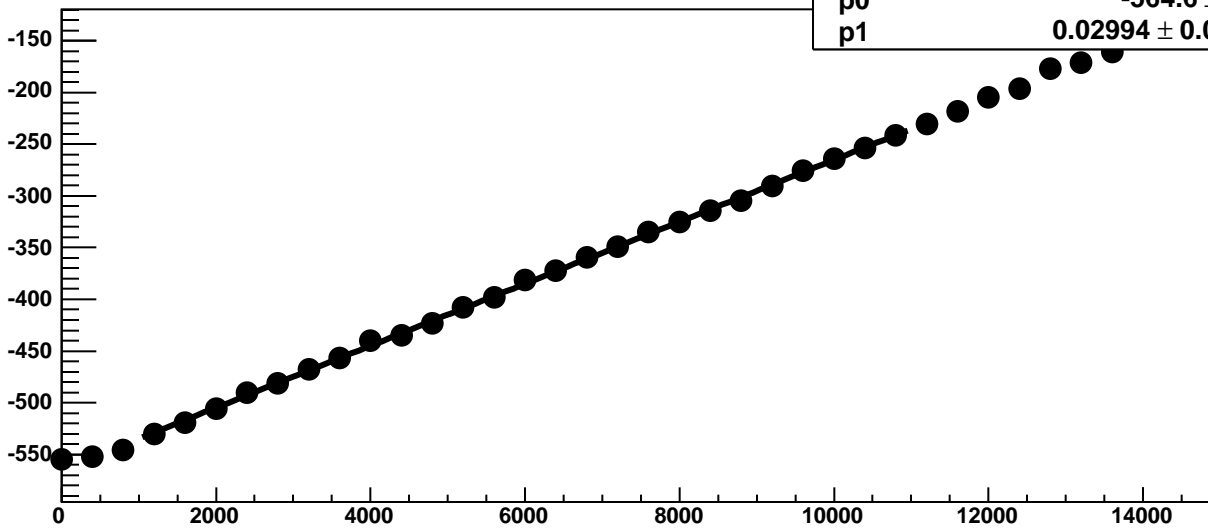
Chip 11, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

33.8 / 23

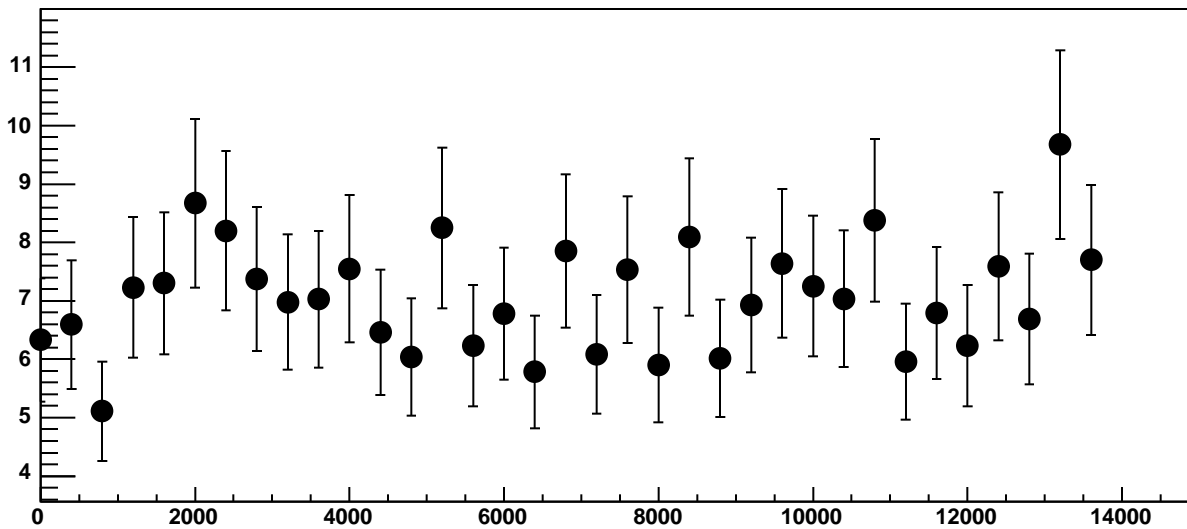
p0

$-564.6 \pm 0.7821$

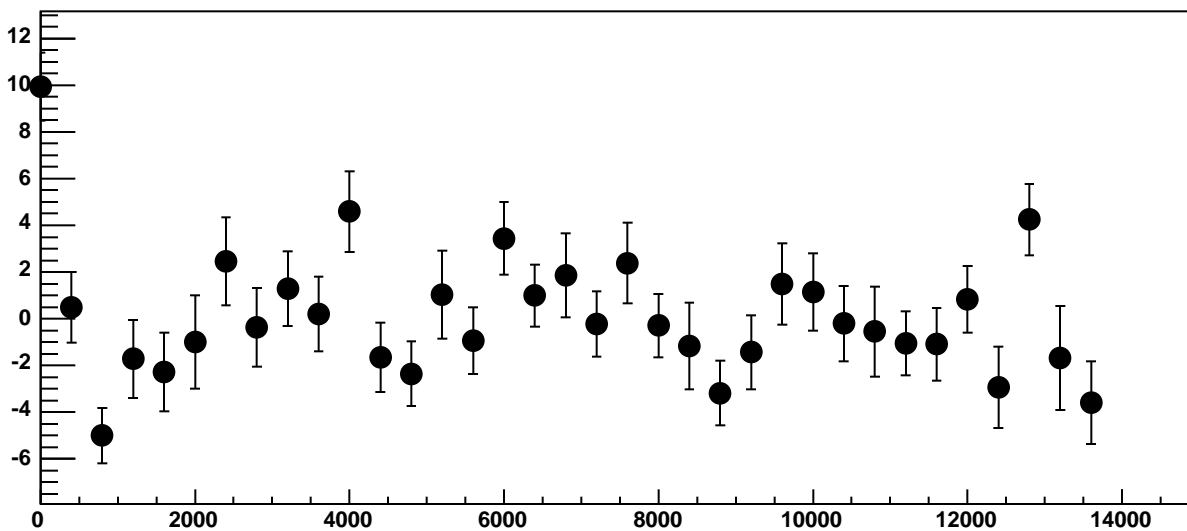
p1

$0.02994 \pm 0.0001172$

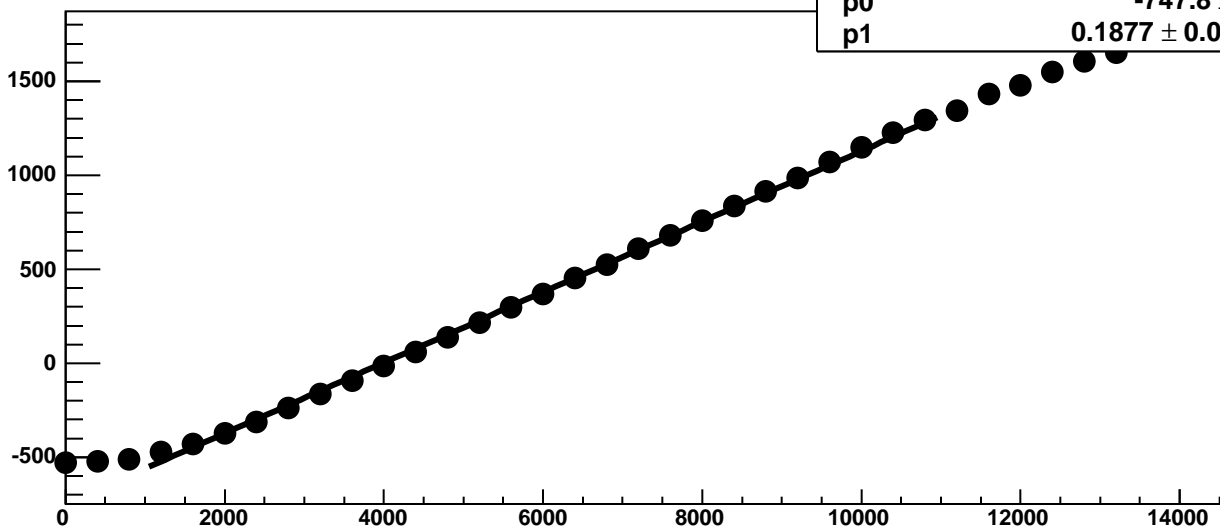
Chip 11, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



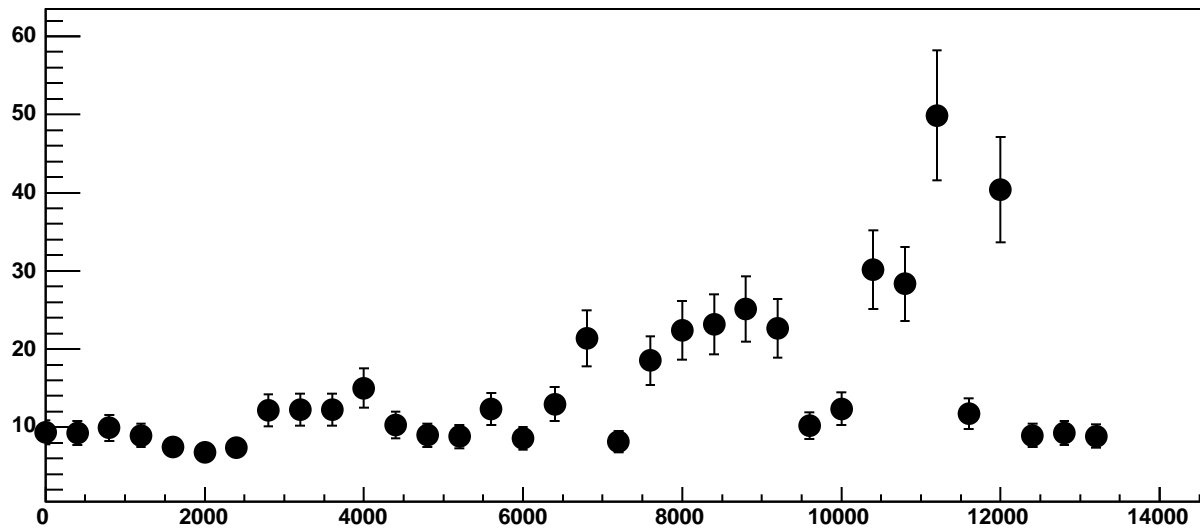
Chip 11, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



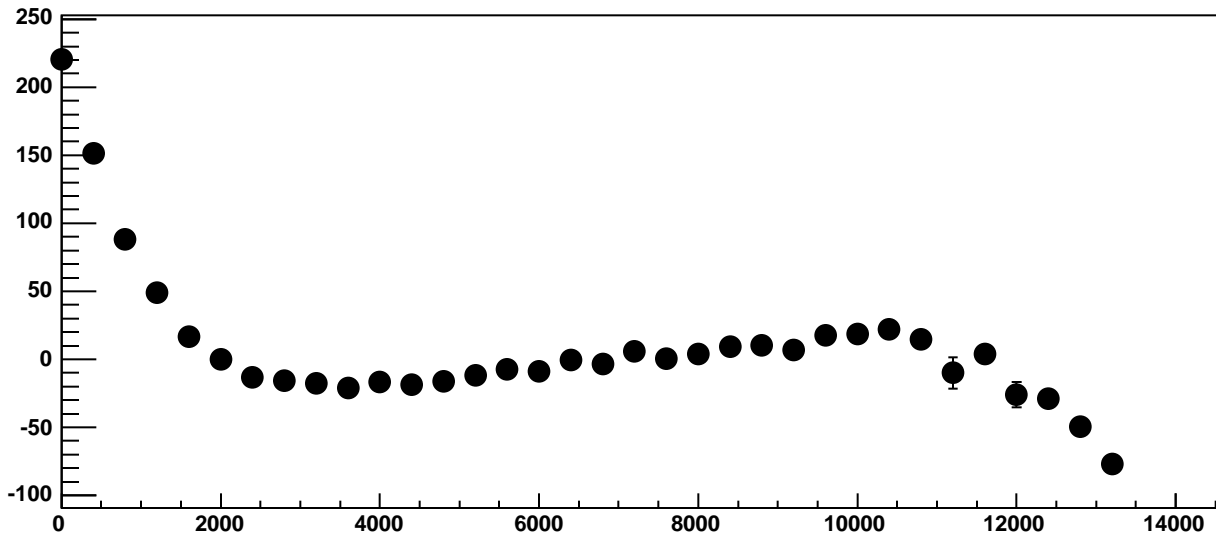
Chip 11, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



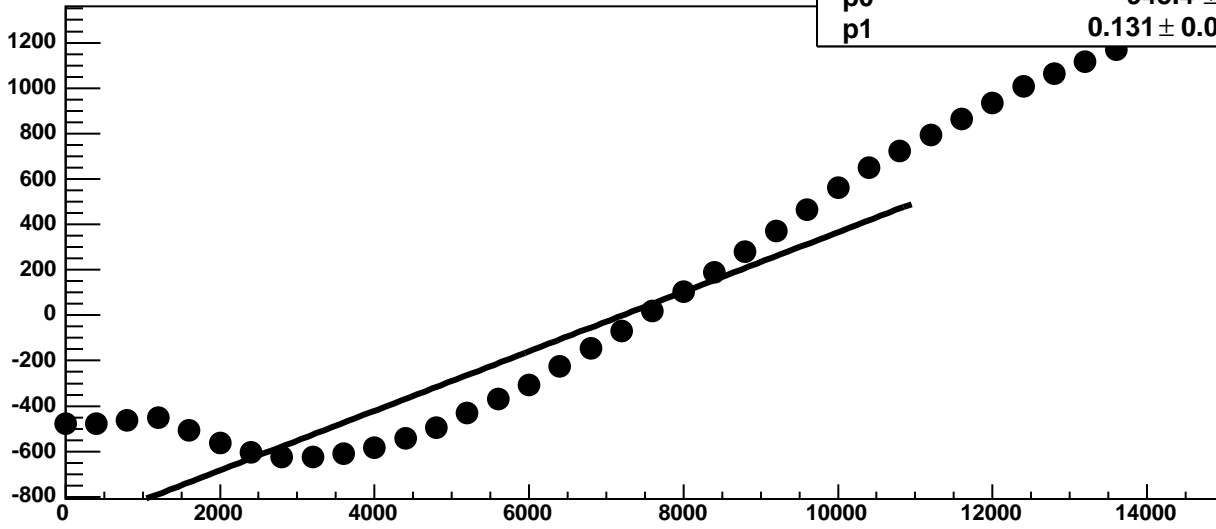
Chip 11, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

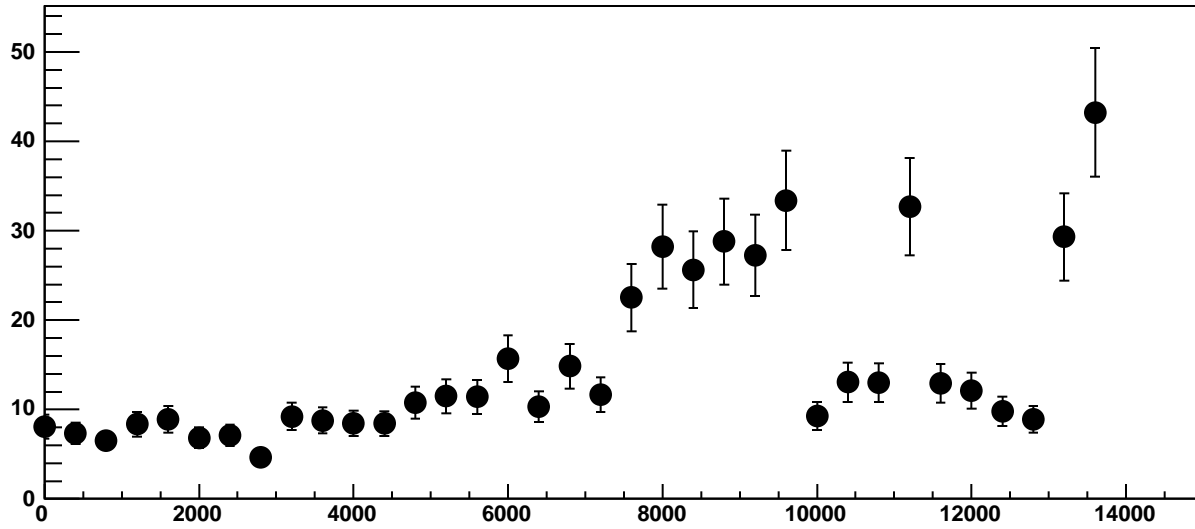


Chip 11, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

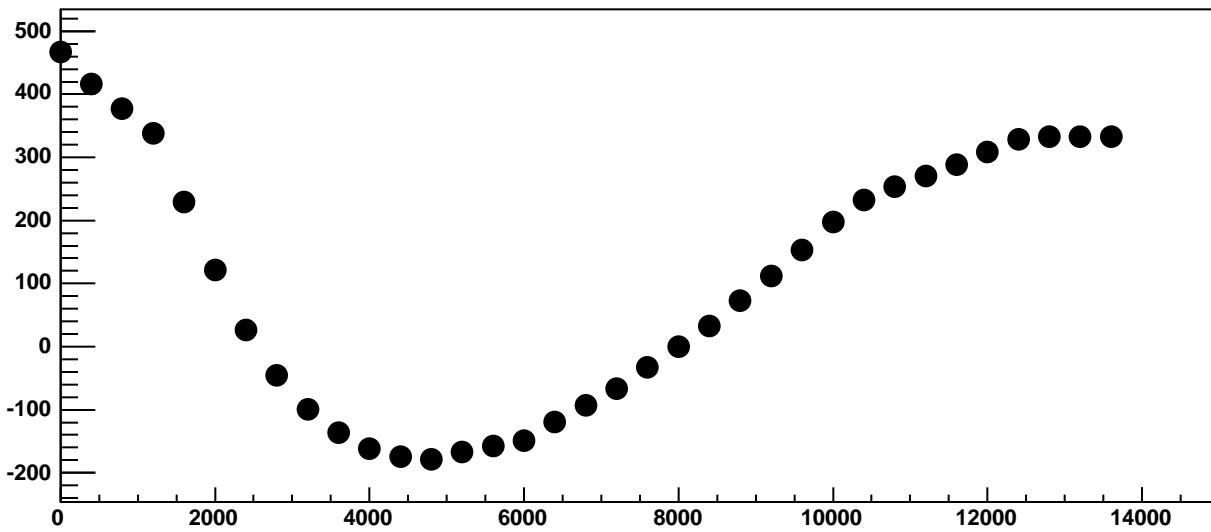


$\chi^2 / \text{ndf}$  1.147e+05 / 23  
p0 -945.4 ± 0.8857  
p1 0.131 ± 0.0001762

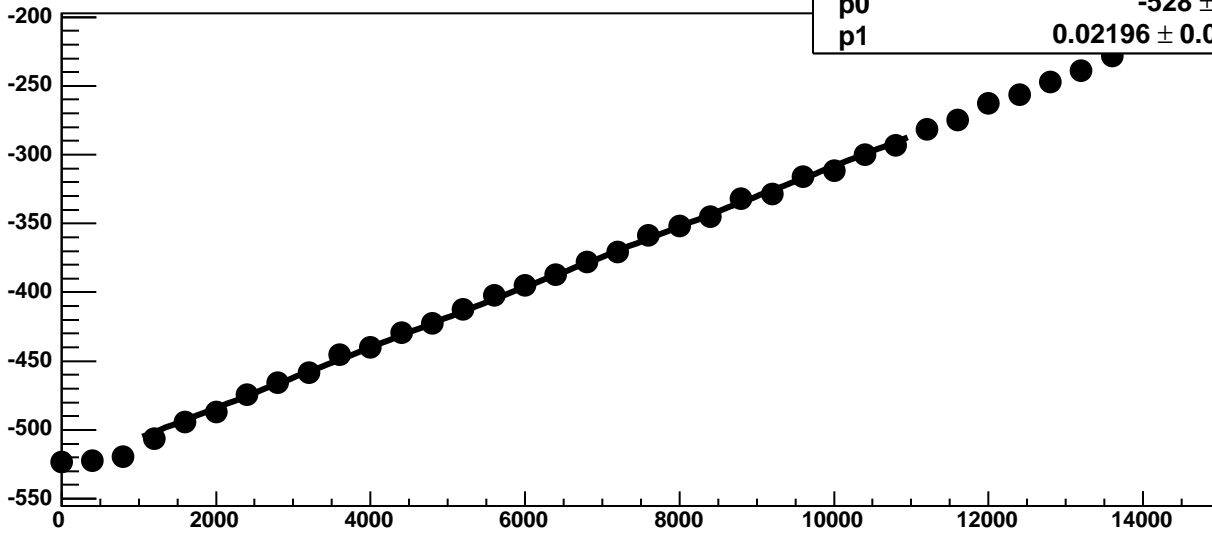
Chip 11, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

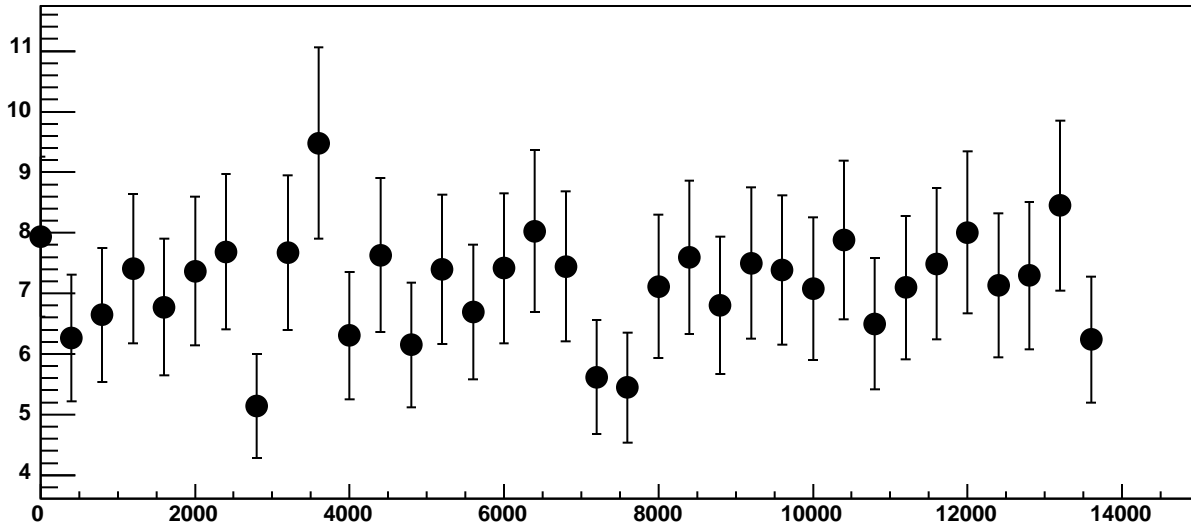


Chip 11, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

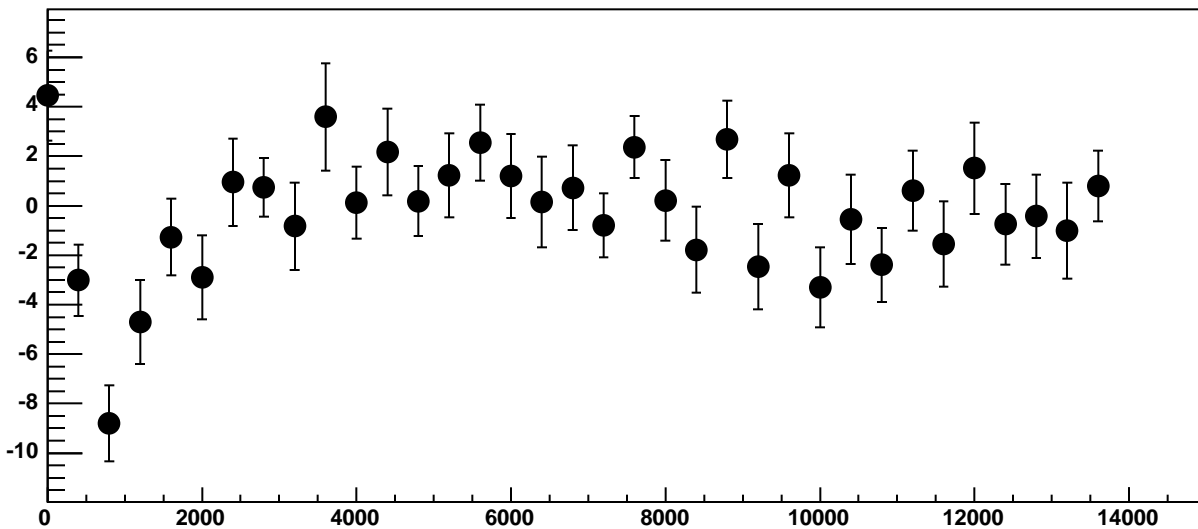


$\chi^2 / \text{ndf}$  37.79 / 23  
p0  $-528 \pm 0.7399$   
p1  $0.02196 \pm 0.0001116$

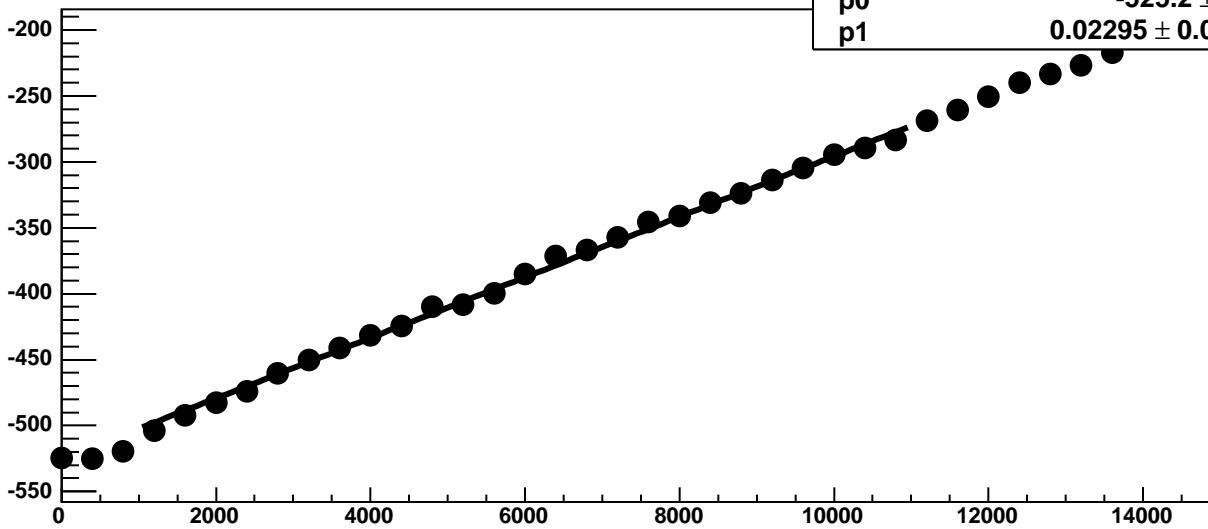
Chip 11, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

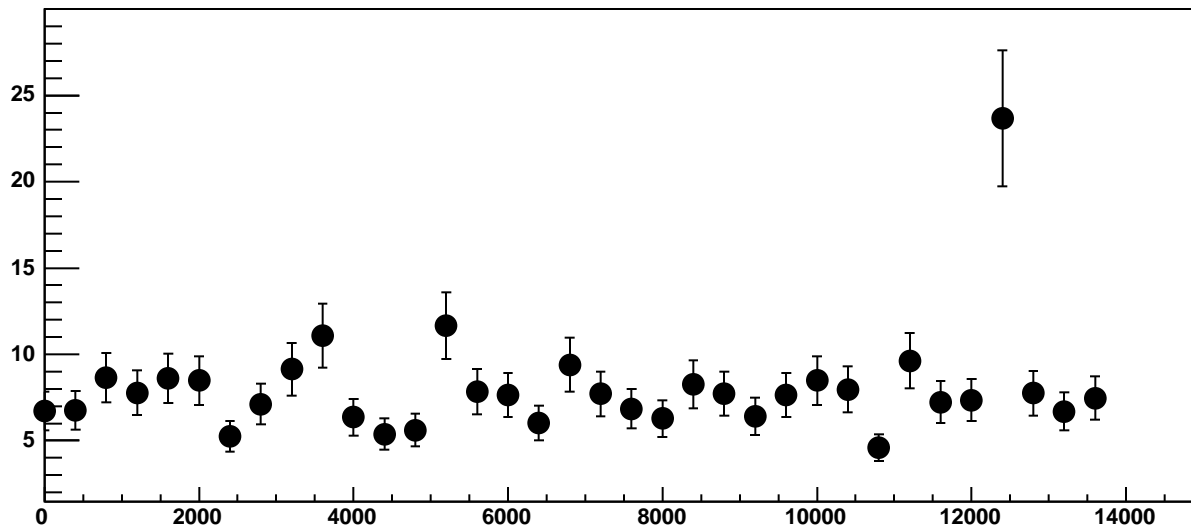


Chip 11, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

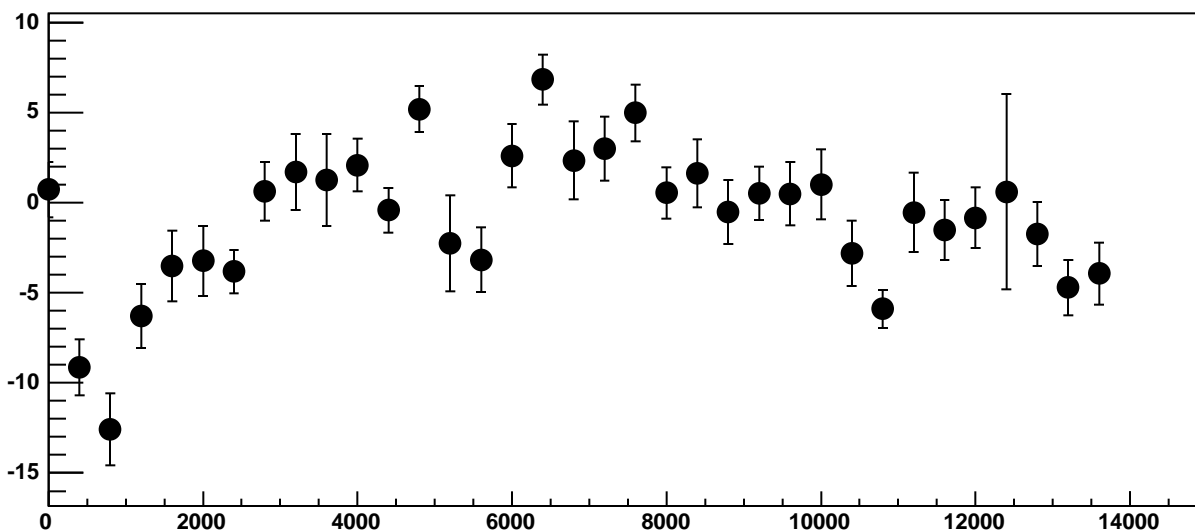


$\chi^2 / \text{ndf}$  127.5 / 23  
p0  $-525.2 \pm 0.7518$   
p1  $0.02295 \pm 0.0001094$

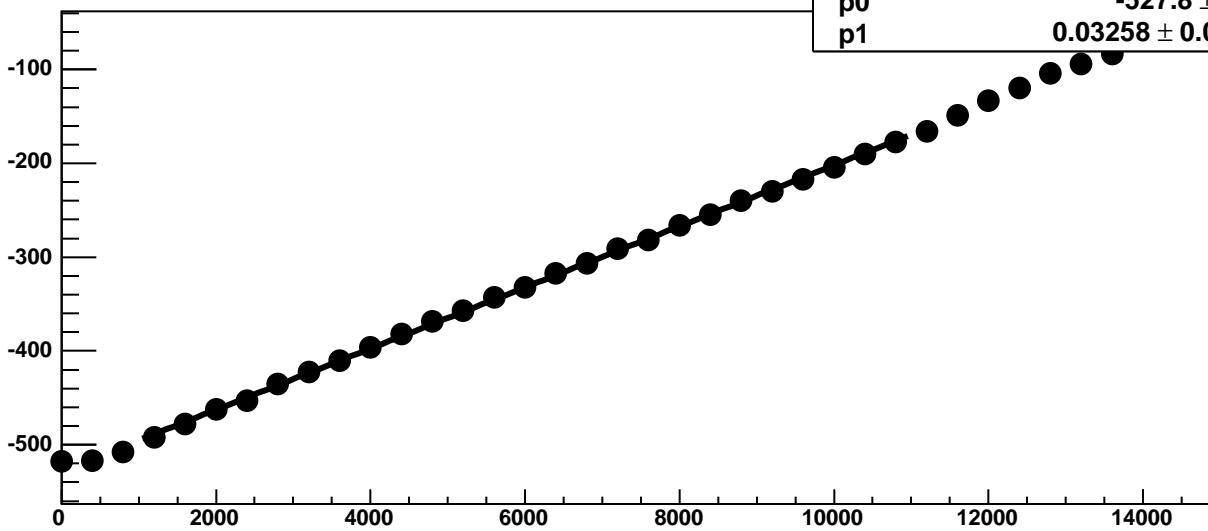
Chip 11, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

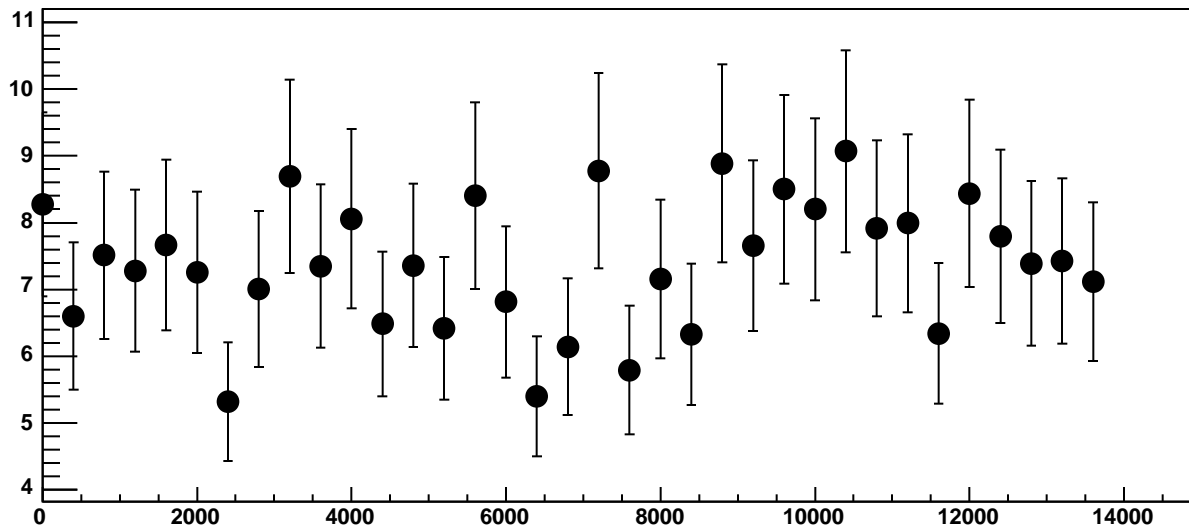


Chip 11, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

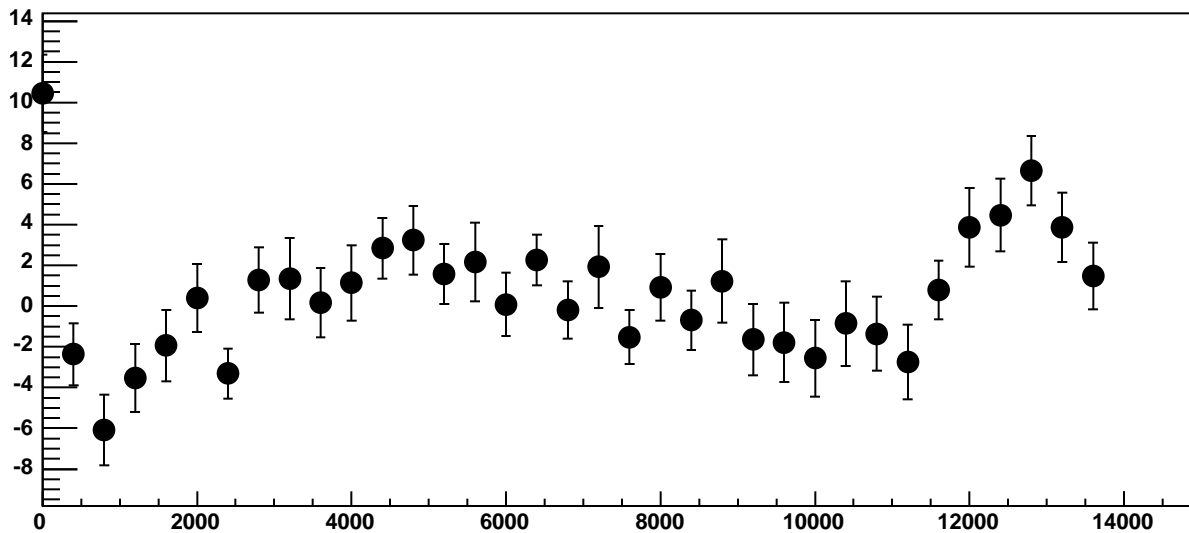


$\chi^2 / \text{ndf}$  35.09 / 23  
p0  $-527.8 \pm 0.7615$   
p1  $0.03258 \pm 0.0001189$

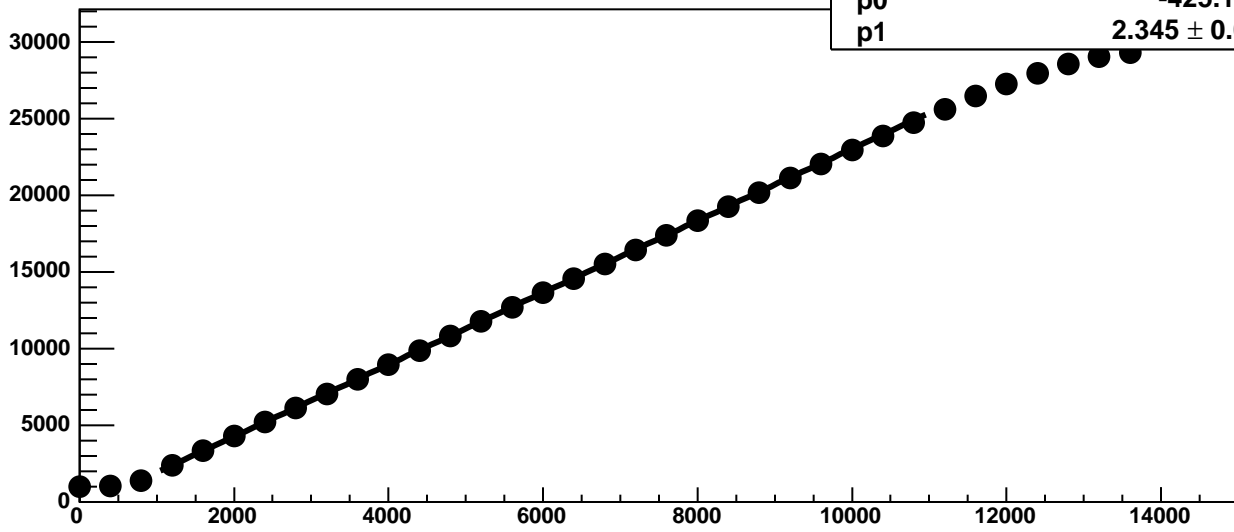
Chip 11, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



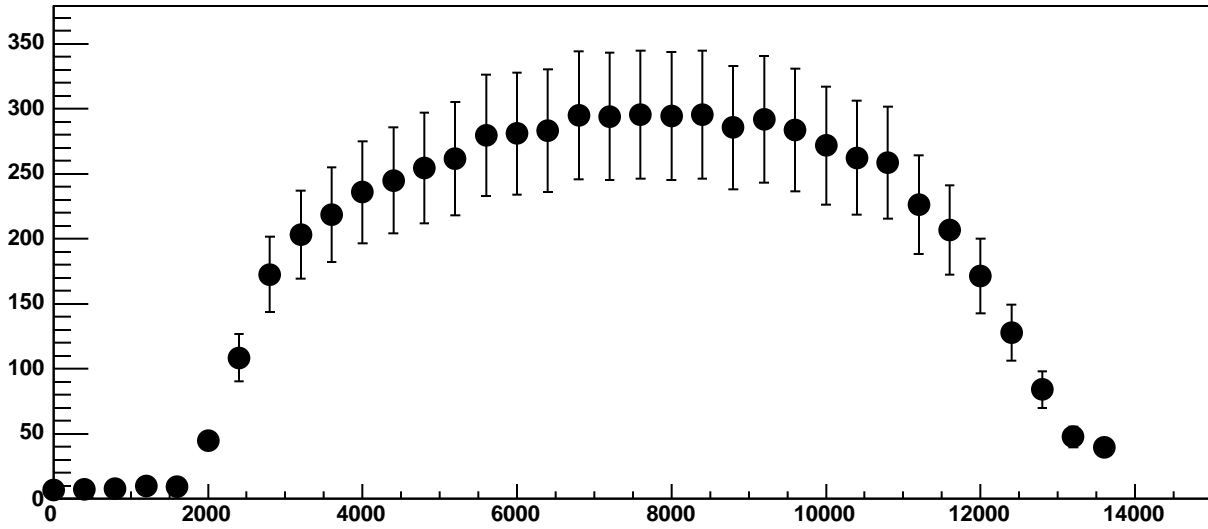
Chip 11, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



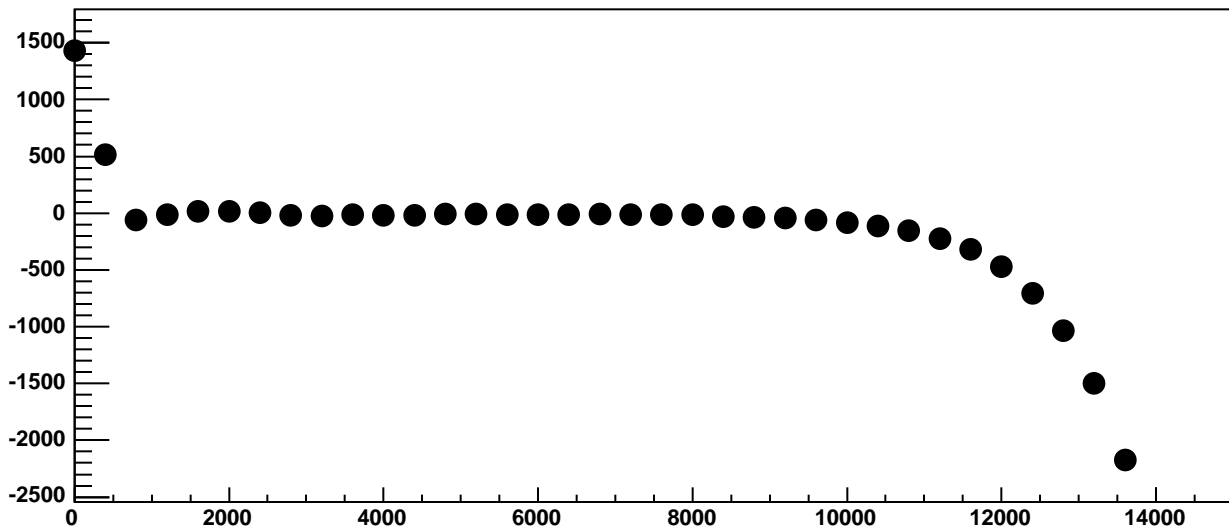
Chip 11, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

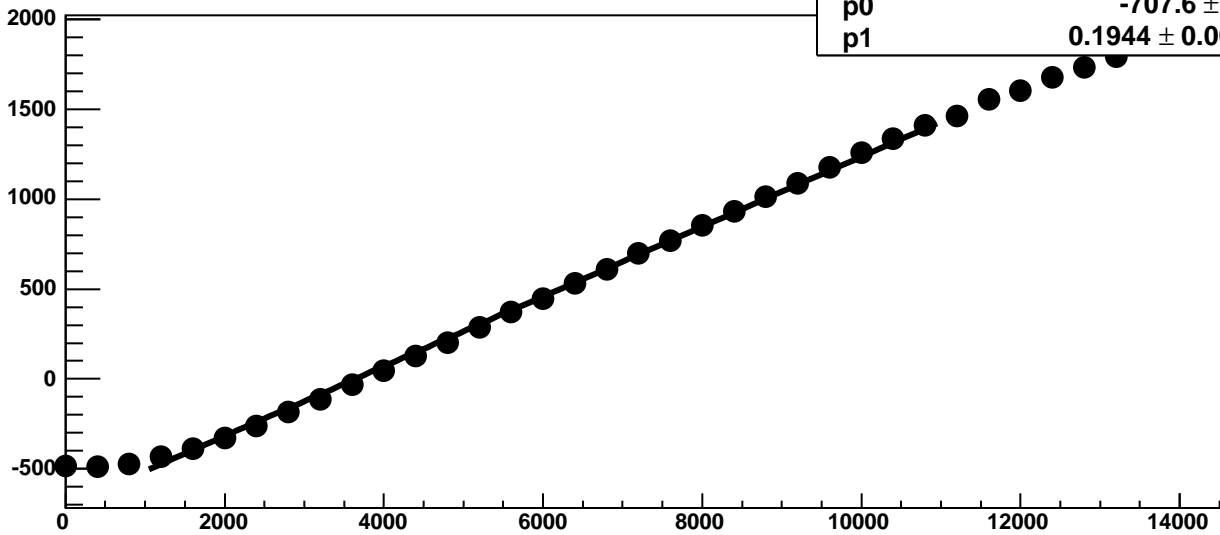


Chip 11, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

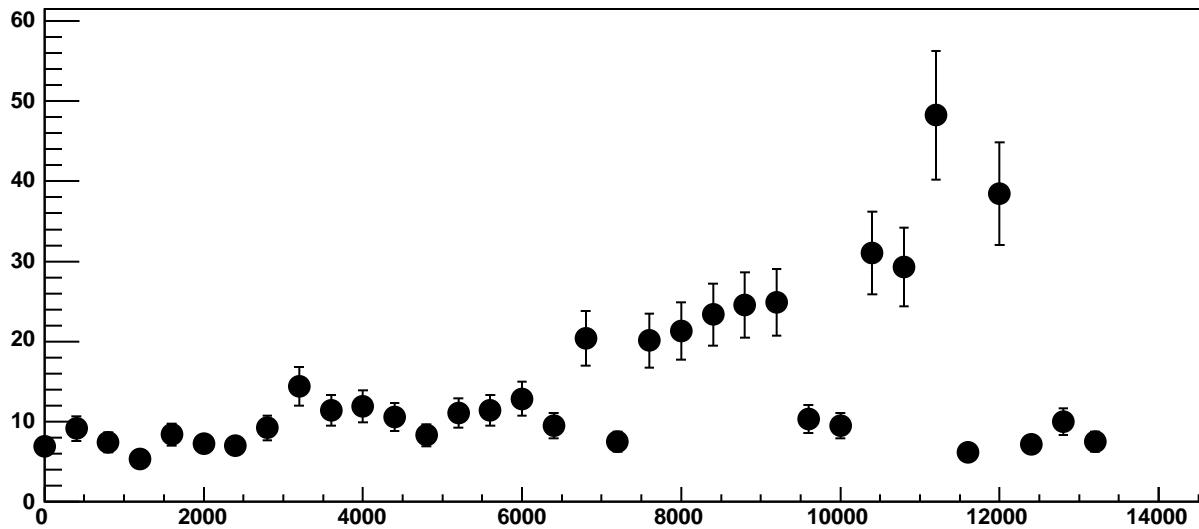




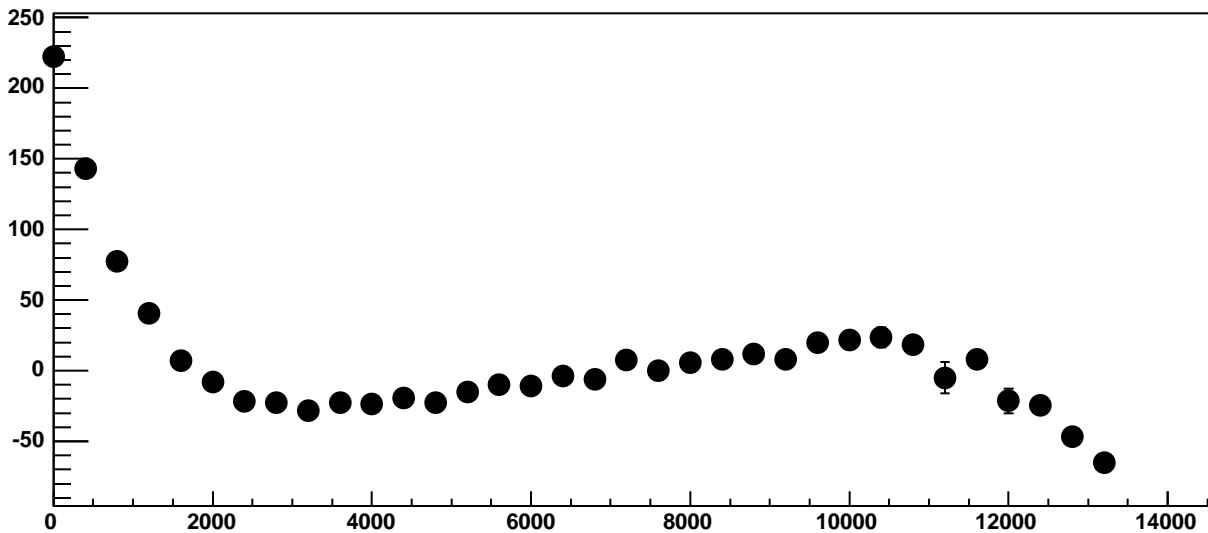
Chip 11, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



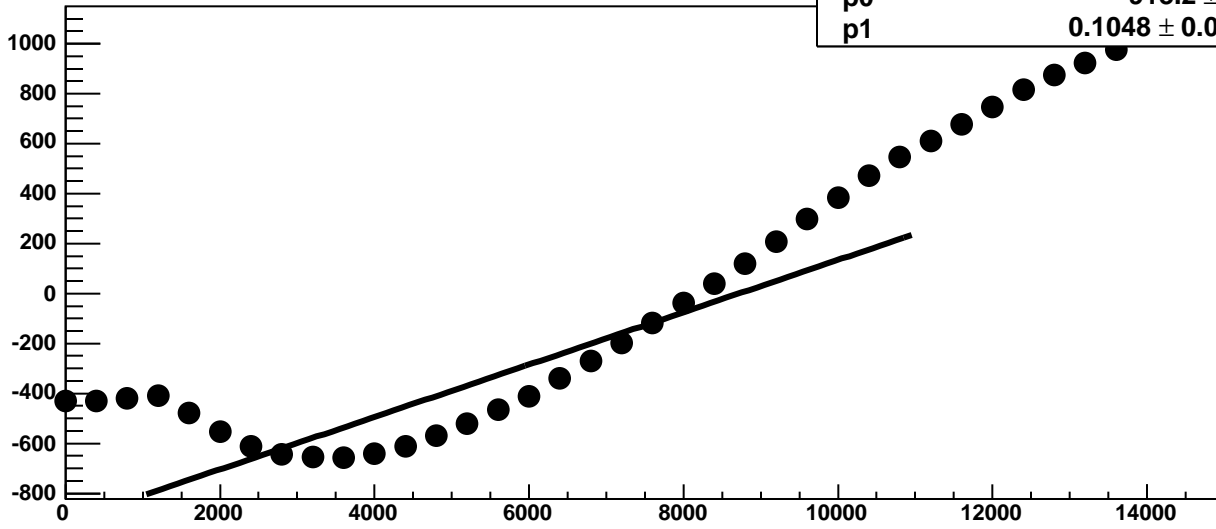
Chip 11, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



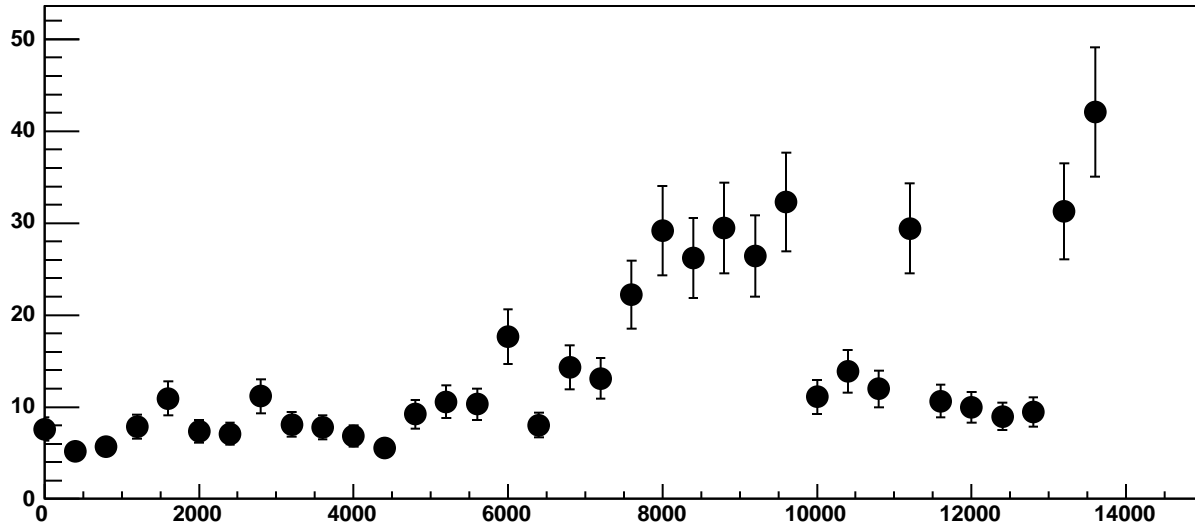
Chip 11, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



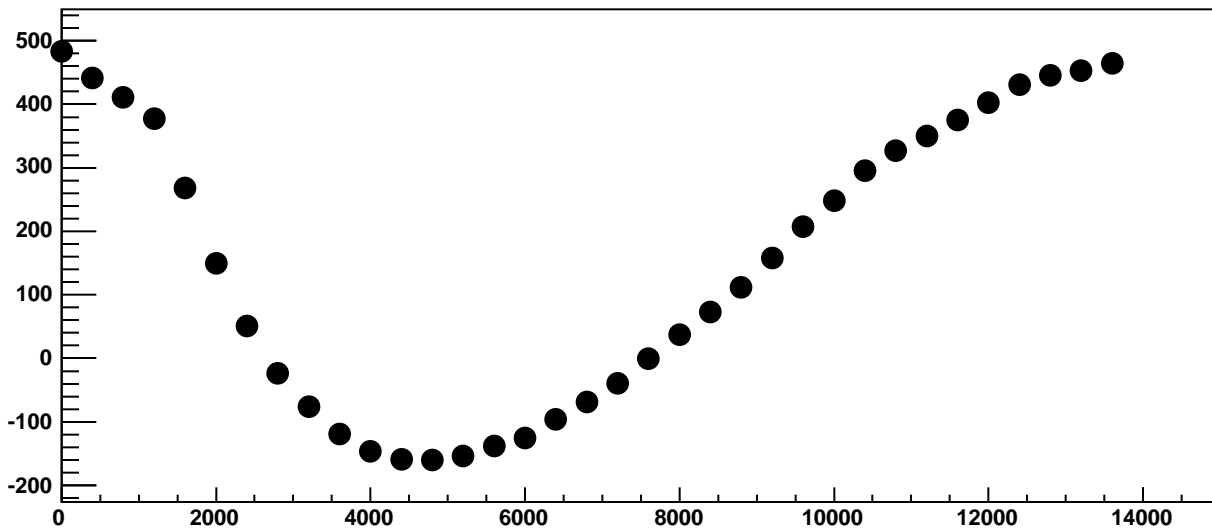
Chip 11, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



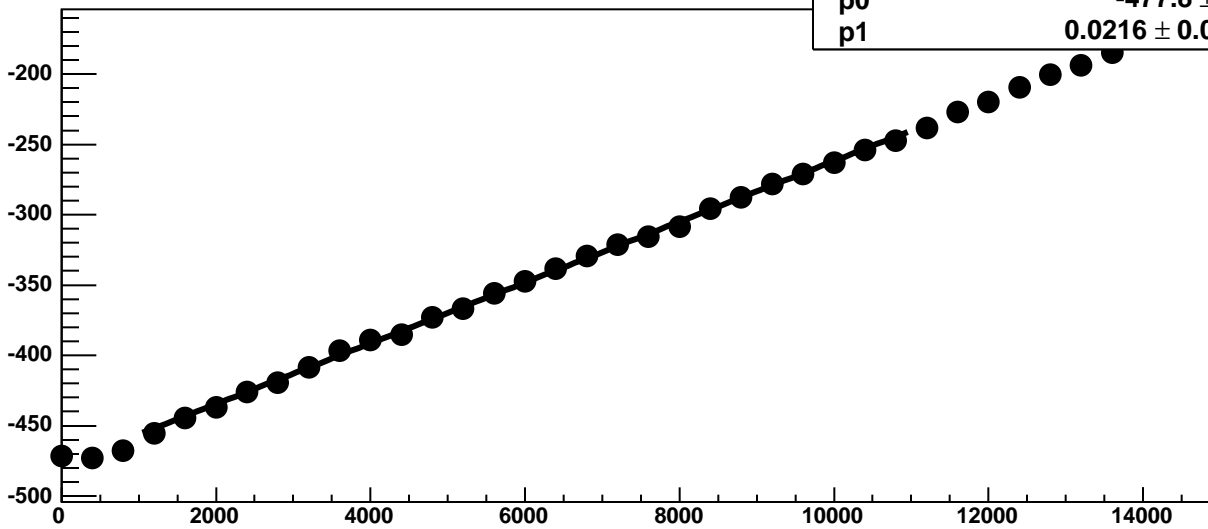
Chip 11, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

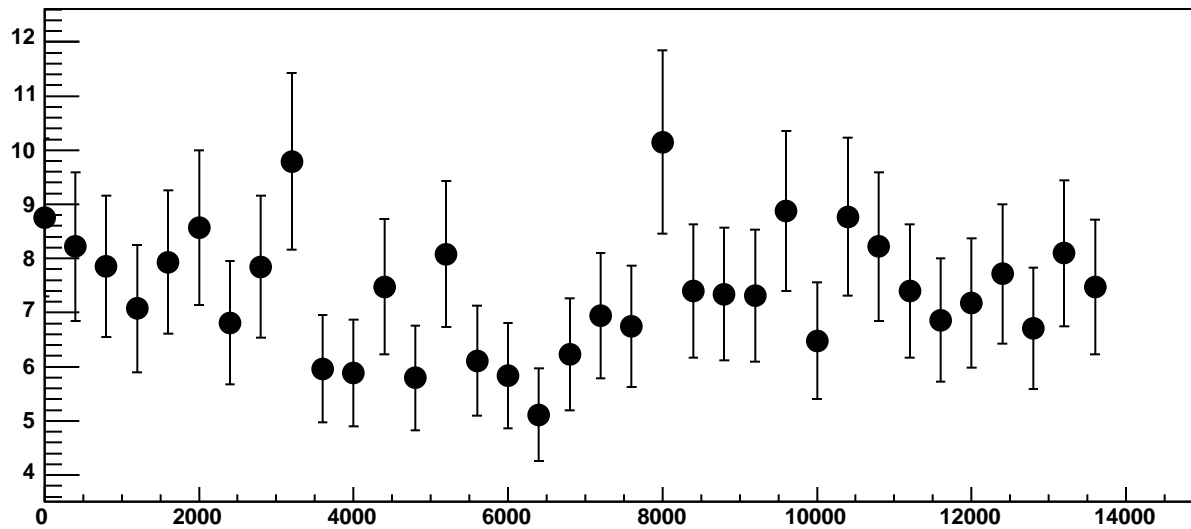


Chip 11, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

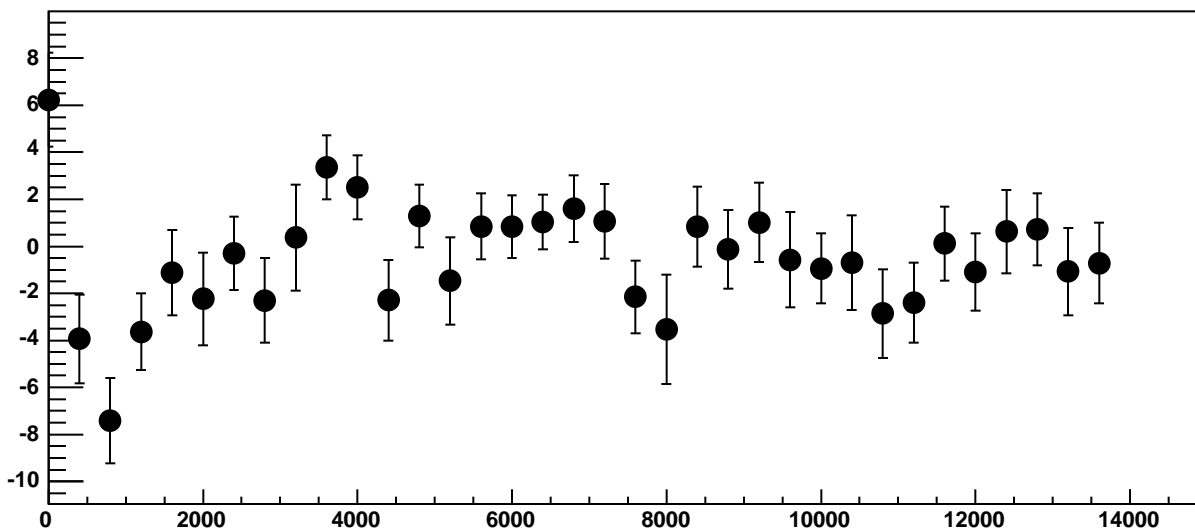


$\chi^2 / \text{ndf}$  32.19 / 23  
p0  $-477.8 \pm 0.7808$   
p1  $0.0216 \pm 0.0001206$

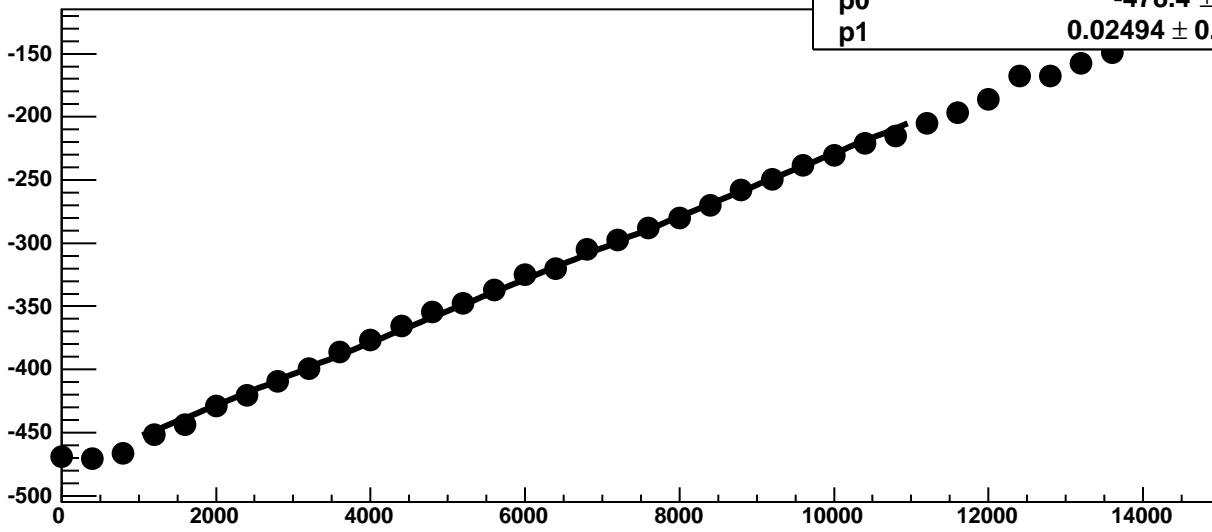
Chip 11, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



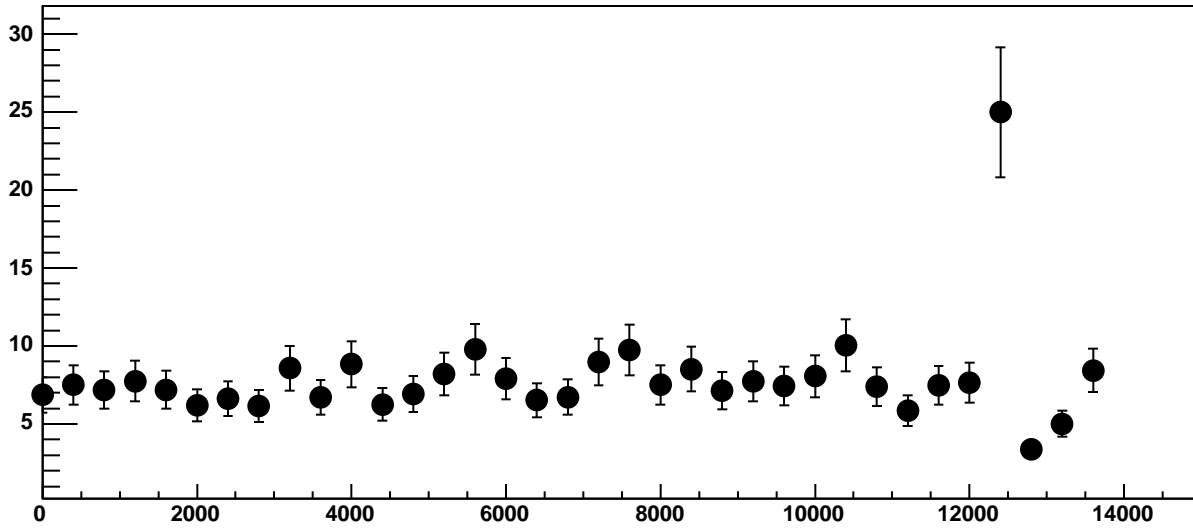
Chip 11, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



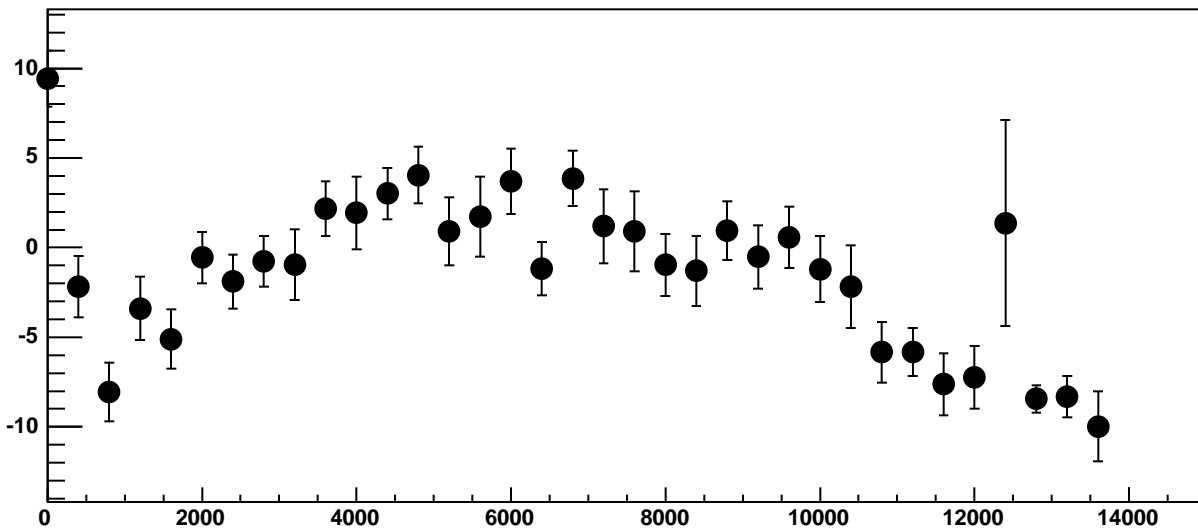
Chip 11, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



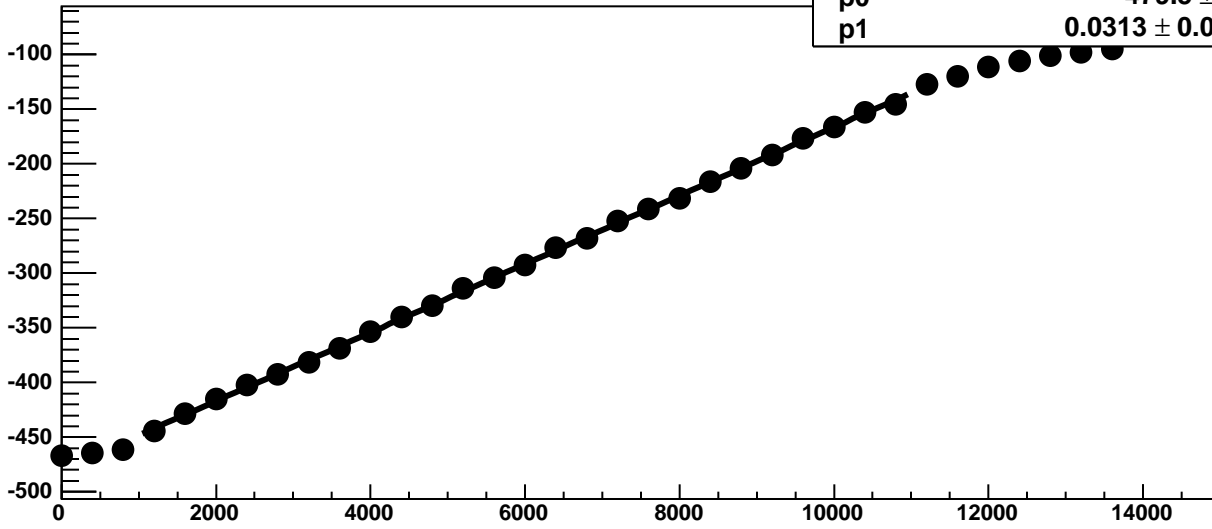
Chip 11, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

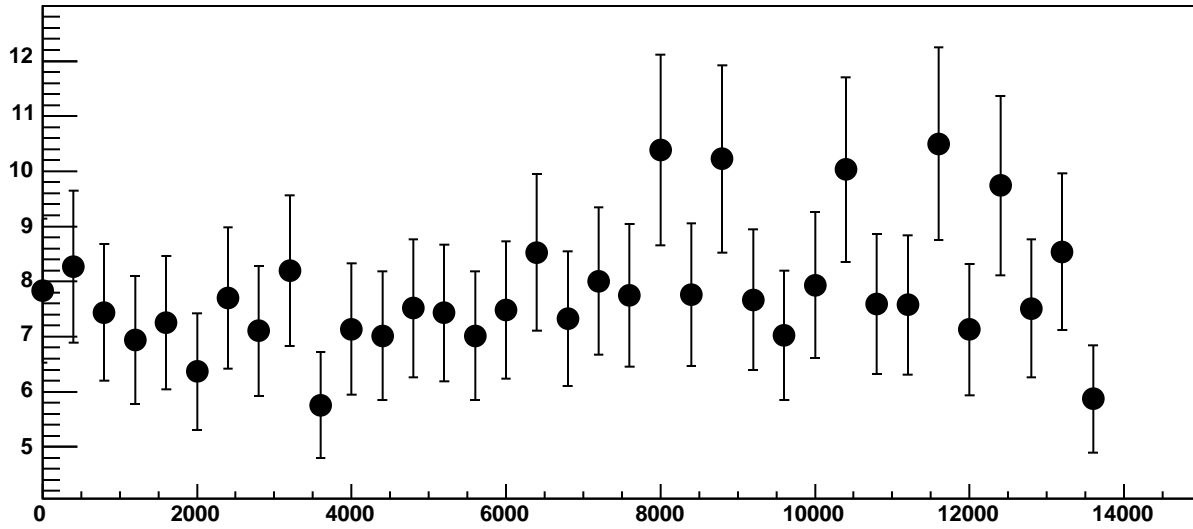


Chip 11, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

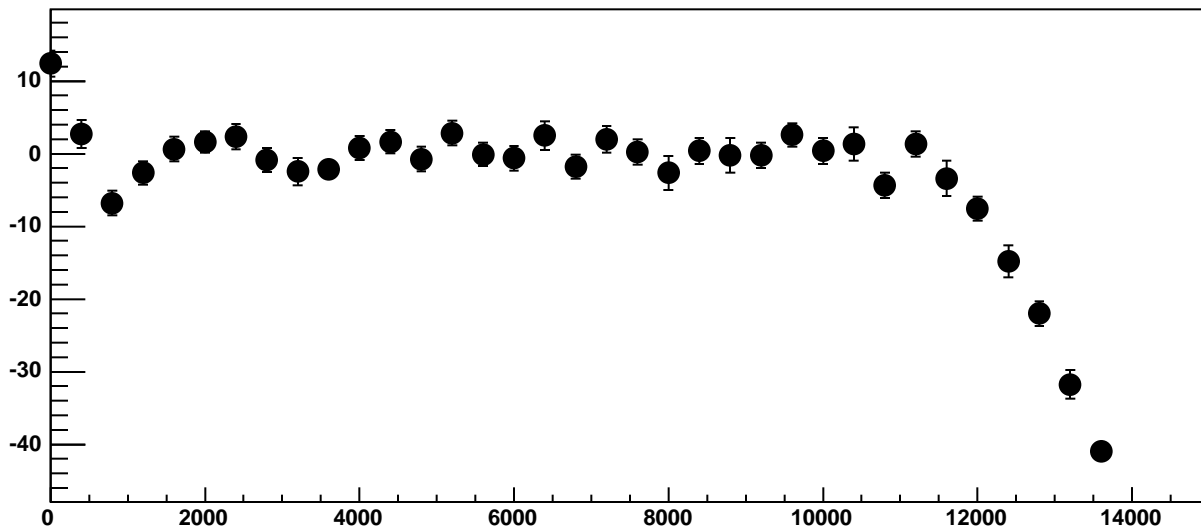


$\chi^2 / \text{ndf}$  29.31 / 23  
p0  $-479.5 \pm 0.7599$   
p1  $0.0313 \pm 0.0001204$

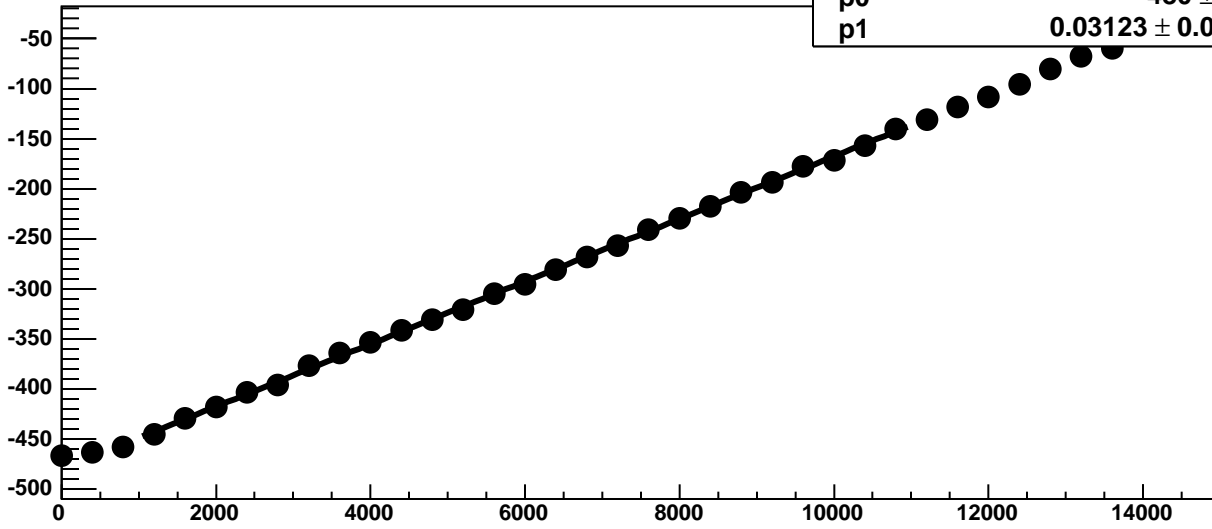
Chip 11, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

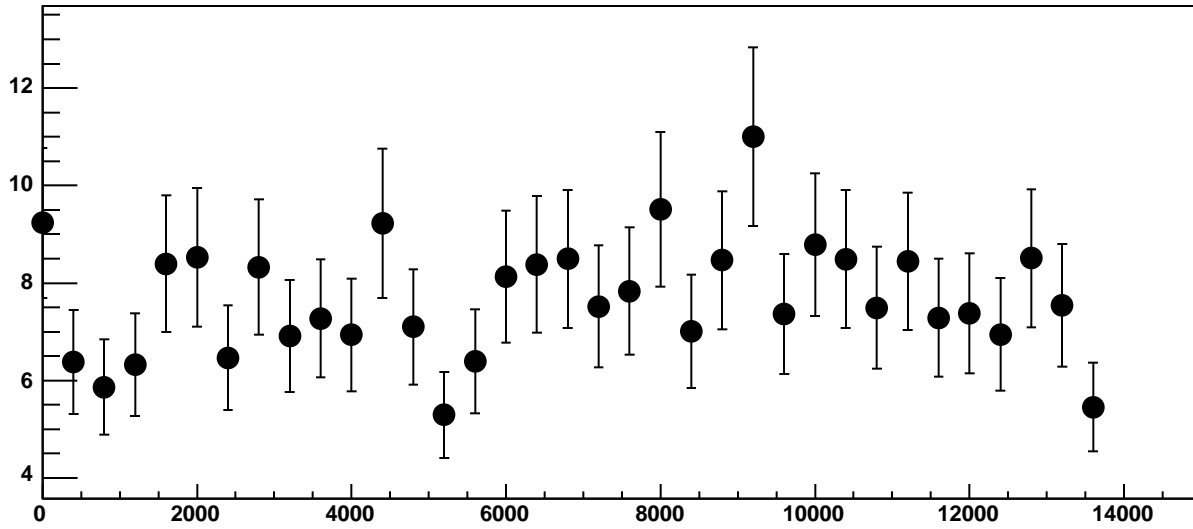


Chip 11, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

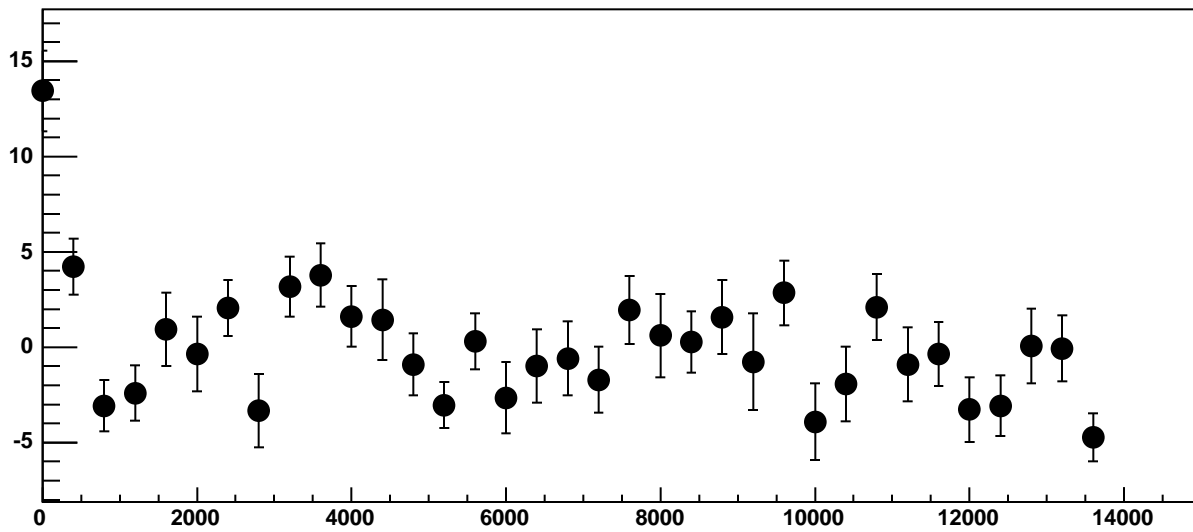


$\chi^2 / \text{ndf}$  39.69 / 23  
p0  $-480 \pm 0.7813$   
p1  $0.03123 \pm 0.0001228$

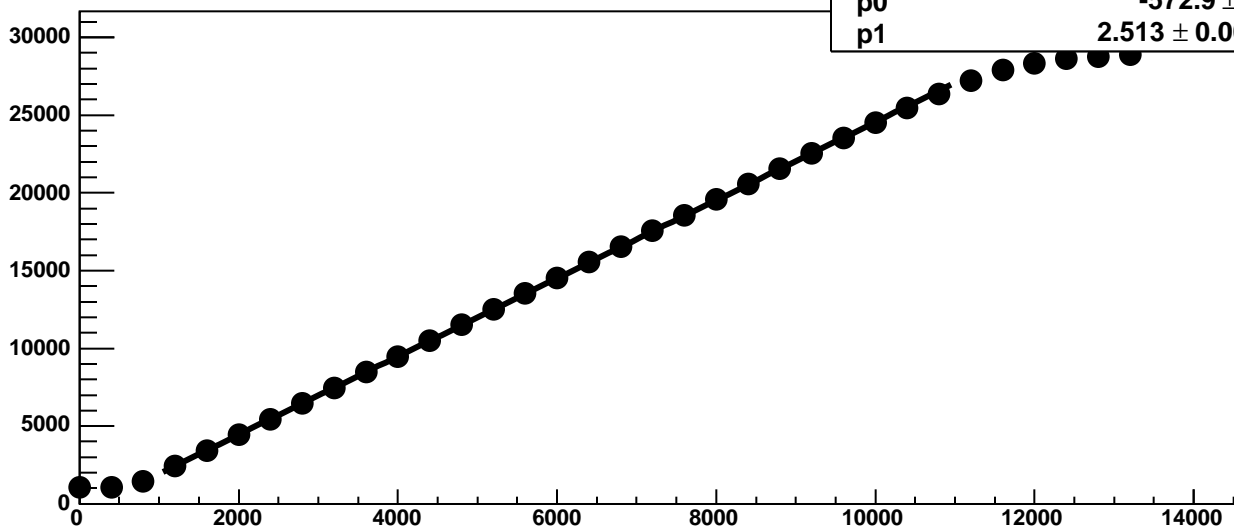
Chip 11, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

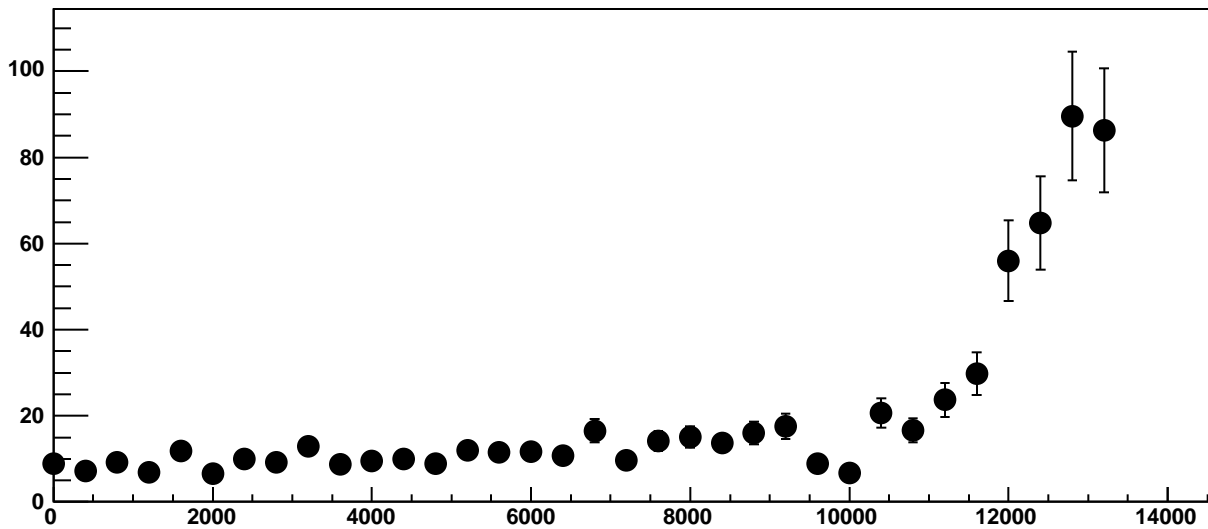


Chip 11, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC

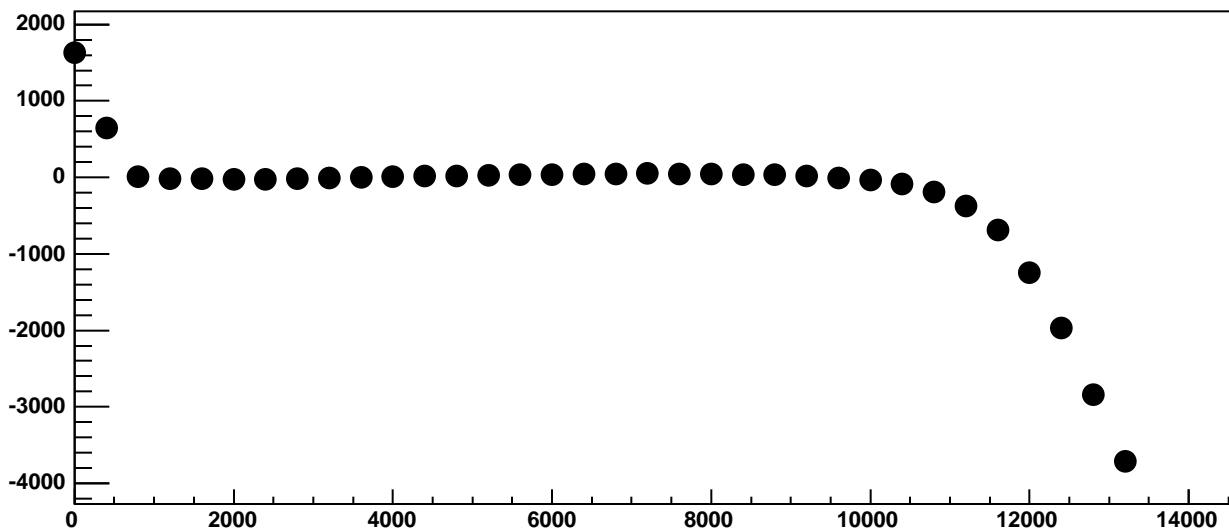


$\chi^2 / \text{ndf}$  6067 / 23  
p0  $-572.9 \pm 0.9561$   
p1  $2.513 \pm 0.0001584$

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC

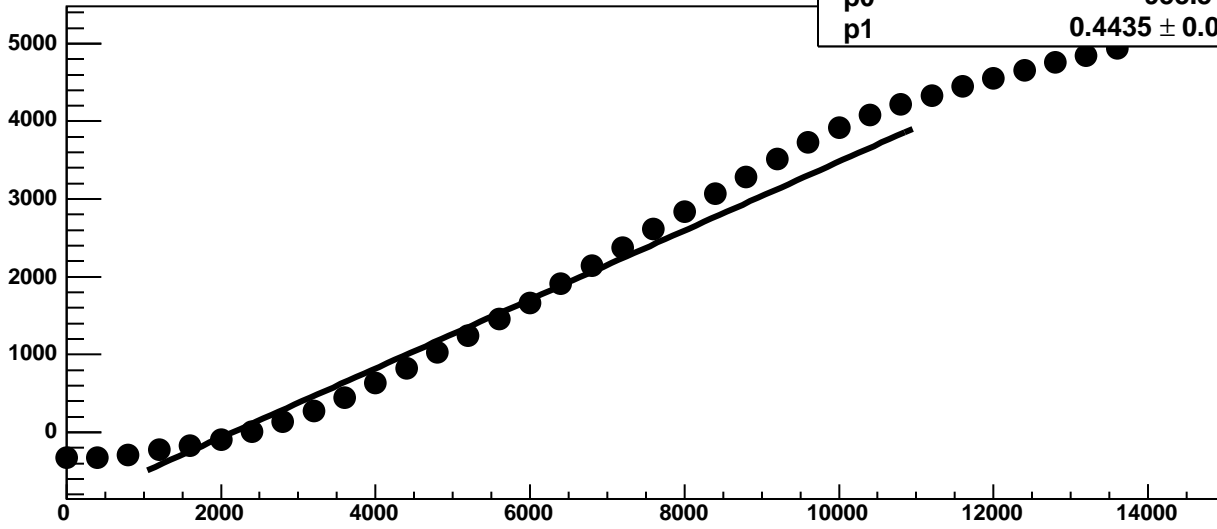


Chip 11, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC

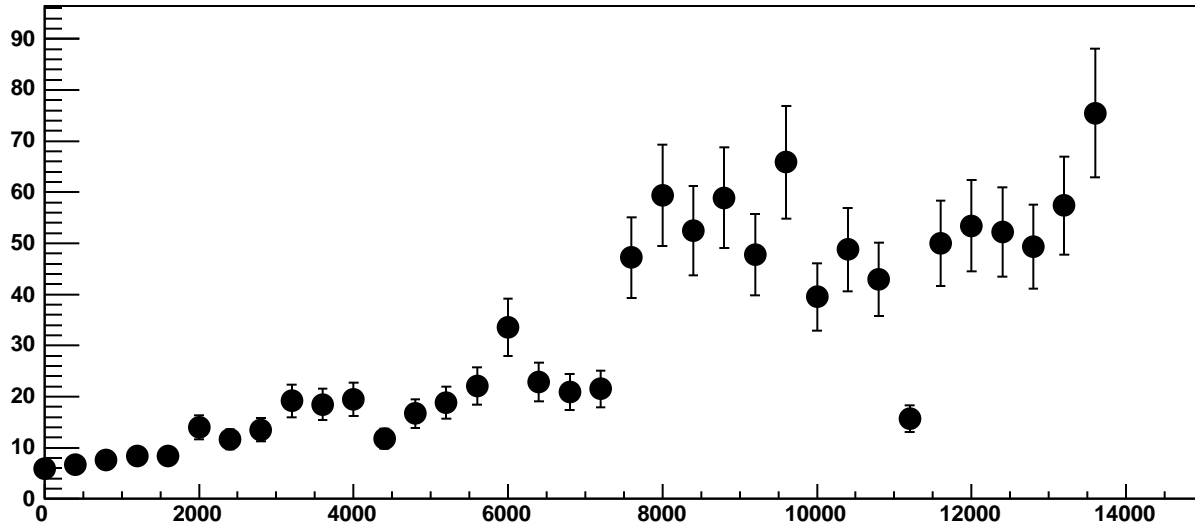


Chip 11, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

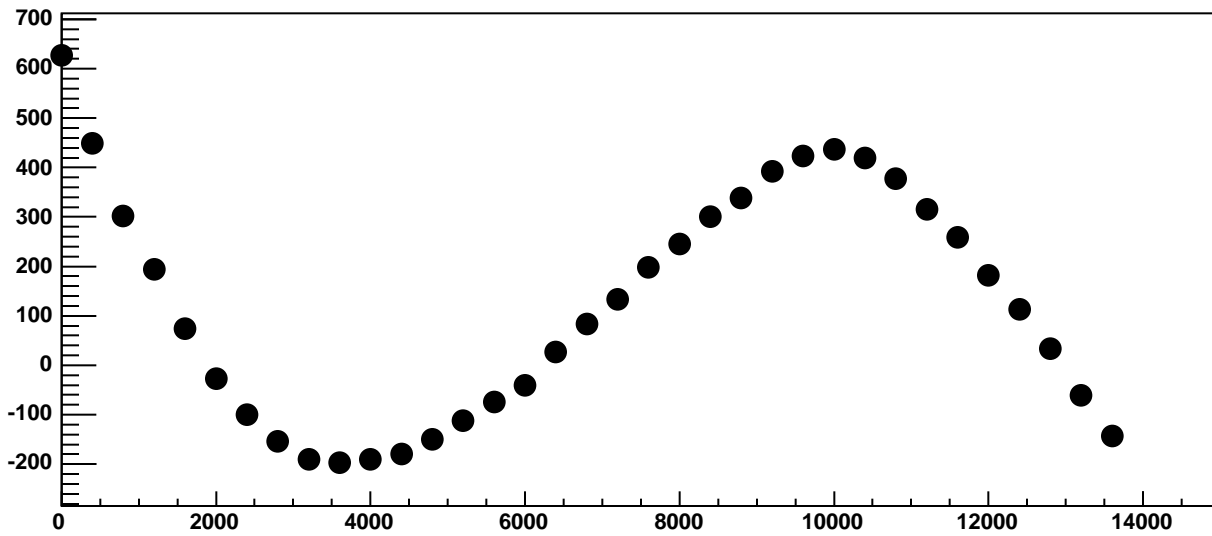
$\chi^2 / \text{ndf}$  3.844e+04 / 23  
p0 -953.5 ± 1.478  
p1 0.4435 ± 0.0003715



Chip 11, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

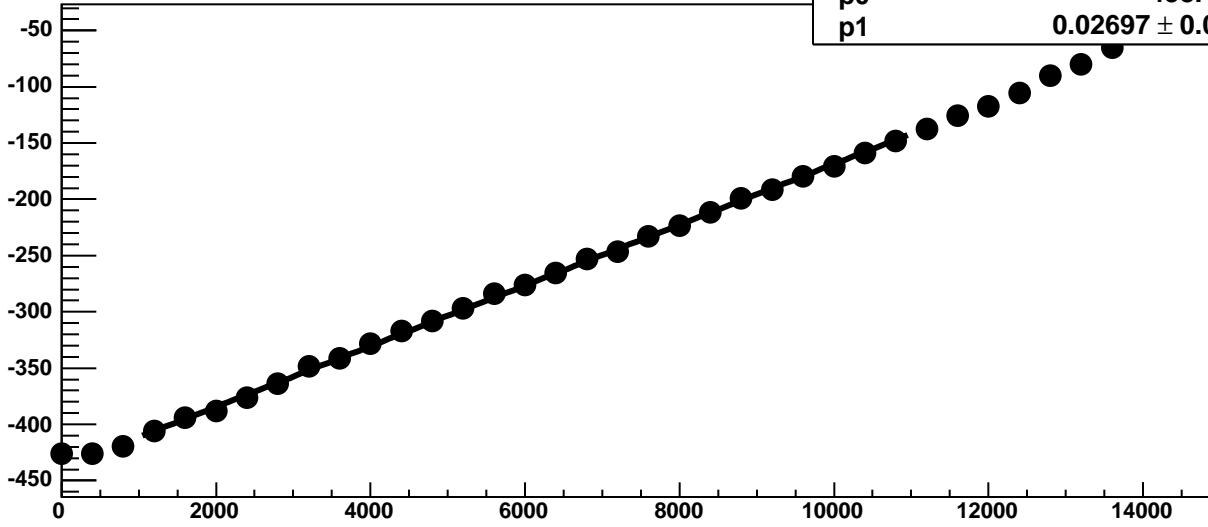


Chip 11, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



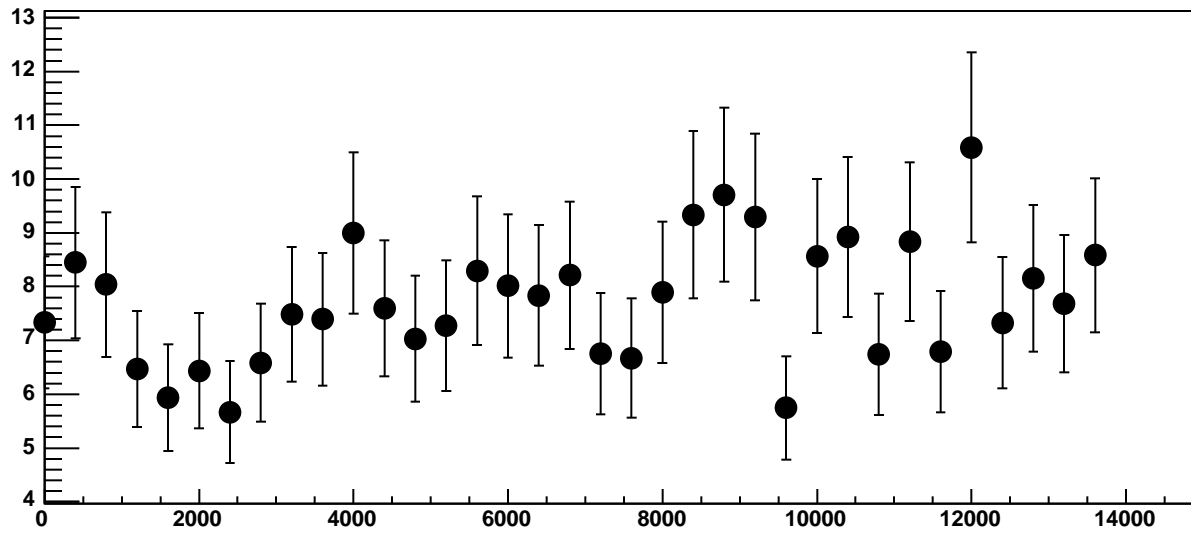


Chip 11, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

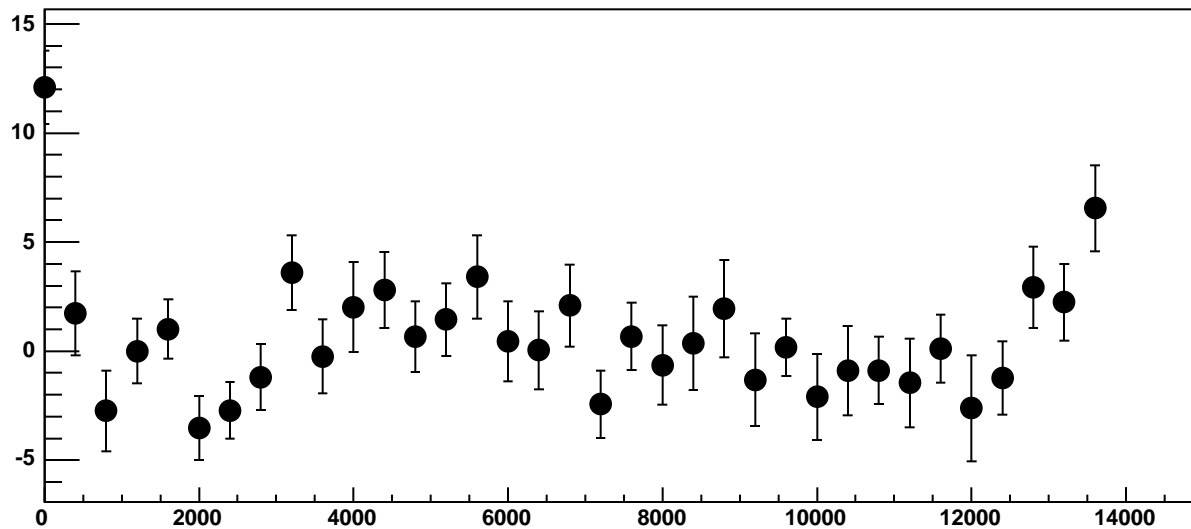


$\chi^2 / \text{ndf}$  30.24 / 23  
p0  $-438.4 \pm 0.71$   
p1  $0.02697 \pm 0.0001115$

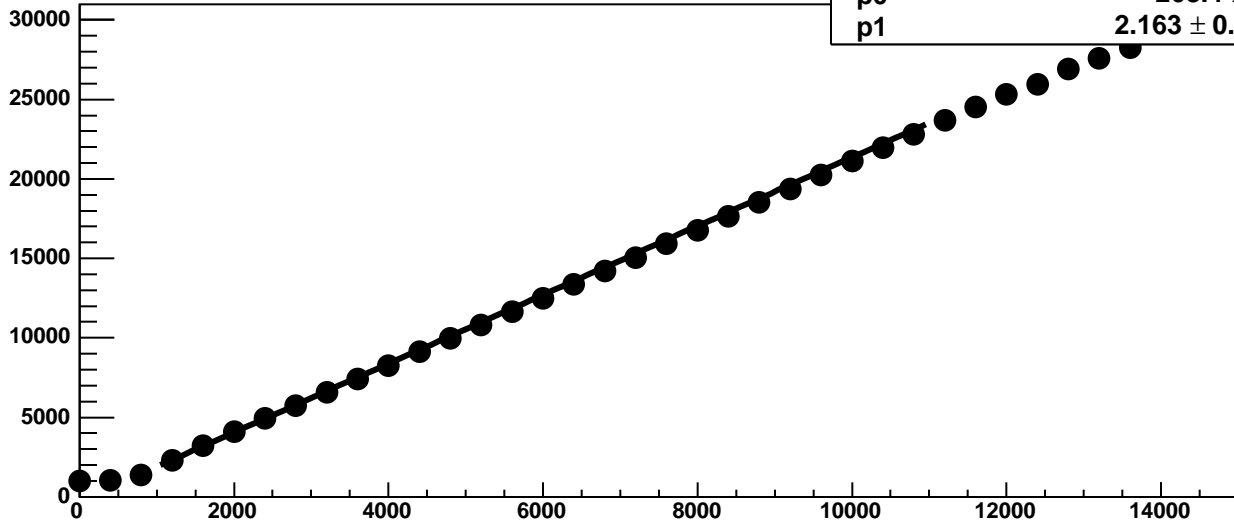
Chip 11, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



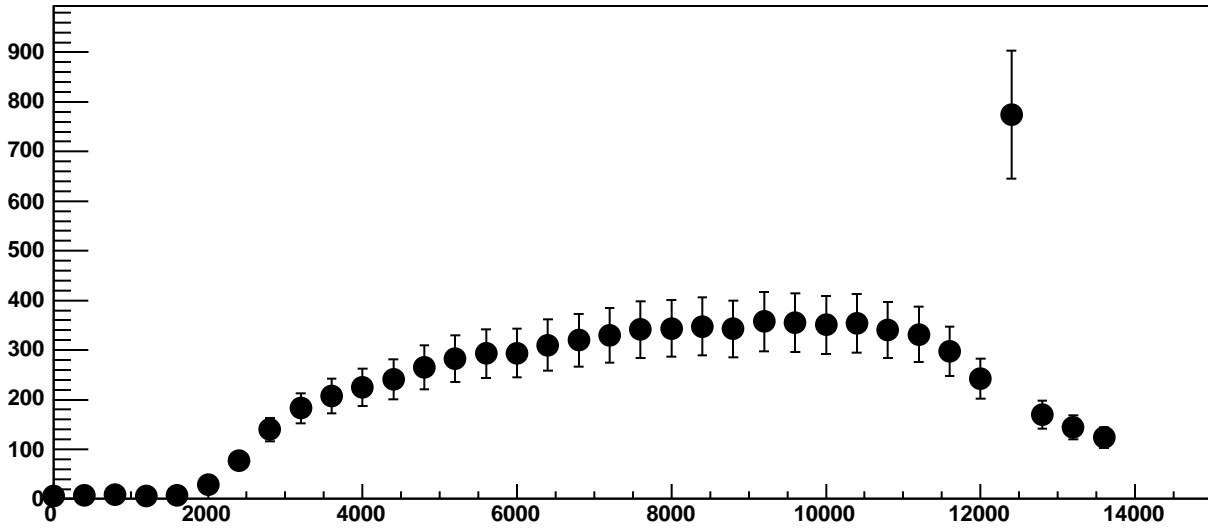
Chip 11, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



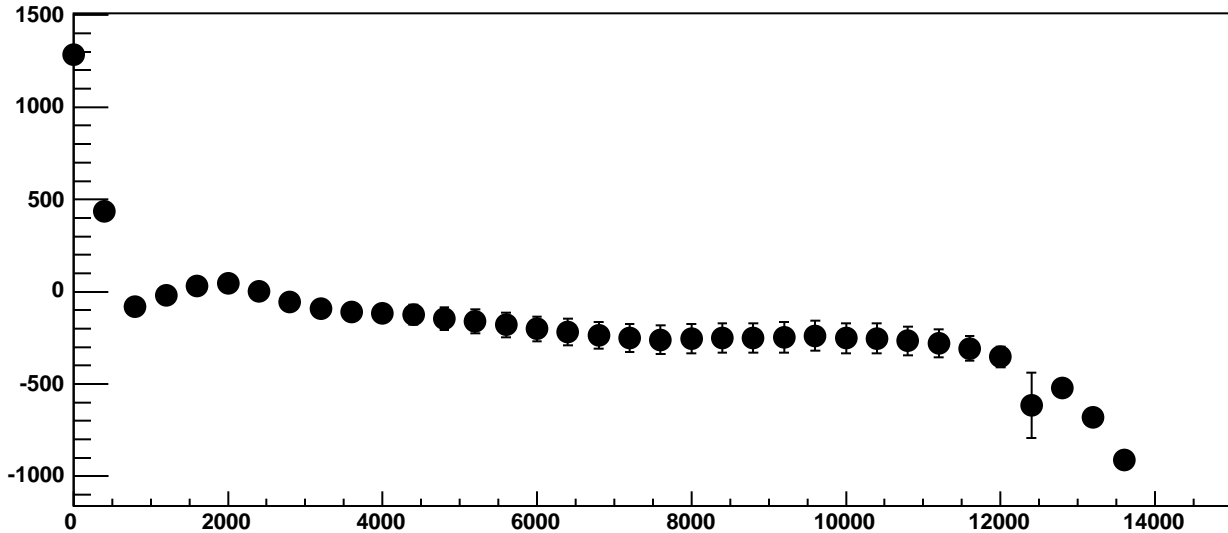
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC



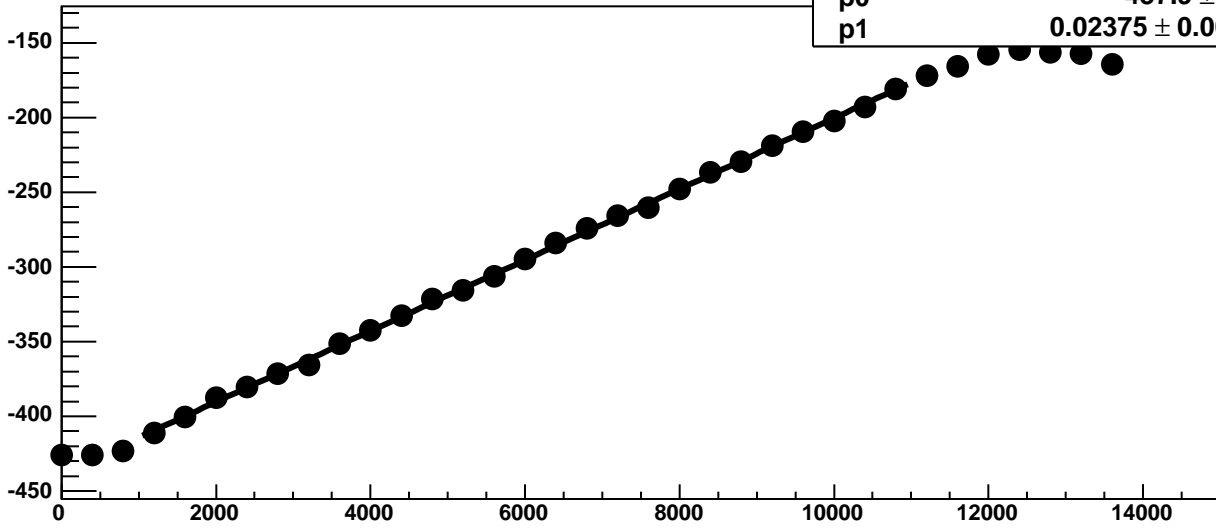
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC



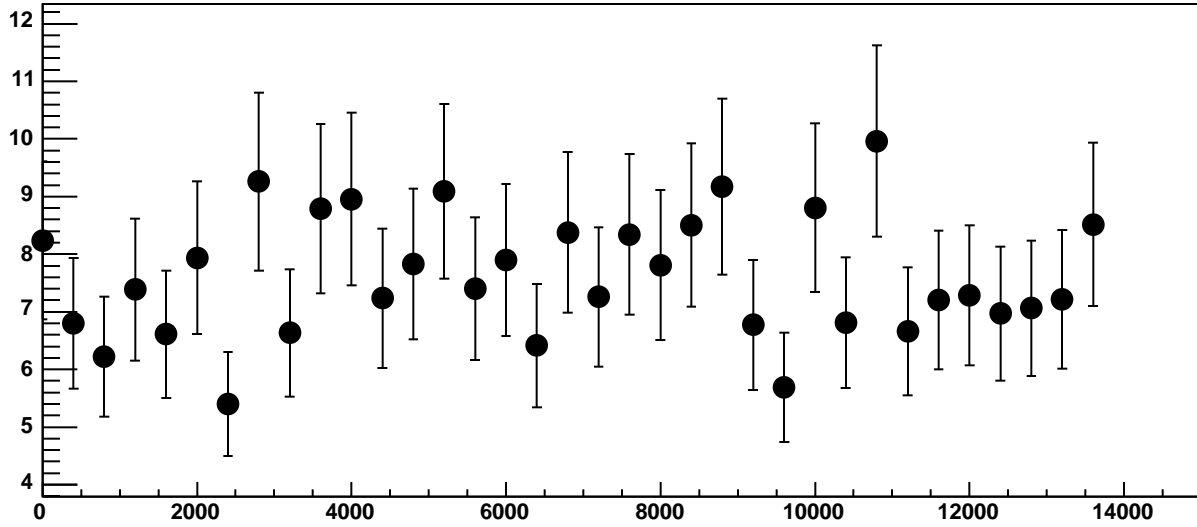
Chip 11, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC



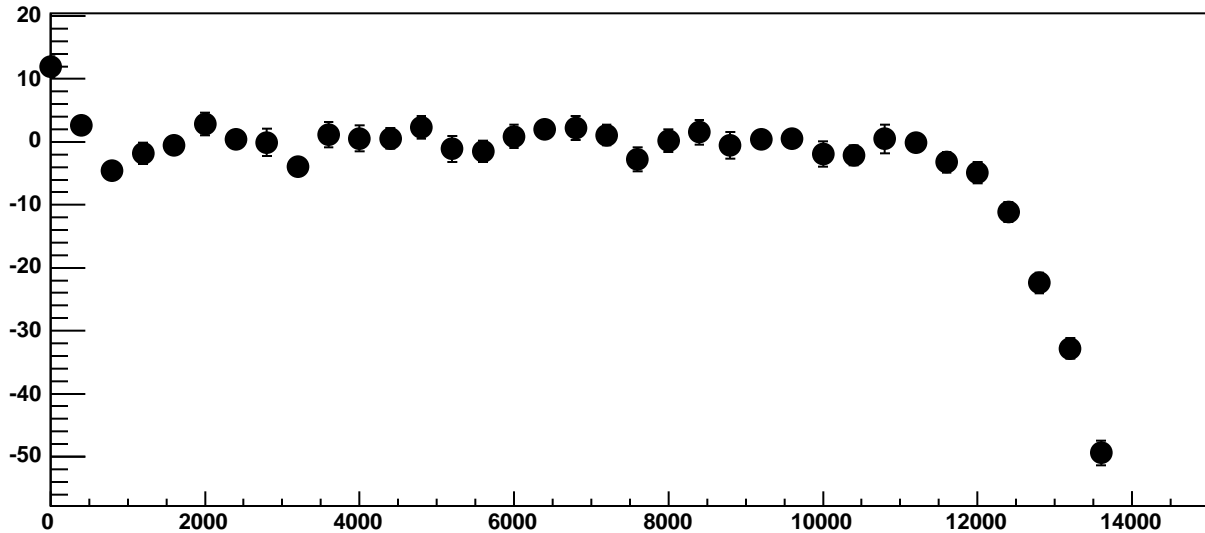
Chip 11, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



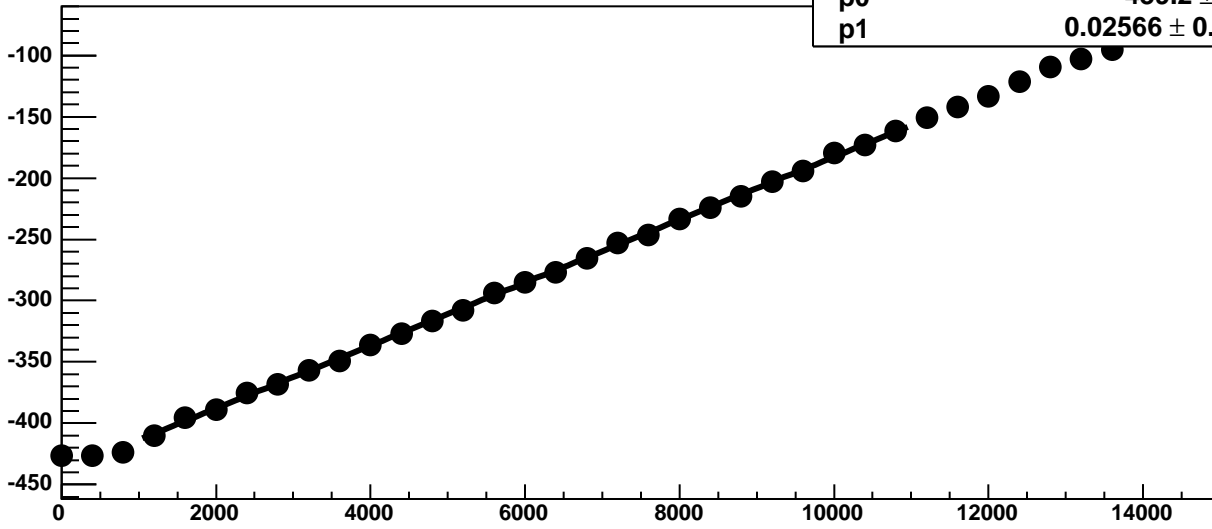
Chip 11, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



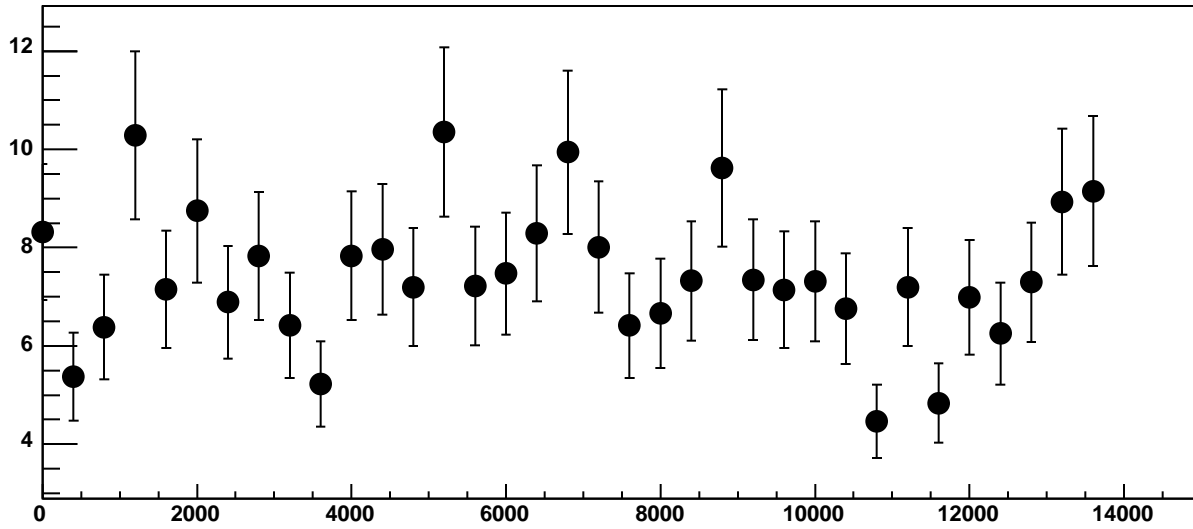
Chip 11, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



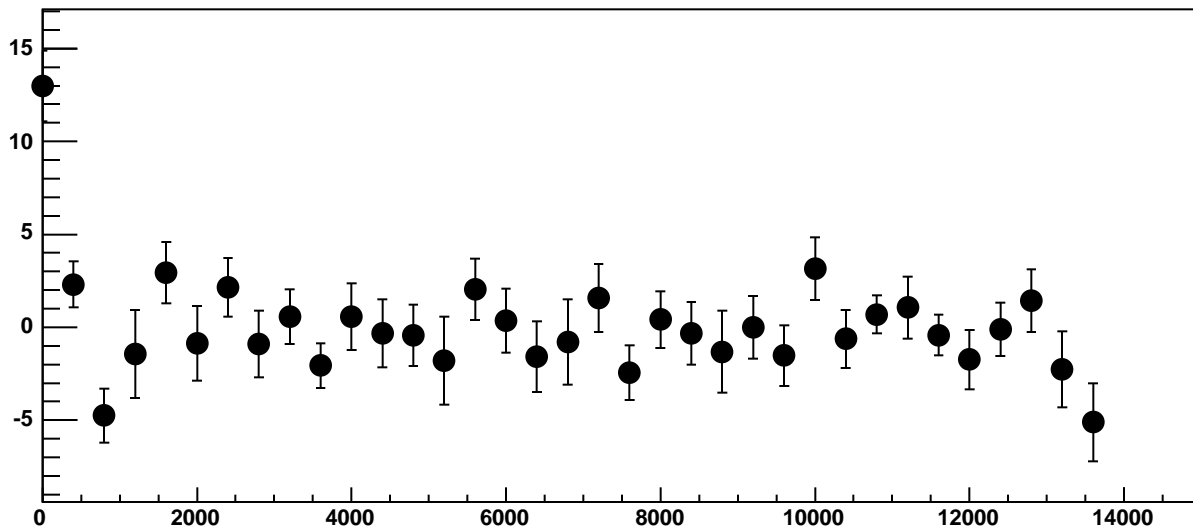
Chip 11, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



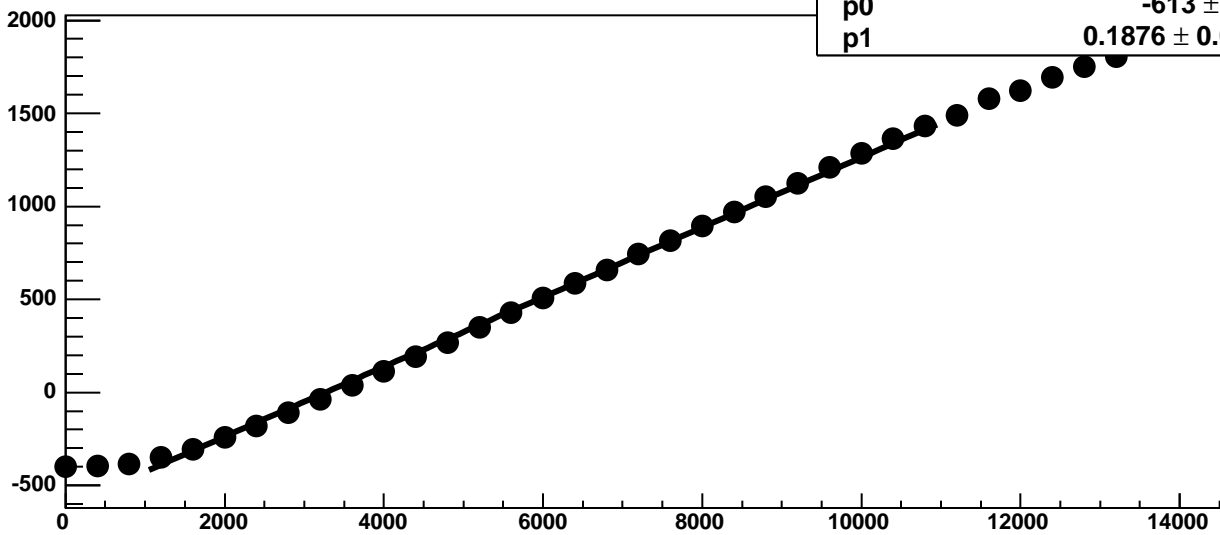
Chip 11, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



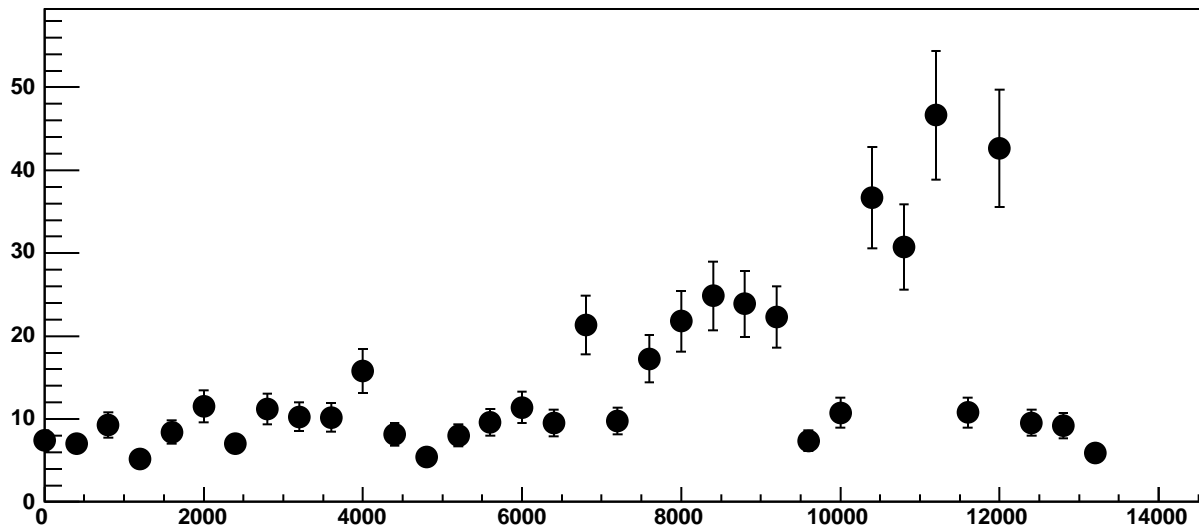
Chip 11, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



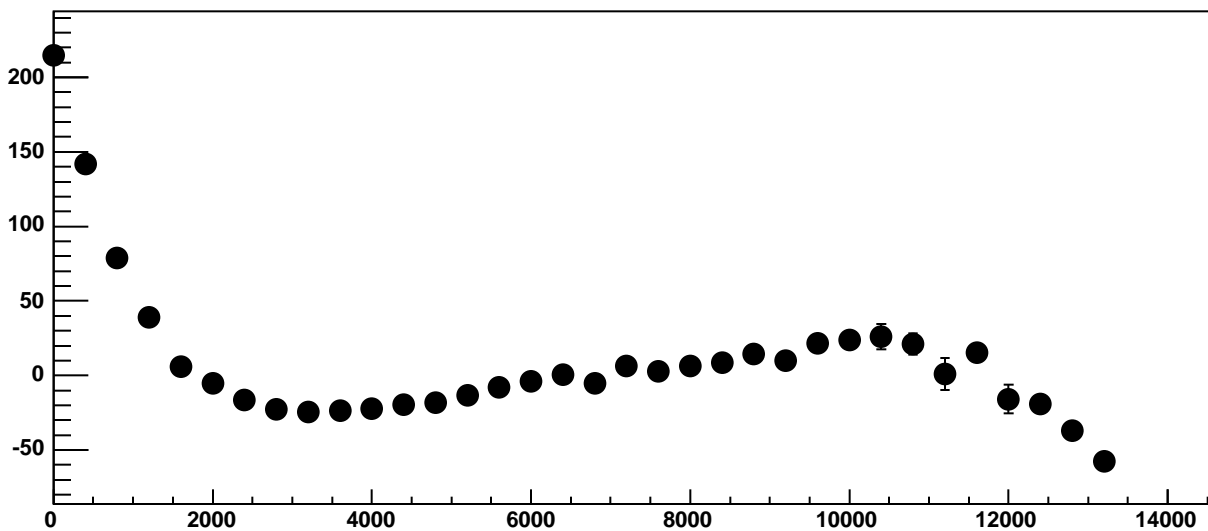
Chip 11, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC



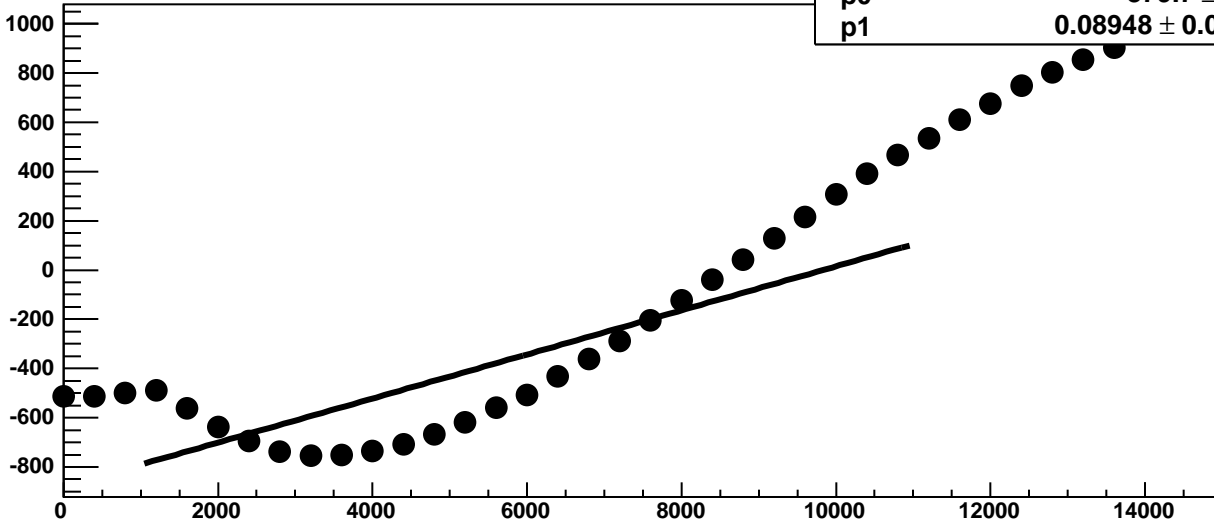
Chip 11, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

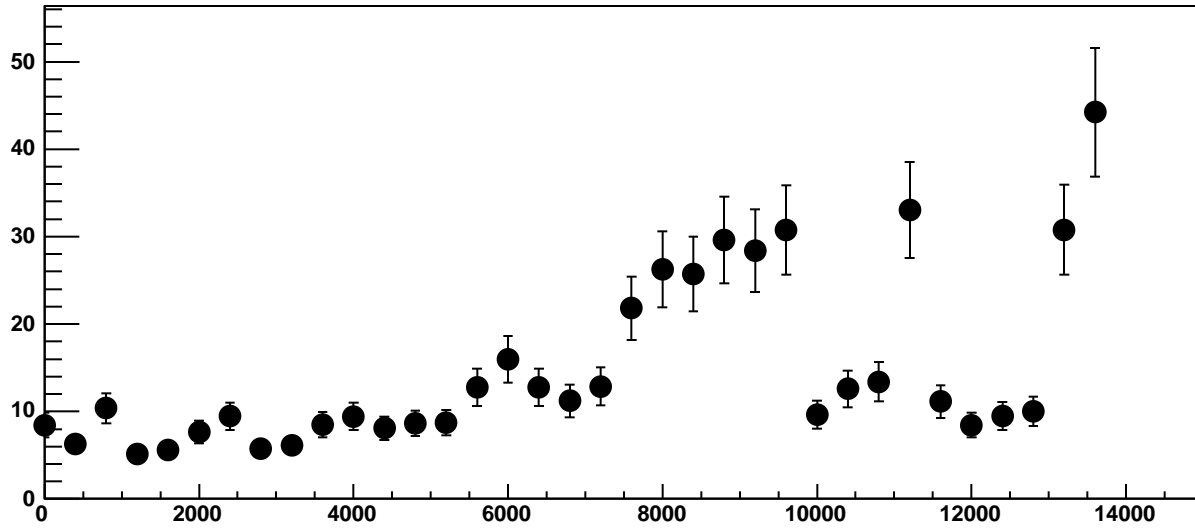


Chip 11, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

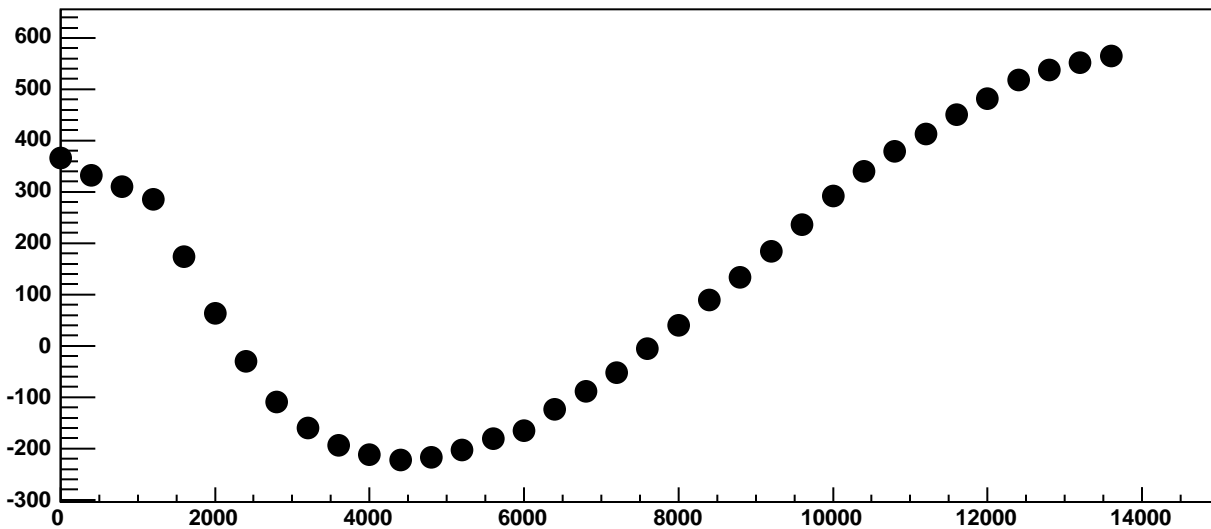


$\chi^2 / \text{ndf}$  2.118e+05 / 23  
p0 -879.7 ± 0.7847  
p1 0.08948 ± 0.0001651

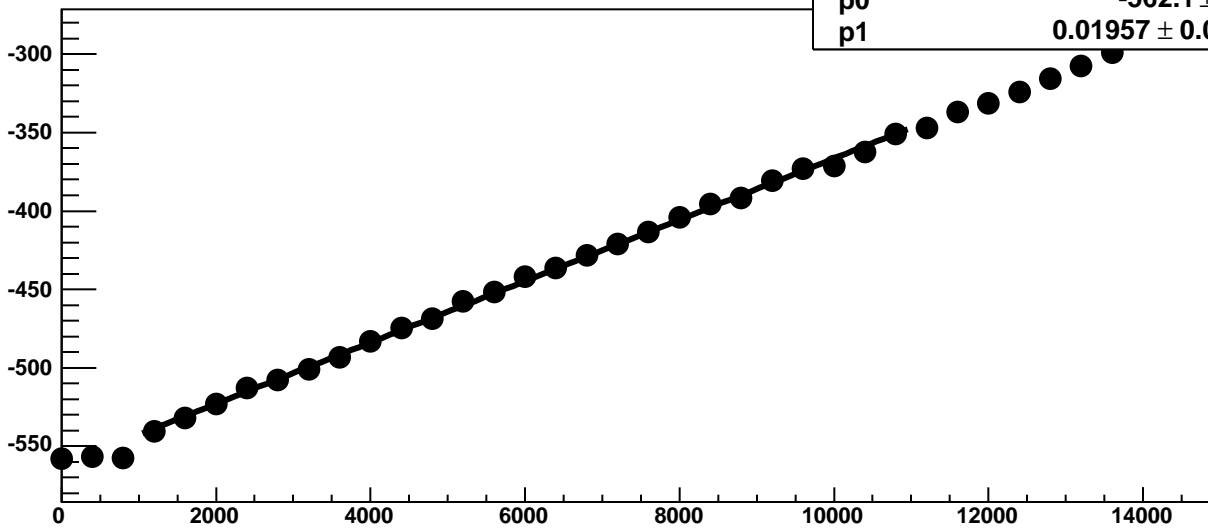
Chip 11, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

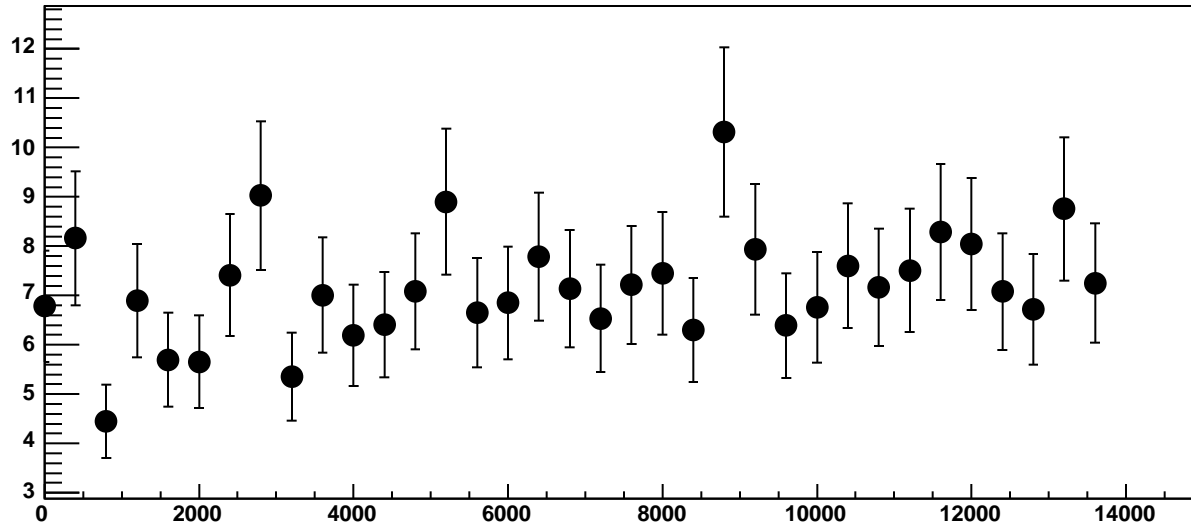


Chip 11, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

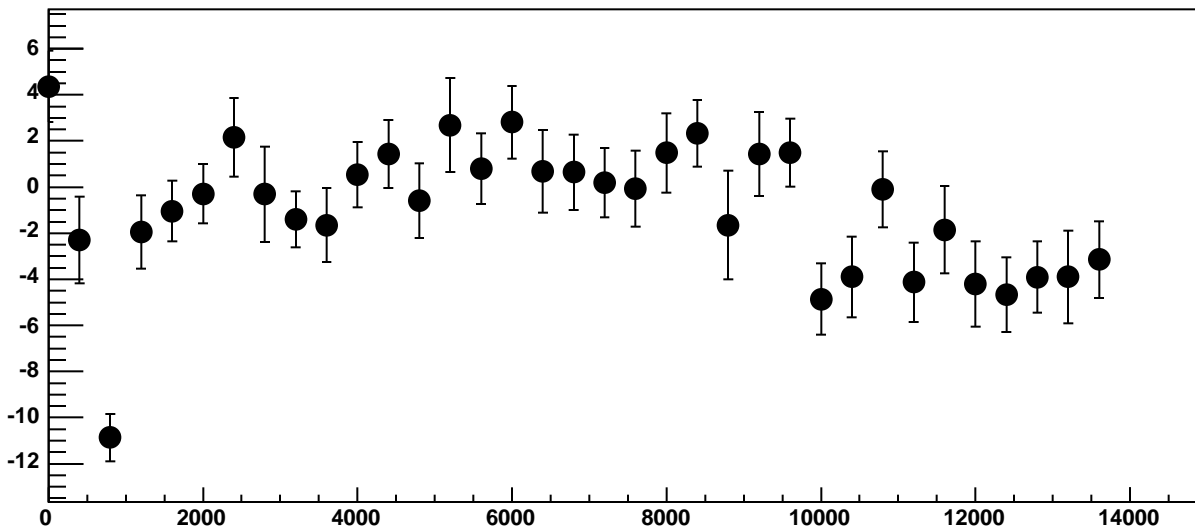


$\chi^2 / \text{ndf}$  33.25 / 23  
p0 -562.1 ± 0.6948  
p1 0.01957 ± 0.0001081

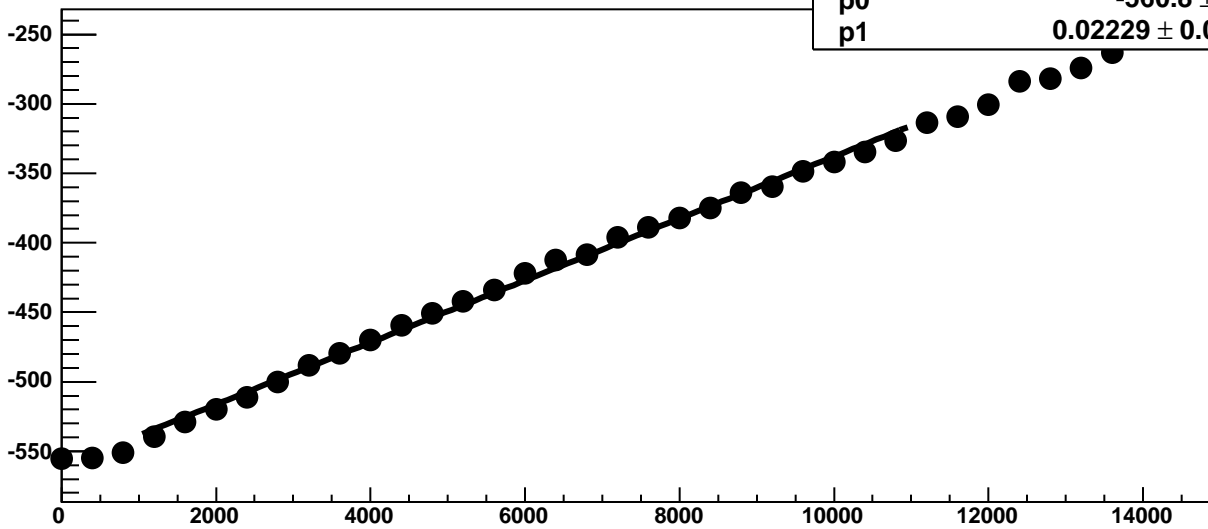
Chip 11, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



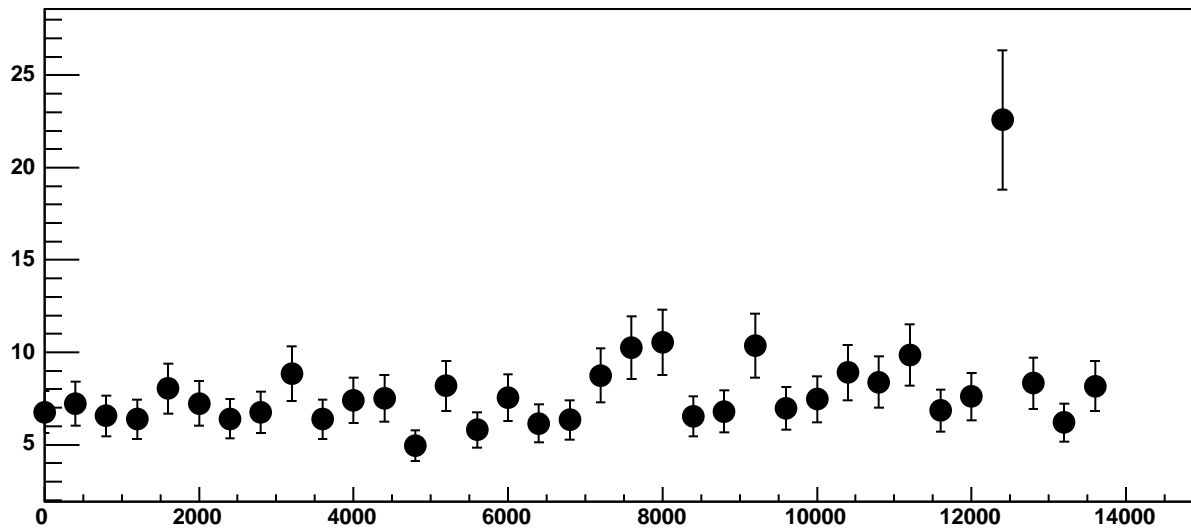
Chip 11, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



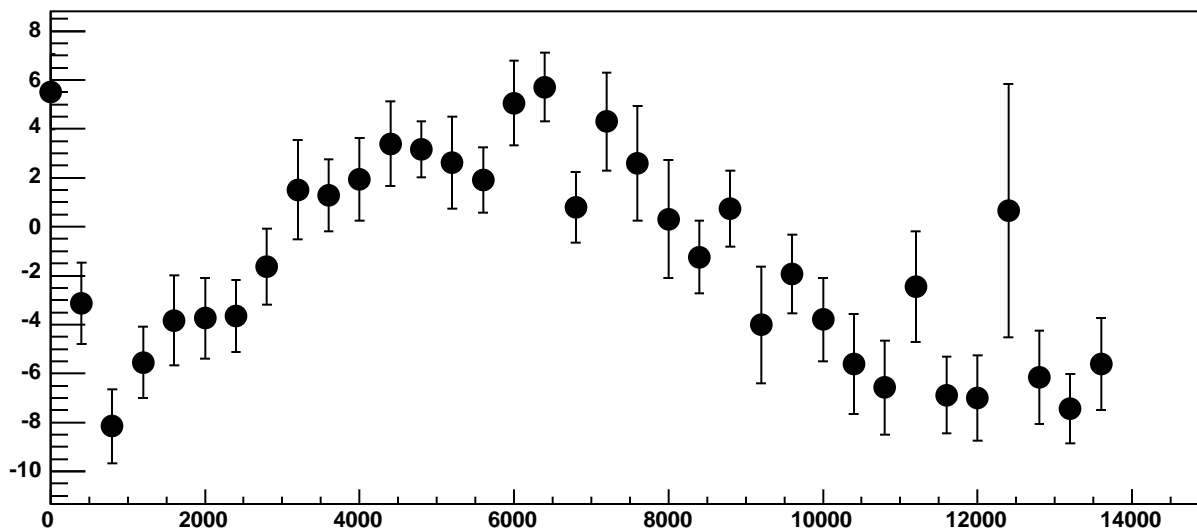
Chip 11, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



Chip 11, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

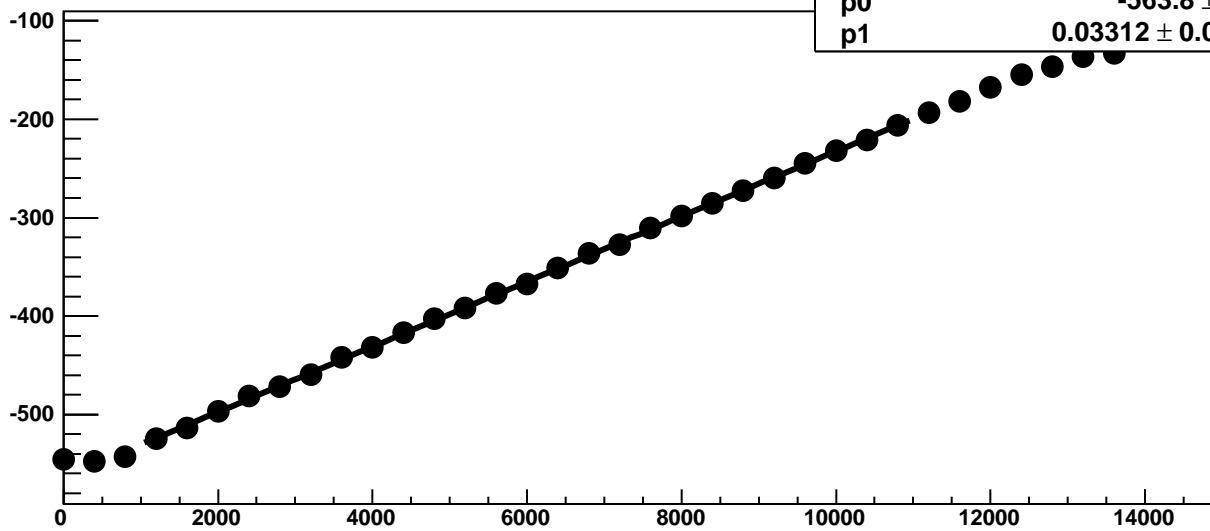


Chip 11, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

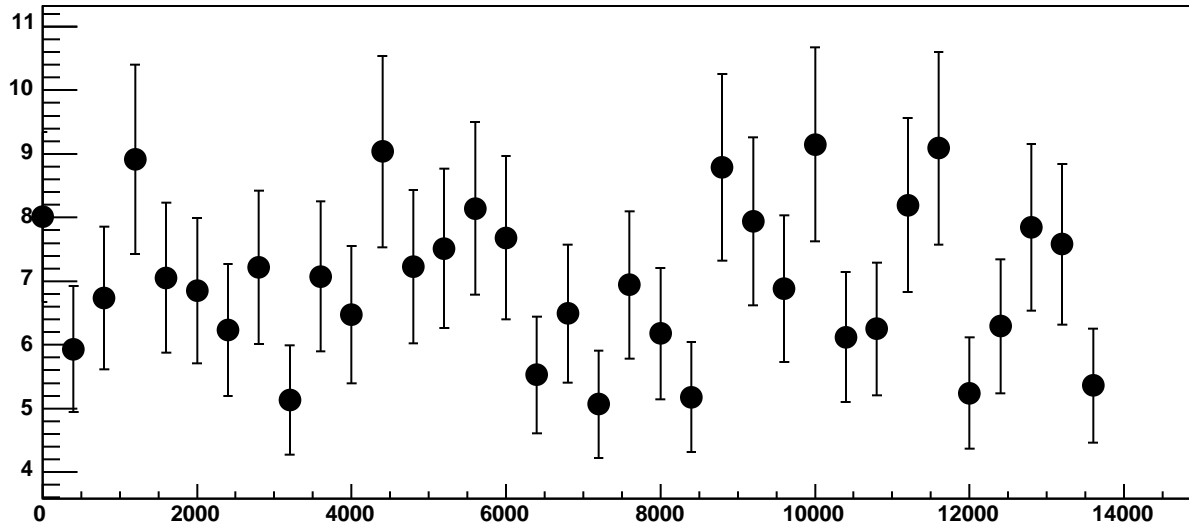




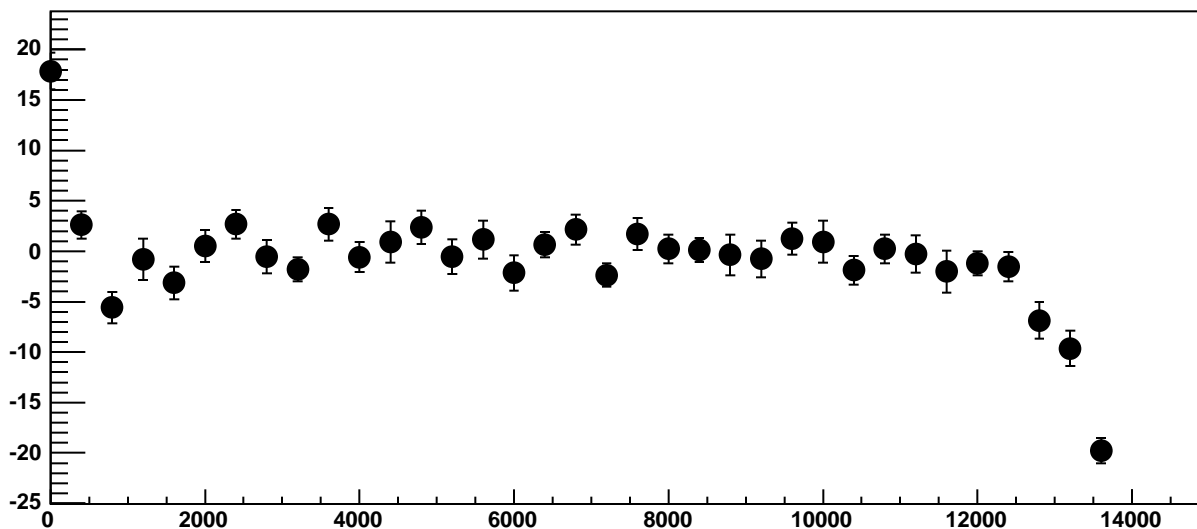
Chip 11, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



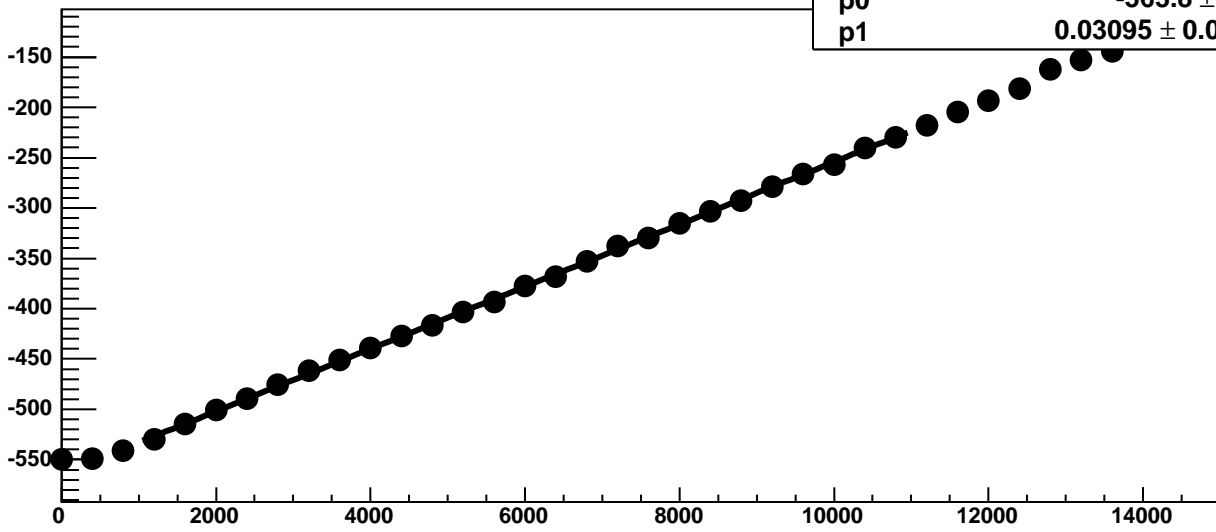
Chip 11, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

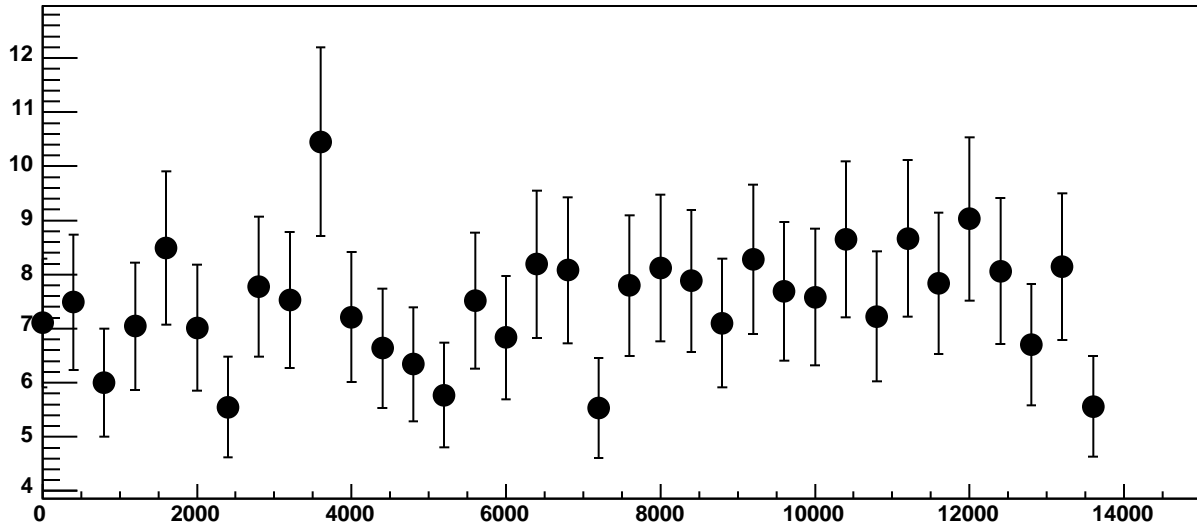


Chip 11, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

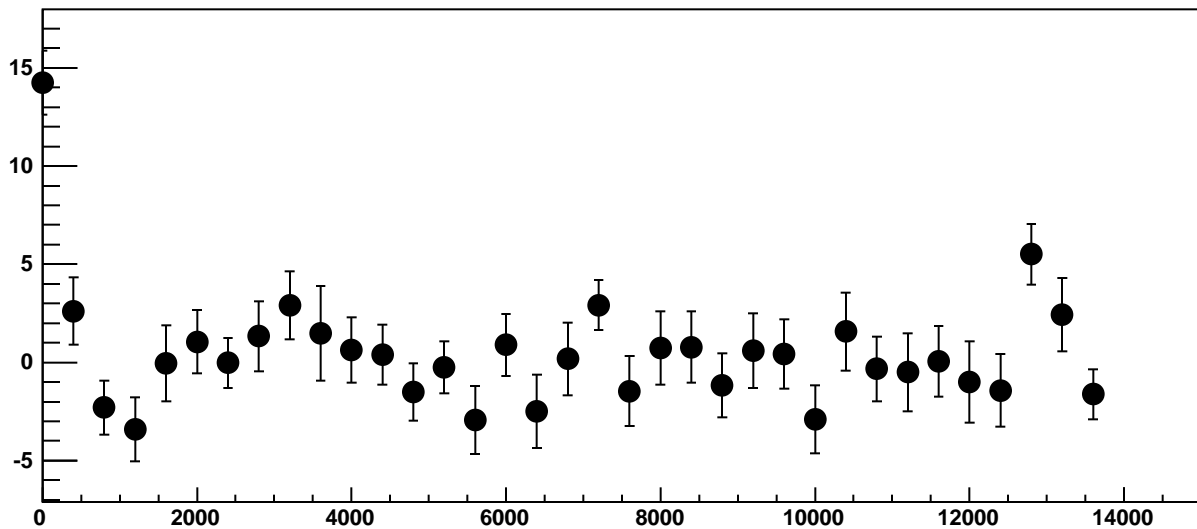


$\chi^2 / \text{ndf}$  25.36 / 23  
p0  $-563.8 \pm 0.7646$   
p1  $0.03095 \pm 0.0001181$

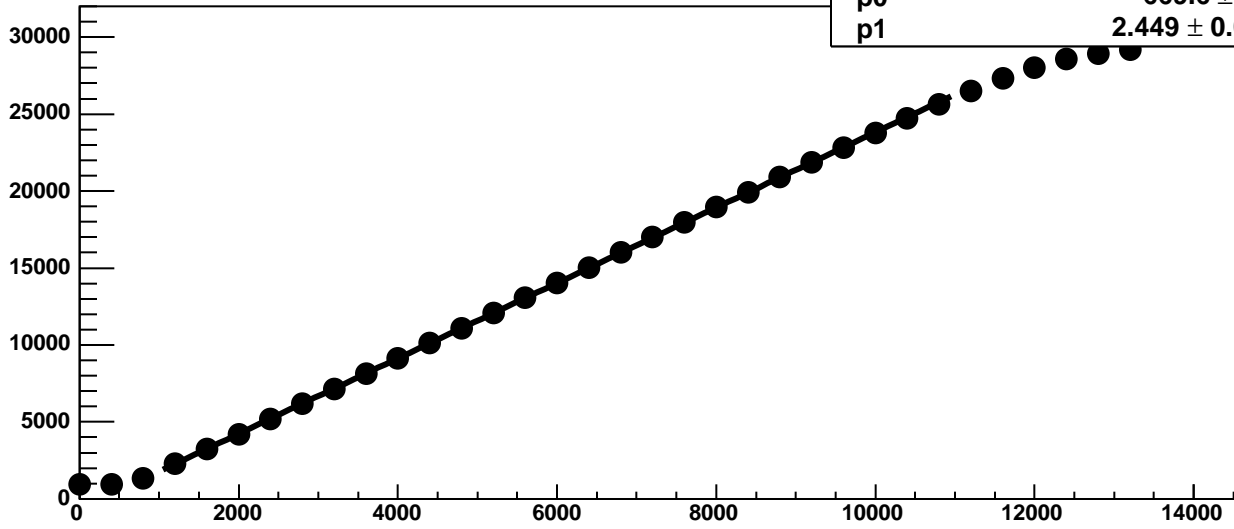
Chip 11, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

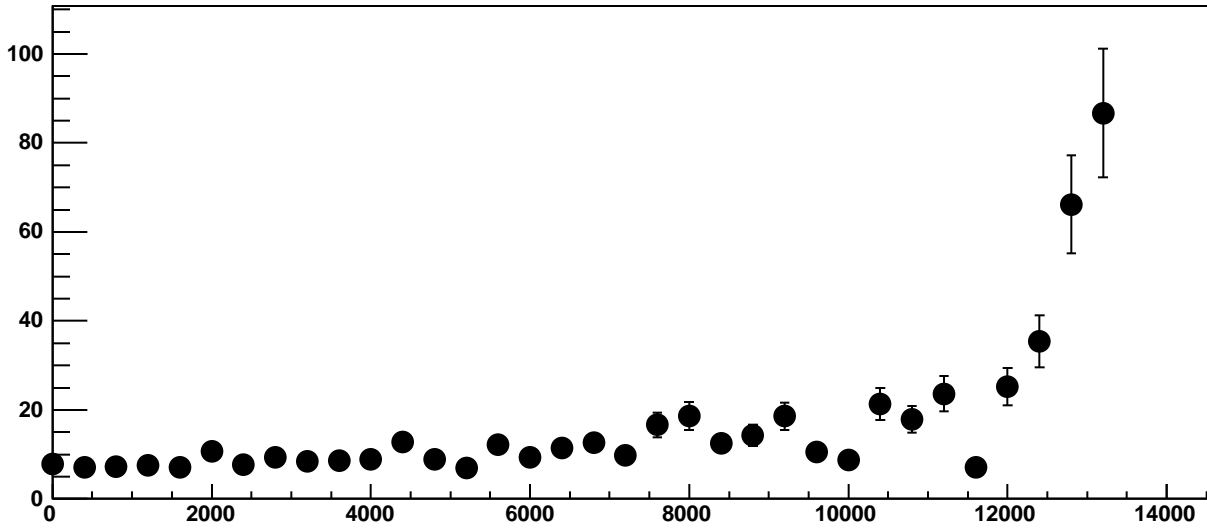


Chip 11, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

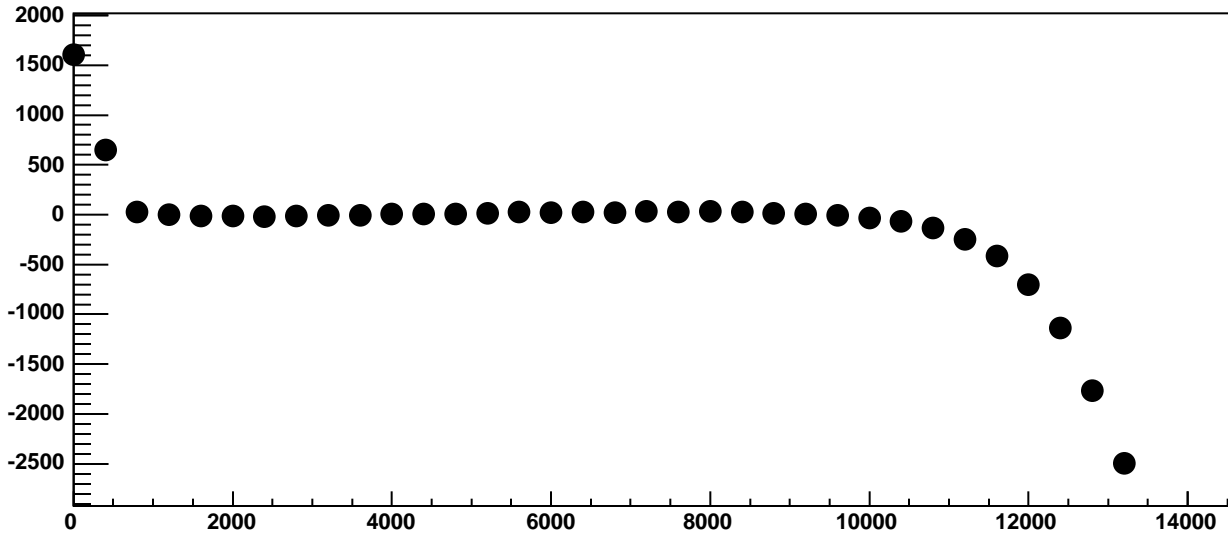


$\chi^2 / \text{ndf}$  2730 / 23  
p0  $-669.6 \pm 0.9526$   
p1  $2.449 \pm 0.000169$

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC

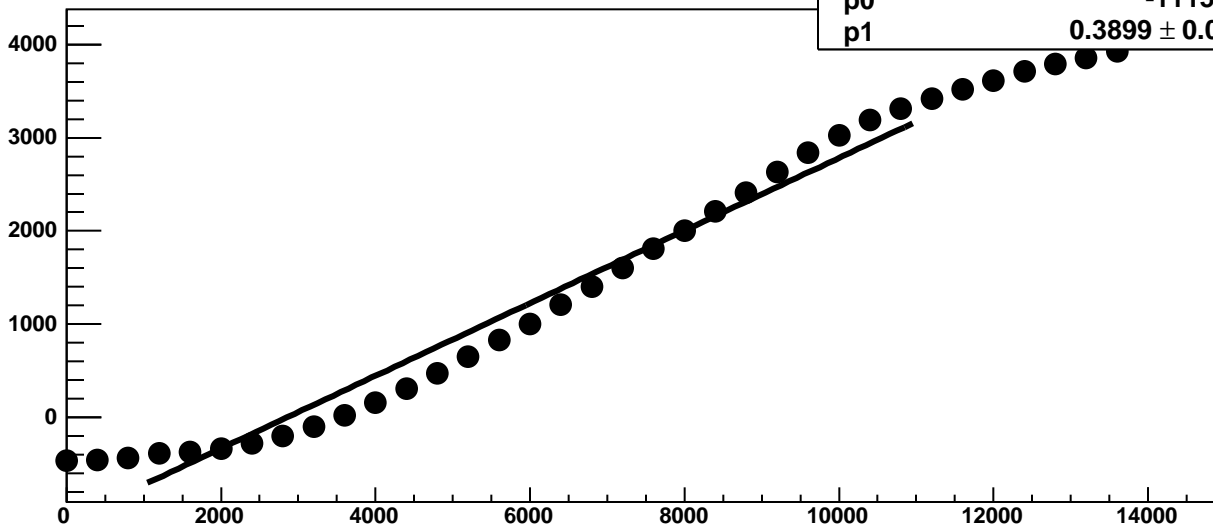


Chip 11, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

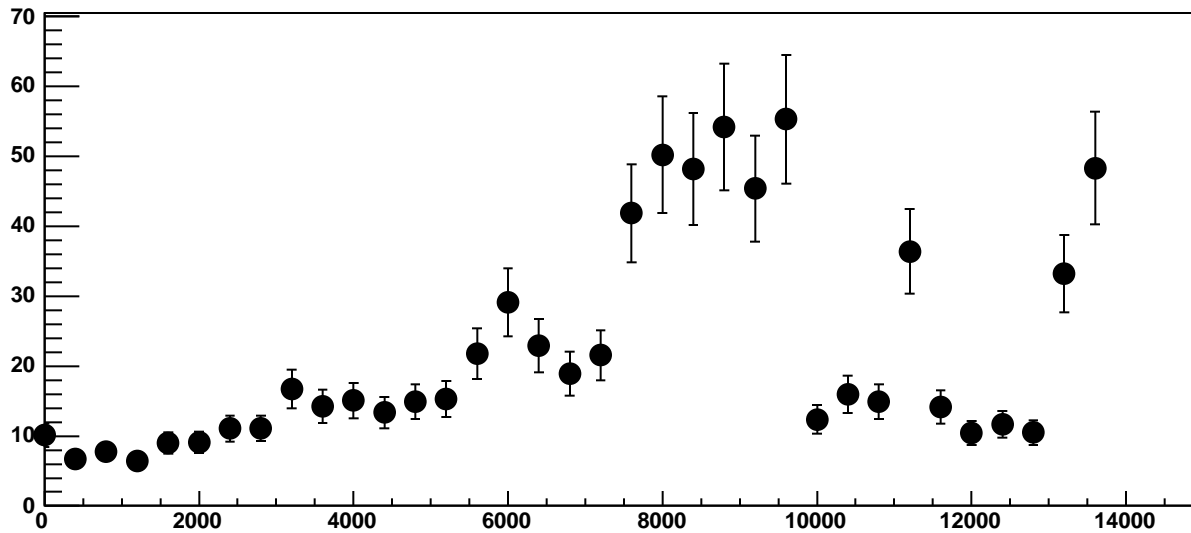


Chip 11, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

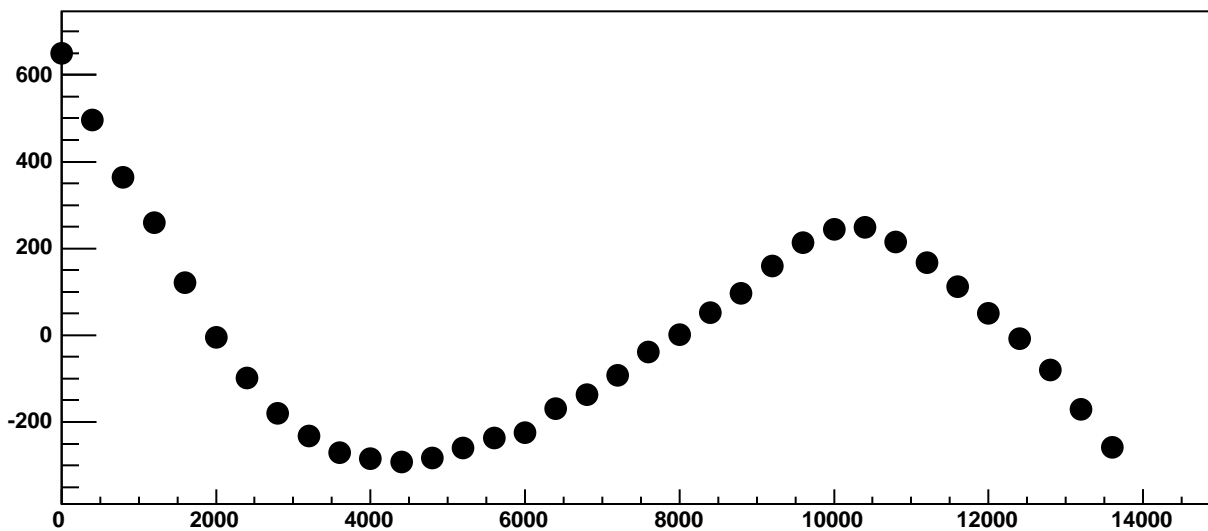
$\chi^2 / \text{ndf}$  1.015e+05 / 23  
p0 -1115 ± 1.099  
p1 0.3899 ± 0.0002215



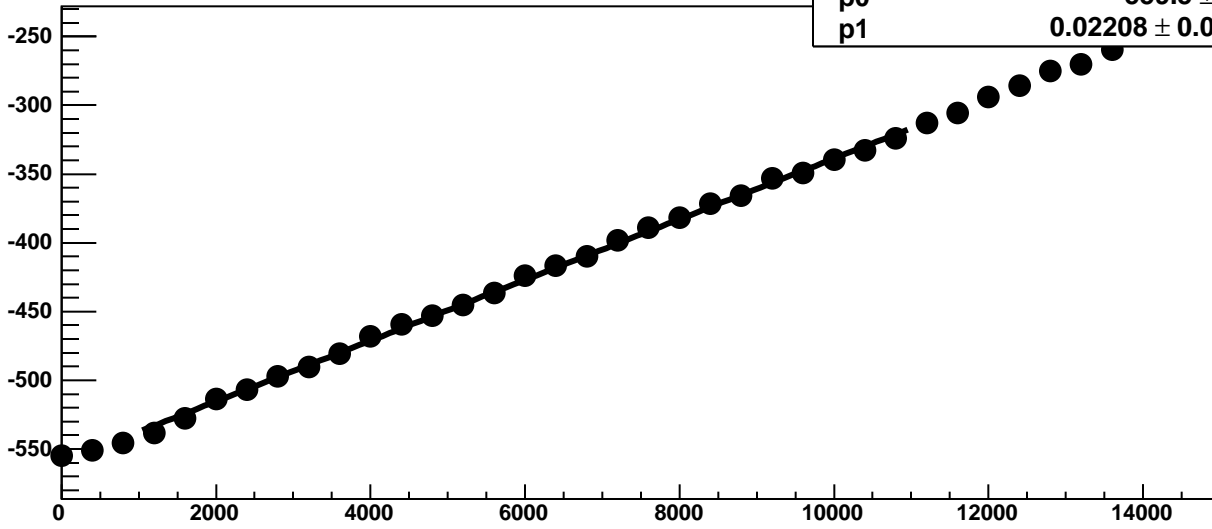
Chip 11, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

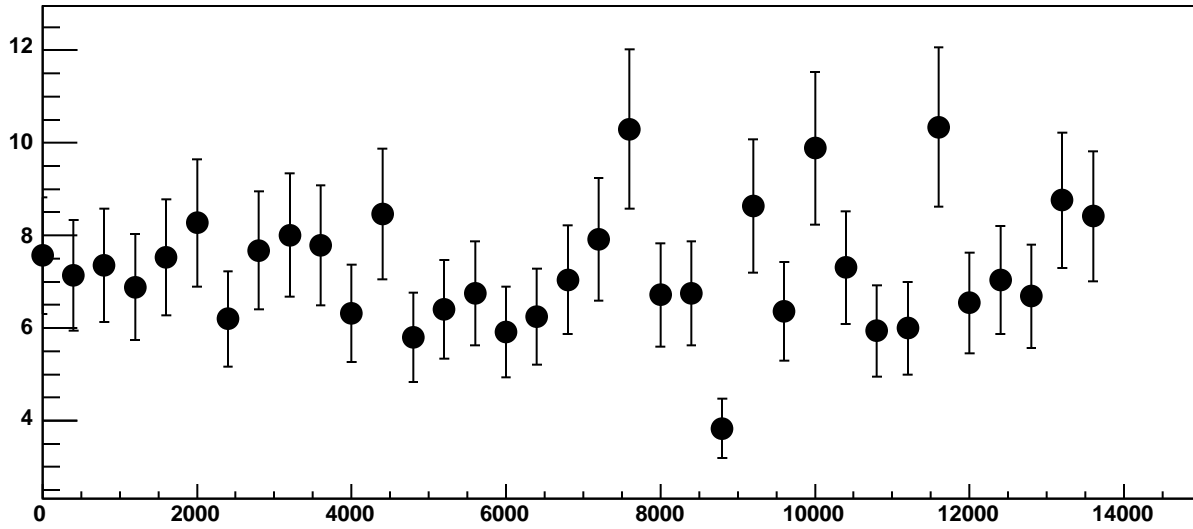


Chip 11, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

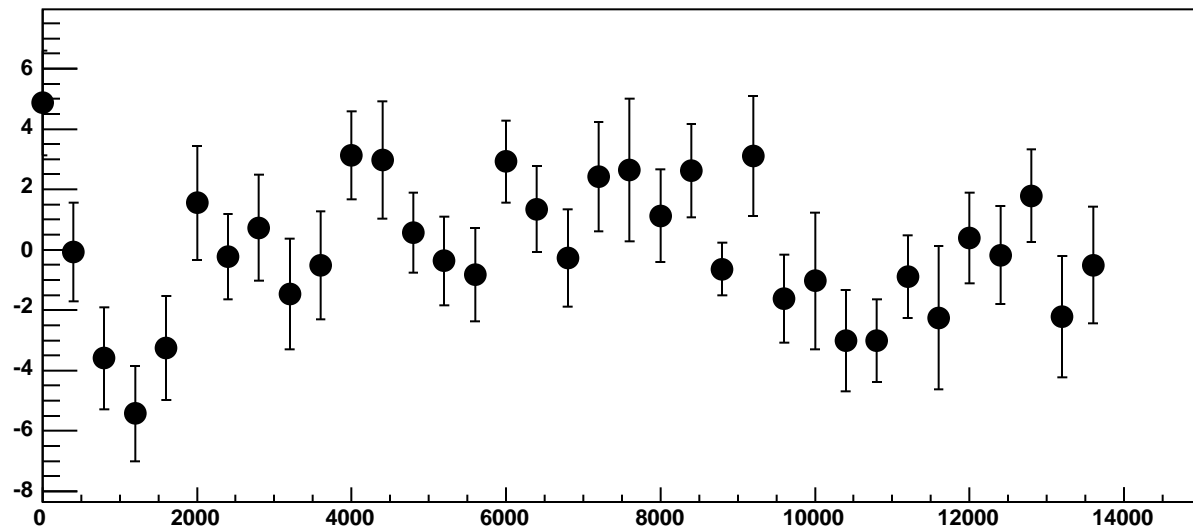


$\chi^2 / \text{ndf}$  48.95 / 23  
p0  $-559.5 \pm 0.7492$   
p1  $0.02208 \pm 0.0001093$

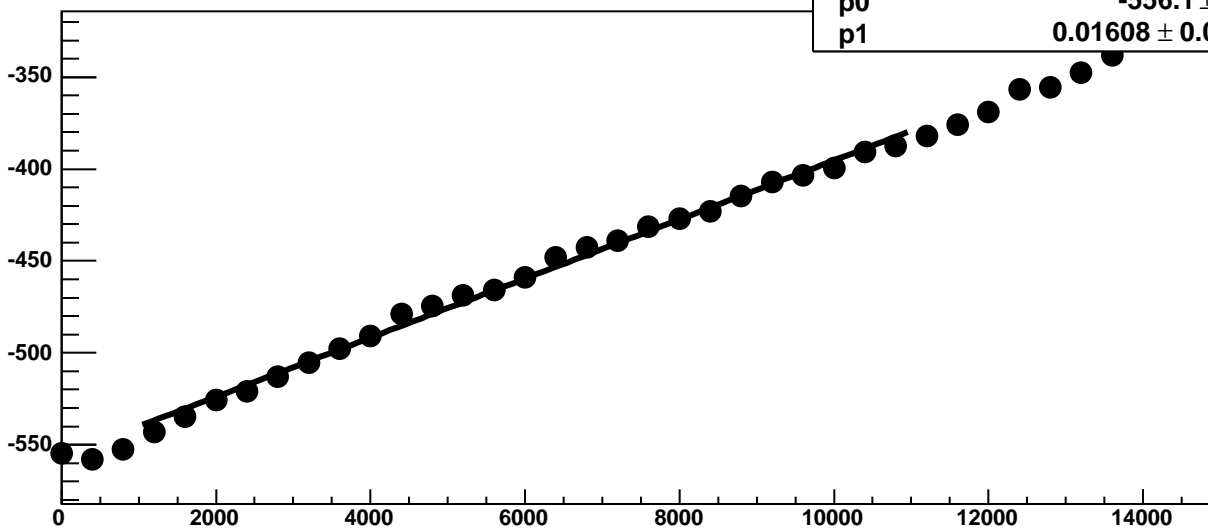
Chip 11, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

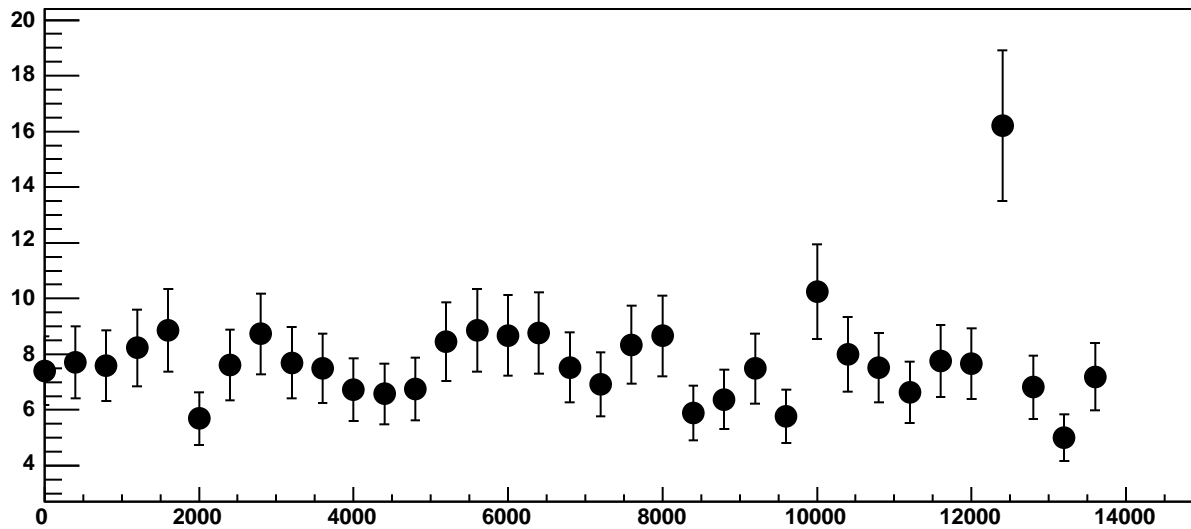


Chip 11, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

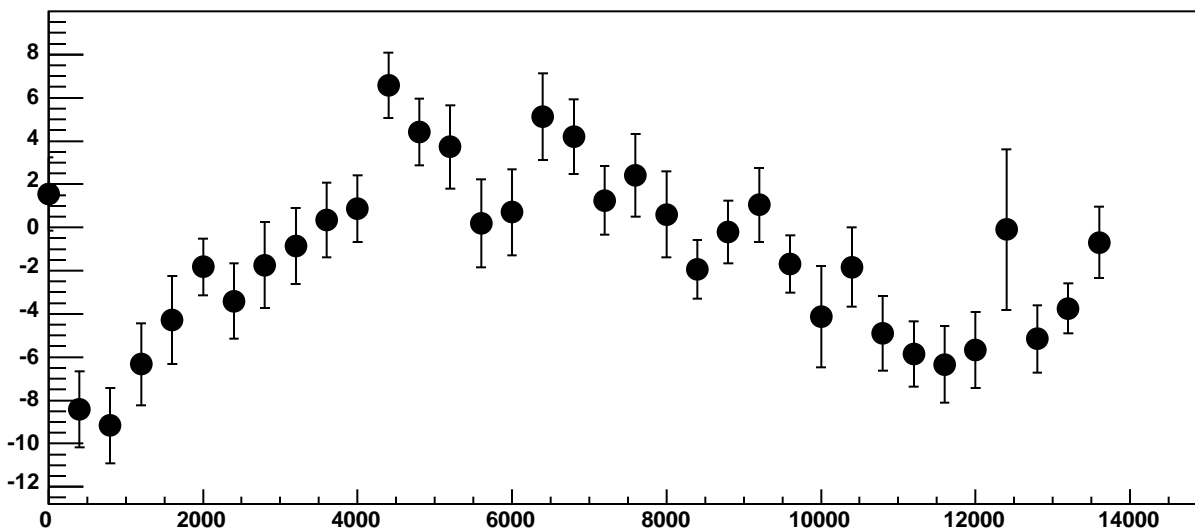


$\chi^2 / \text{ndf}$  84.84 / 23  
p0  $-556.1 \pm 0.7897$   
p1  $0.01608 \pm 0.0001179$

Chip 11, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

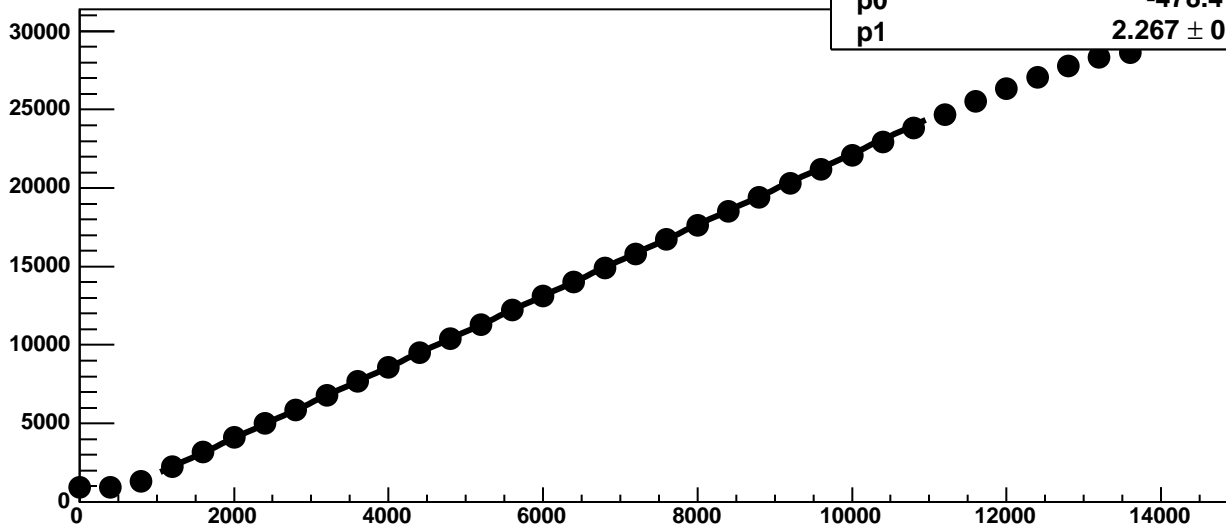


Chip 11, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

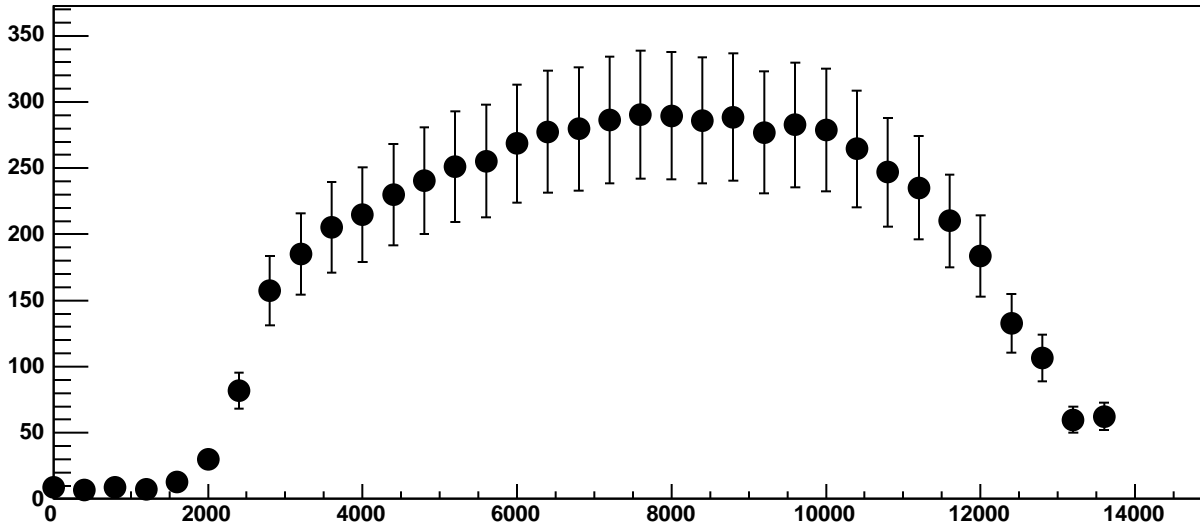


Chip 11, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC

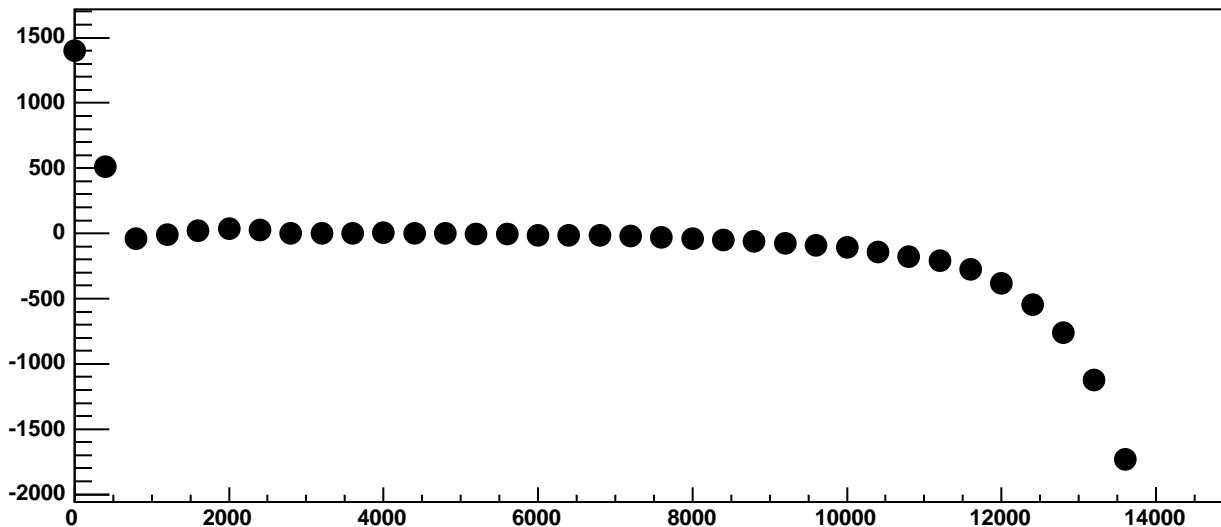
$\chi^2 / \text{ndf}$  153.4 / 23  
p0  $-478.4 \pm 3.304$   
p1  $2.267 \pm 0.002125$



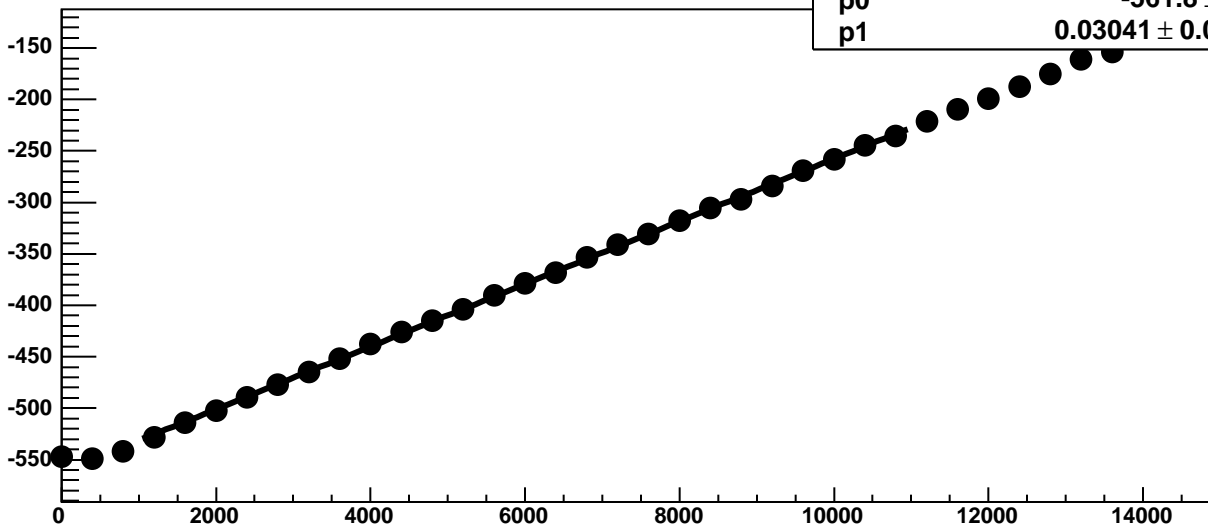
Chip 11, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC



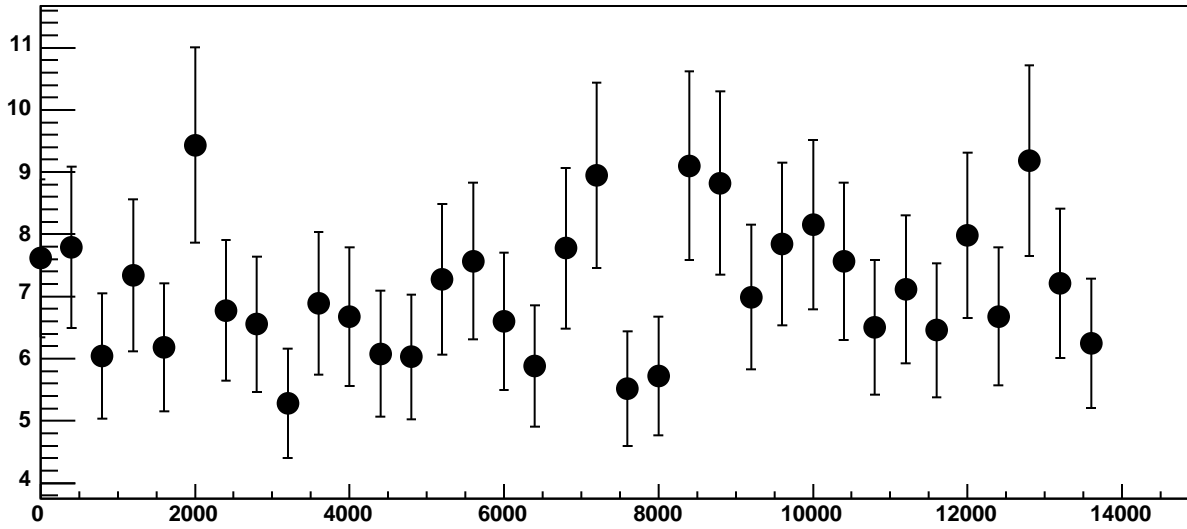
Chip 11, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC



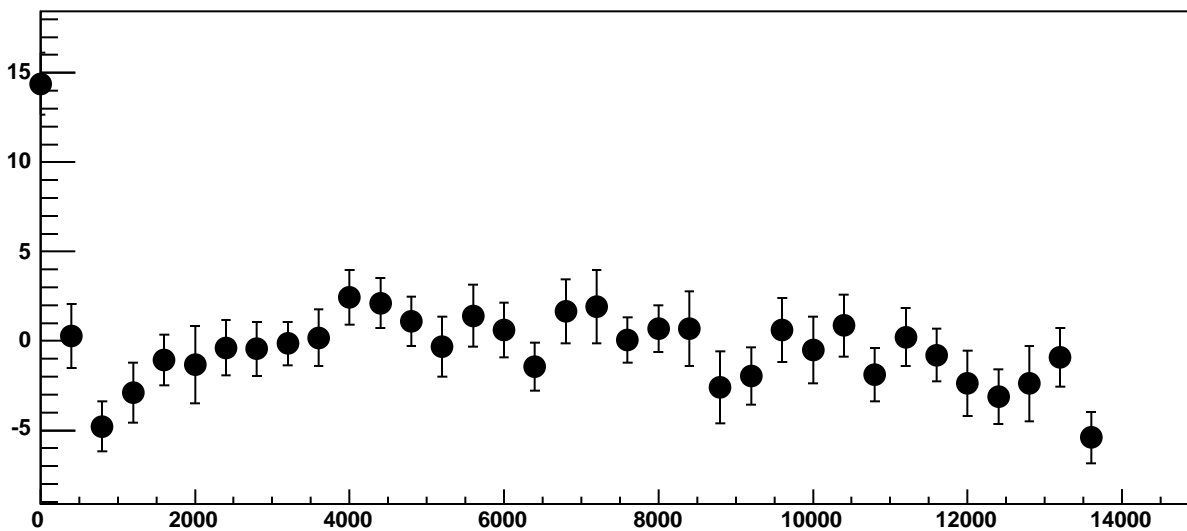
Chip 11, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



Chip 11, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

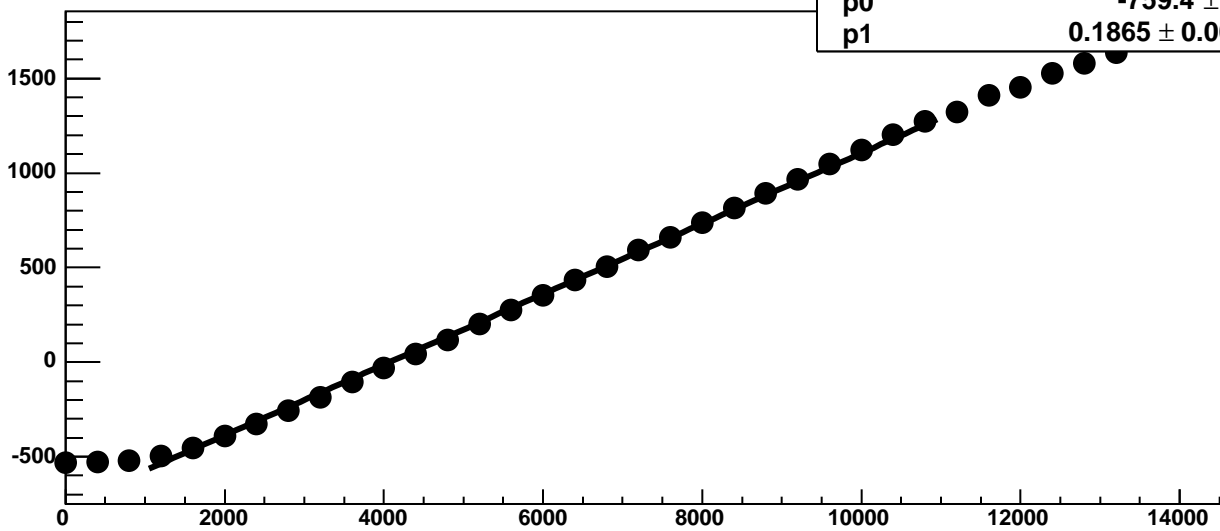


Chip 11, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

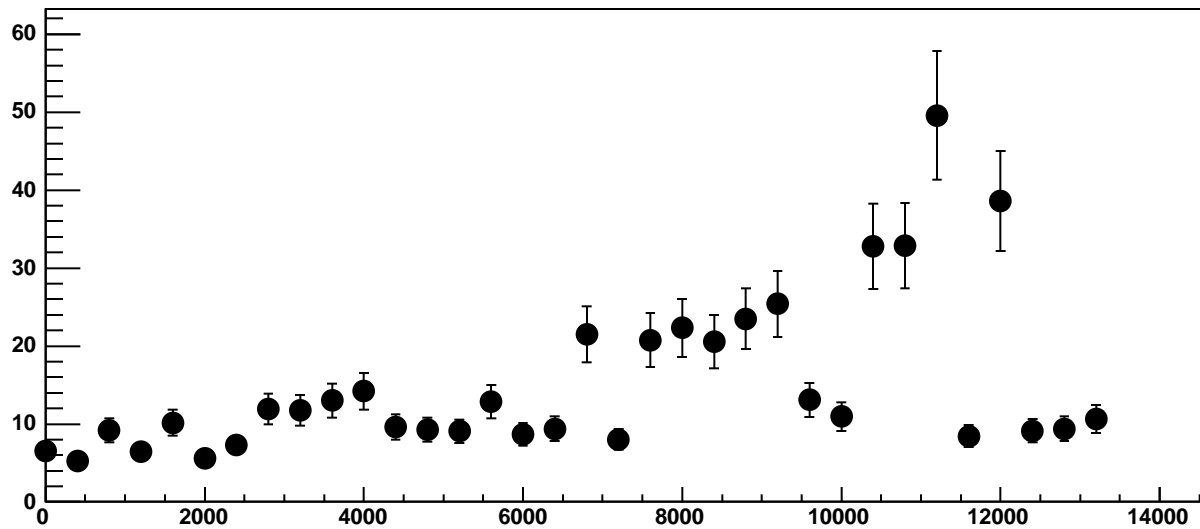




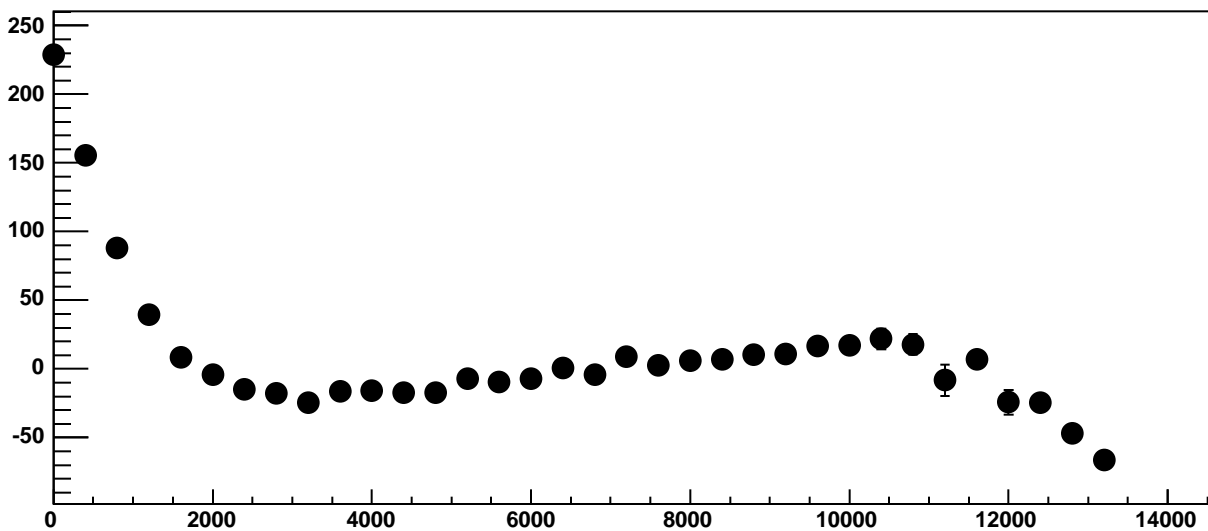
Chip 11, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

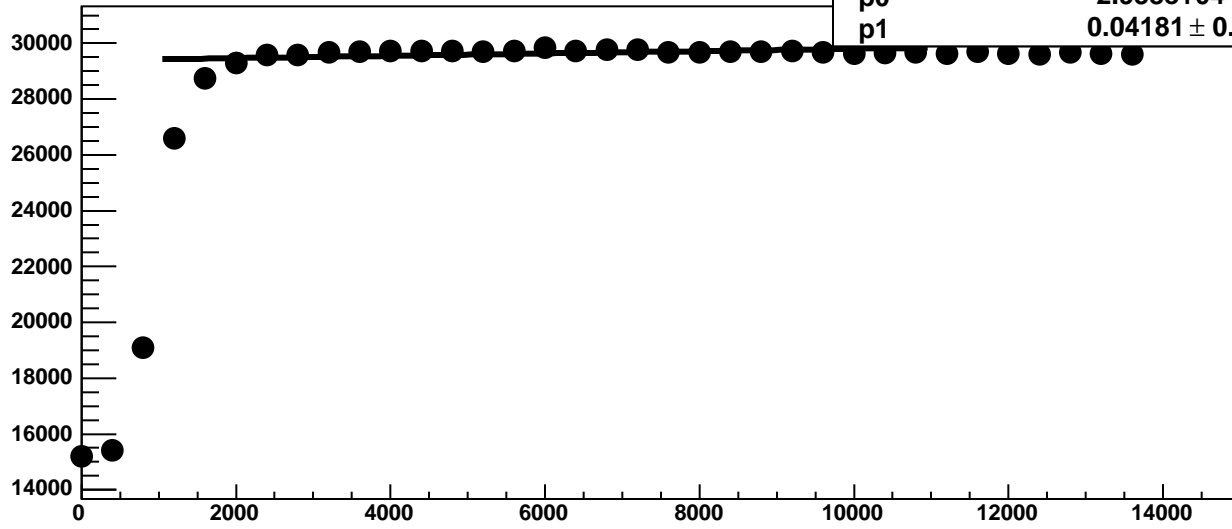
1687 / 23

p0

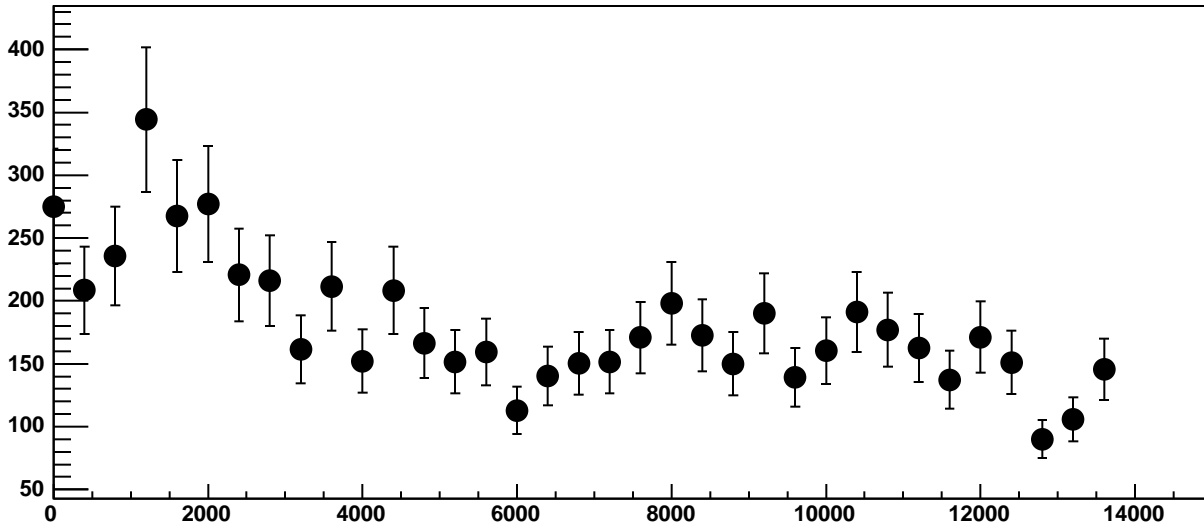
$2.938\text{e}+04 \pm 21.92$

p1

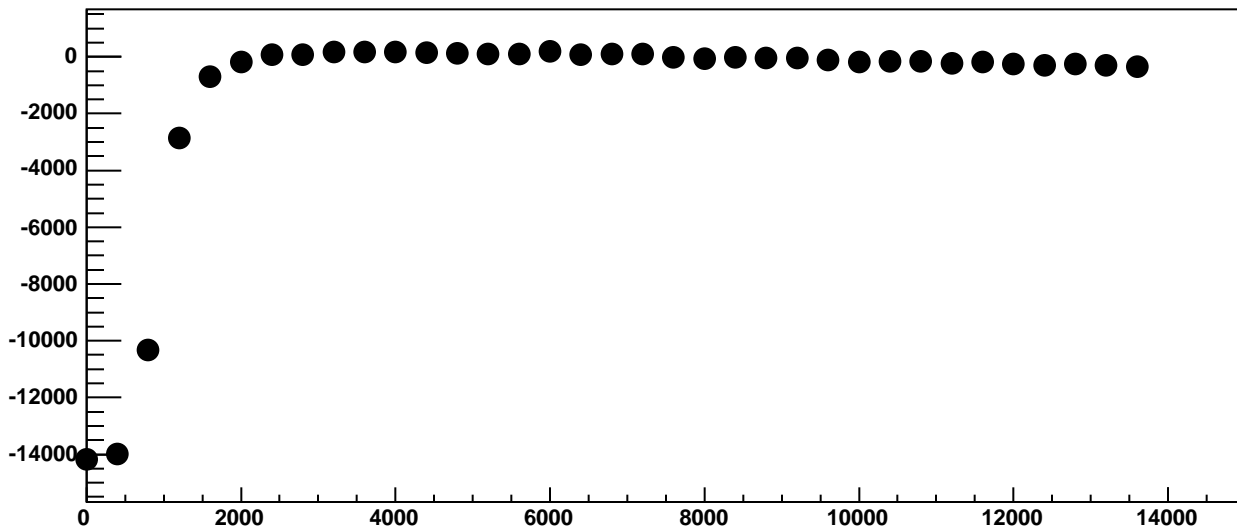
$0.04181 \pm 0.003139$



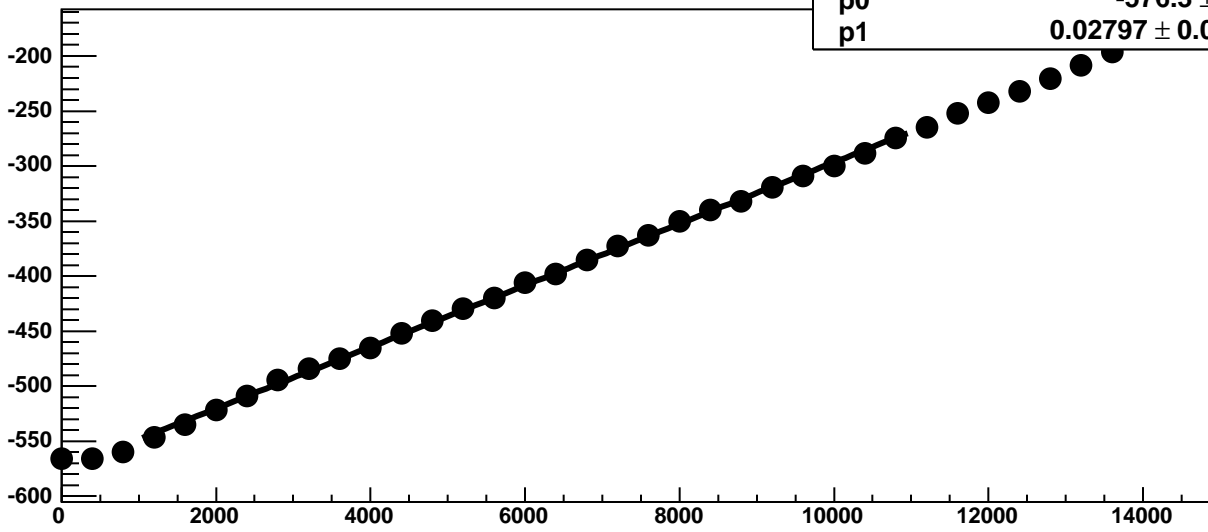
Chip 11, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

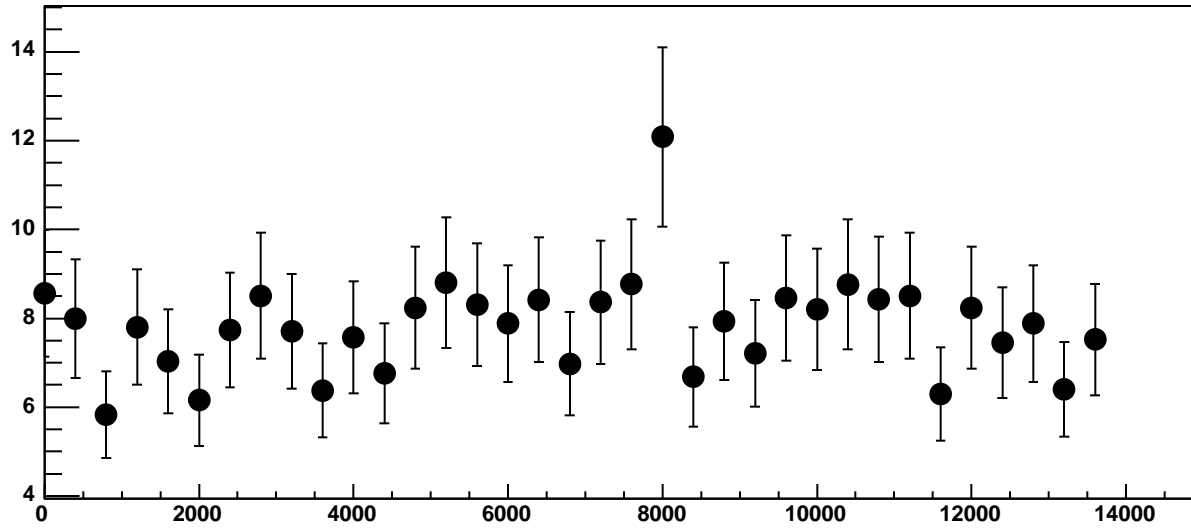


Chip 11, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

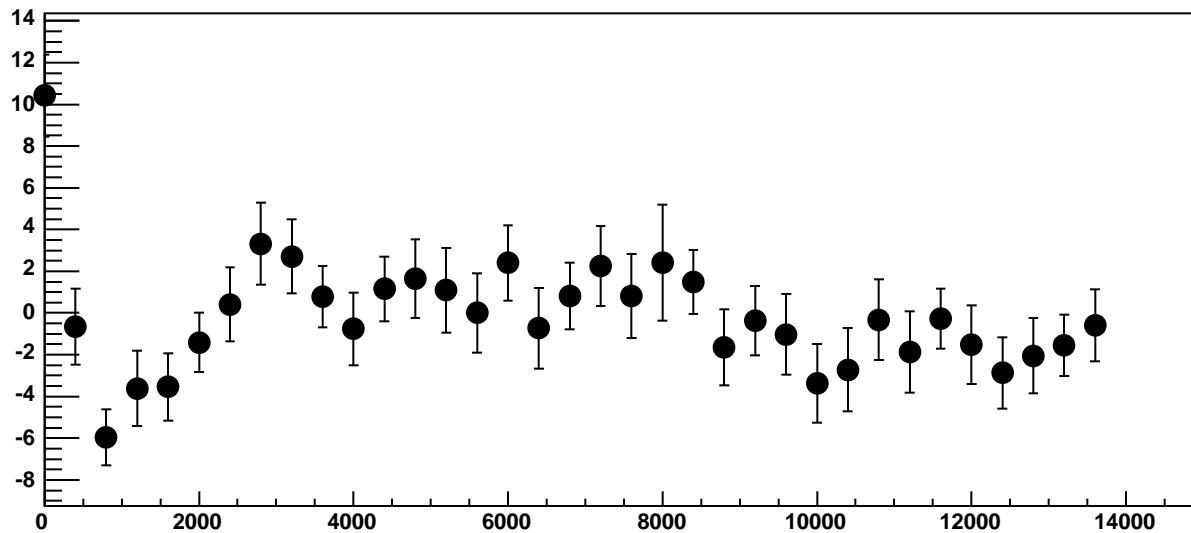


$\chi^2 / \text{ndf}$  28.88 / 23  
p0  $-576.3 \pm 0.7869$   
p1  $0.02797 \pm 0.0001228$

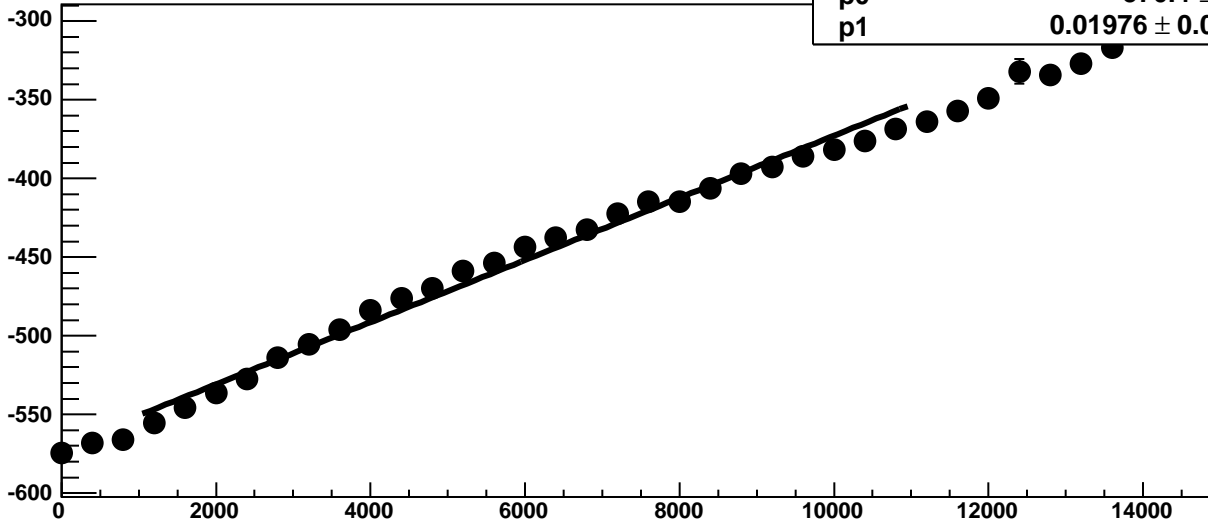
Chip 11, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



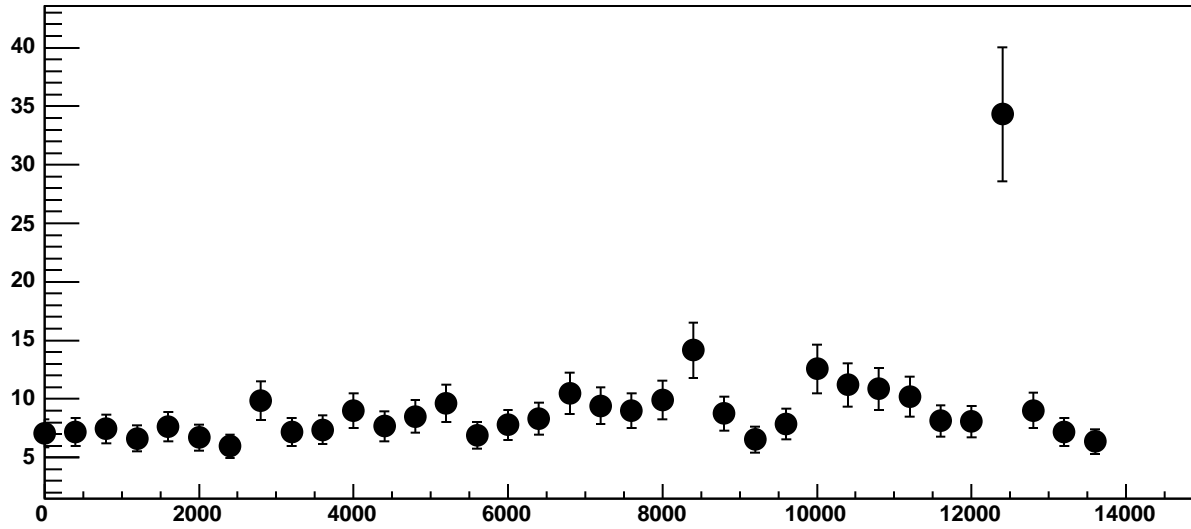
Chip 11, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



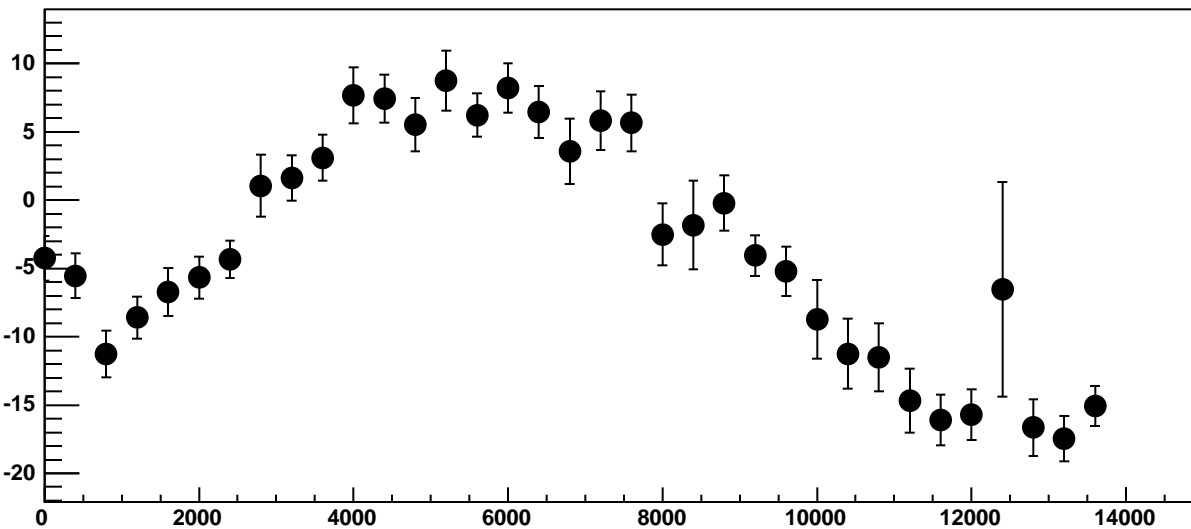
Chip 11, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



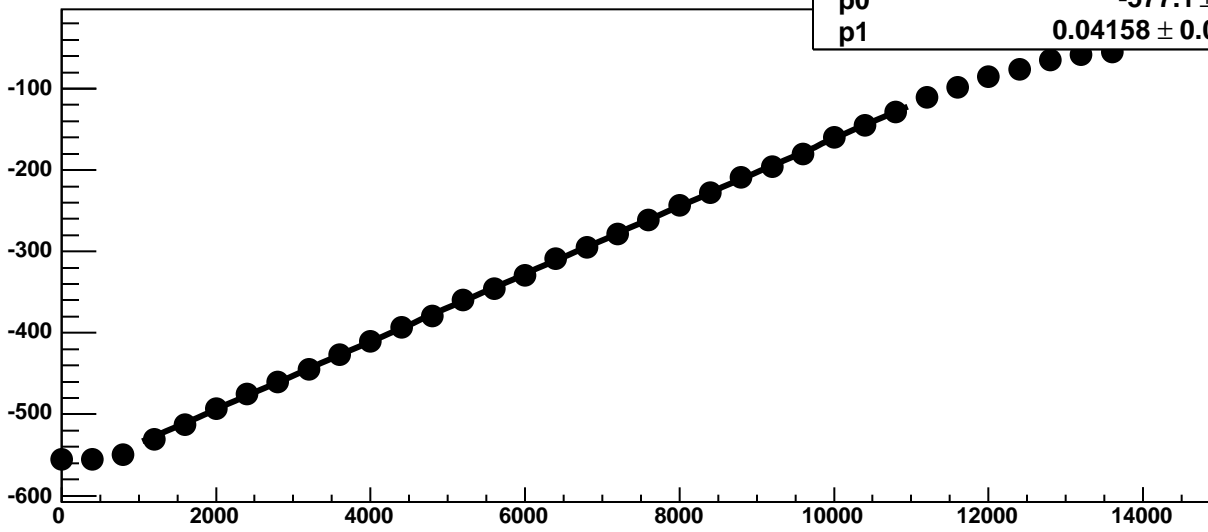
Chip 11, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



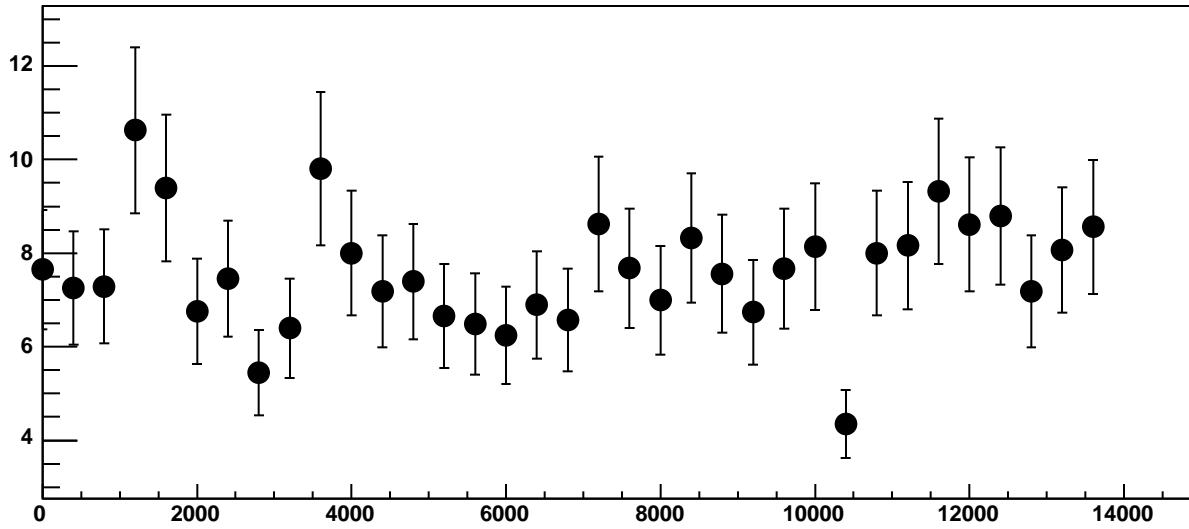
Chip 11, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



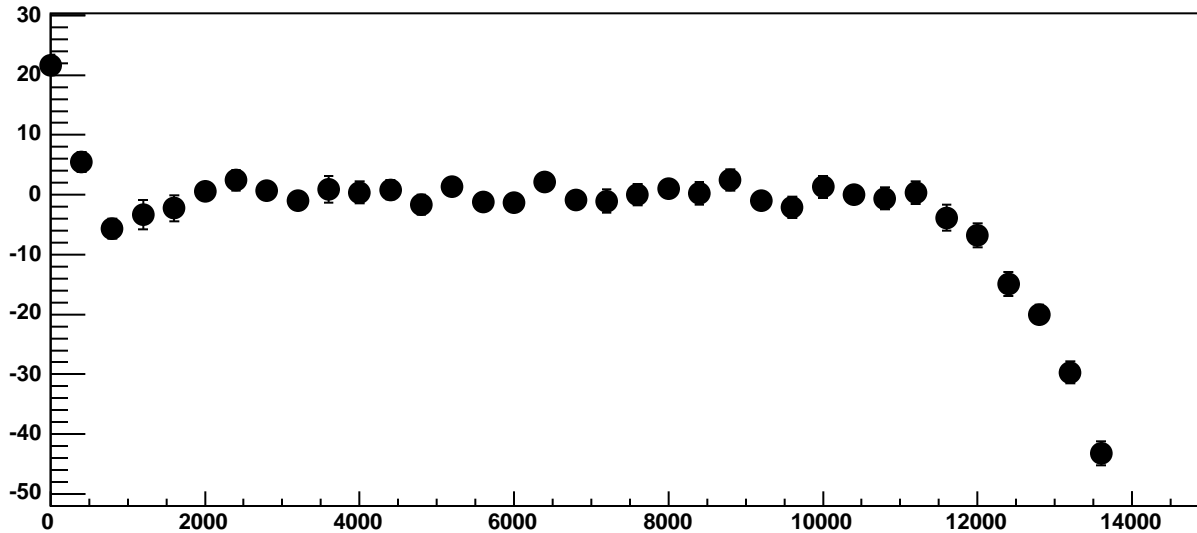
Chip 11, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC



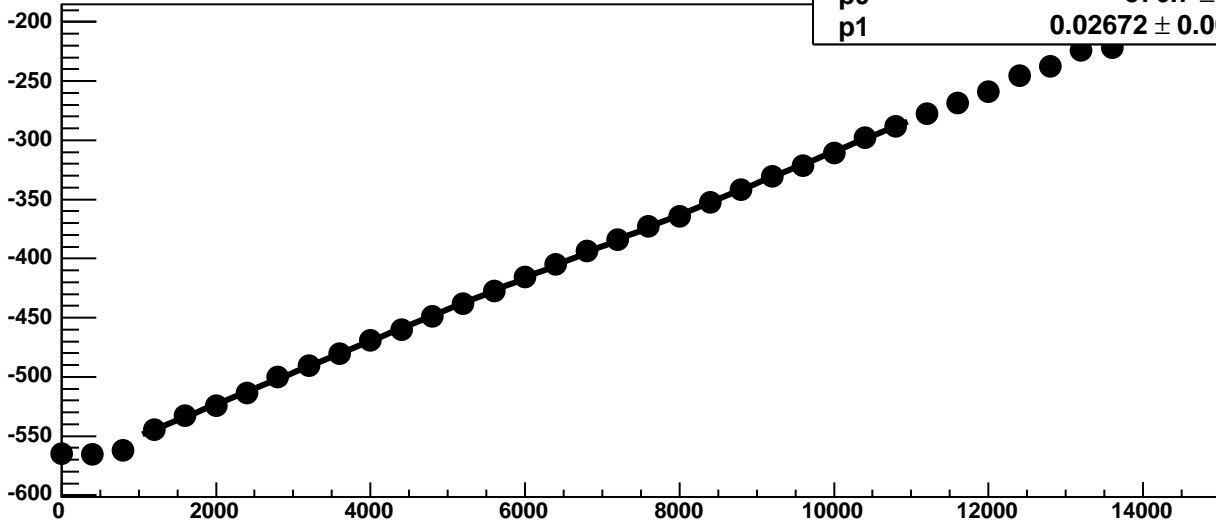
Chip 11, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC



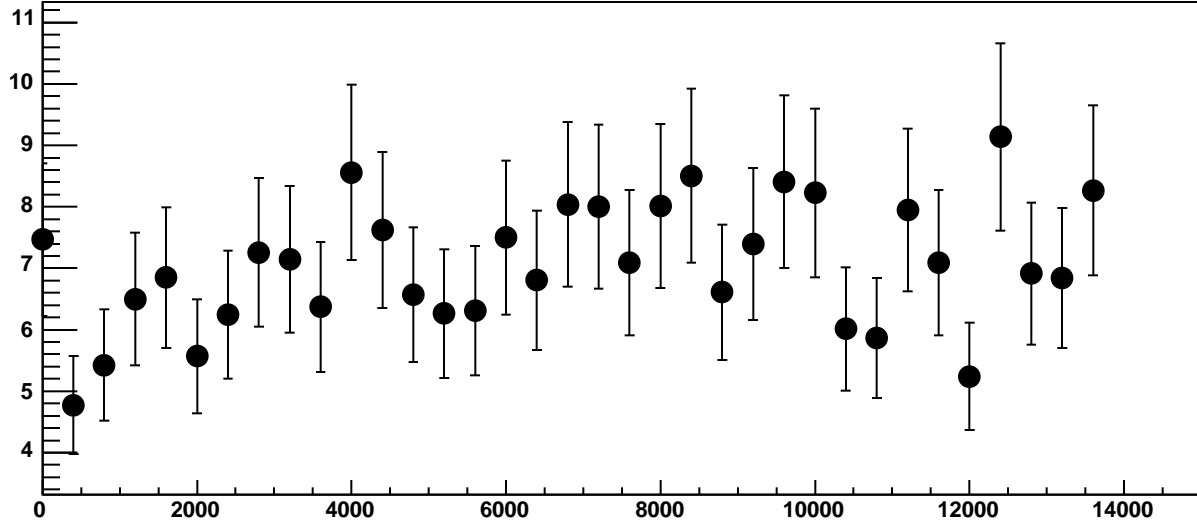
Chip 11, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC



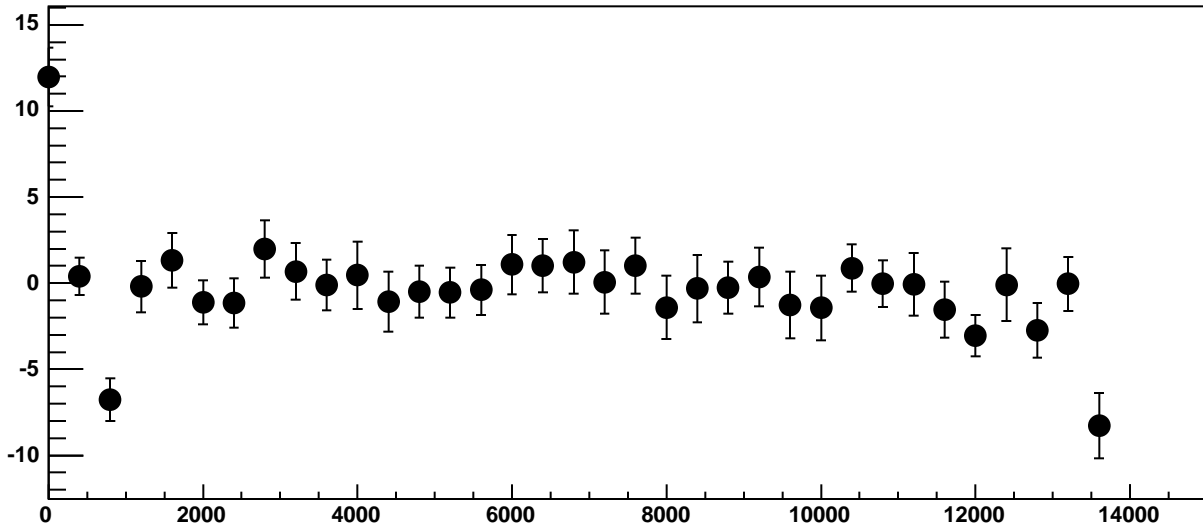
Chip 11, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



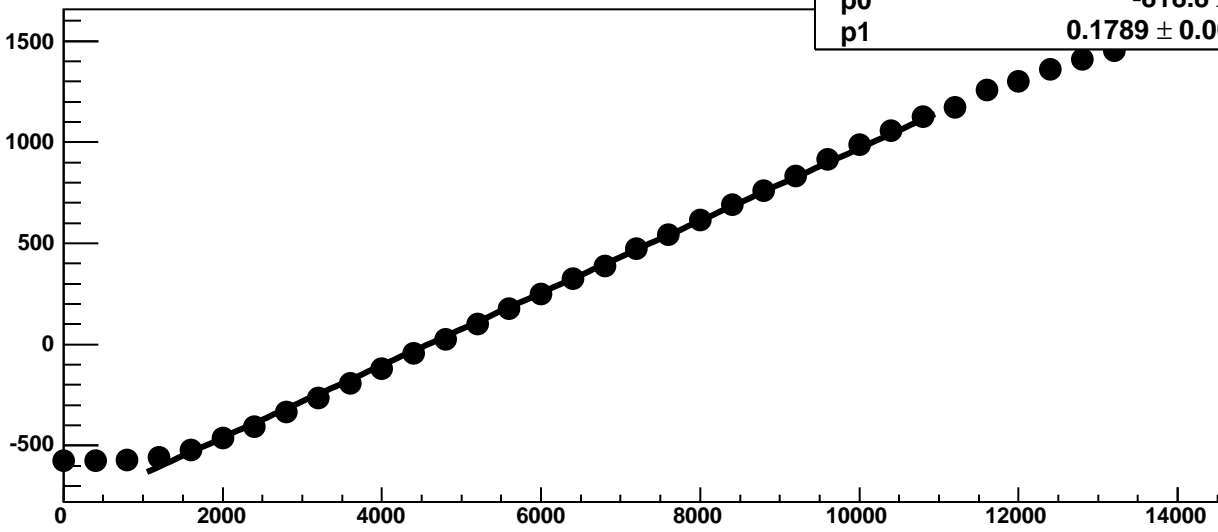
Chip 11, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



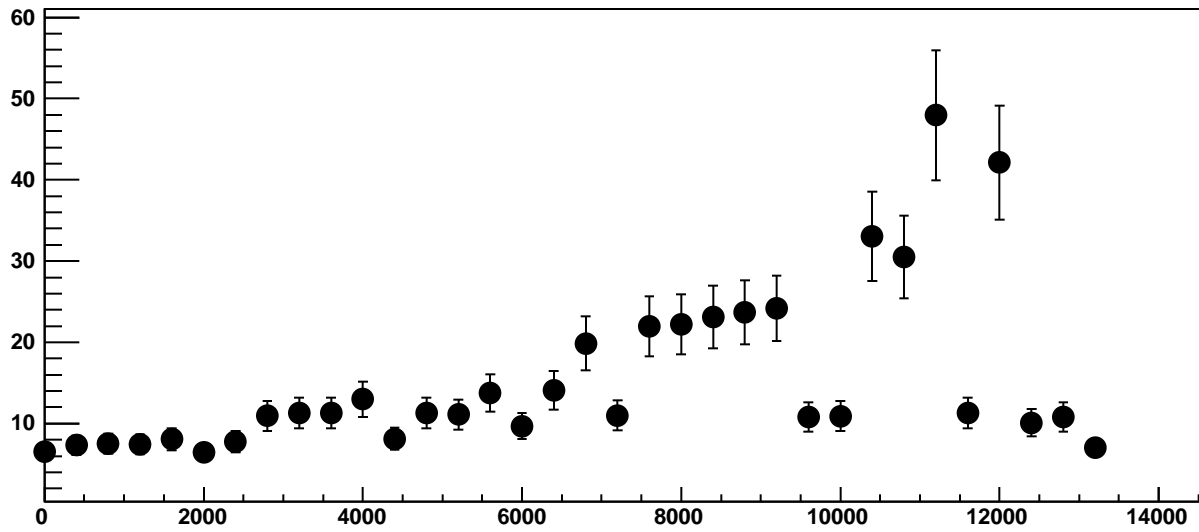
Chip 11, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



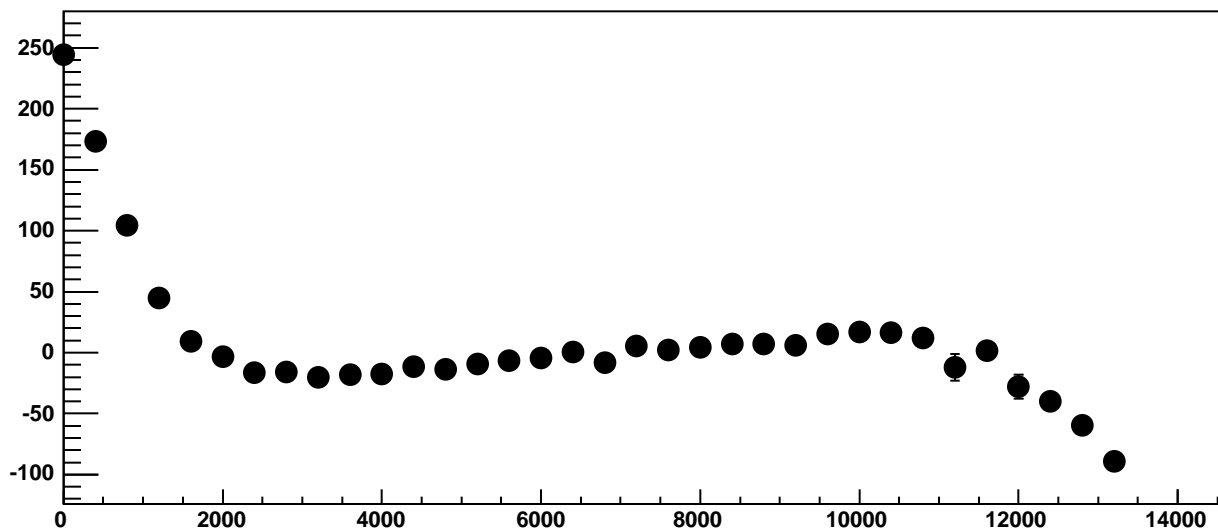
Chip 11, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

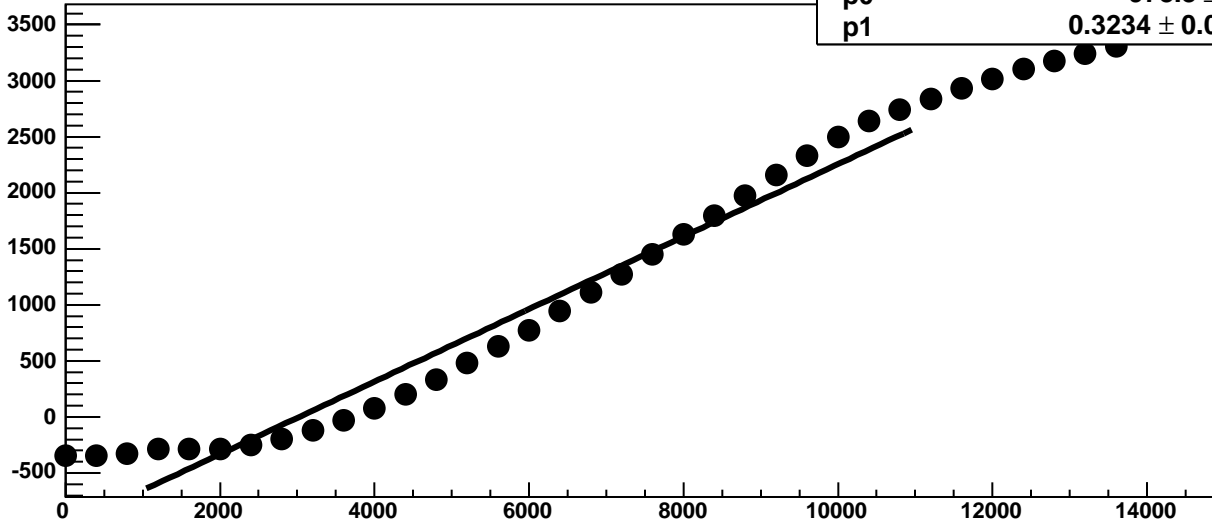


Chip 11, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

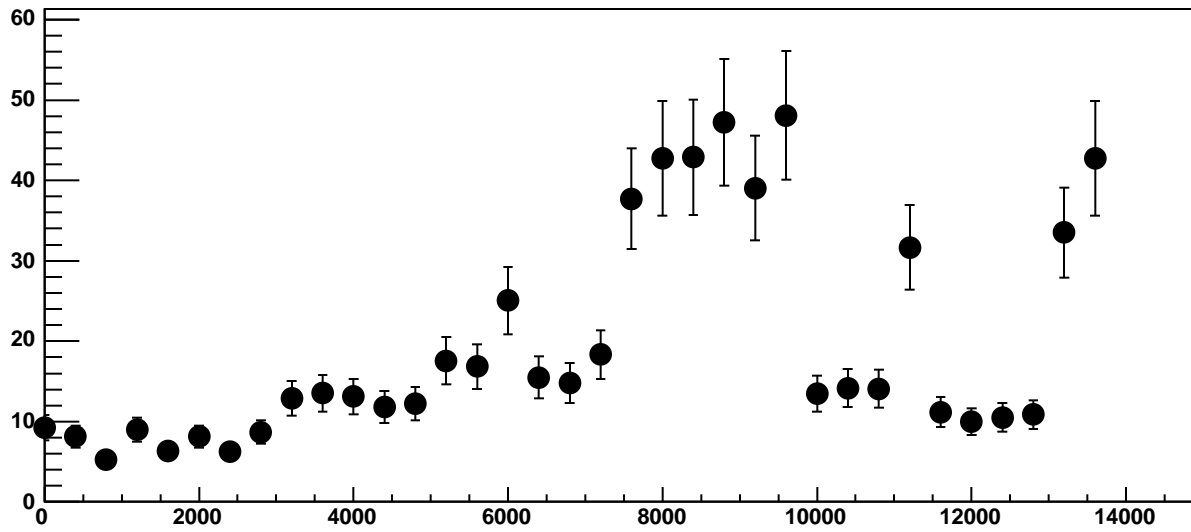


Chip 11, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

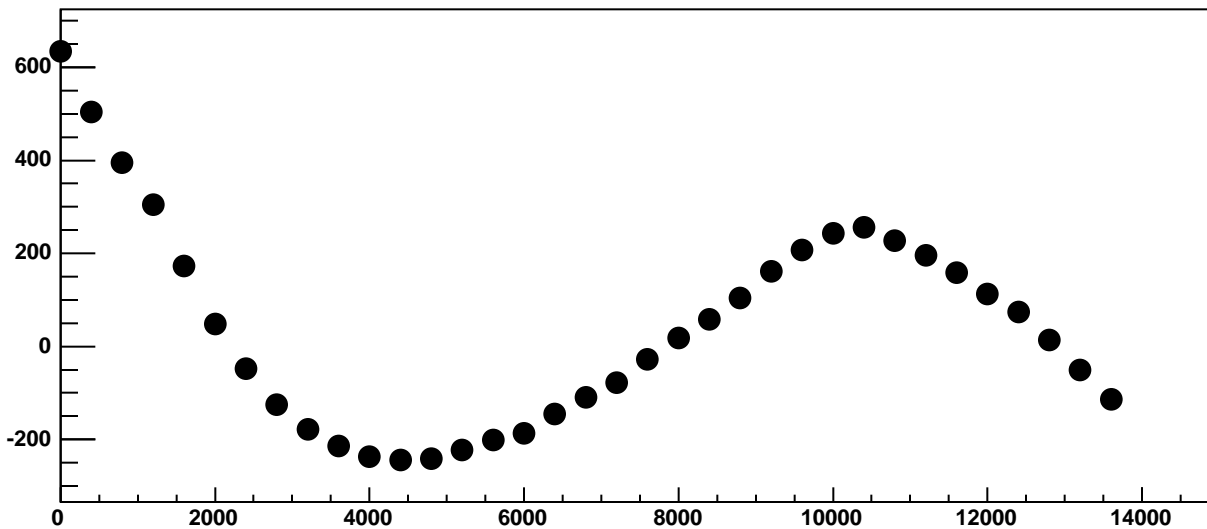
$\chi^2 / \text{ndf}$  9.996e+04 / 23  
p0 -978.8 ± 0.9889  
p1 0.3234 ± 0.0002085



Chip 11, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

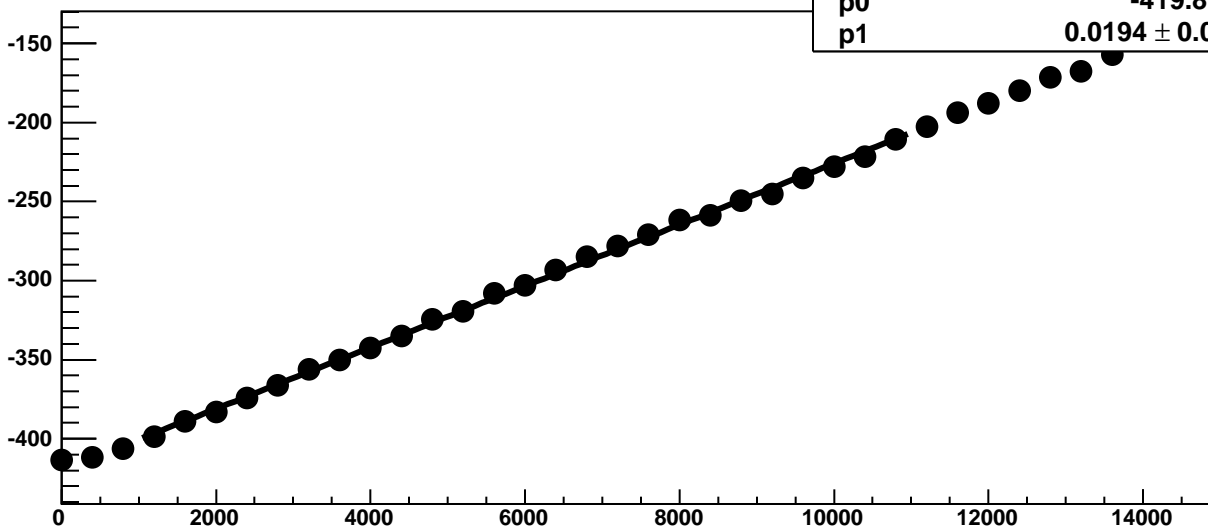


Chip 11, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



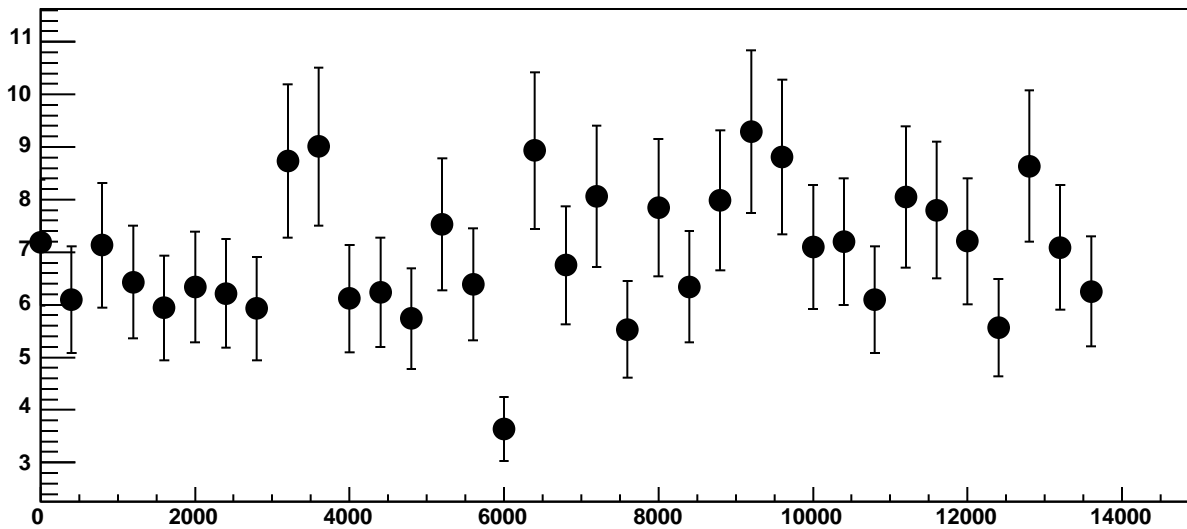


Chip 11, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

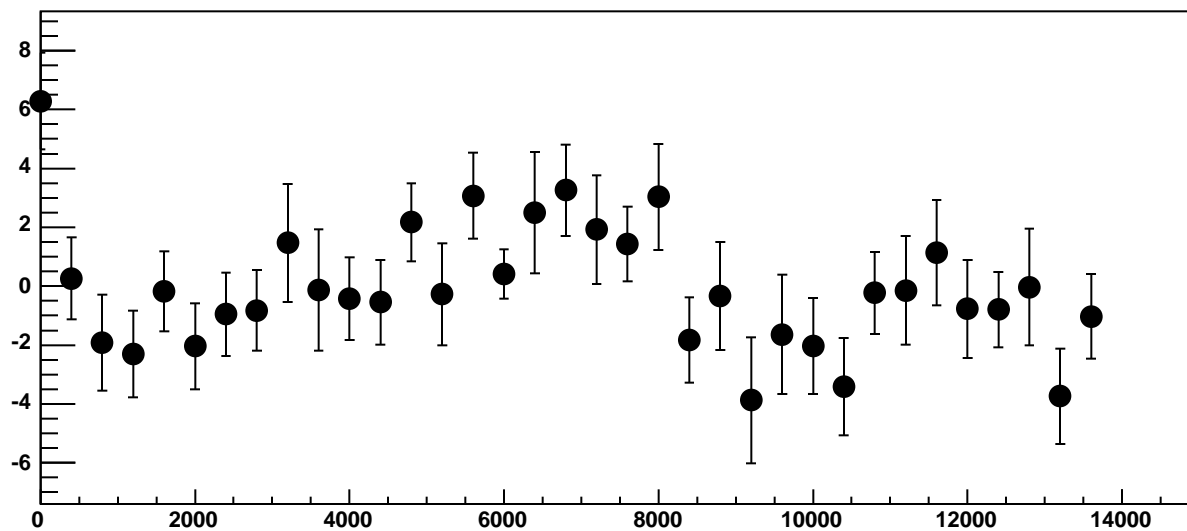


$\chi^2 / \text{ndf}$  35.95 / 23  
p0  $-419.8 \pm 0.688$   
p1  $0.0194 \pm 0.0001075$

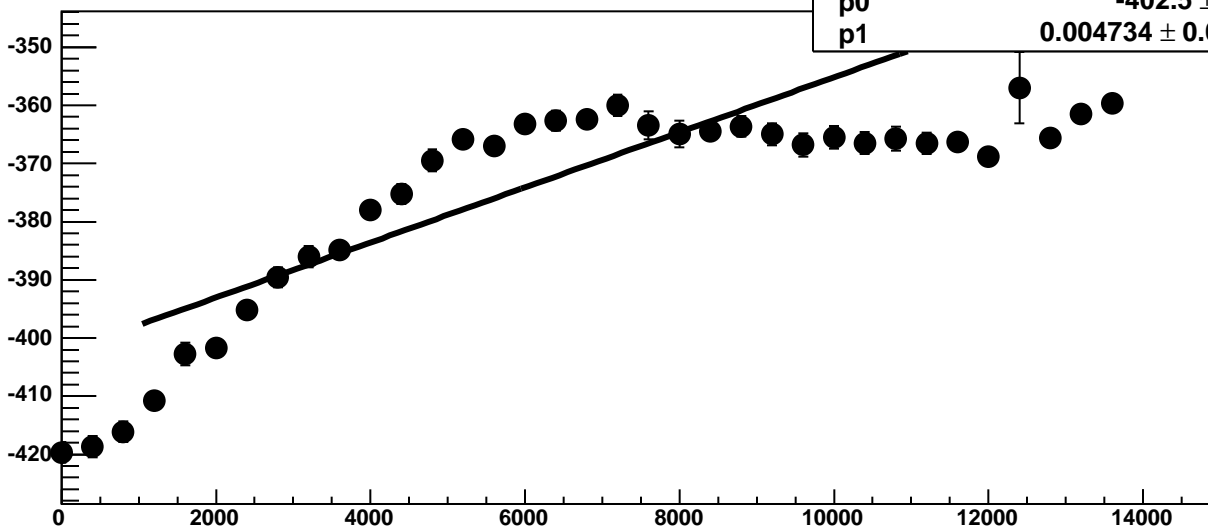
Chip 11, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



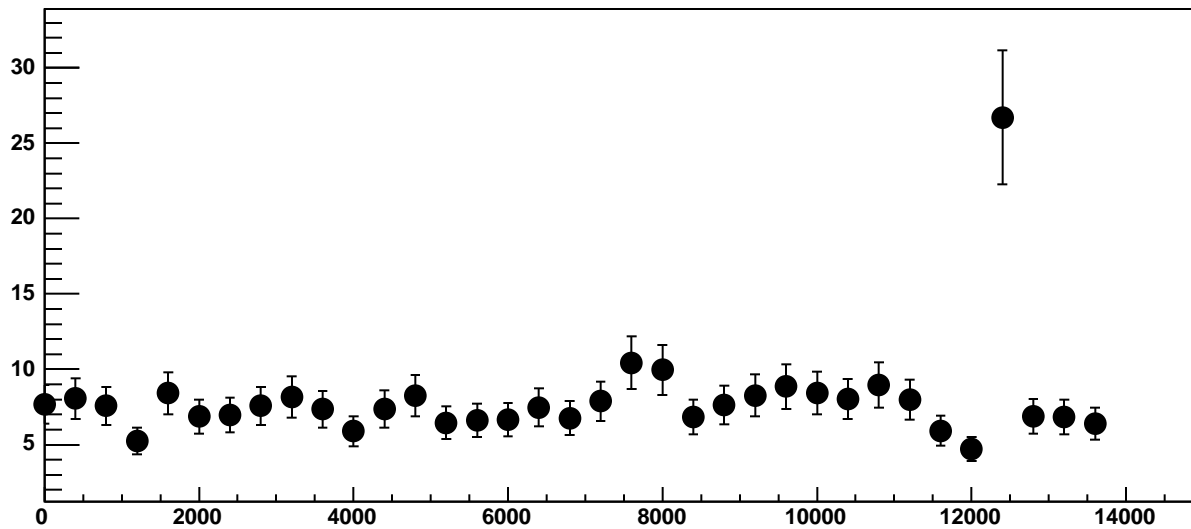
Chip 11, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



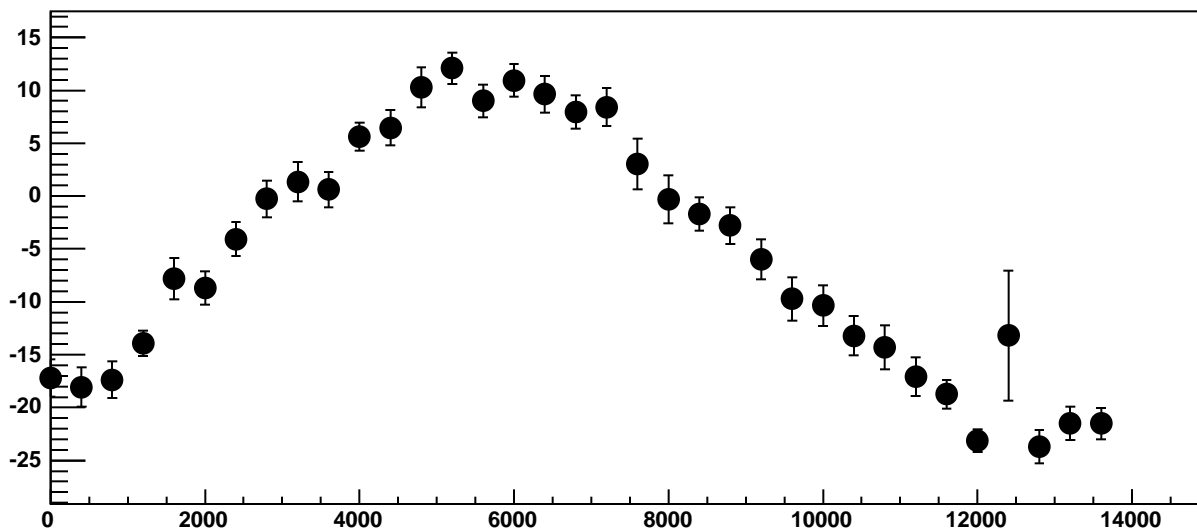
Chip 11, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



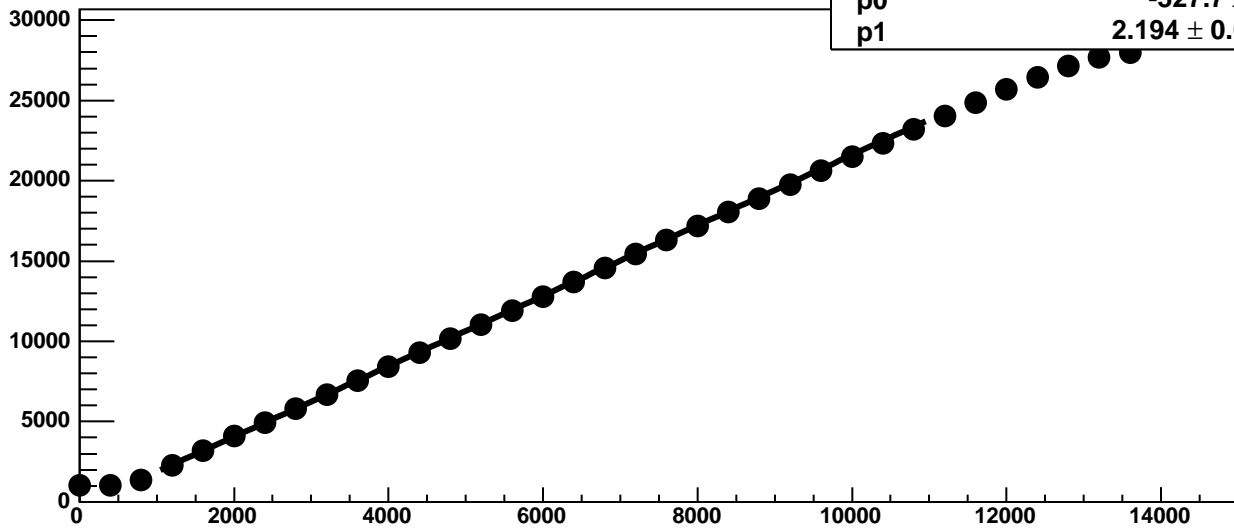
Chip 11, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



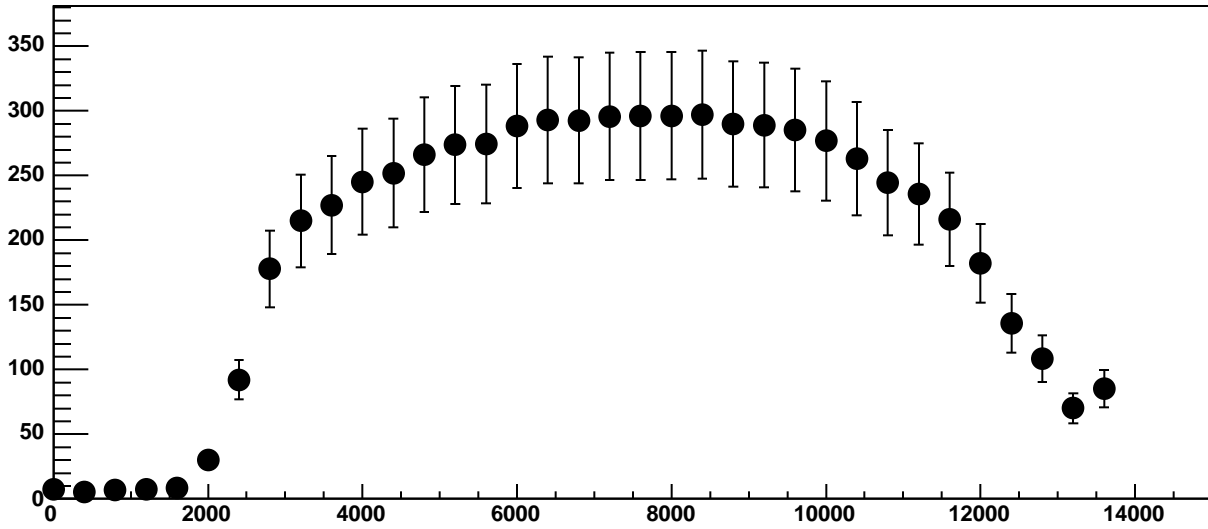
Chip 11, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



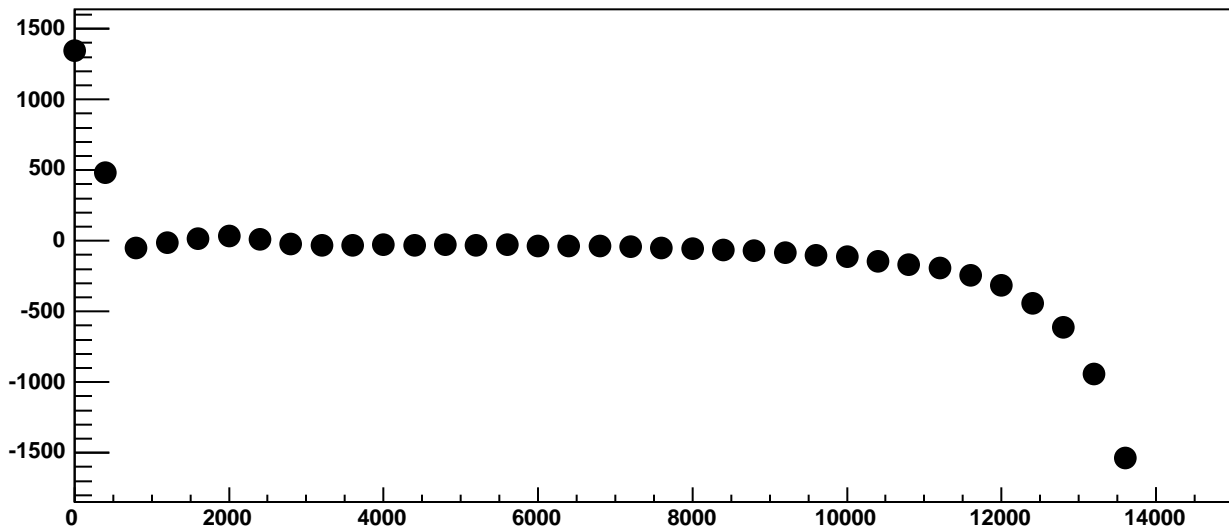
Chip 11, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC



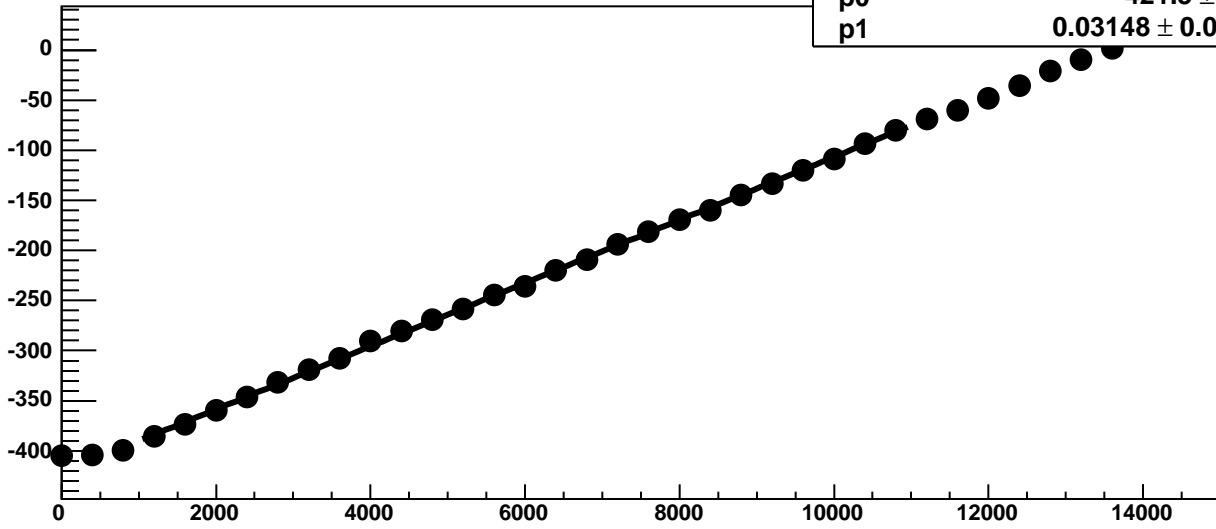
Chip 11, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC

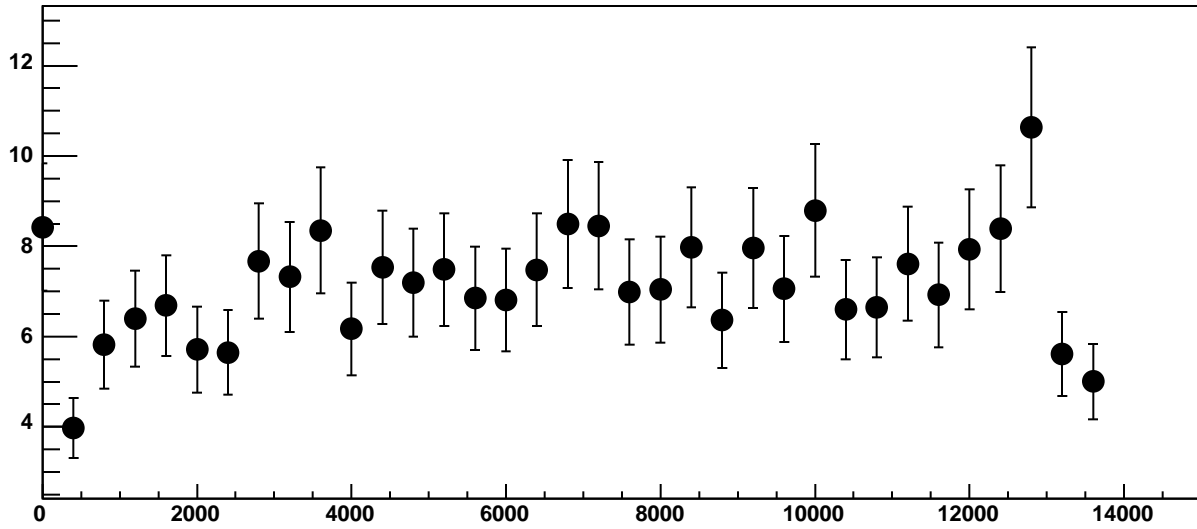


Chip 11, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

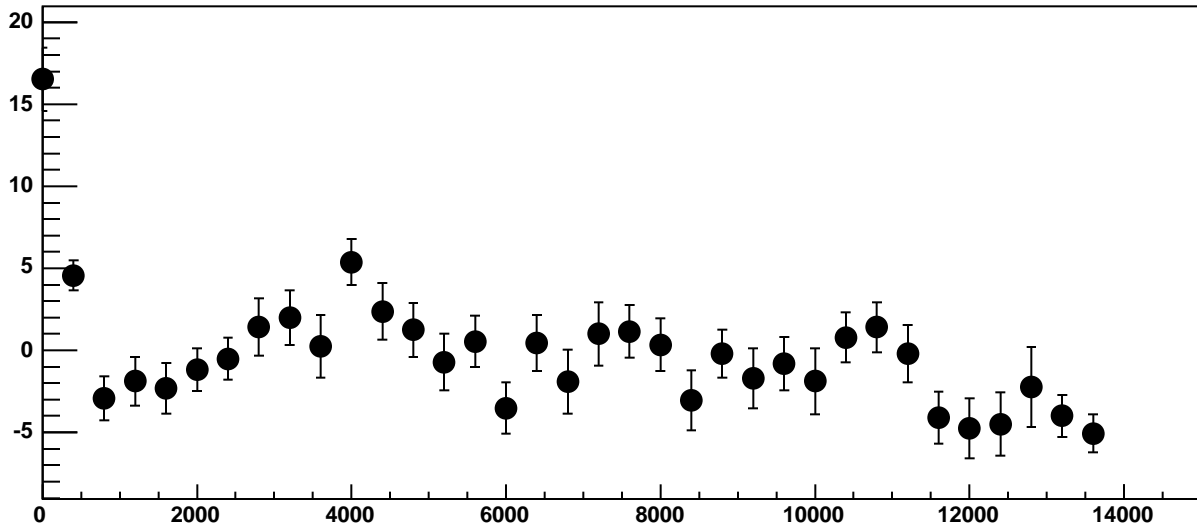


$\chi^2 / \text{ndf}$  37.04 / 23  
p0  $-421.5 \pm 0.7019$   
p1  $0.03148 \pm 0.0001081$

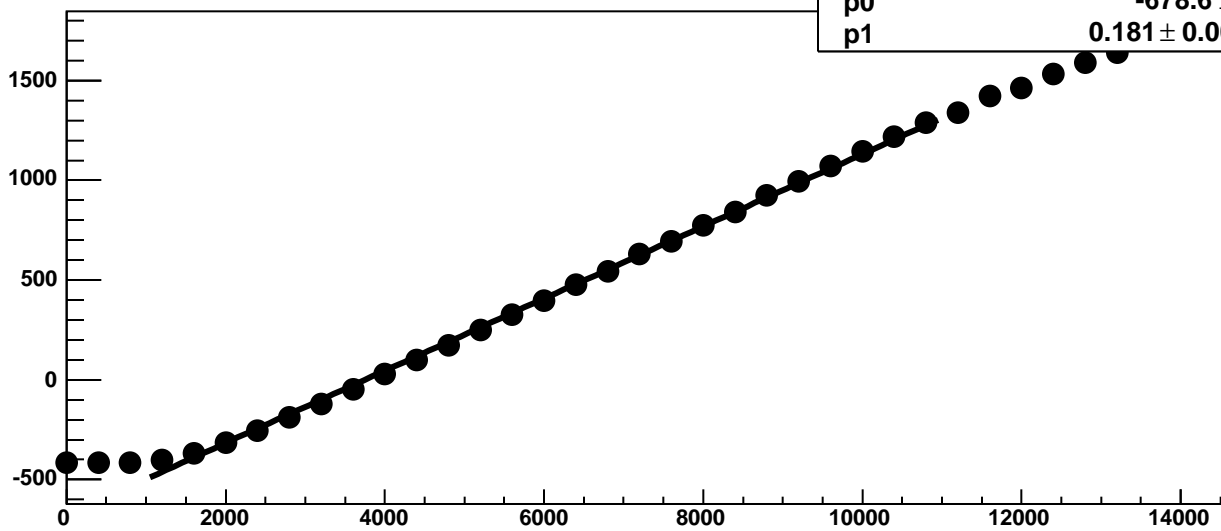
Chip 11, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



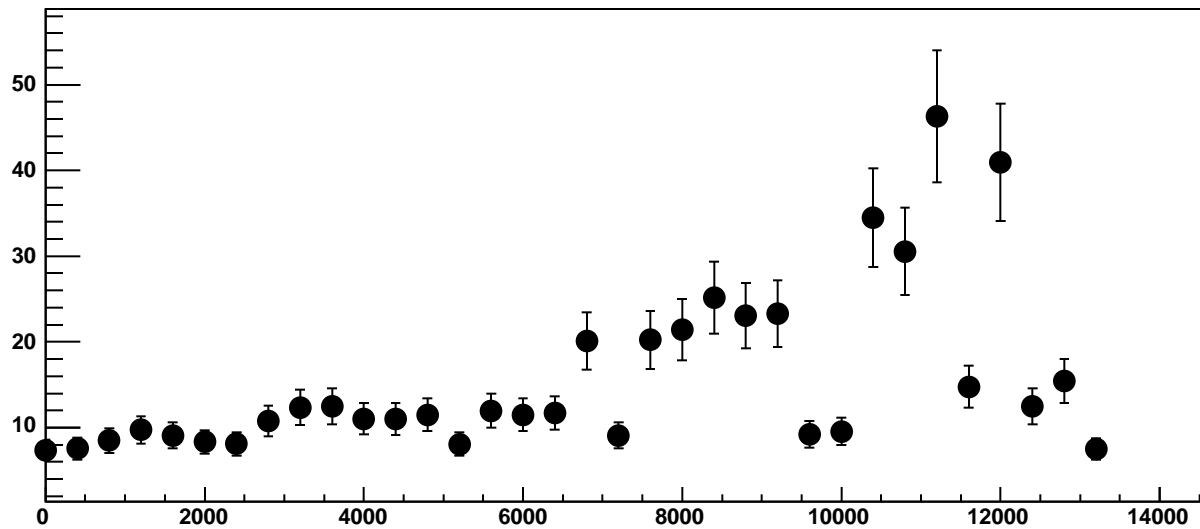
Chip 11, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



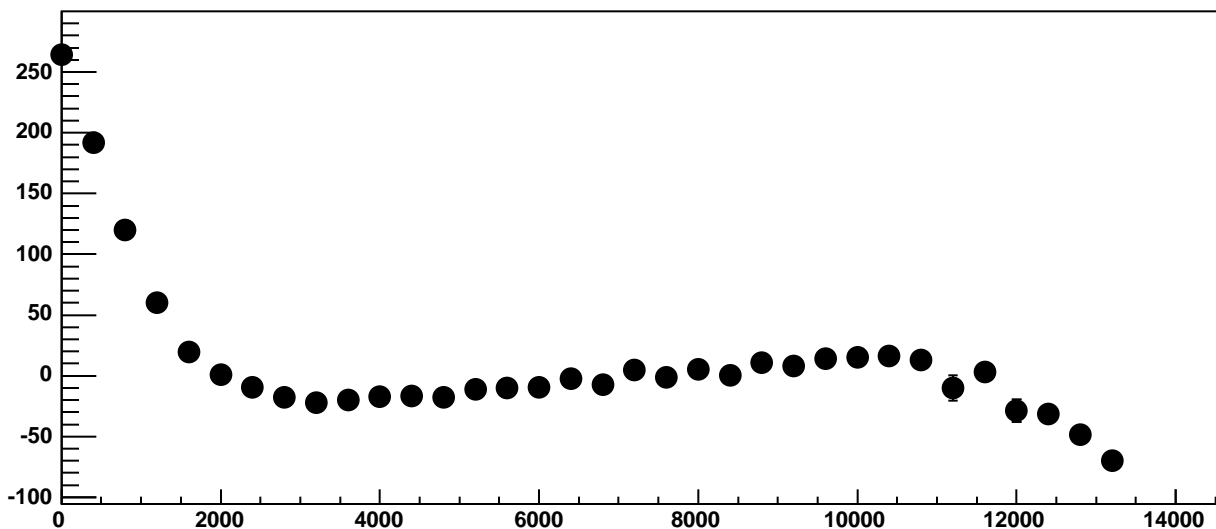
Chip 11, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



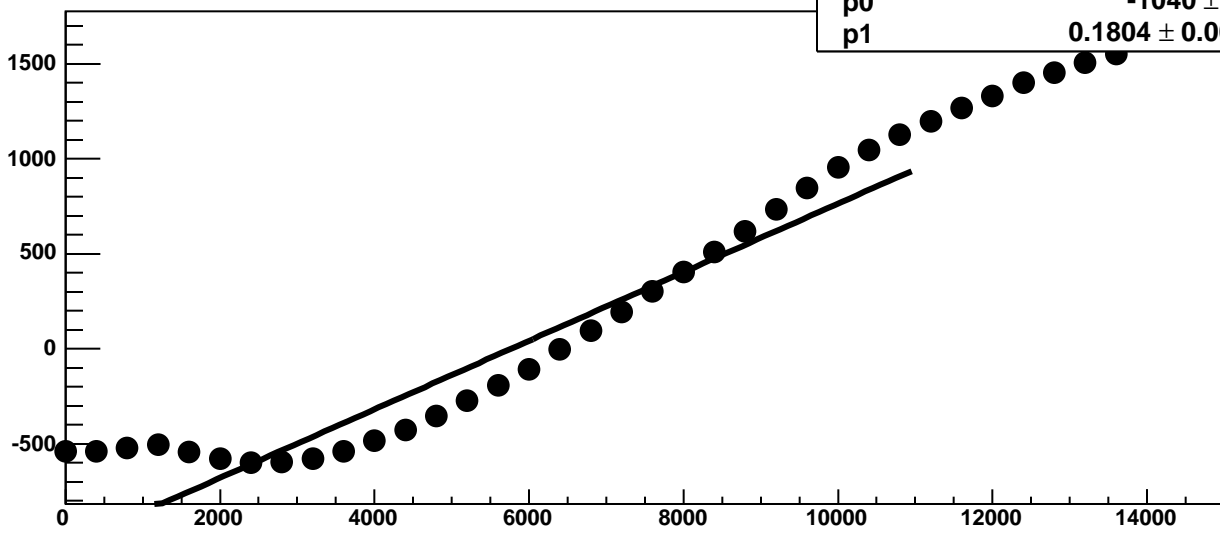
Chip 11, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



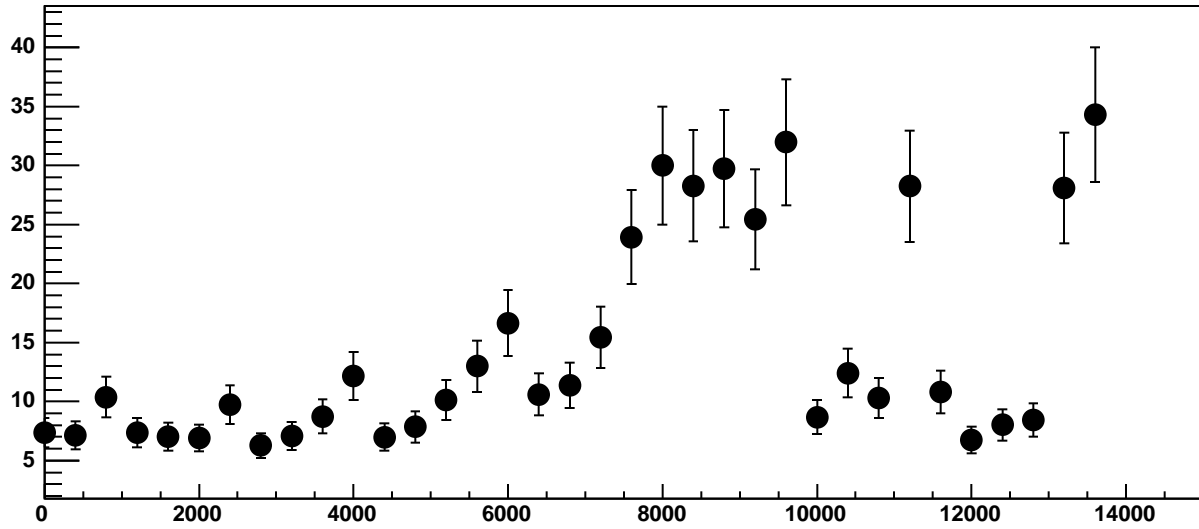
Chip 11, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



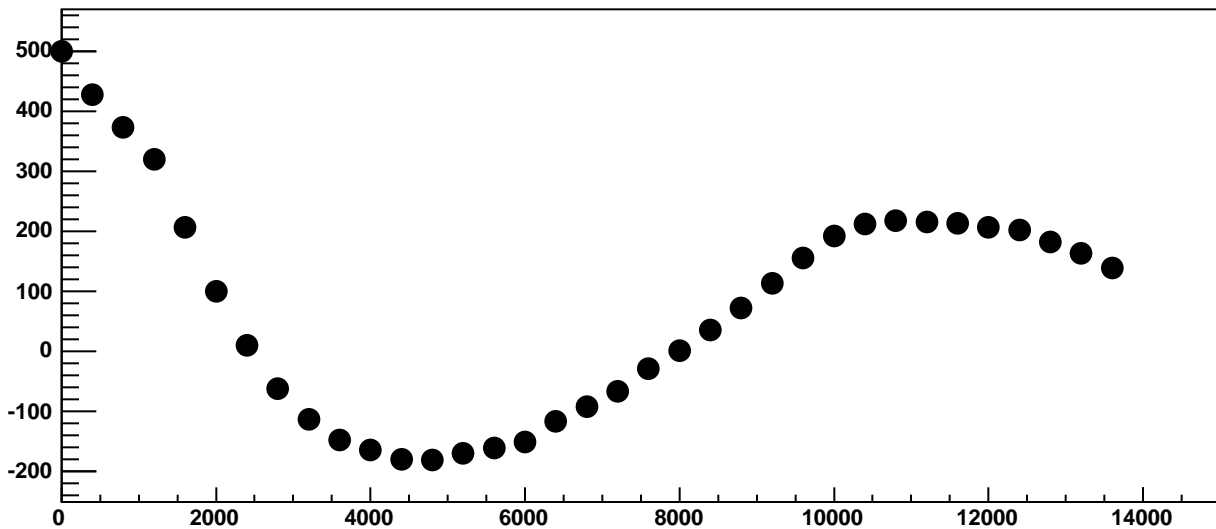
Chip 11, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC



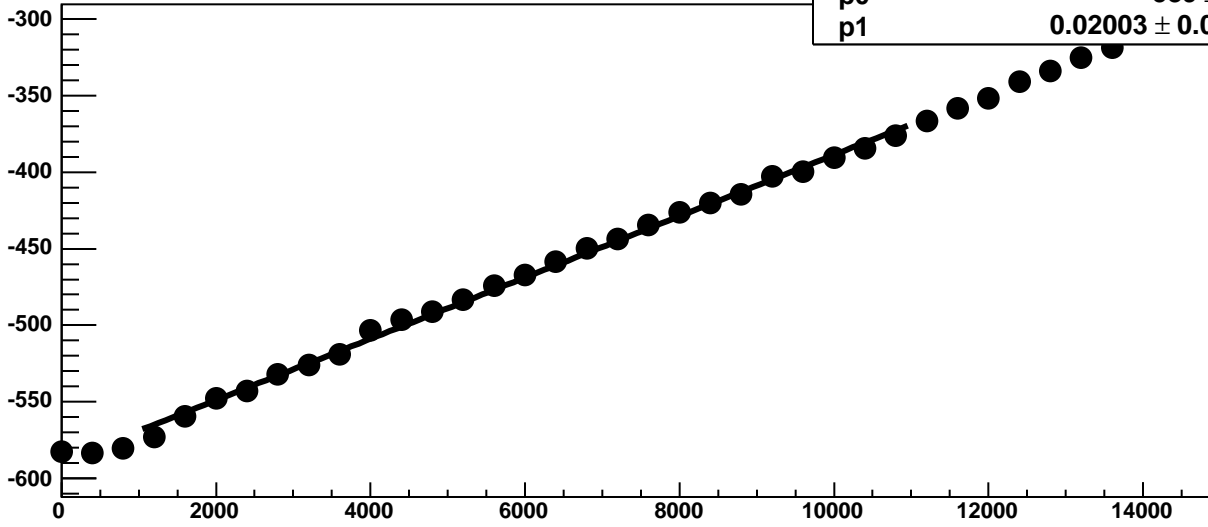
Chip 11, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

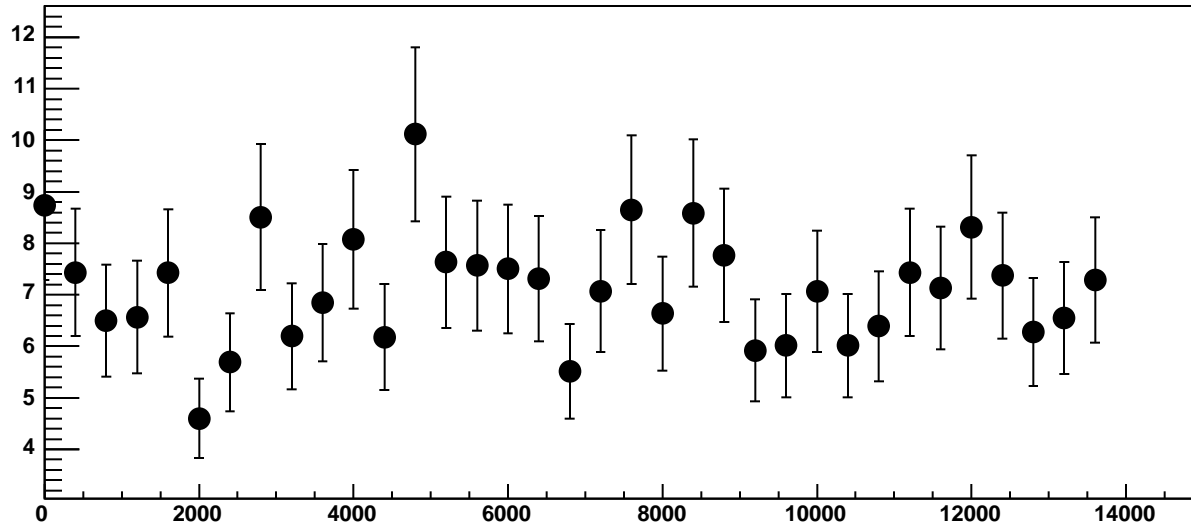


Chip 11, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

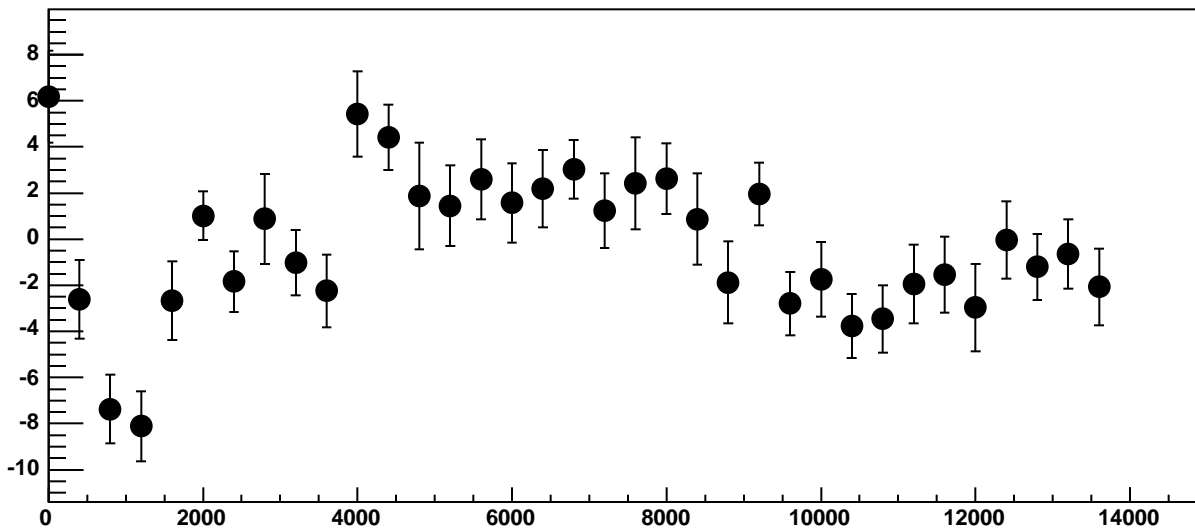


$\chi^2 / \text{ndf}$  94.01 / 23  
p0  $-589 \pm 0.6741$   
p1  $0.02003 \pm 0.0001014$

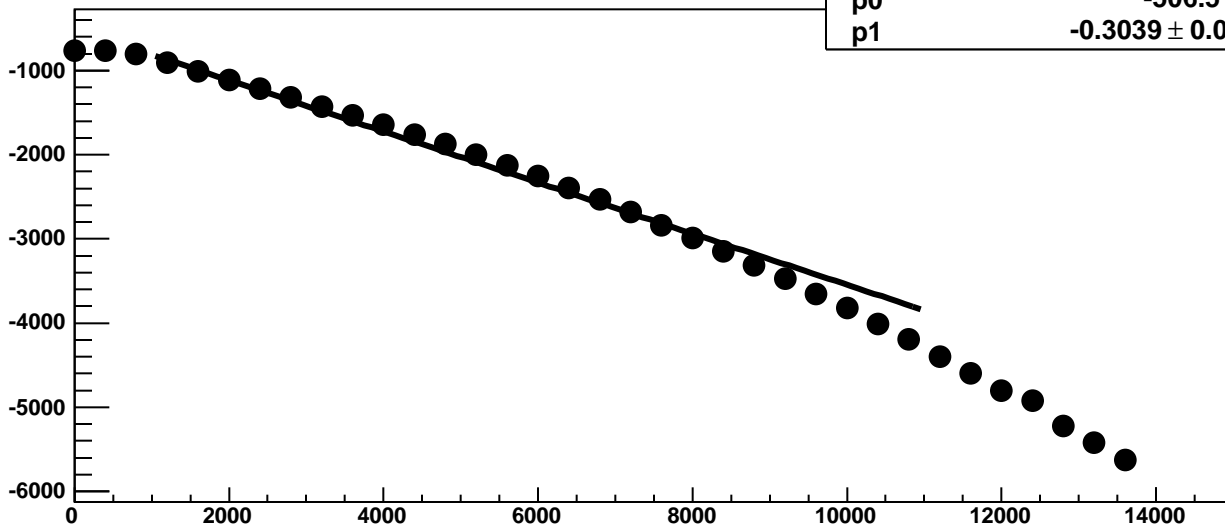
Chip 11, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$

4852 / 23

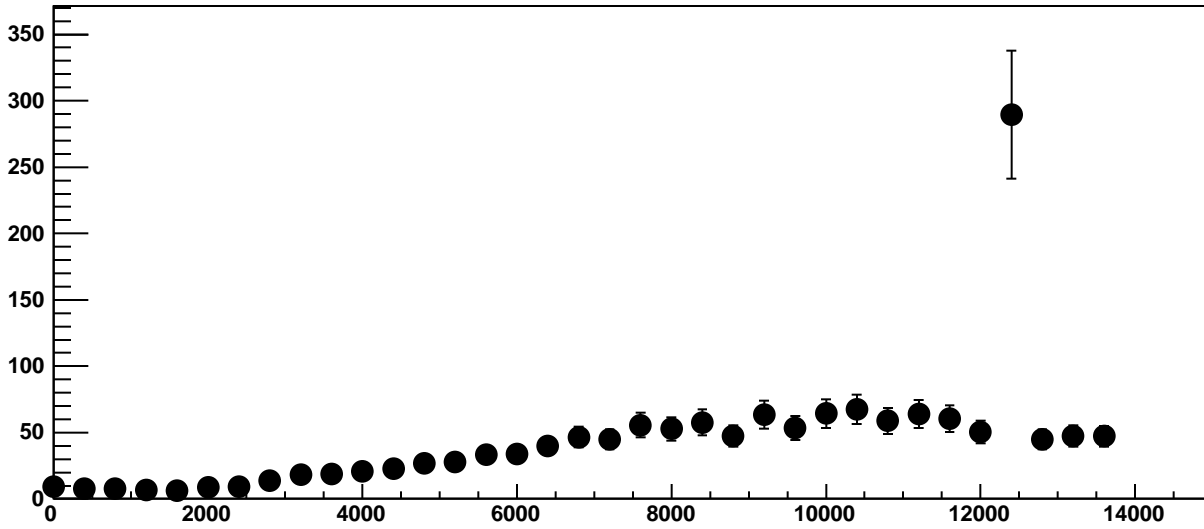
p0

$-506.5 \pm 1.294$

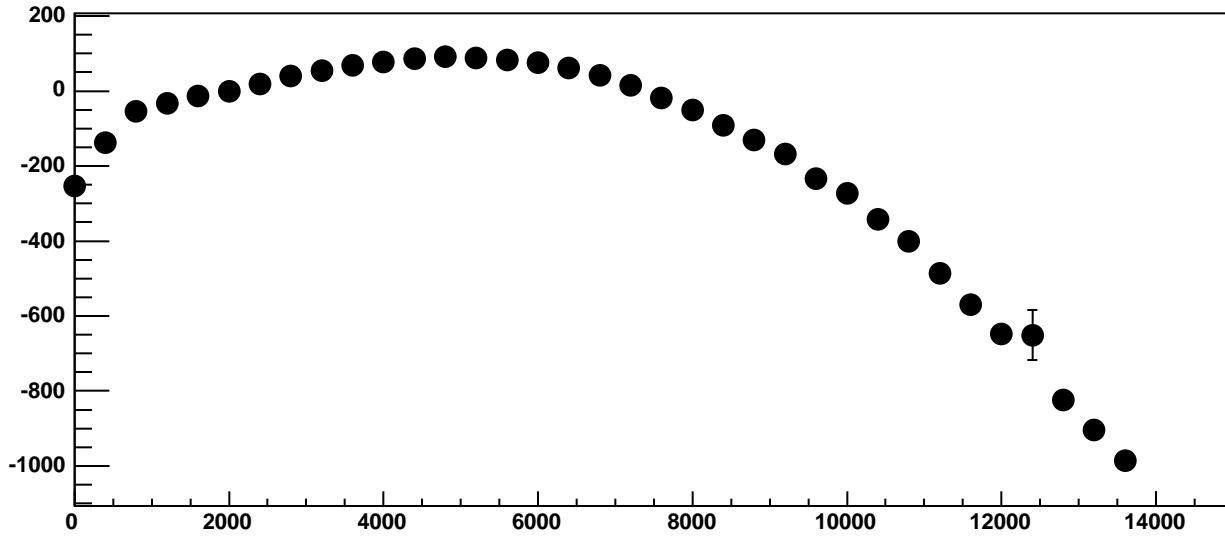
p1

$-0.3039 \pm 0.0004435$

Chip 11, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

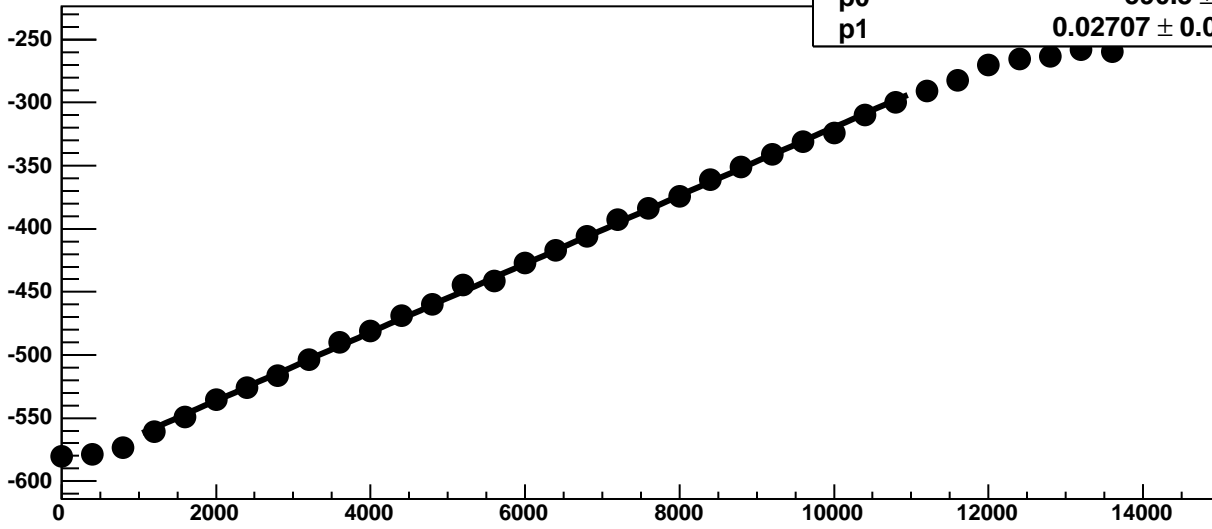


Chip 11, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

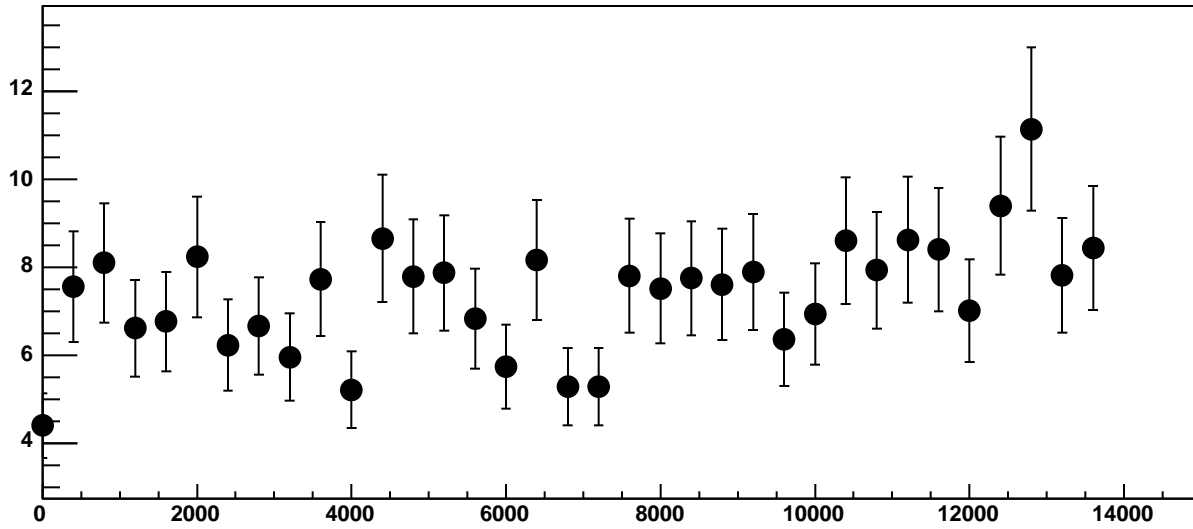




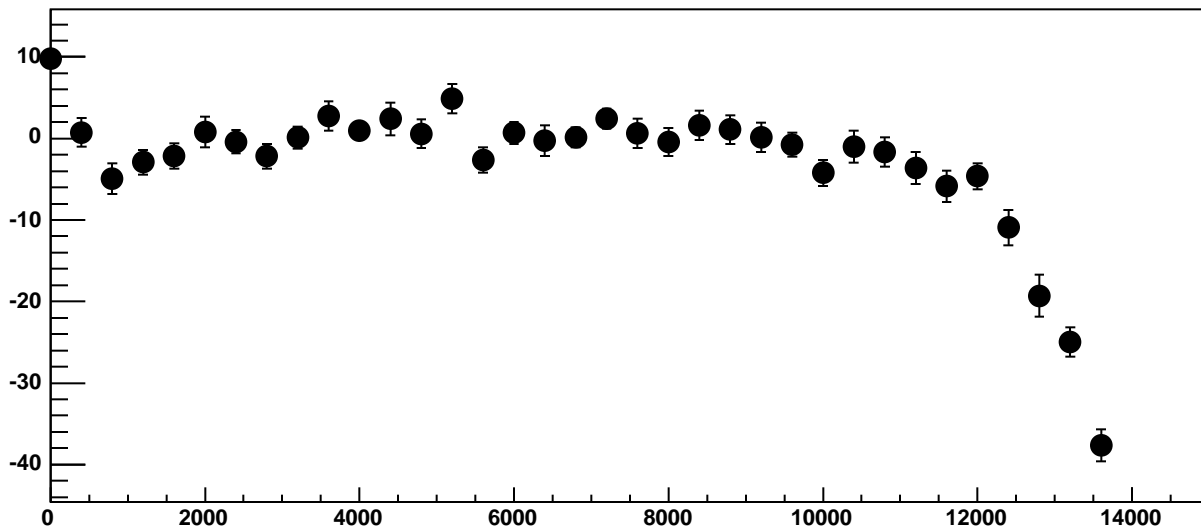
Chip 11, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC



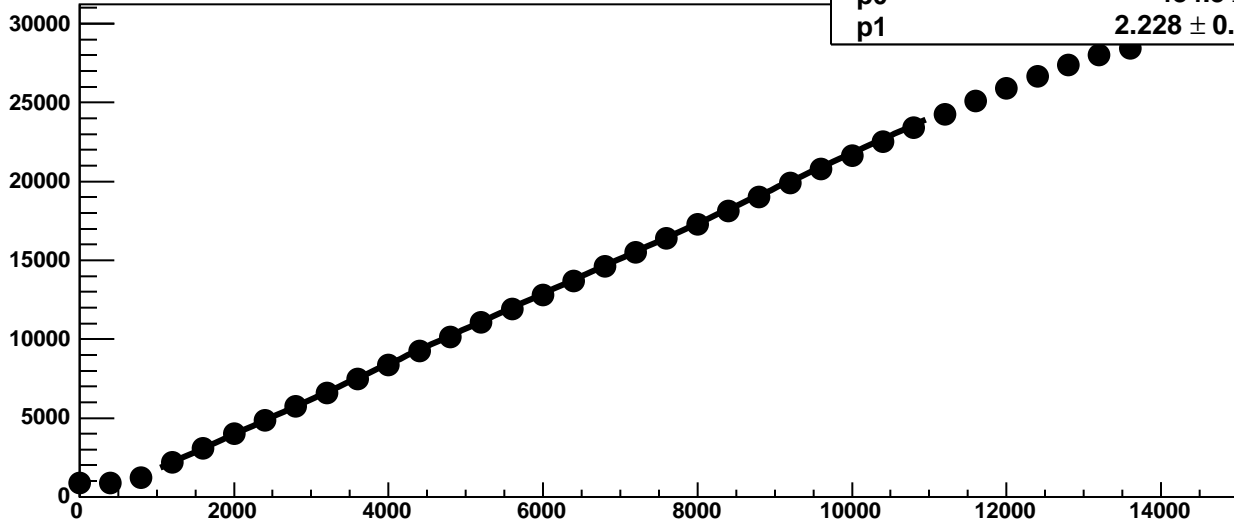
Chip 11, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC



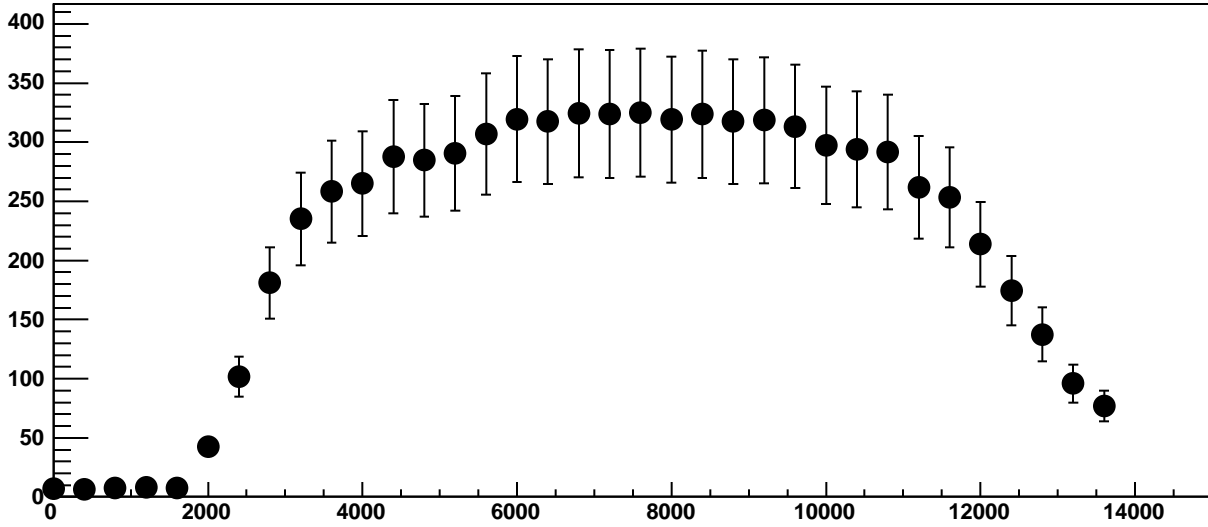
Chip 11, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC



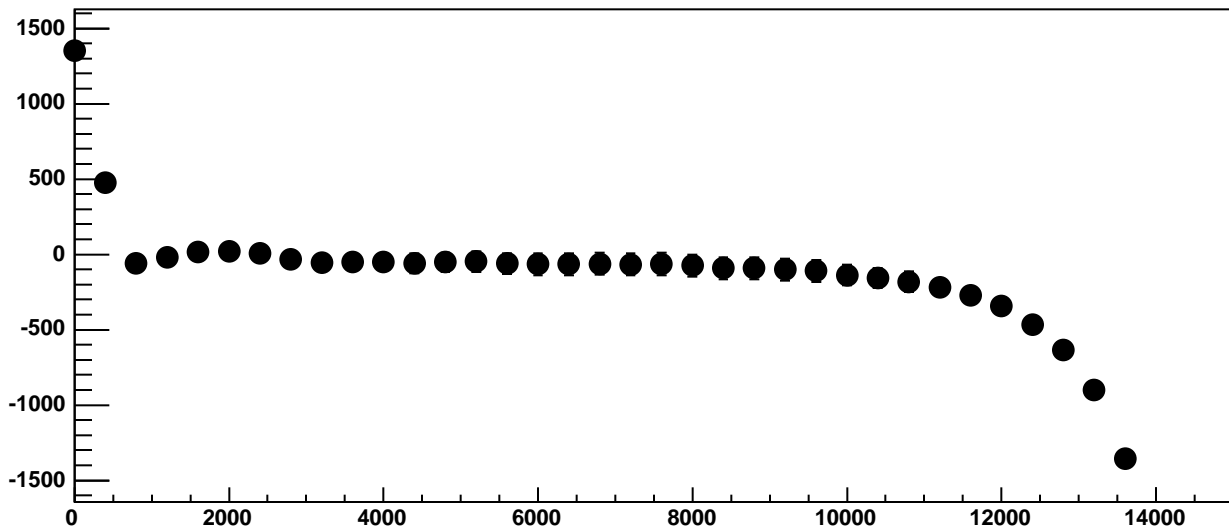
Chip 11, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

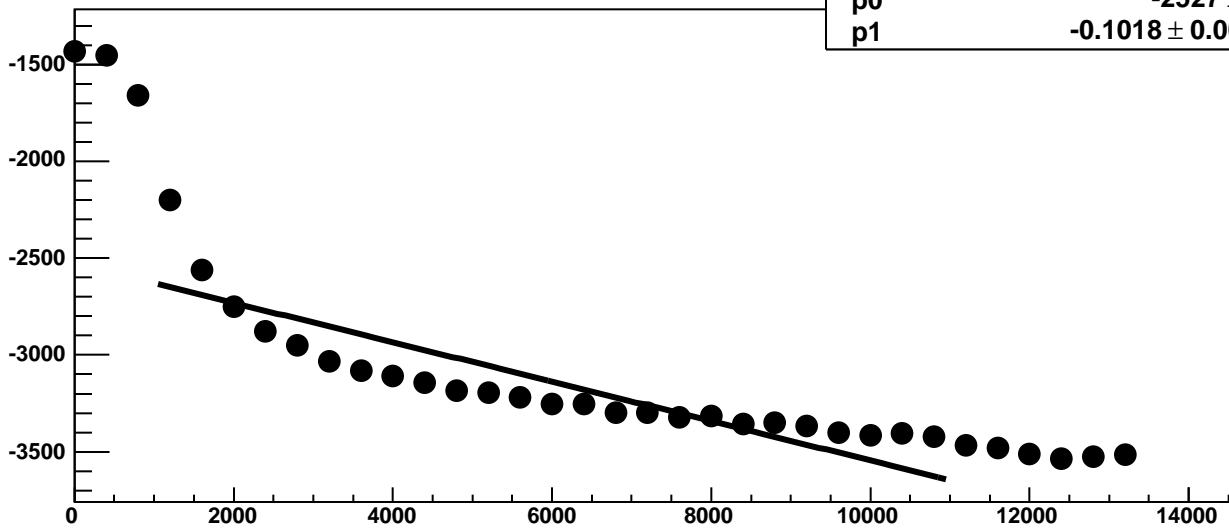


Chip 11, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

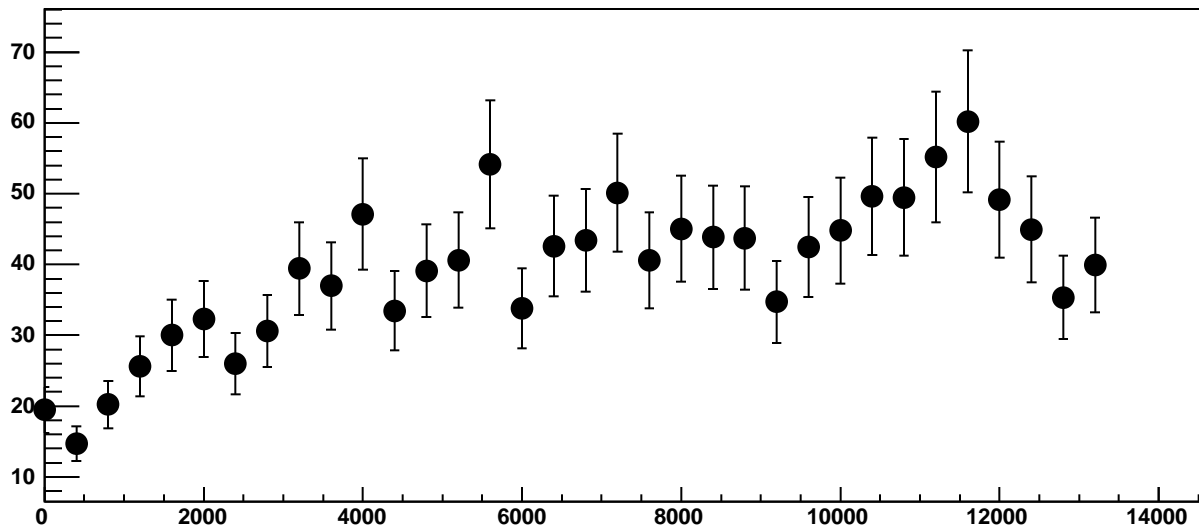


Chip 11, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

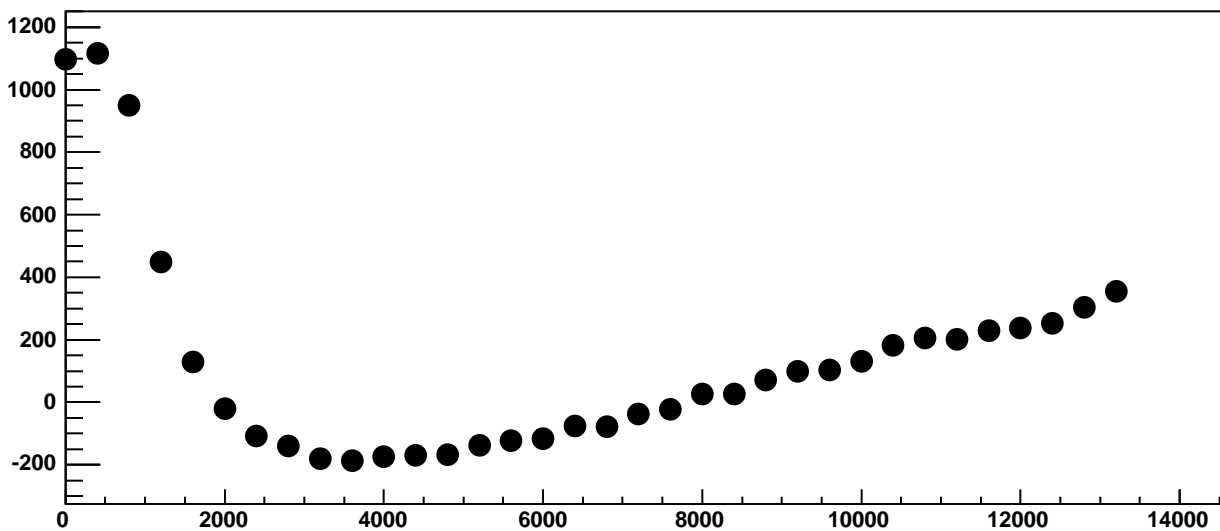
$\chi^2 / \text{ndf}$  1.062e+04 / 23  
p0 -2527 ± 3.437  
p1 -0.1018 ± 0.0005827



Chip 11, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

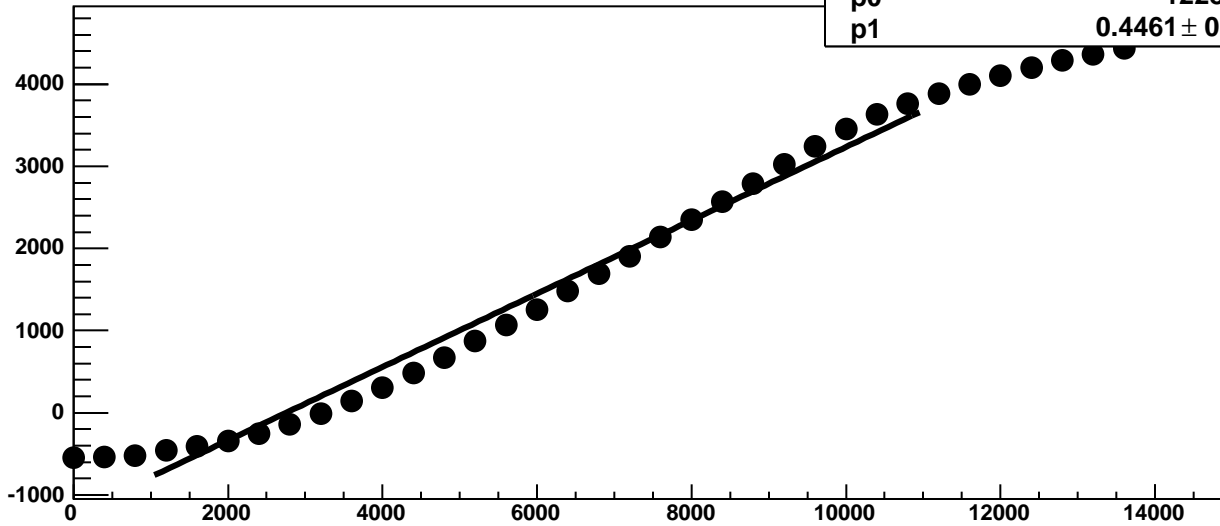


Chip 11, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

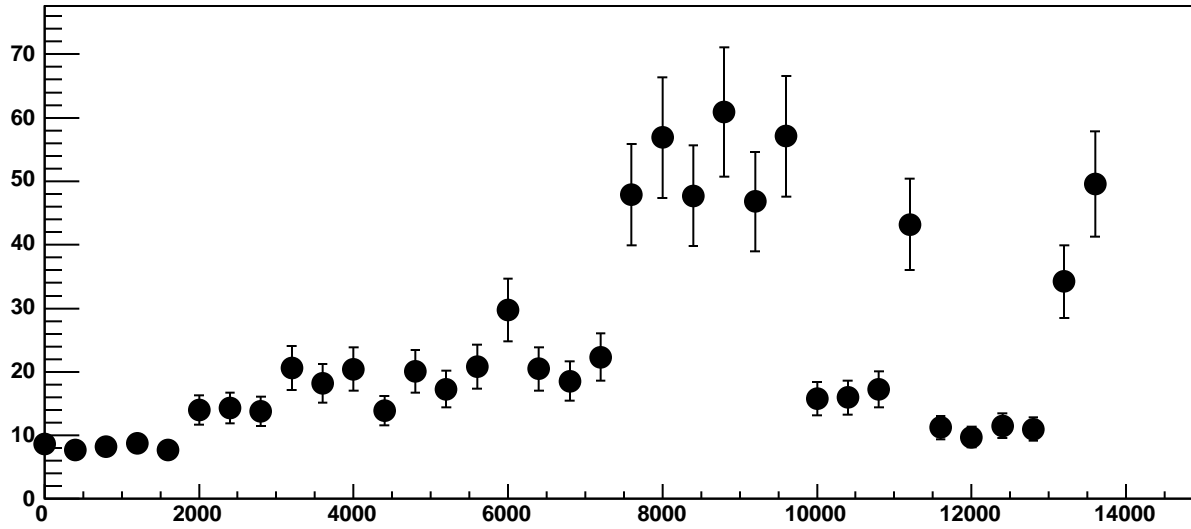


Chip 11, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

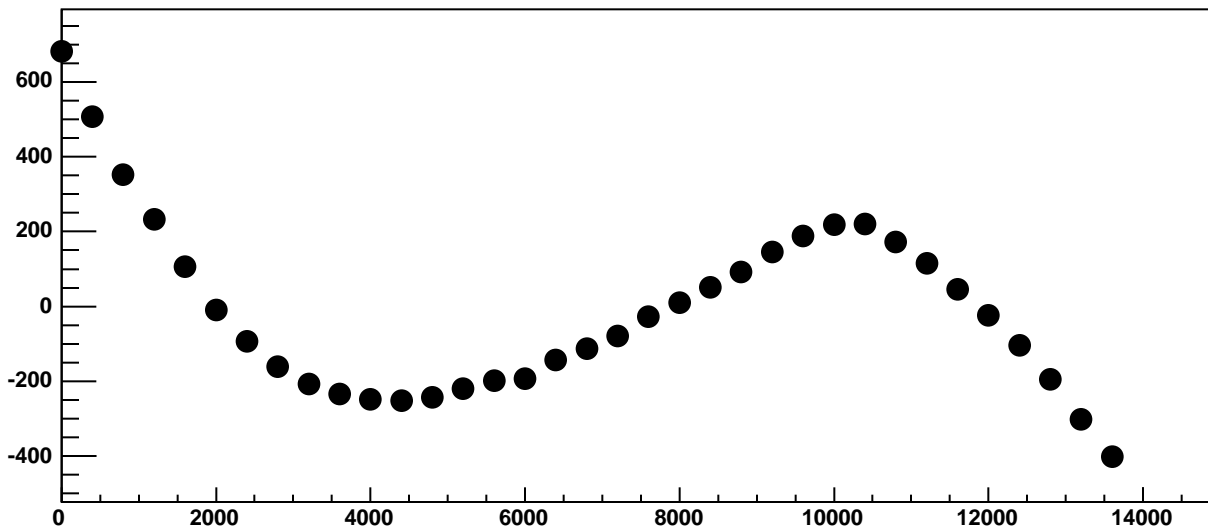
$\chi^2 / \text{ndf}$  5.448e+04 / 23  
p0 -1228 ± 1.301  
p1 0.4461 ± 0.000253



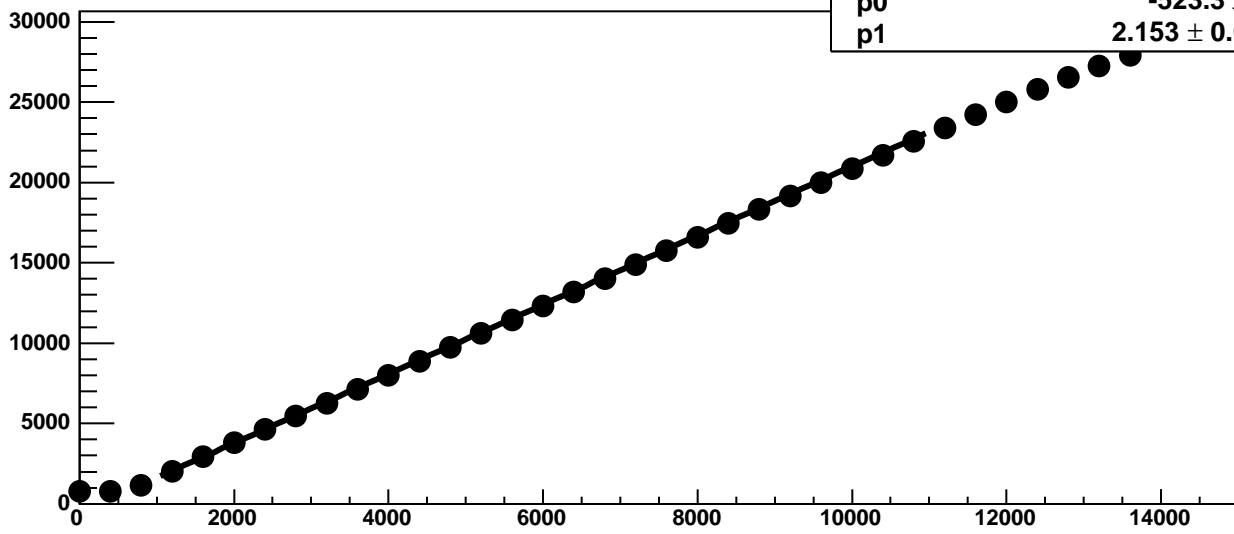
Chip 11, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



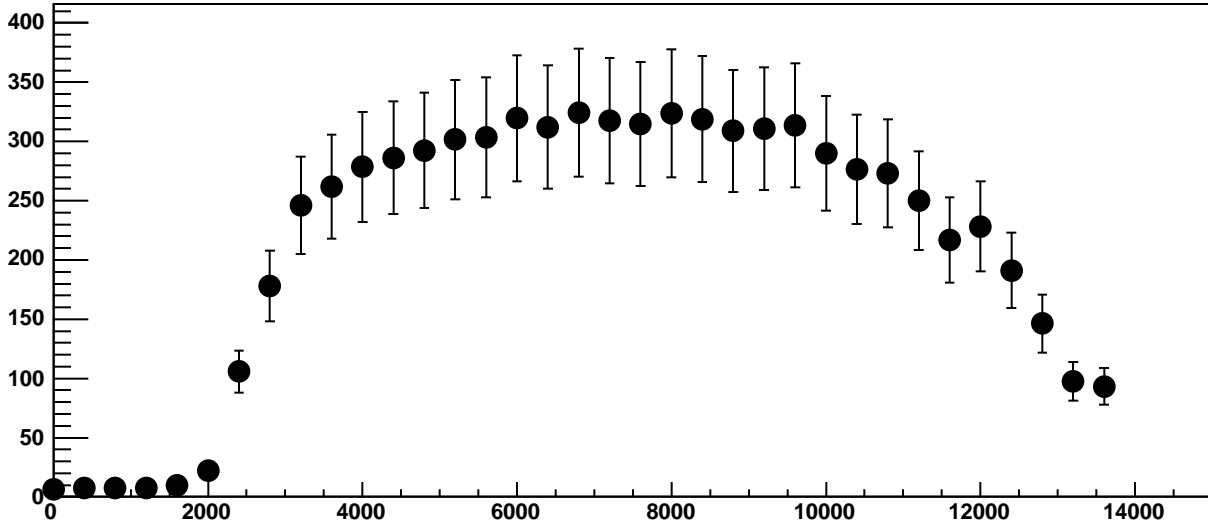
Chip 11, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



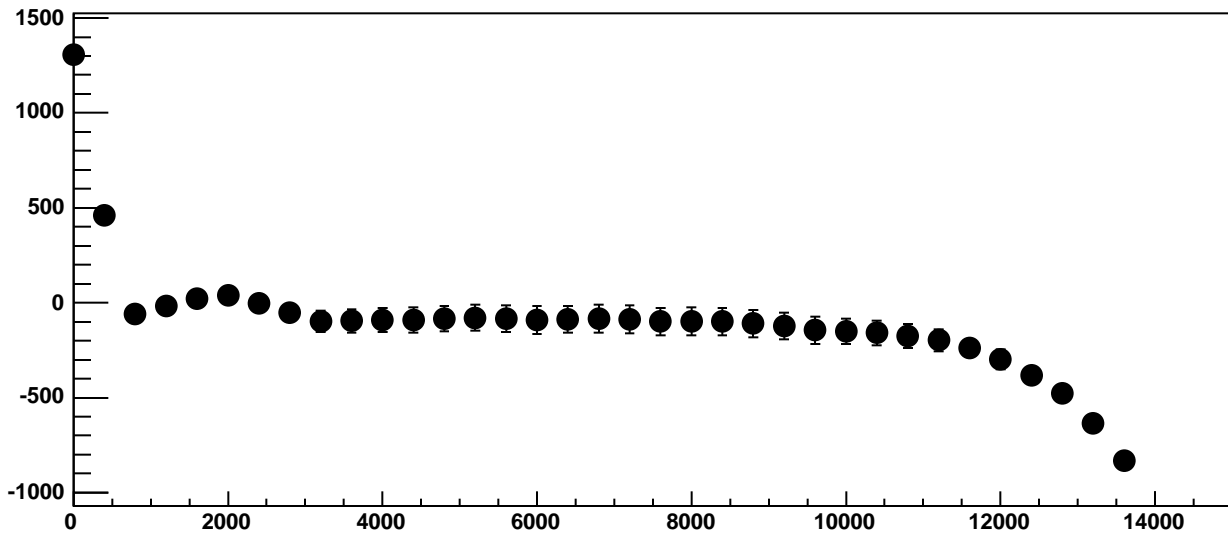
Chip 11, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



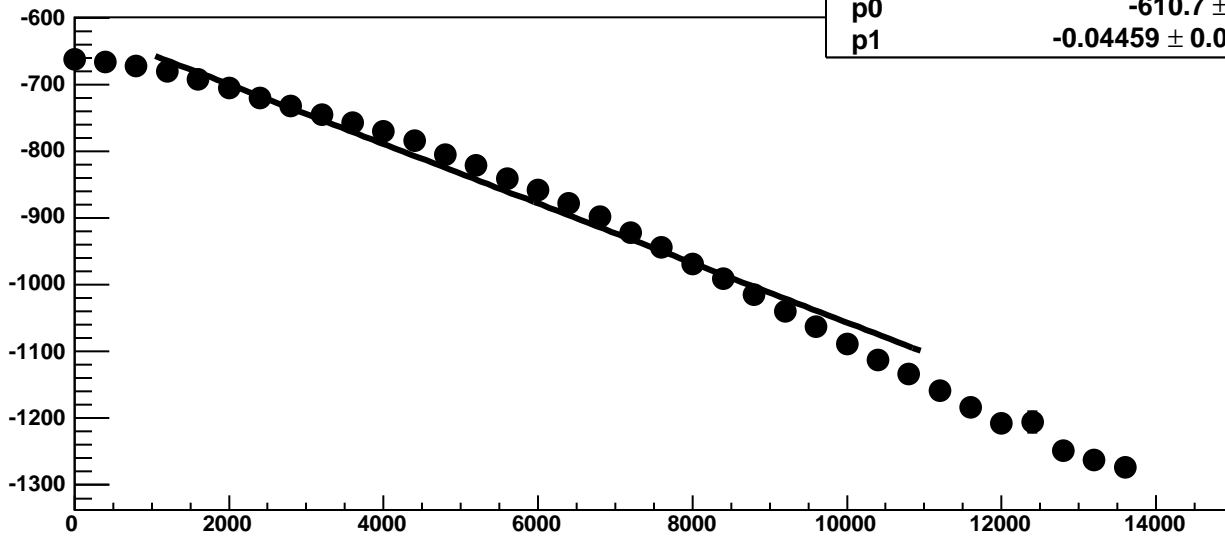
Chip 11, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC



Chip 11, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

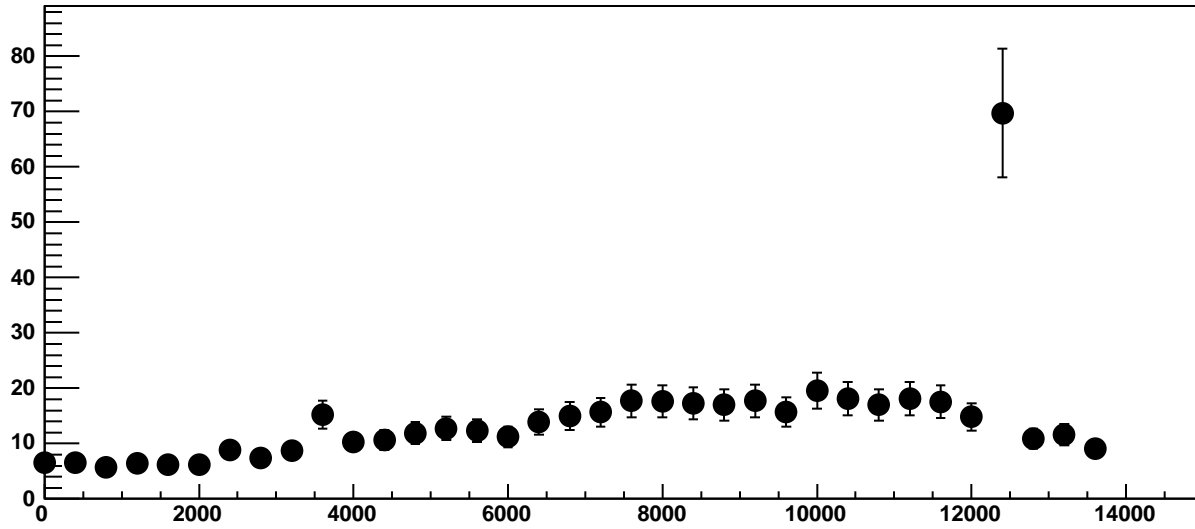


Chip 11, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

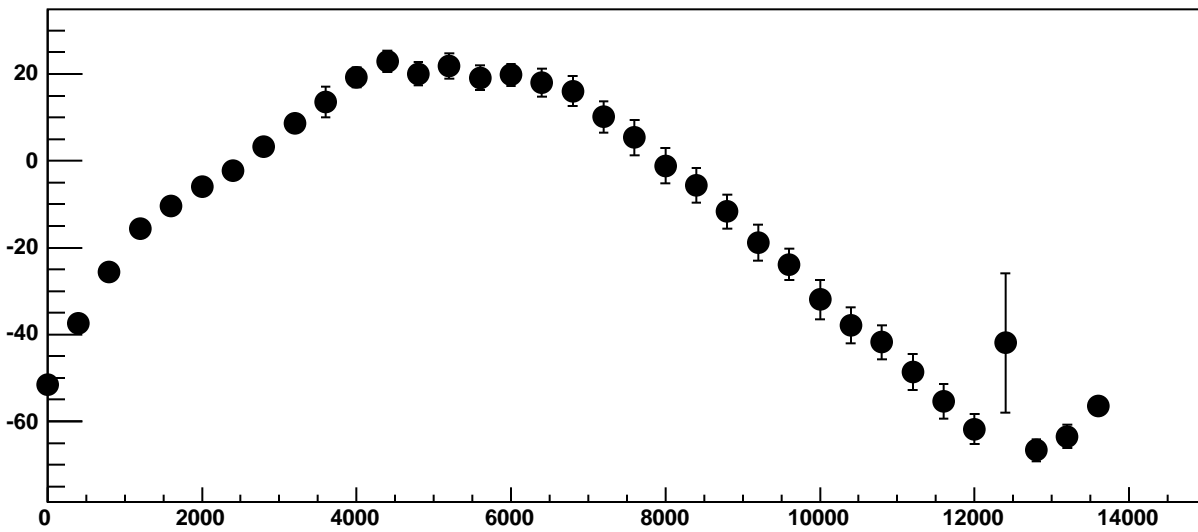


$\chi^2 / \text{ndf}$	978.5 / 23
p0	-610.7 ± 0.8948
p1	-0.04459 ± 0.0001867

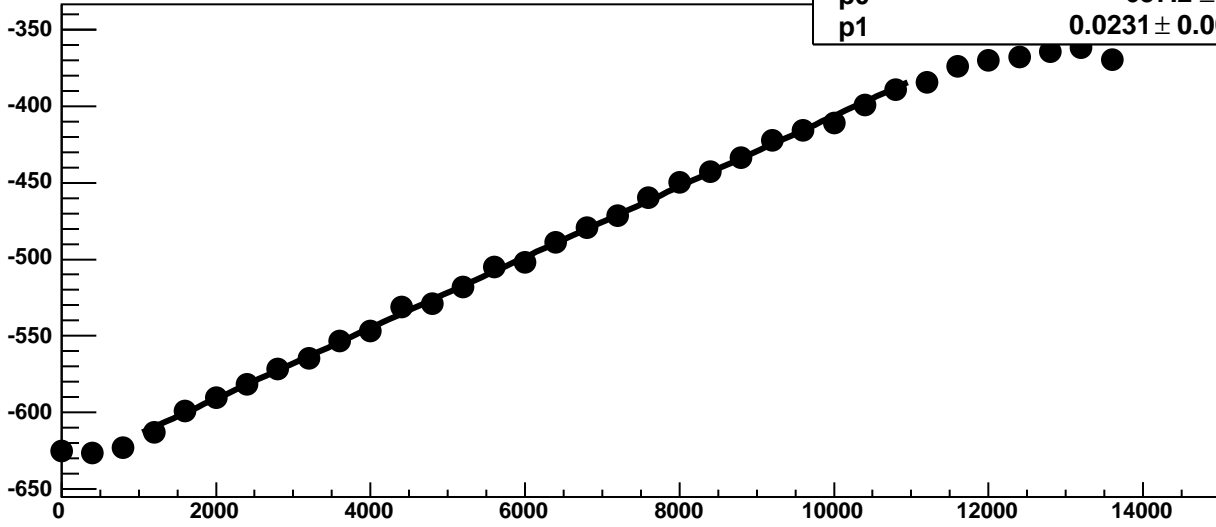
Chip 11, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



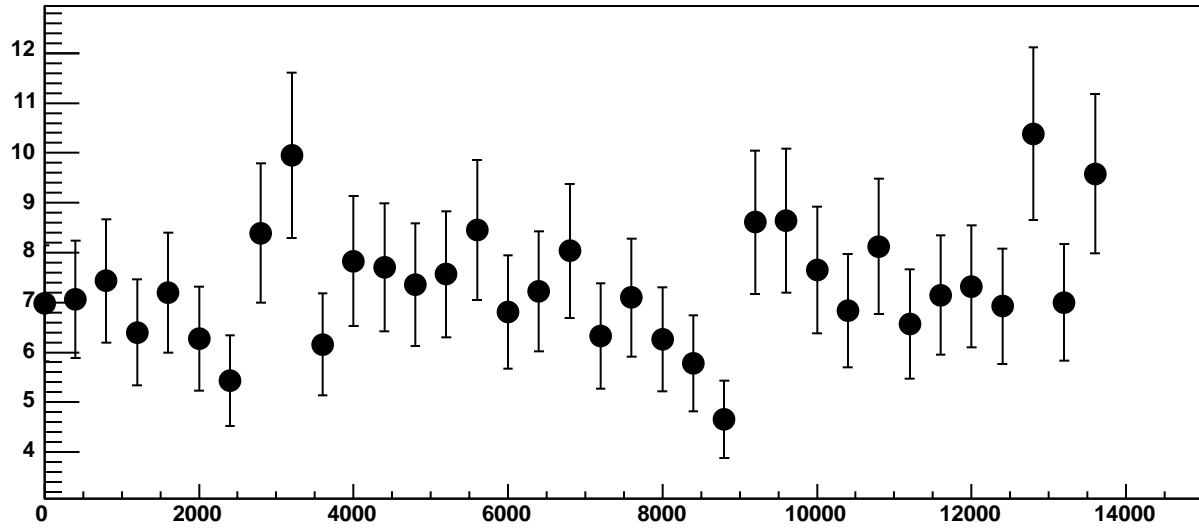
Chip 11, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



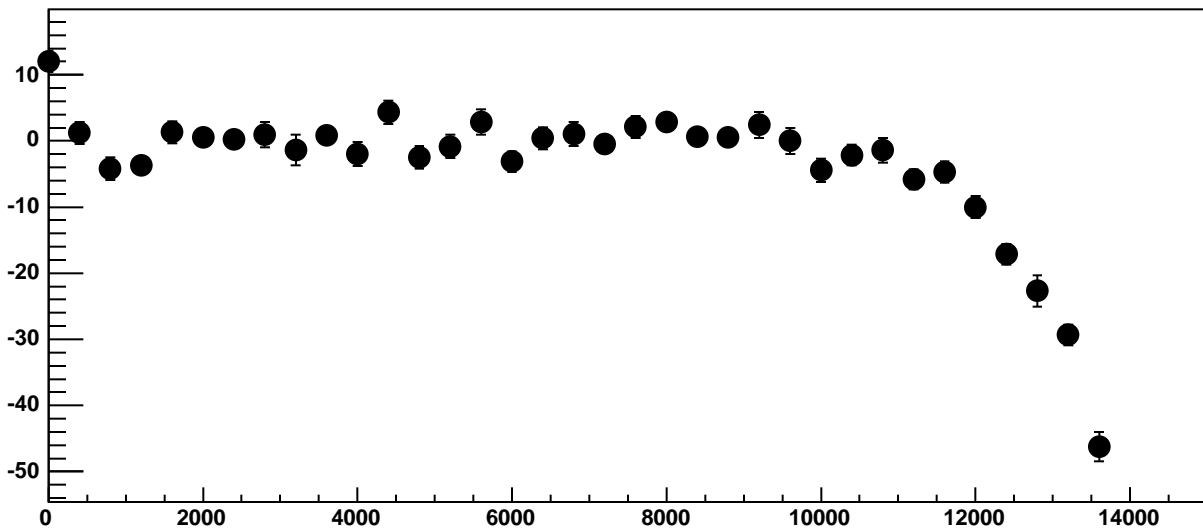
Chip 11, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC



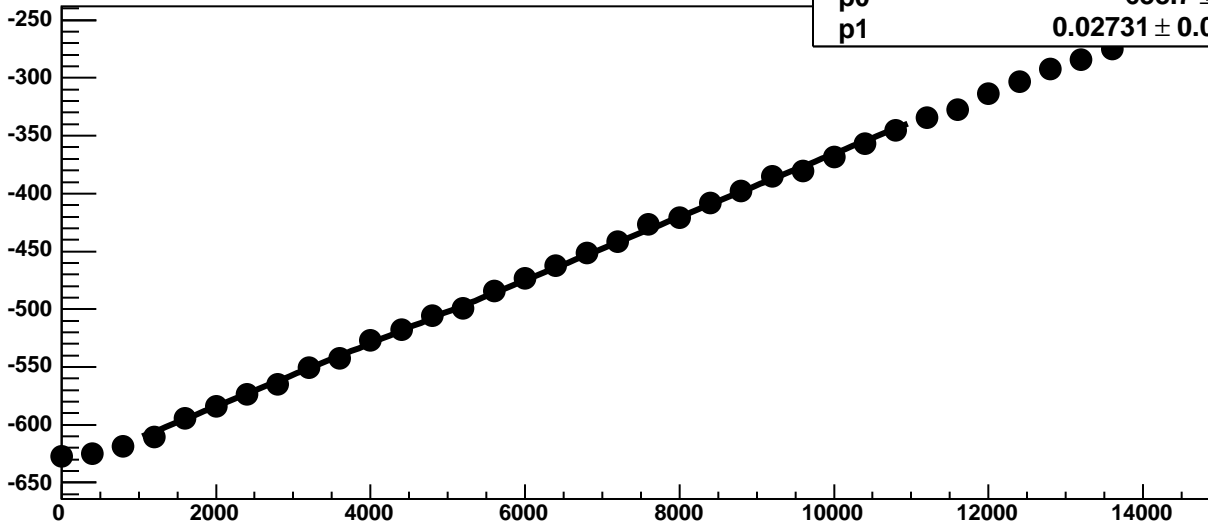
Chip 11, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC



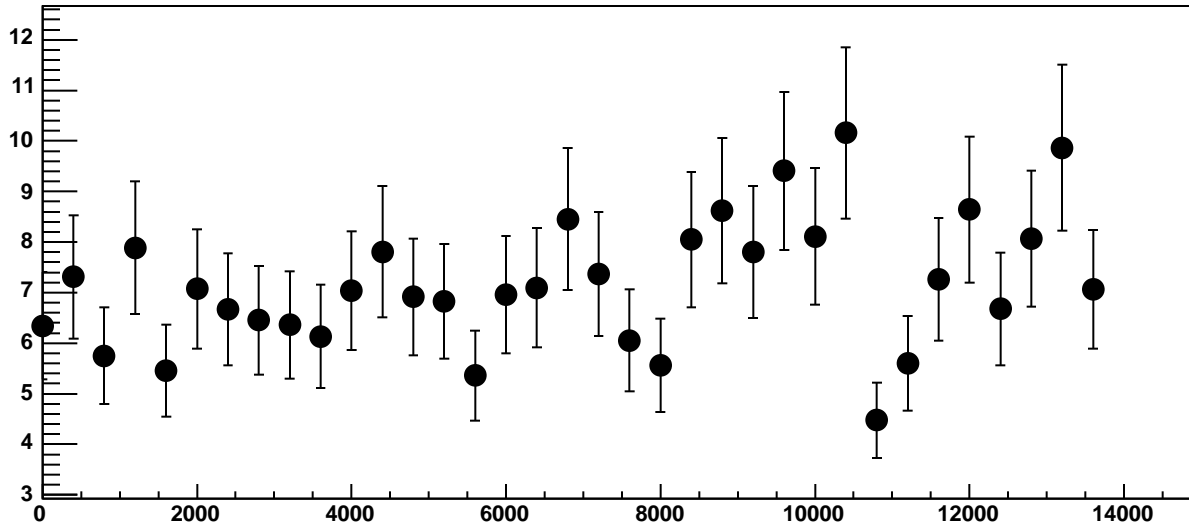
Chip 11, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



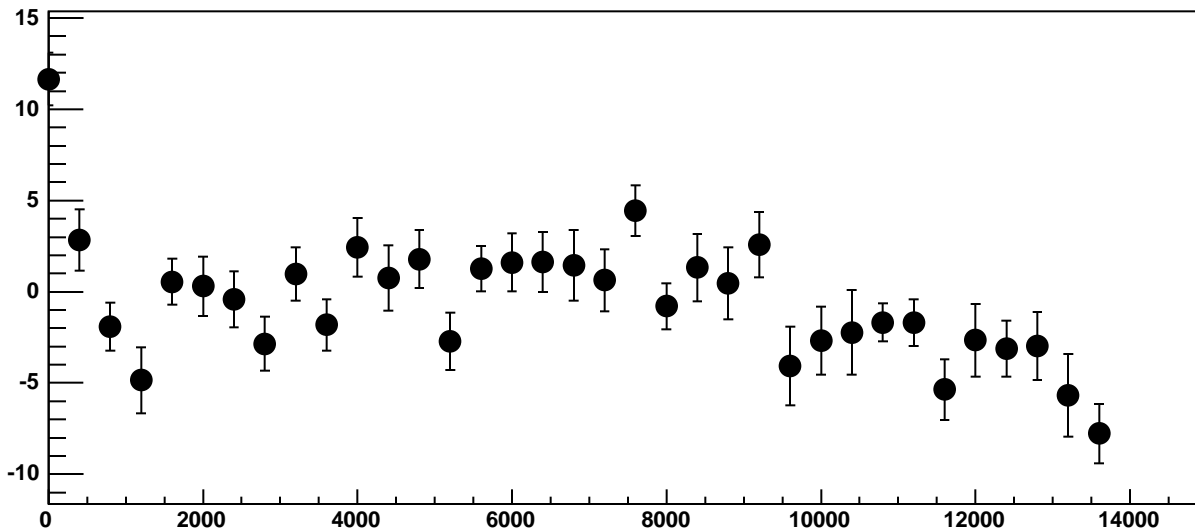
Chip 11, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



Chip 11, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

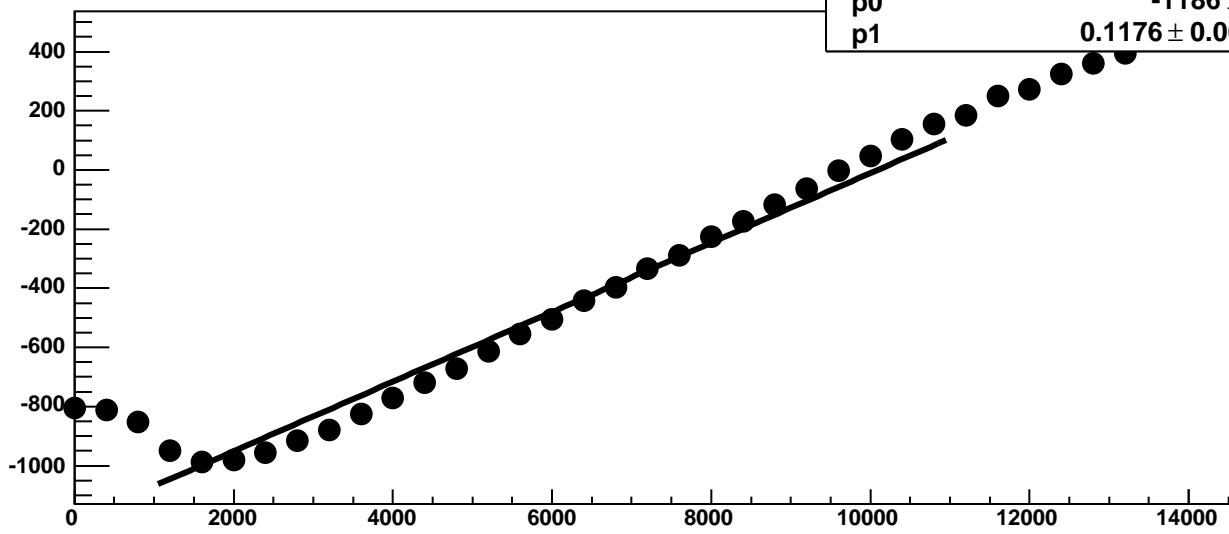


Chip 11, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

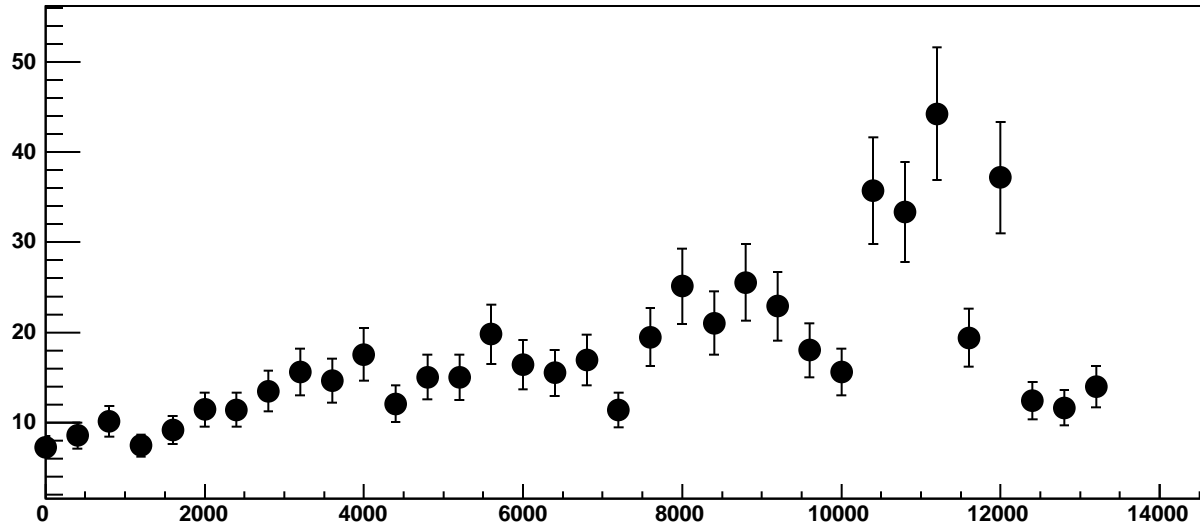




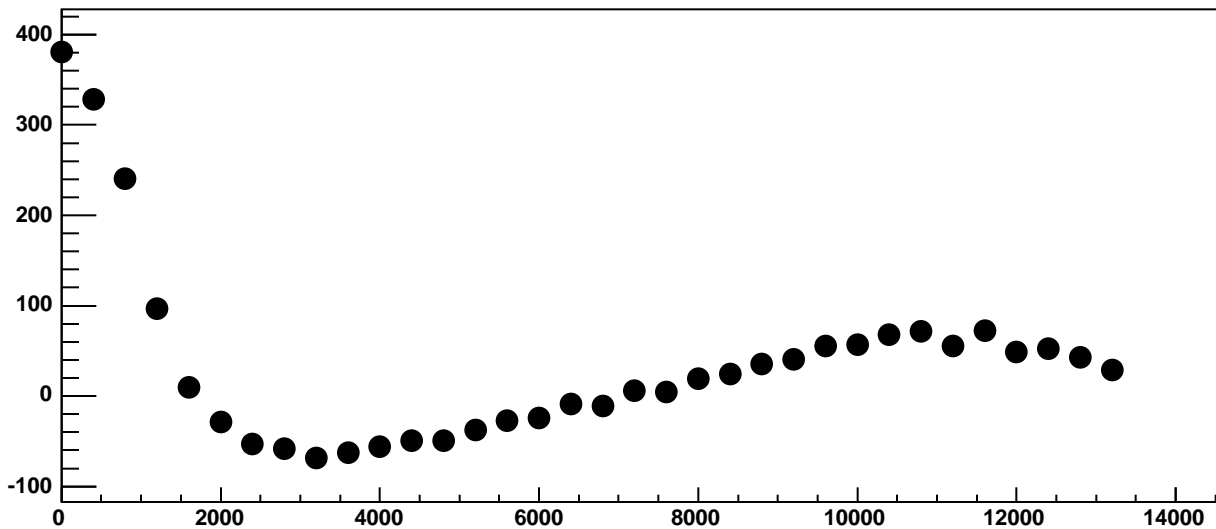
Chip 11, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



Chip 11, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

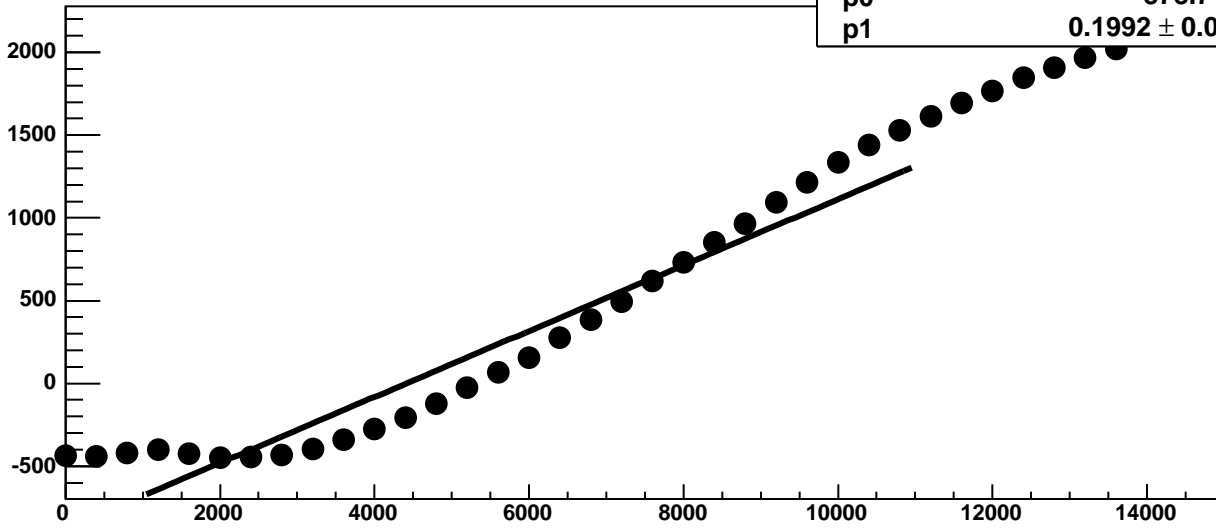


Chip 11, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

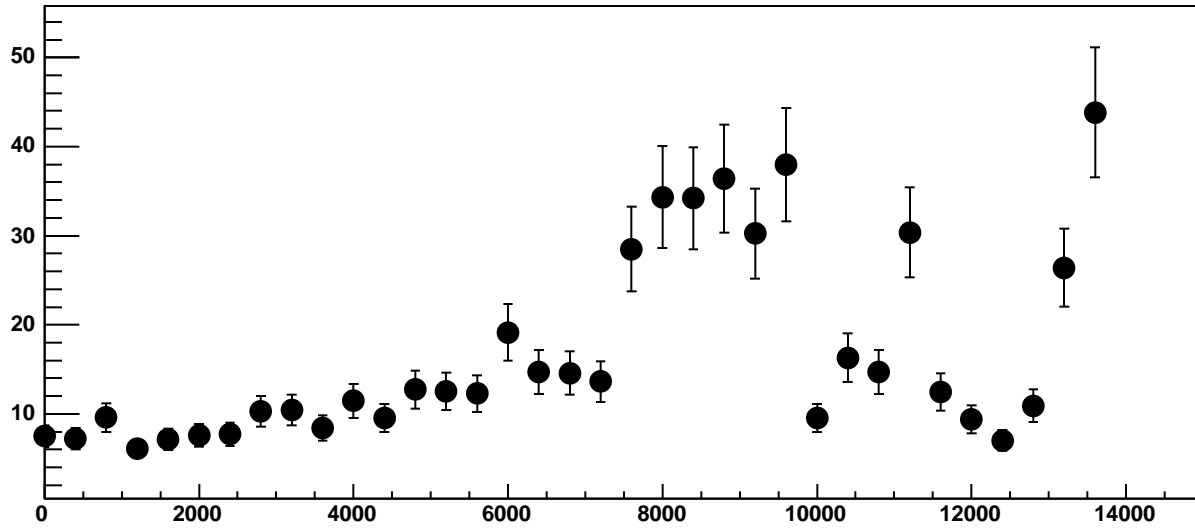


Chip 11, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

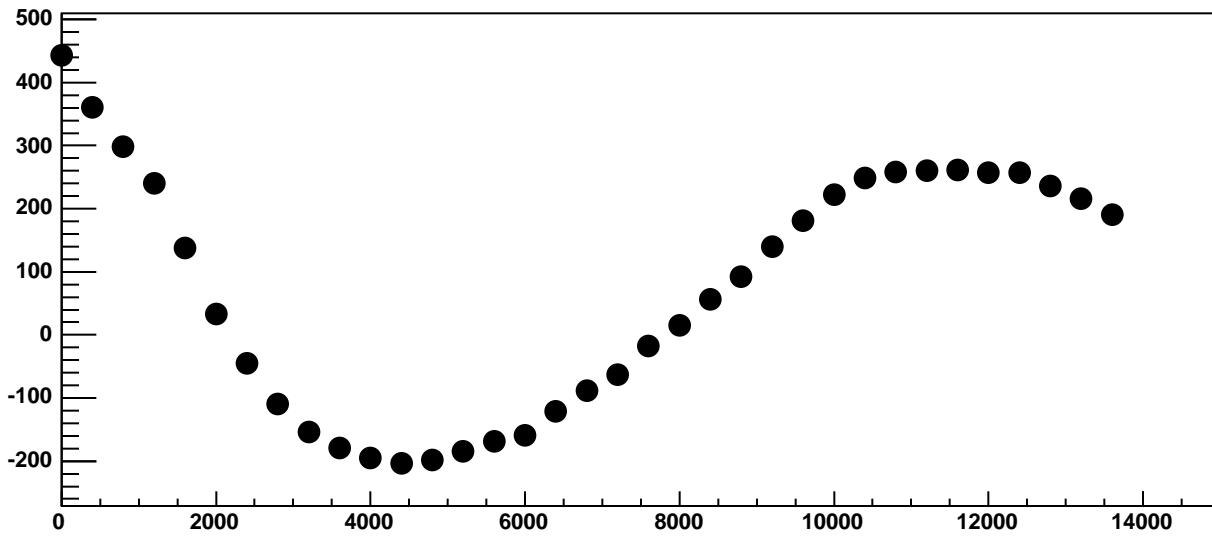
$\chi^2 / \text{ndf}$  1.042e+05 / 23  
p0 -878.7 ± 0.923  
p1 0.1992 ± 0.0001876



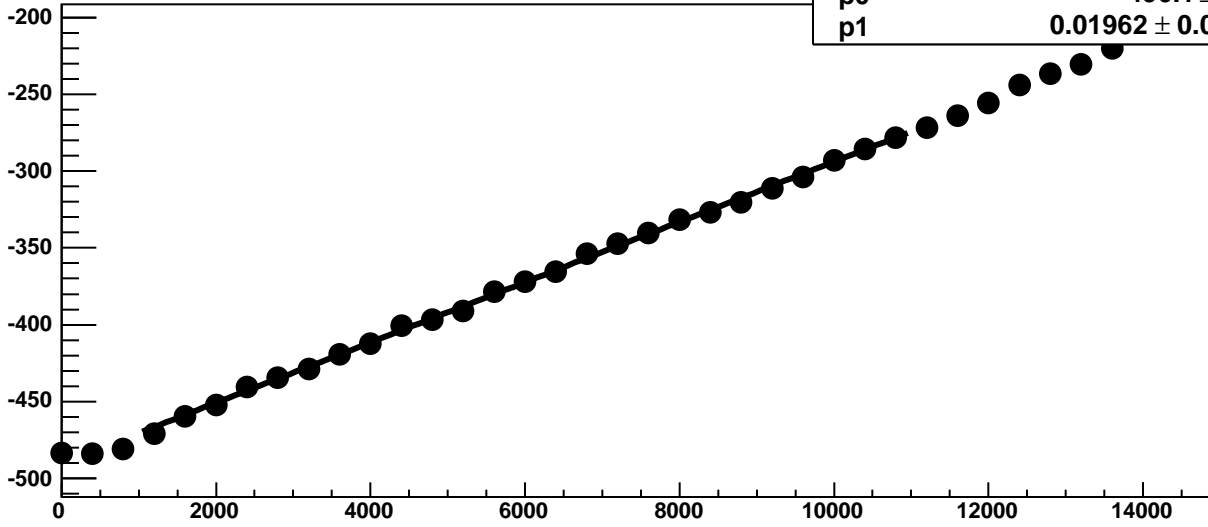
Chip 11, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

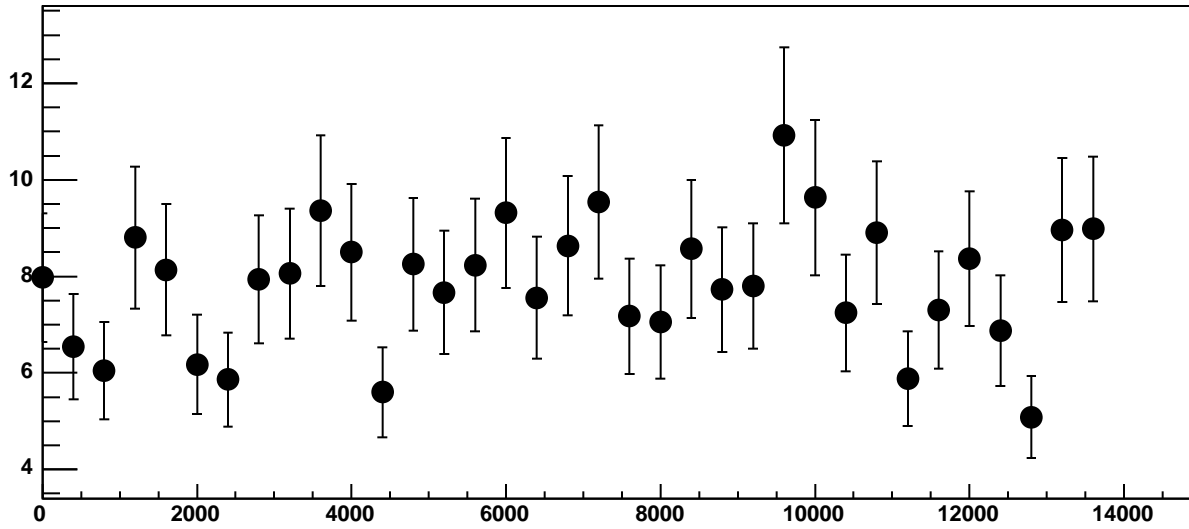


Chip 11, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

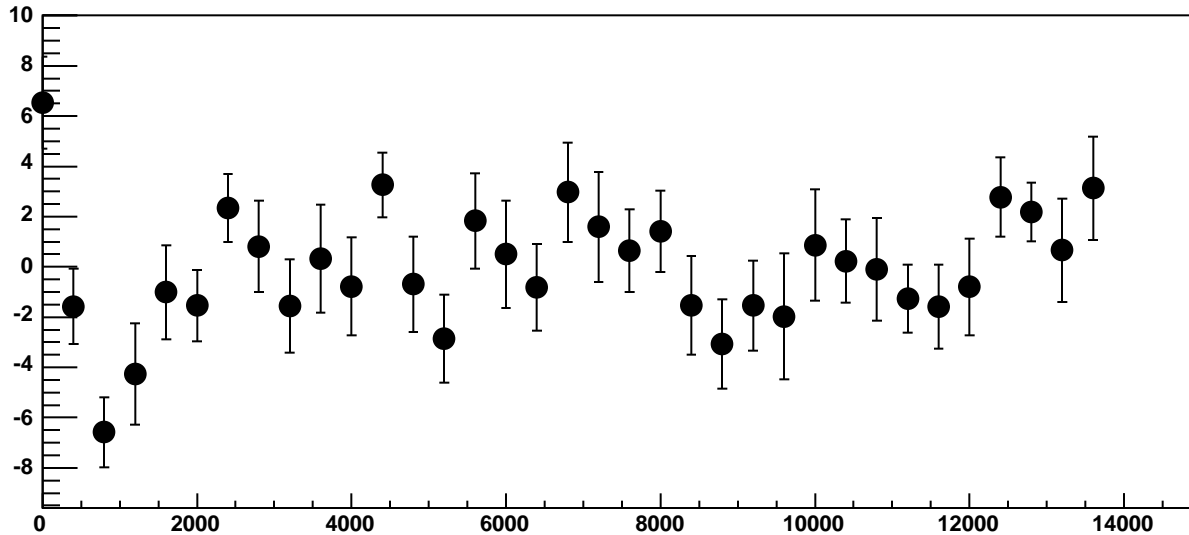


$\chi^2 / \text{ndf}$  29.33 / 23  
p0  $-490.1 \pm 0.7998$   
p1  $0.01962 \pm 0.0001254$

Chip 11, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

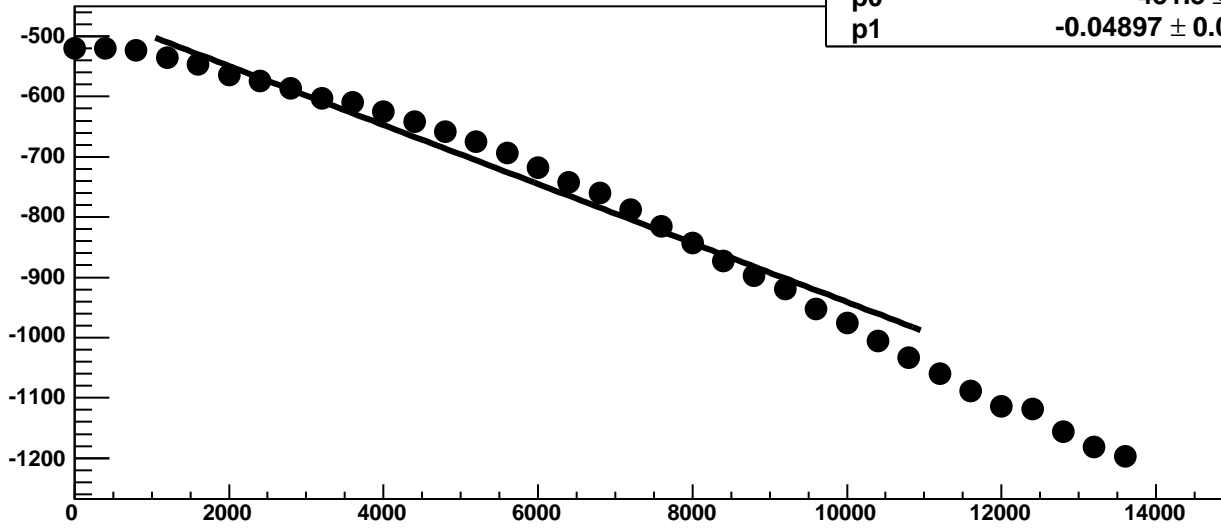
2723 / 23

p0

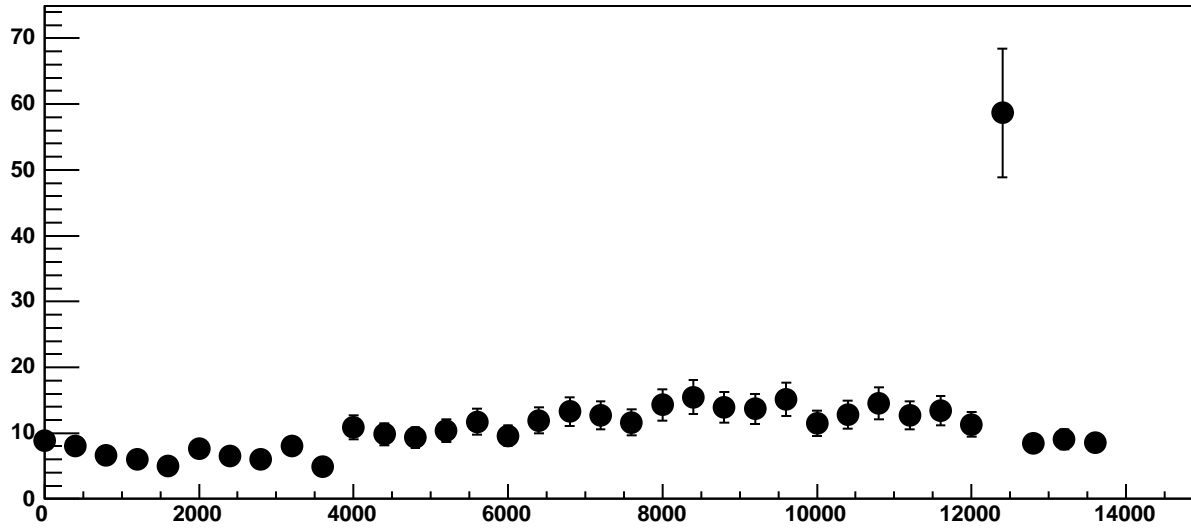
$-451.3 \pm 0.7609$

p1

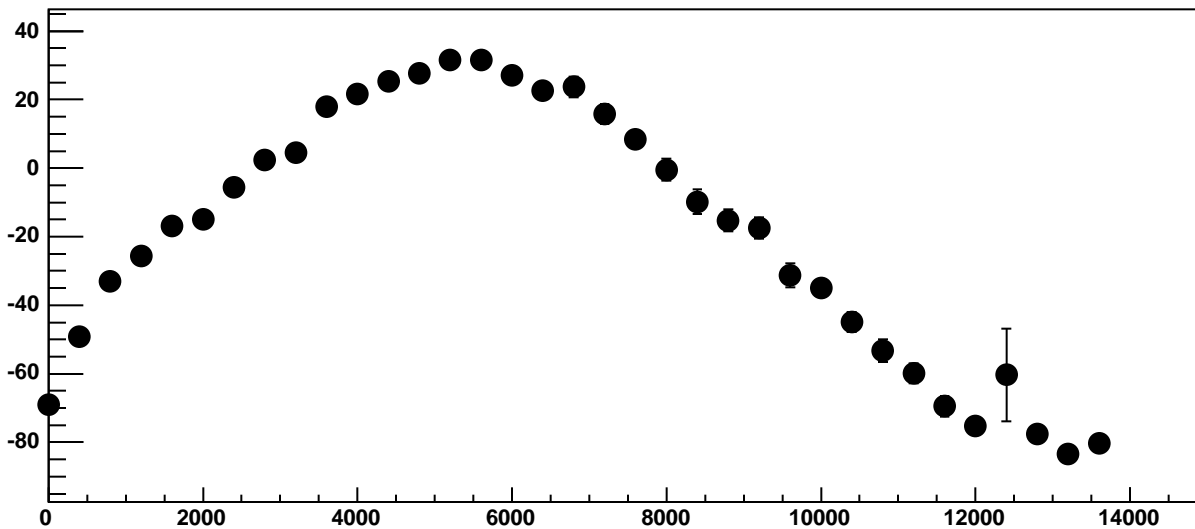
$-0.04897 \pm 0.0001531$



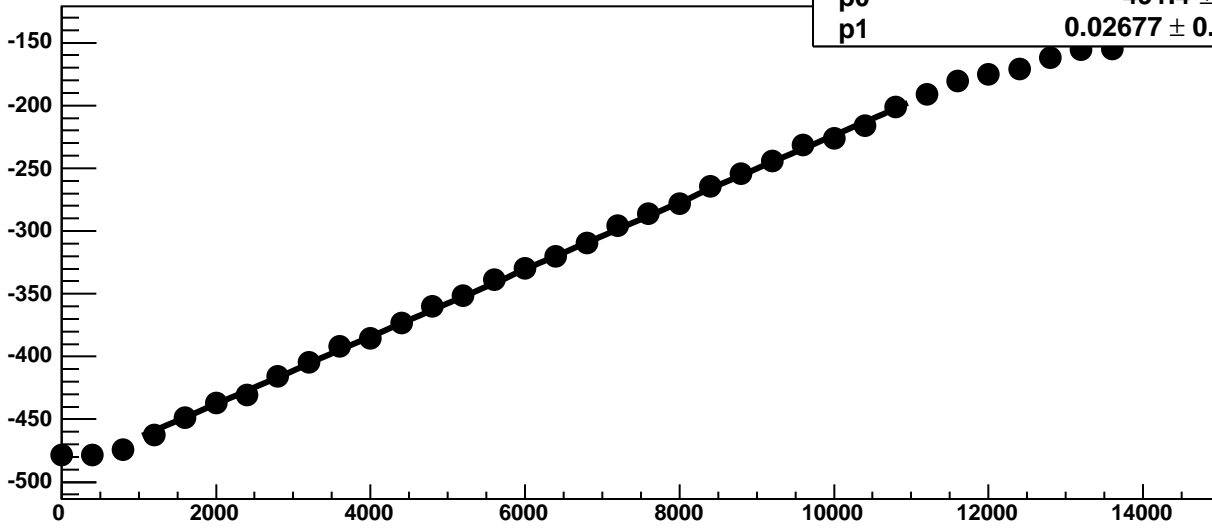
Chip 11, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

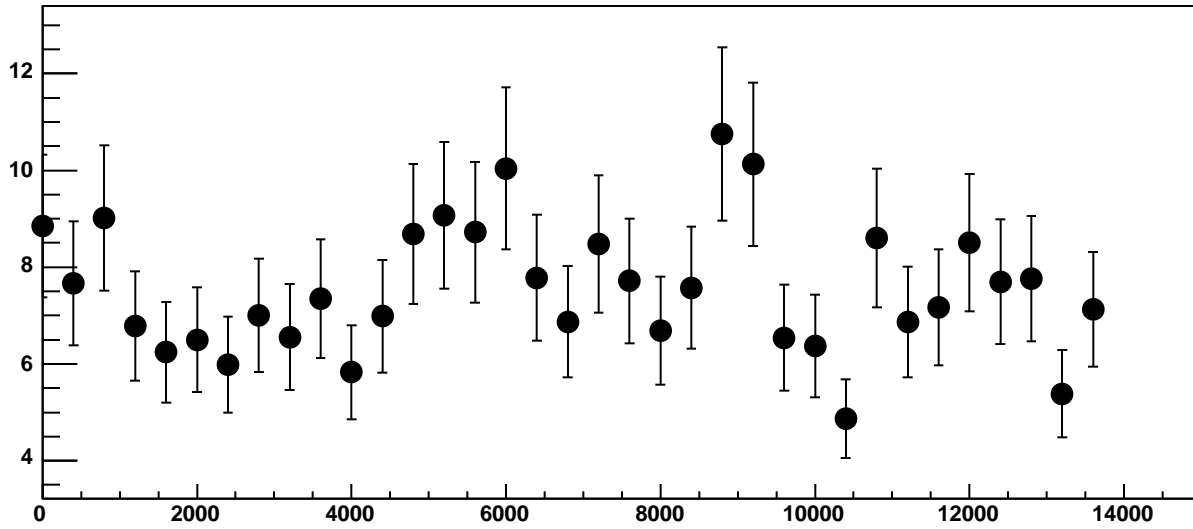


Chip 11, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

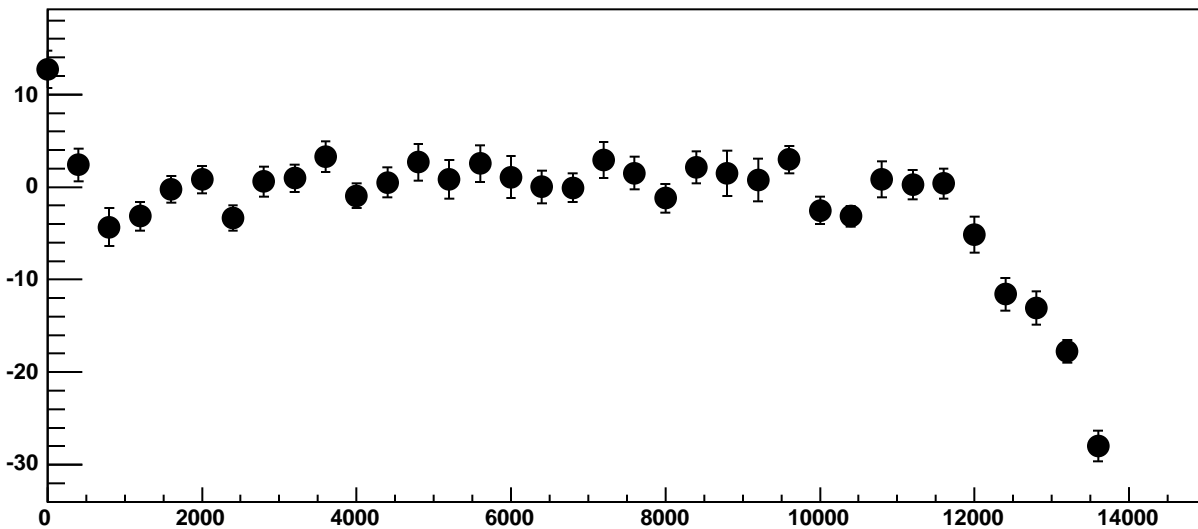


$\chi^2 / \text{ndf}$  39.53 / 23  
p0  $-491.4 \pm 0.7023$   
p1  $0.02677 \pm 0.000106$

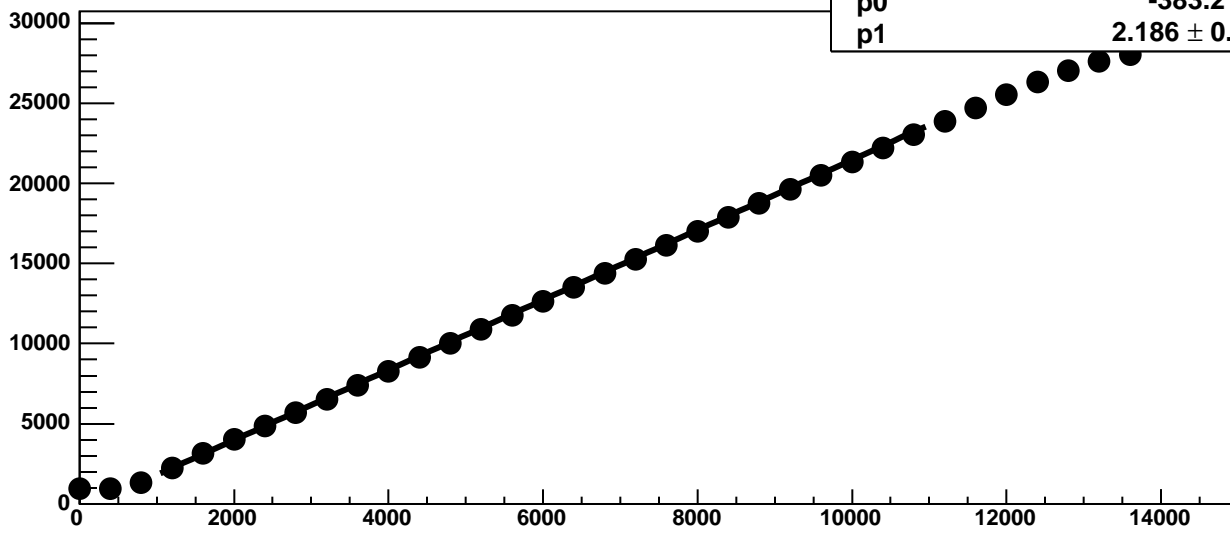
Chip 11, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

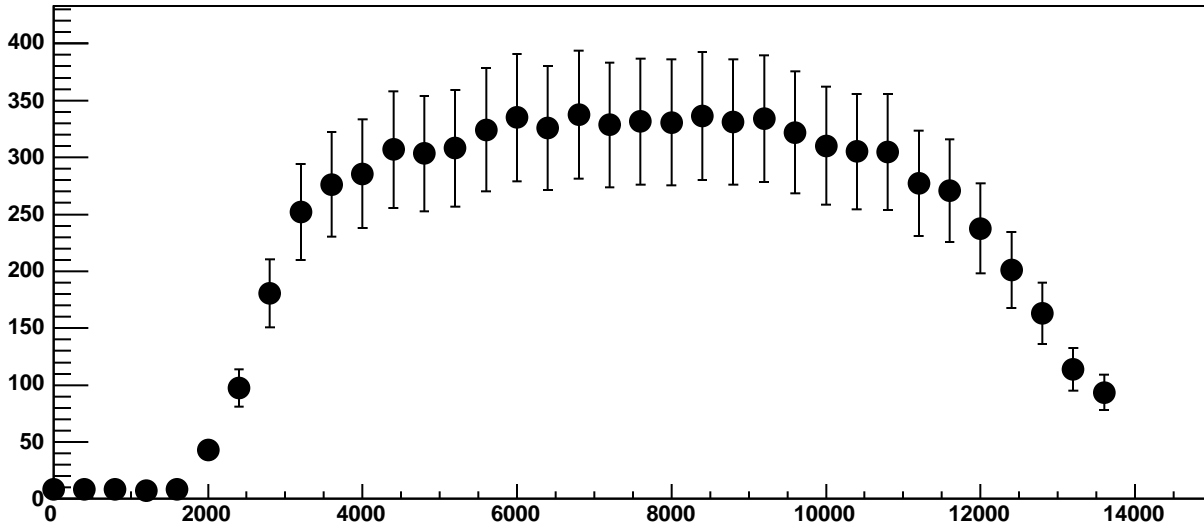


Chip 11, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC

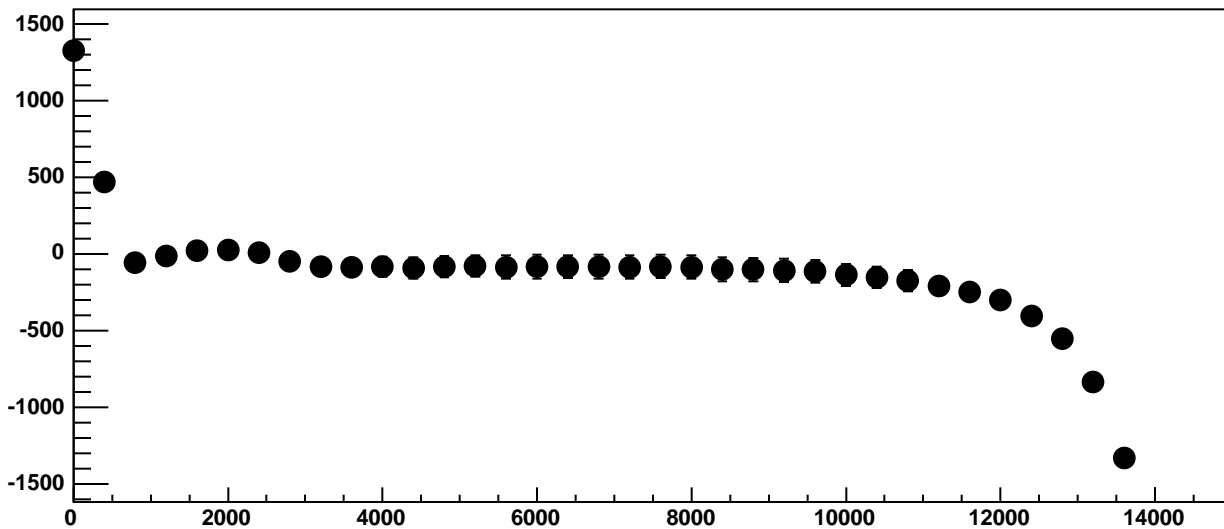


$\chi^2 / \text{ndf}$  237.7 / 23  
p0  $-383.2 \pm 3.669$   
p1  $2.186 \pm 0.002437$

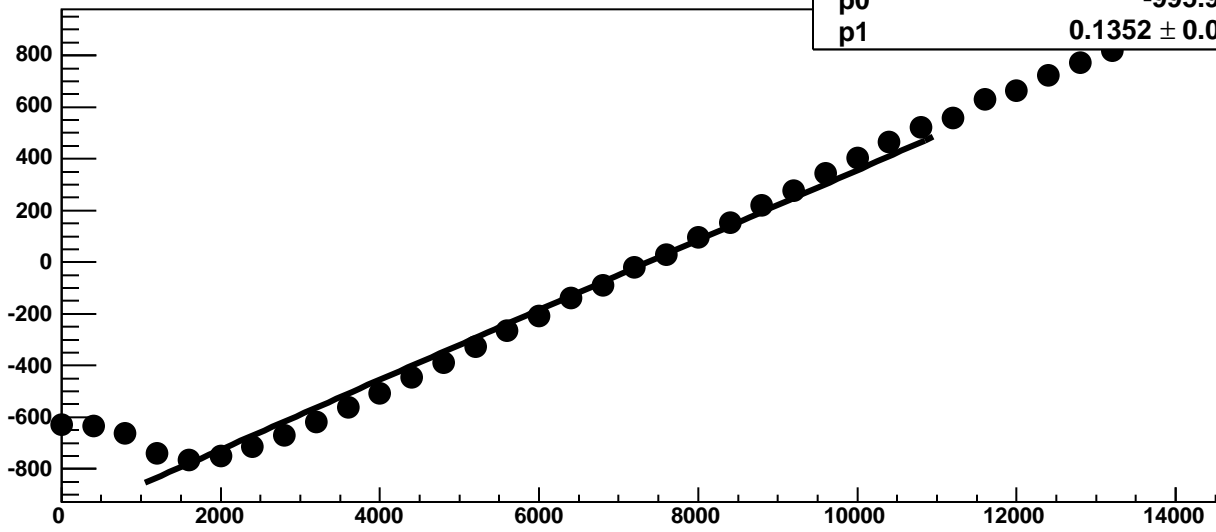
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC



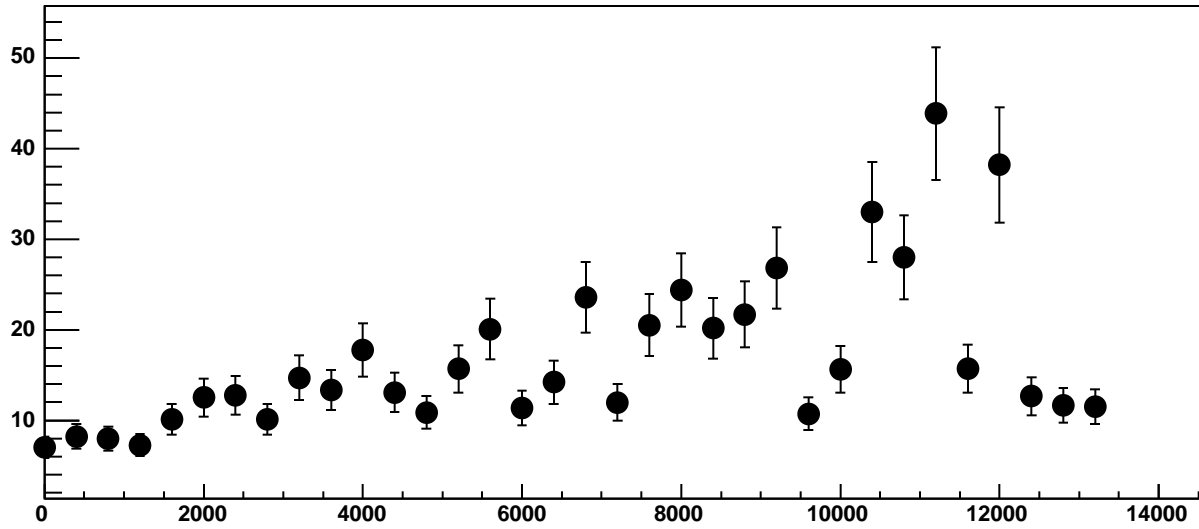
Chip 11, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC



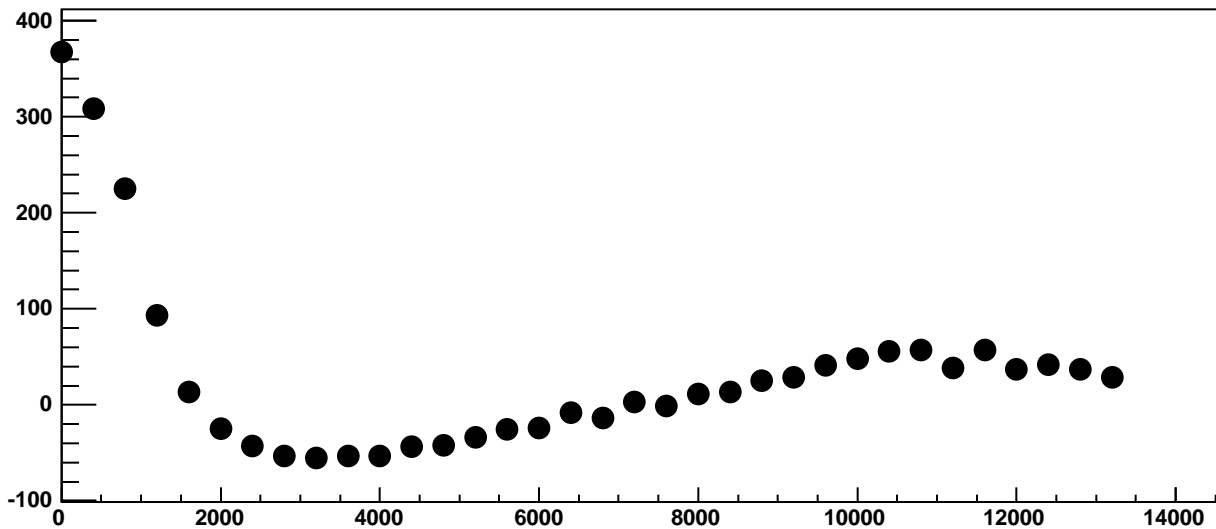
Chip 11, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



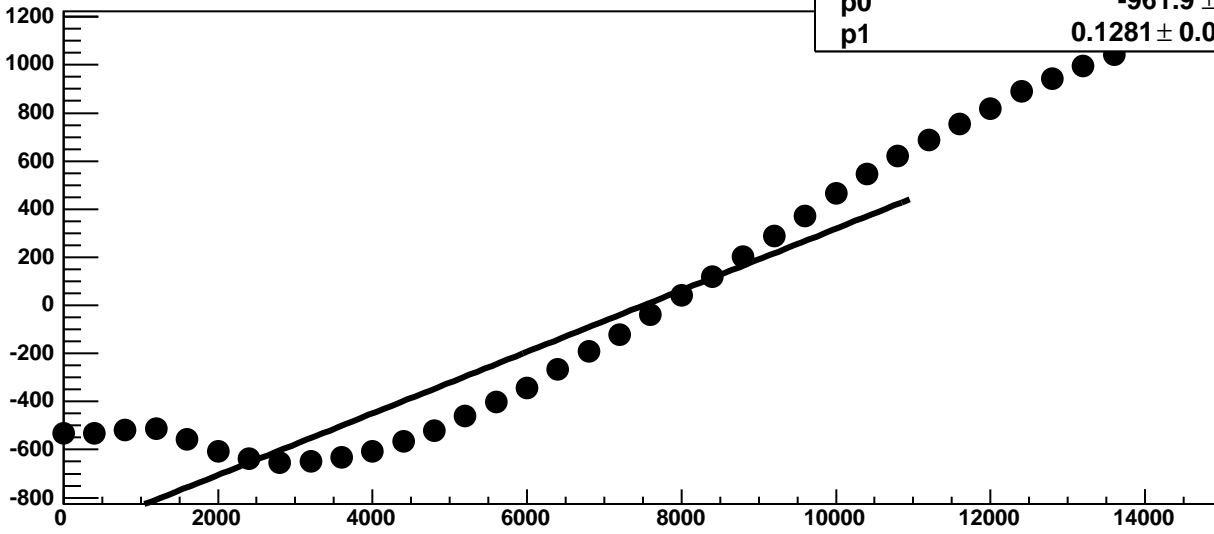
Chip 11, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

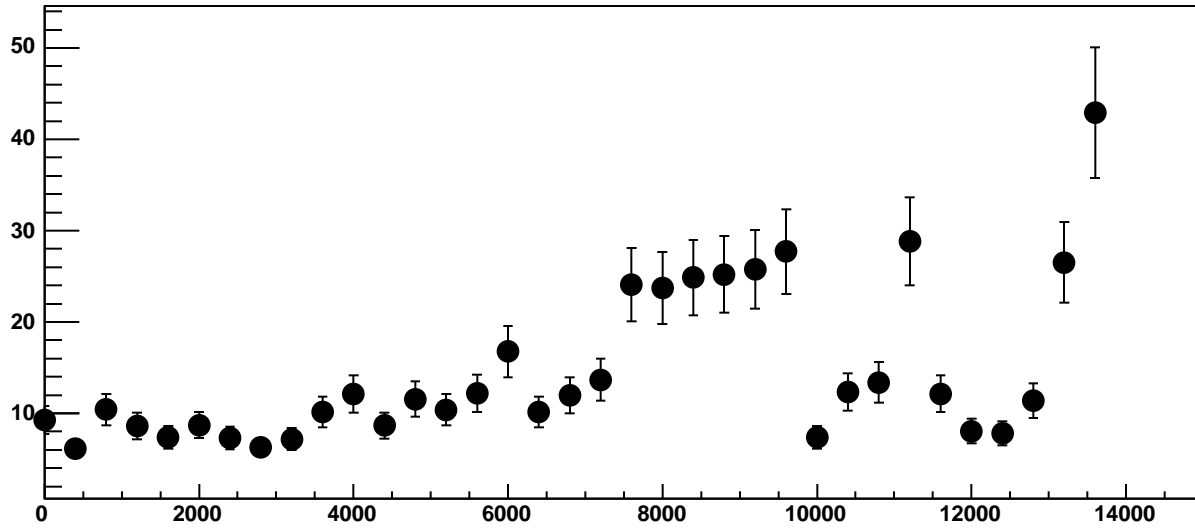


Chip 11, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

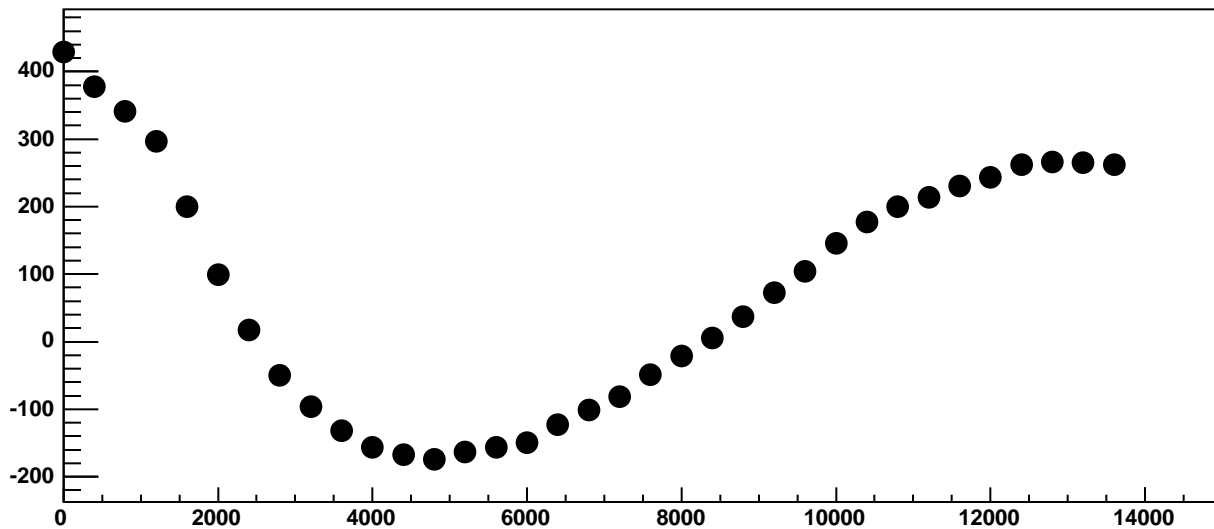


$\chi^2 / \text{ndf}$	9.185e+04 / 23
p0	-961.9 ± 0.9125
p1	0.1281 ± 0.0001672

Chip 11, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

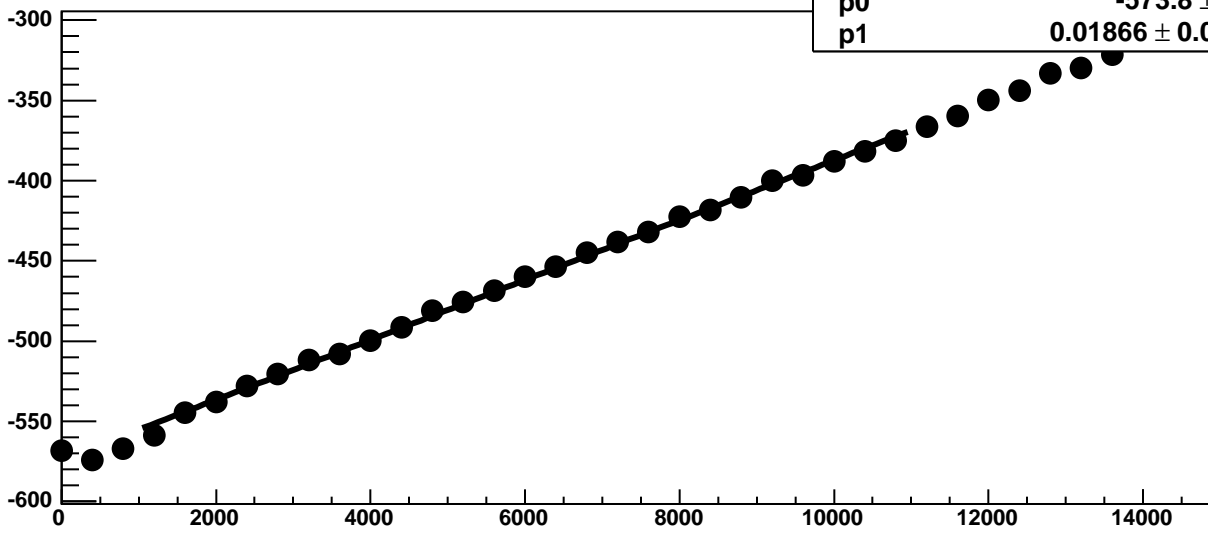


Chip 11, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



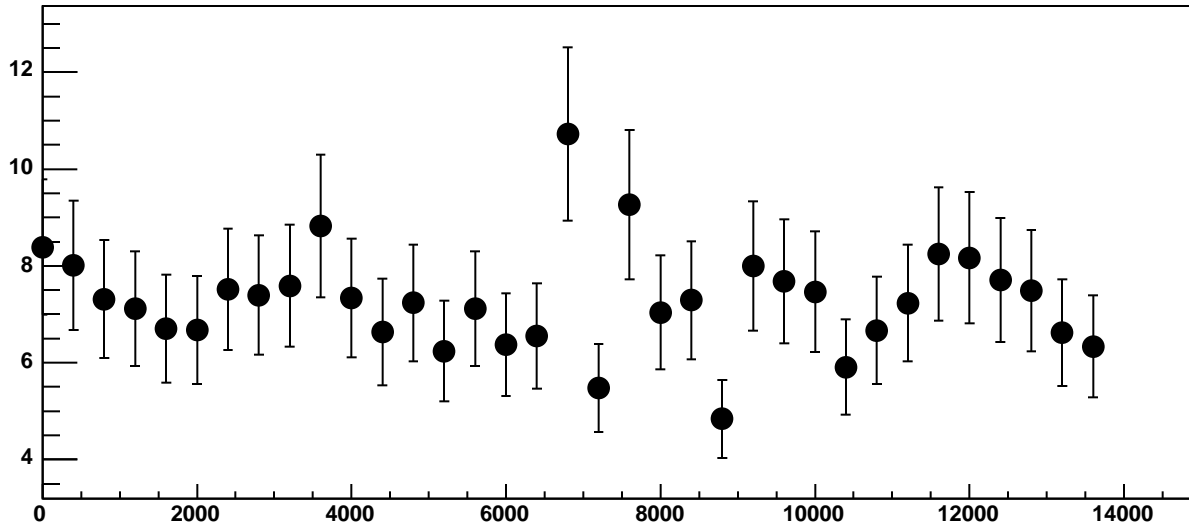


Chip 11, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

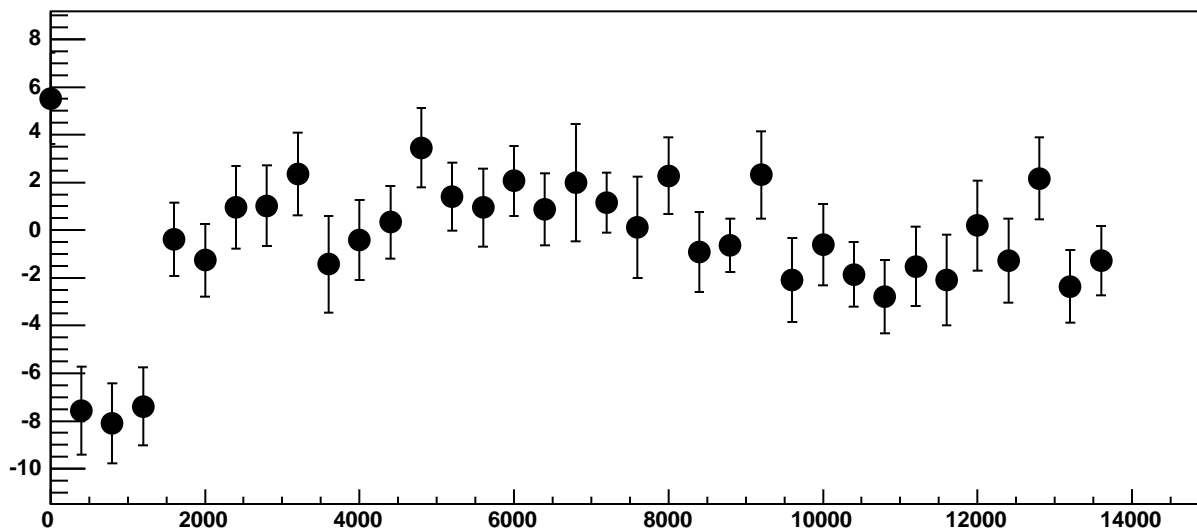


$\chi^2 / \text{ndf}$  44.79 / 23  
p0  $-573.8 \pm 0.7466$   
p1  $0.01866 \pm 0.0001095$

Chip 11, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC



Chip 11, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC

$\chi^2 / \text{ndf}$

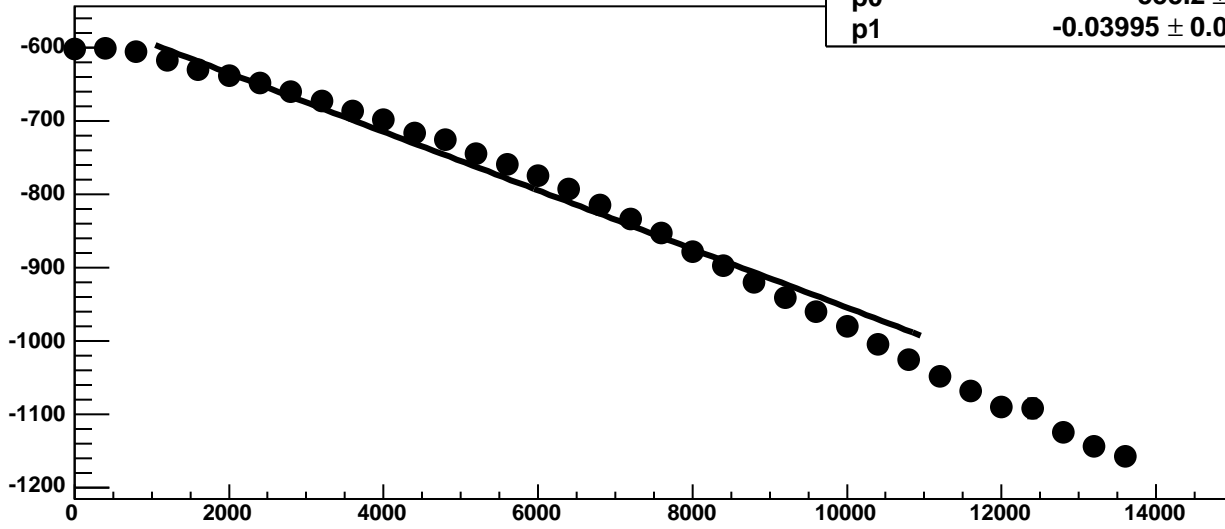
1364 / 23

p0

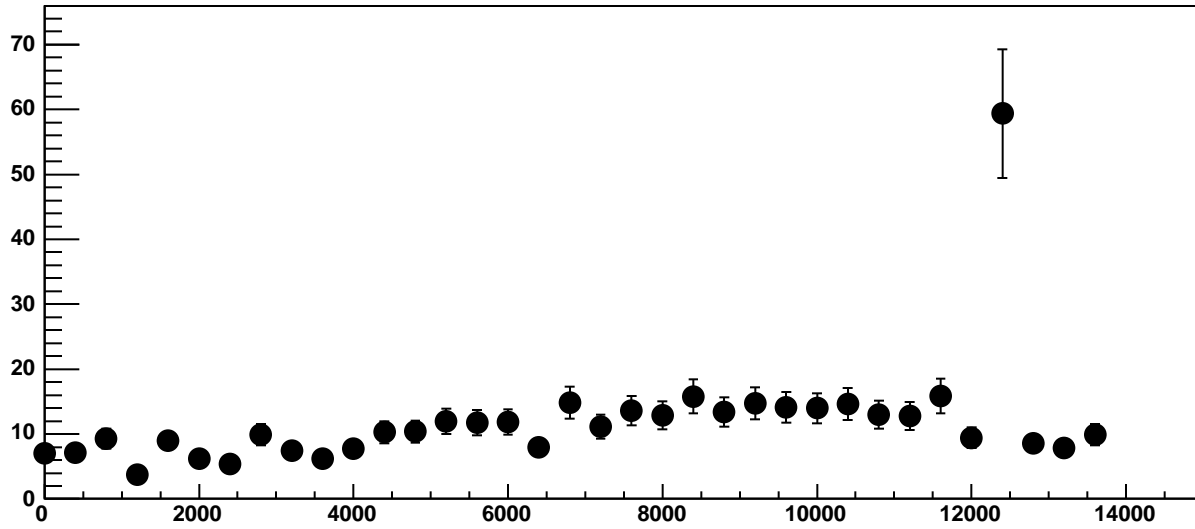
$-555.2 \pm 0.7095$

p1

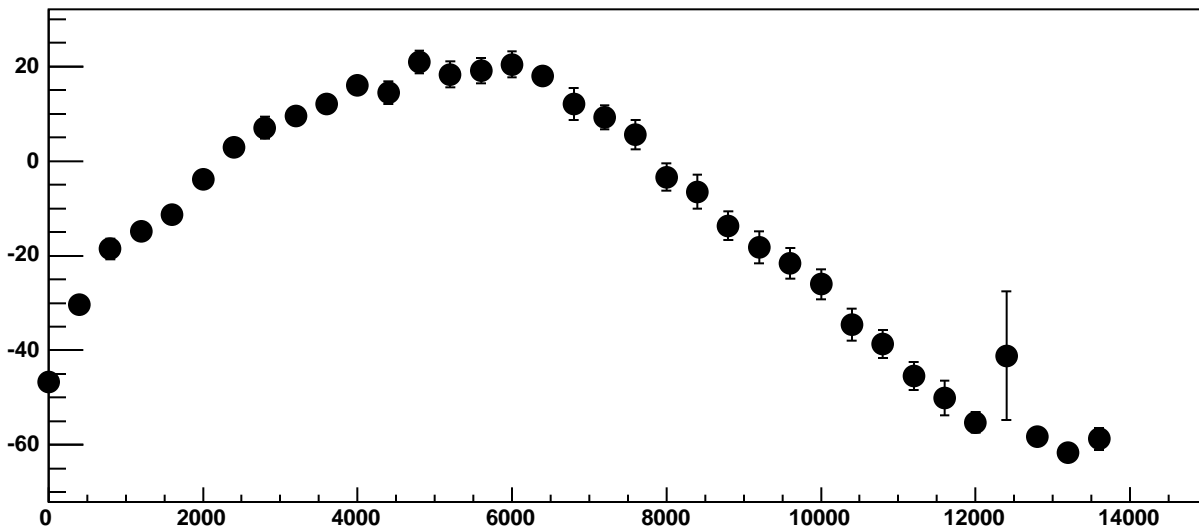
$-0.03995 \pm 0.0001473$



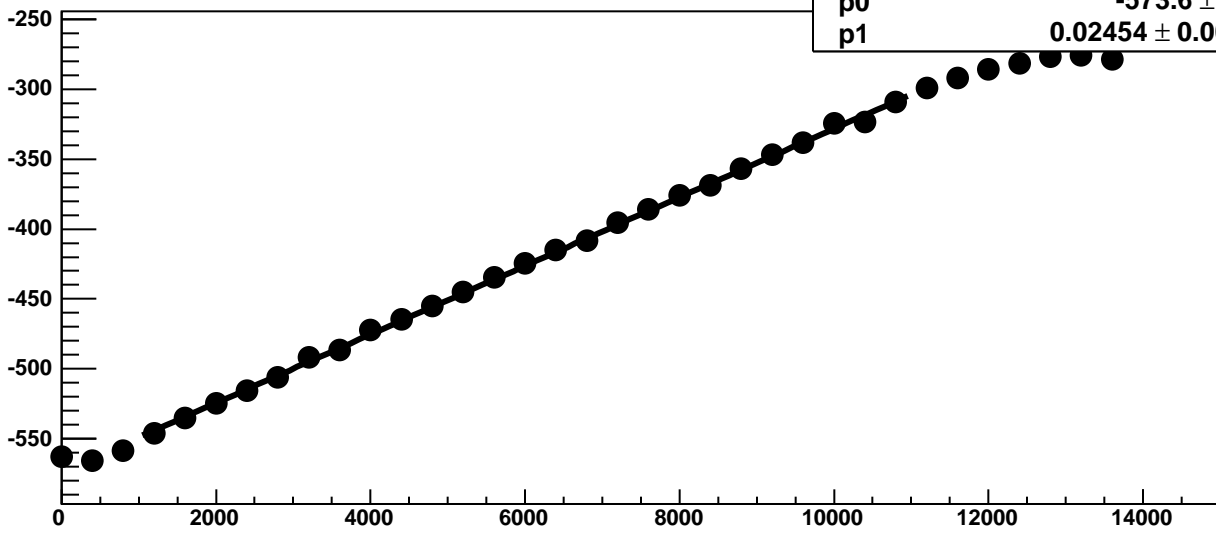
Chip 11, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC



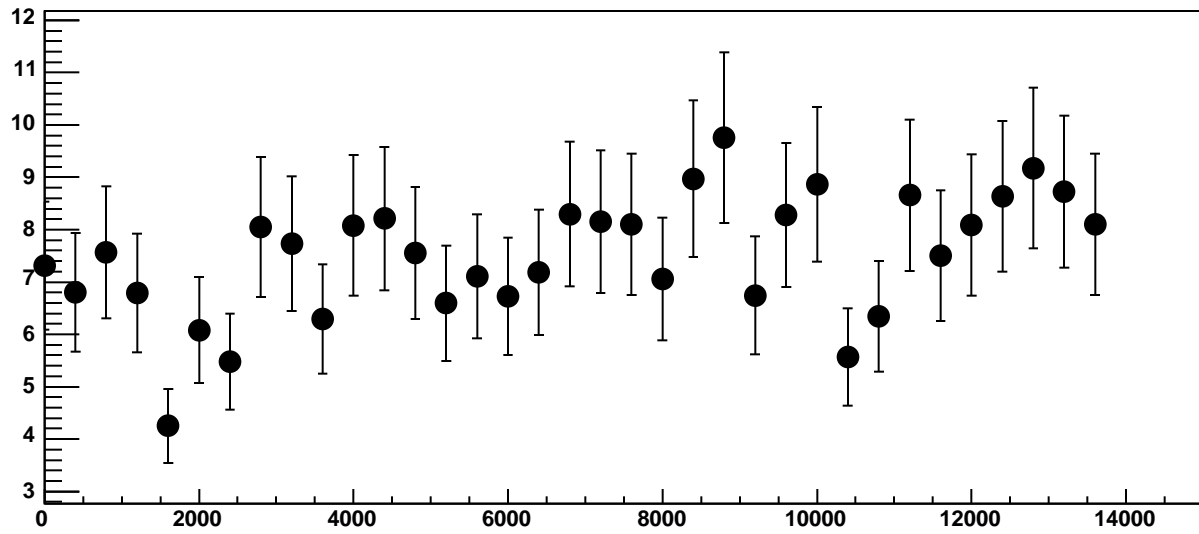
Chip 11, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC



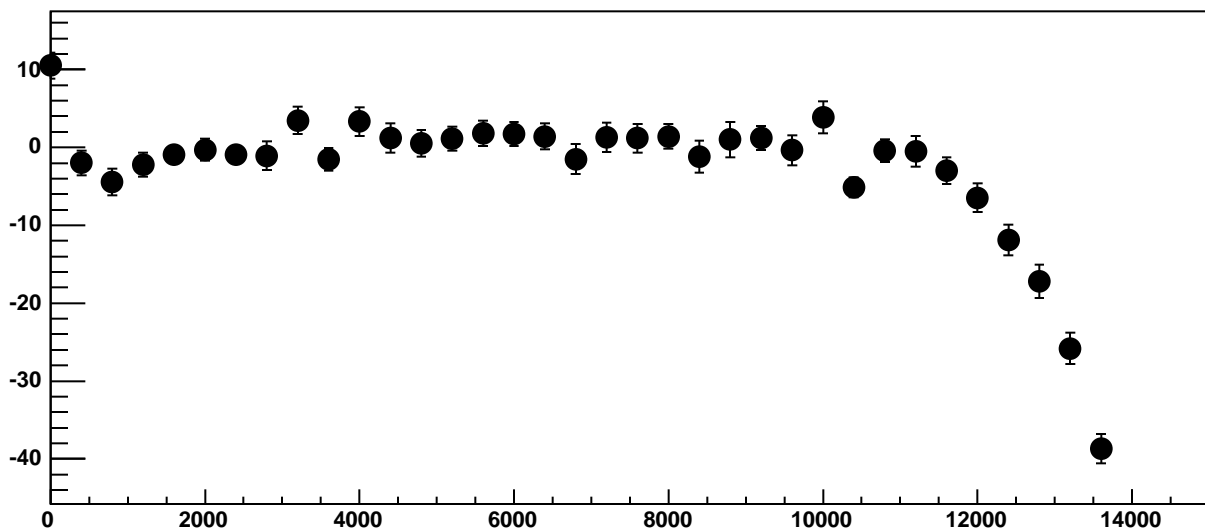
Chip 11, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



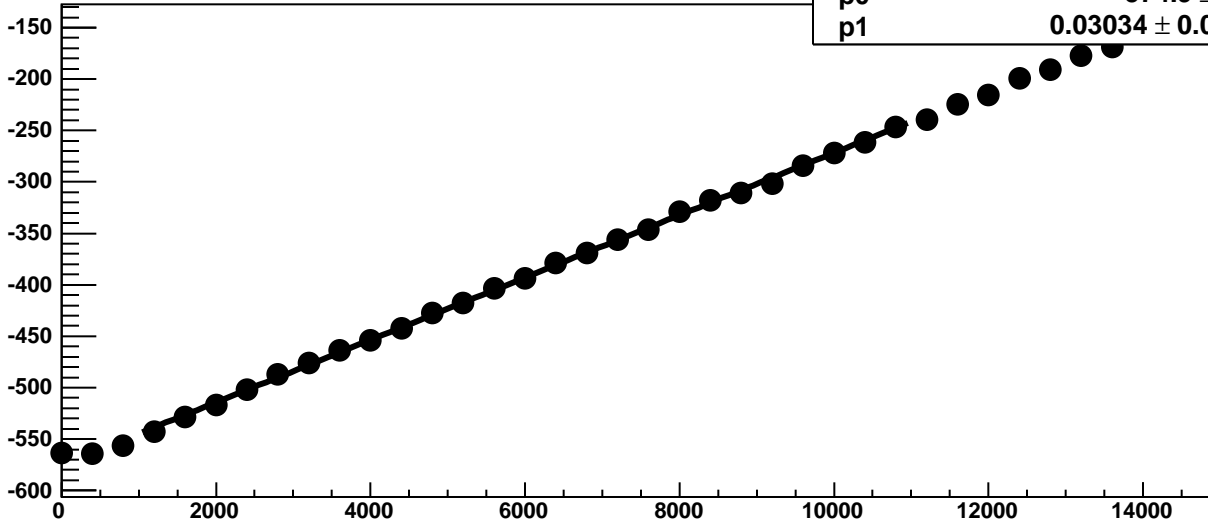
Chip 11, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

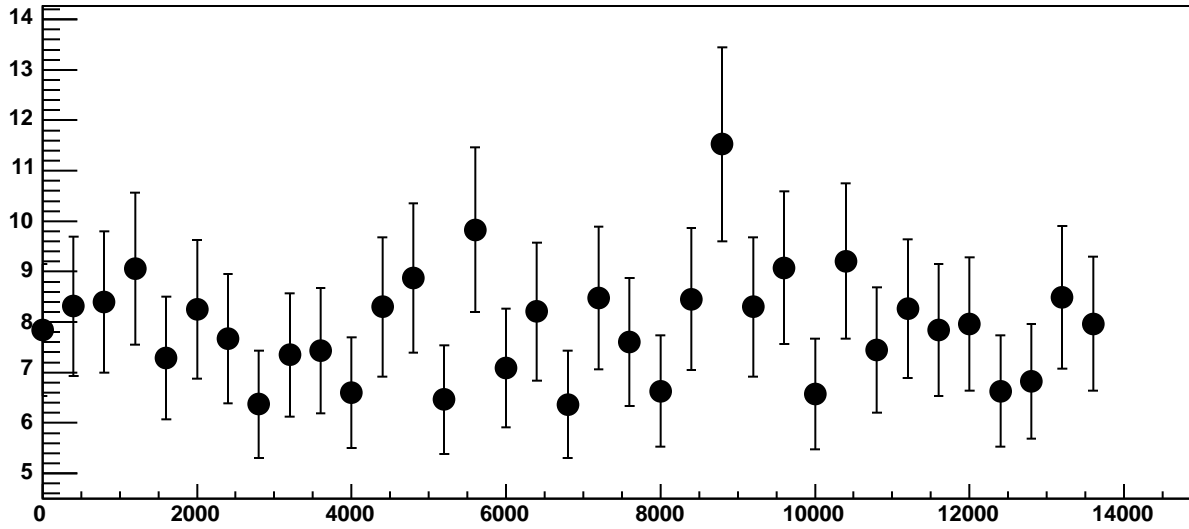


Chip 11, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

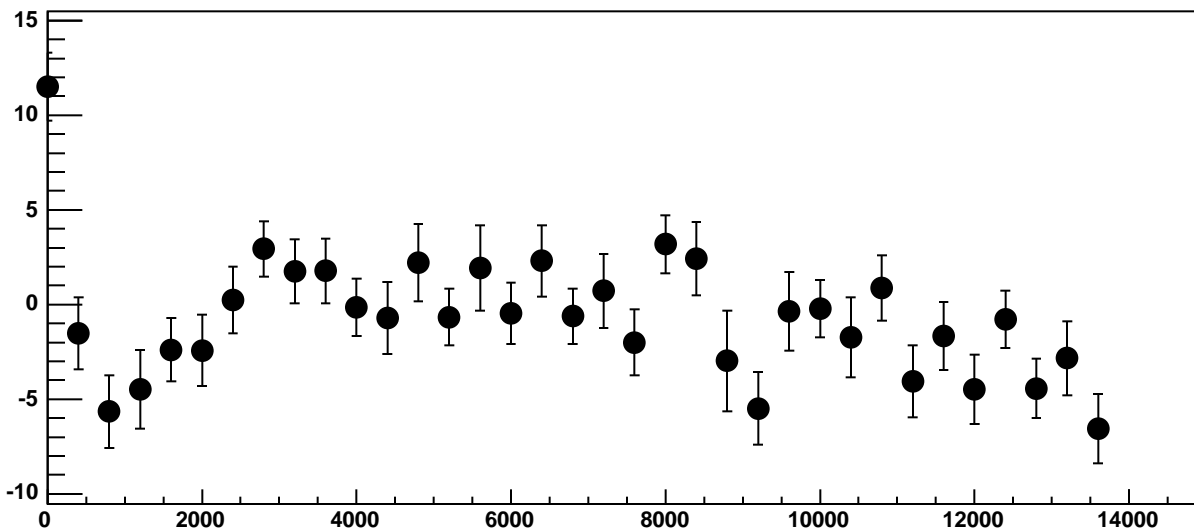


$\chi^2 / \text{ndf}$  36.48 / 23  
p0  $-574.9 \pm 0.8127$   
p1  $0.03034 \pm 0.0001246$

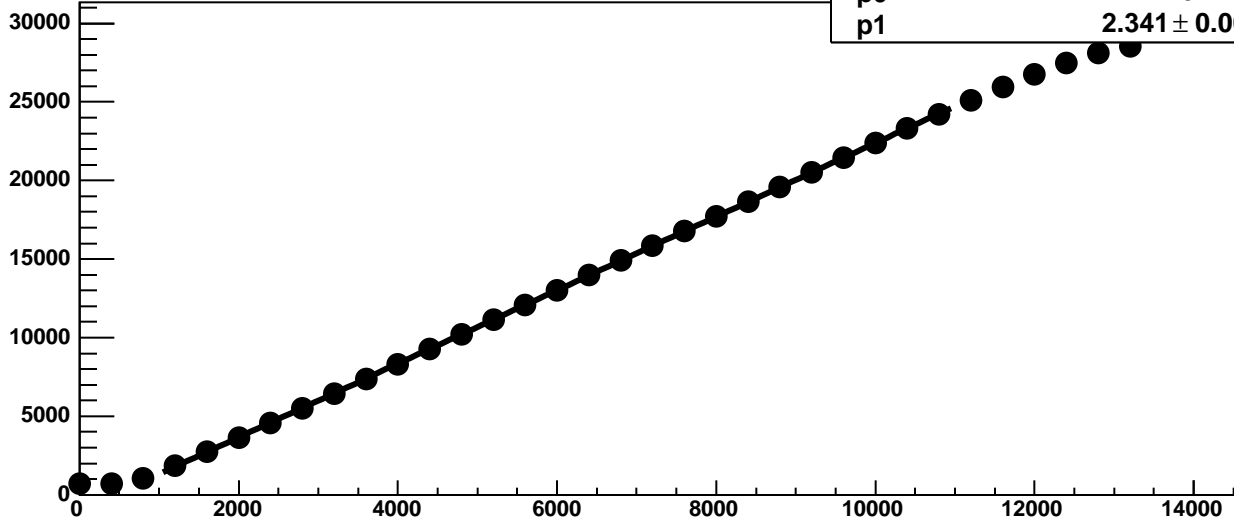
Chip 11, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC



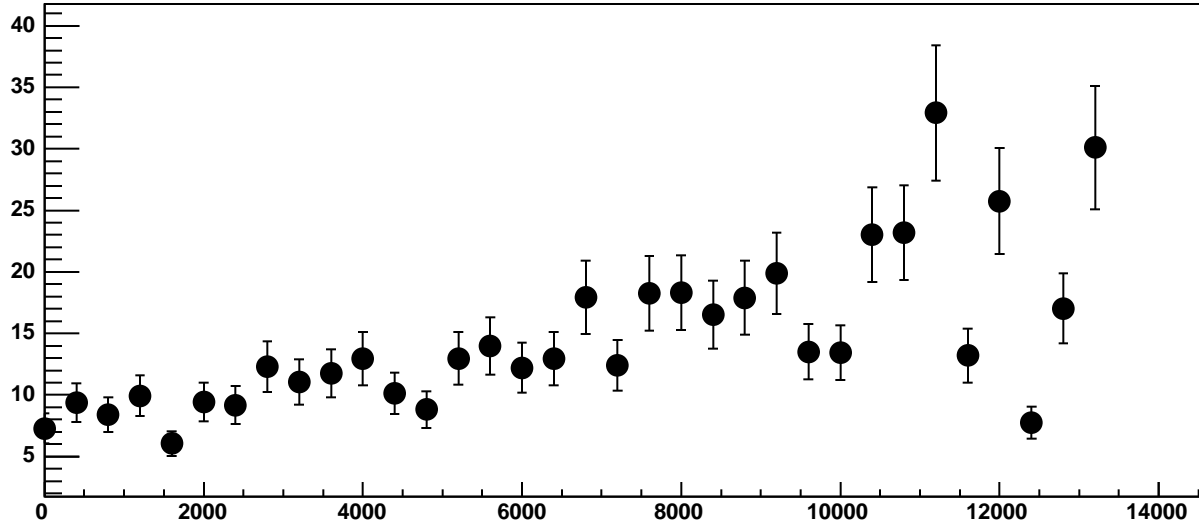
Chip 11, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



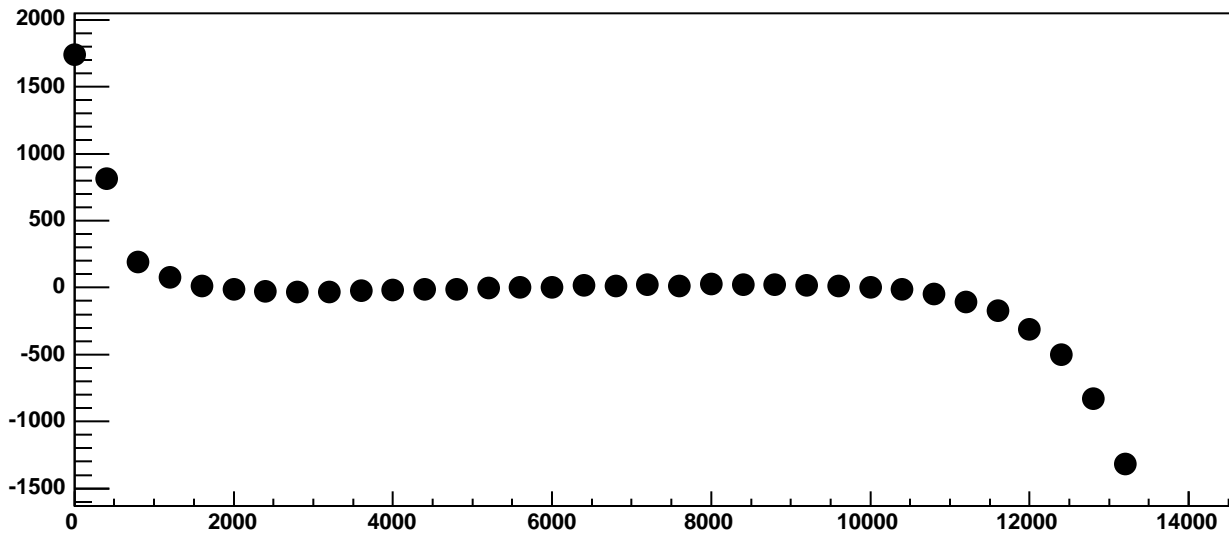
Chip 11, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 11, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC

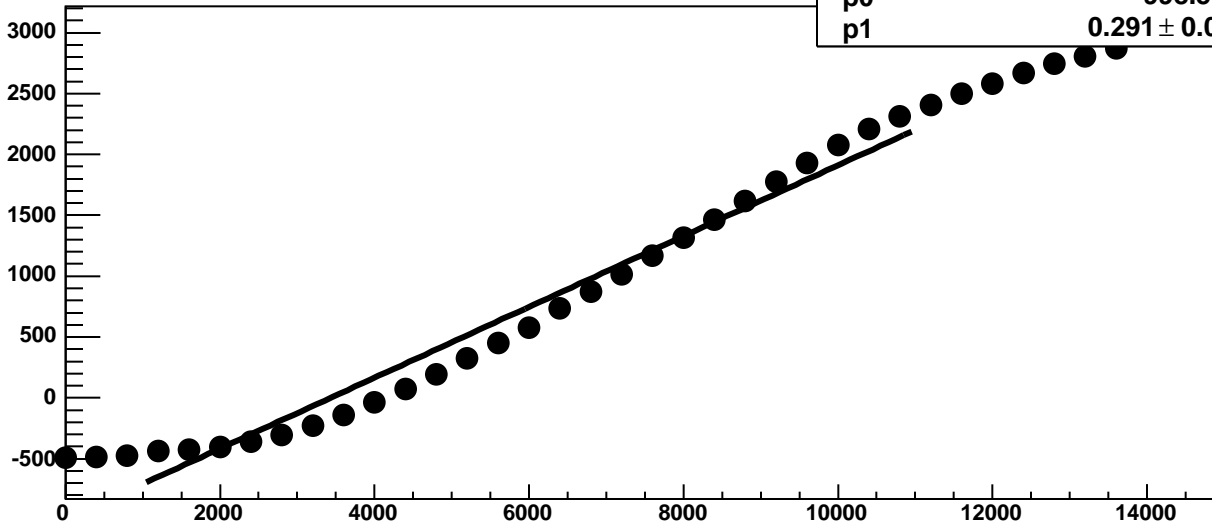


Chip 11, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC

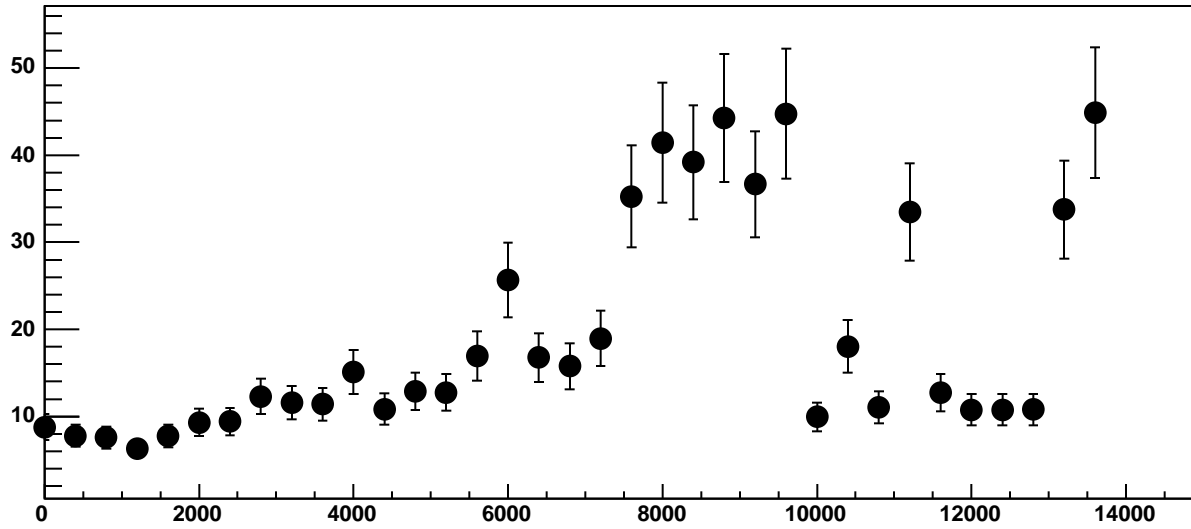


Chip 11, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

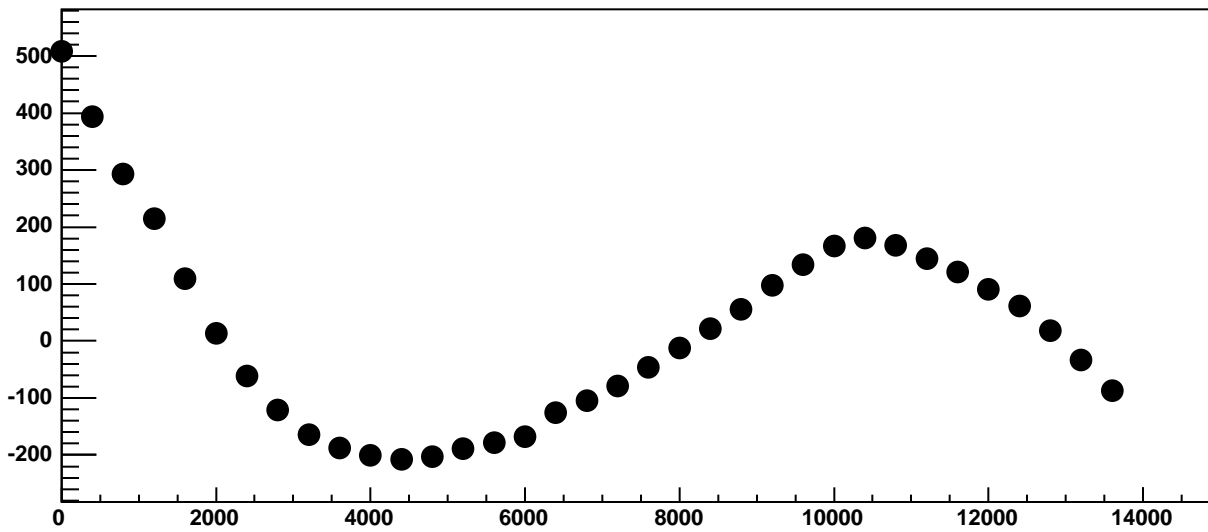
$\chi^2 / \text{ndf}$  7.39e+04 / 23  
p0 -998.5 ± 1.004  
p1 0.291 ± 0.0001915



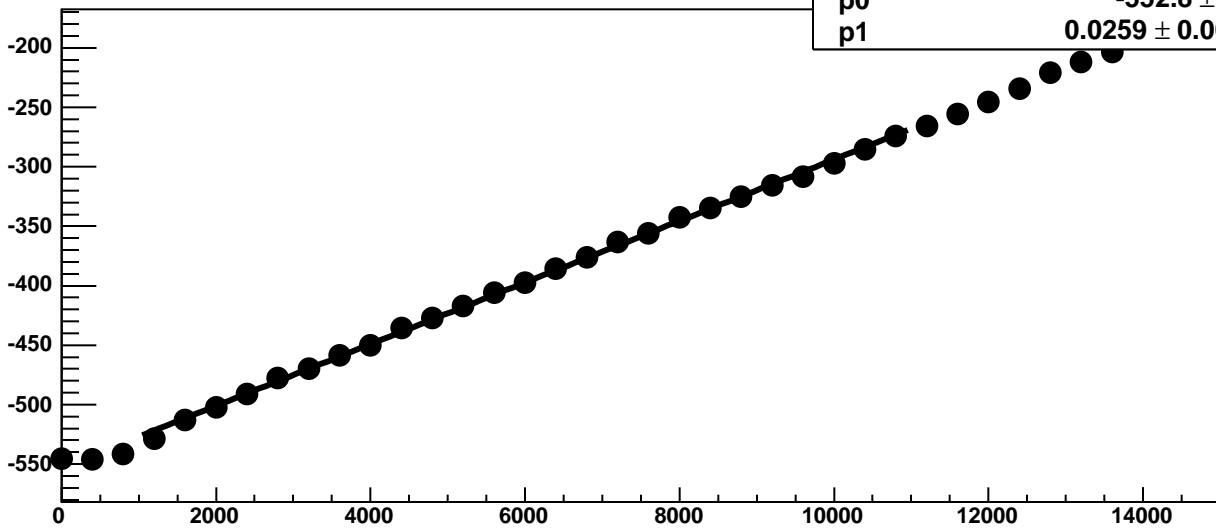
Chip 11, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

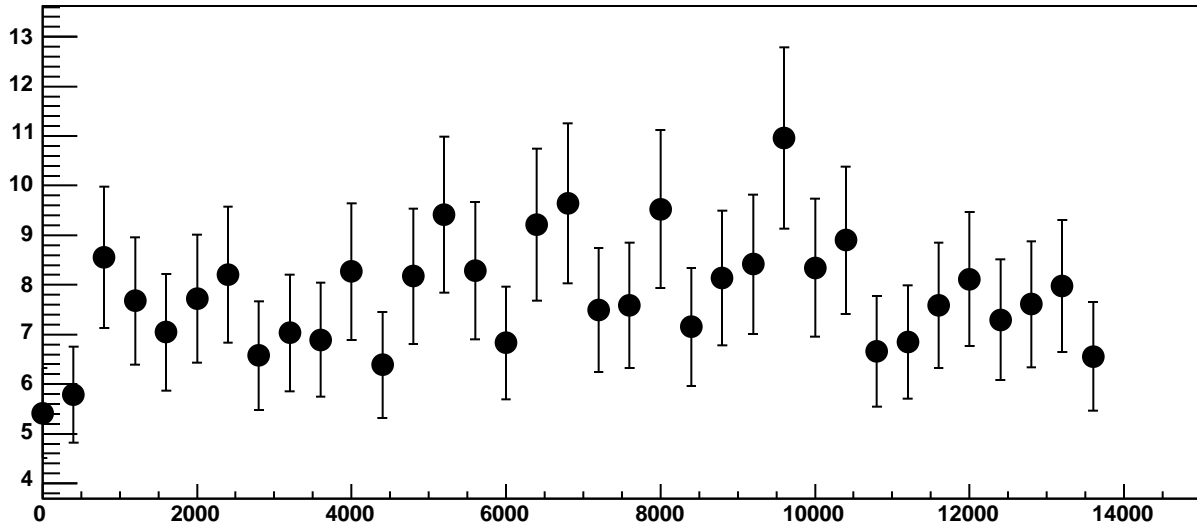


Chip 11, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

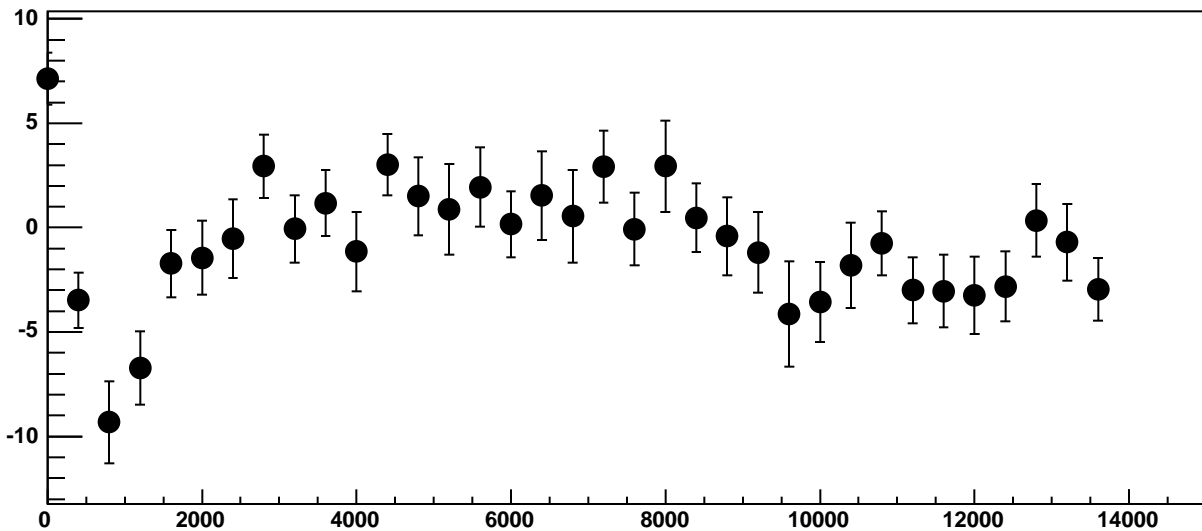


$\chi^2 / \text{ndf}$  40.24 / 23  
p0  $-552.8 \pm 0.7922$   
p1  $0.0259 \pm 0.0001229$

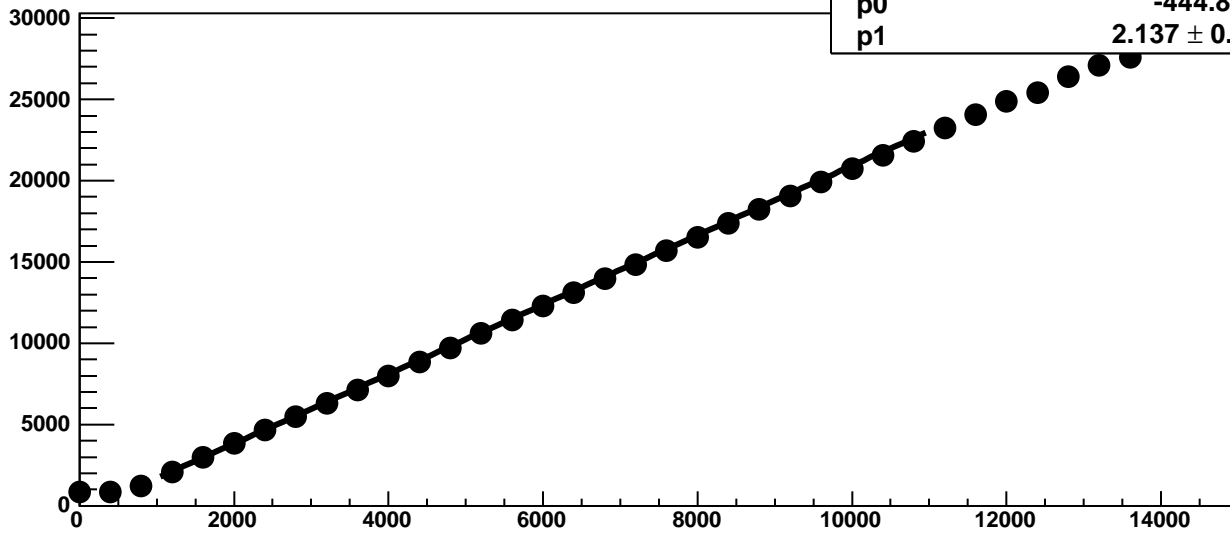
Chip 11, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

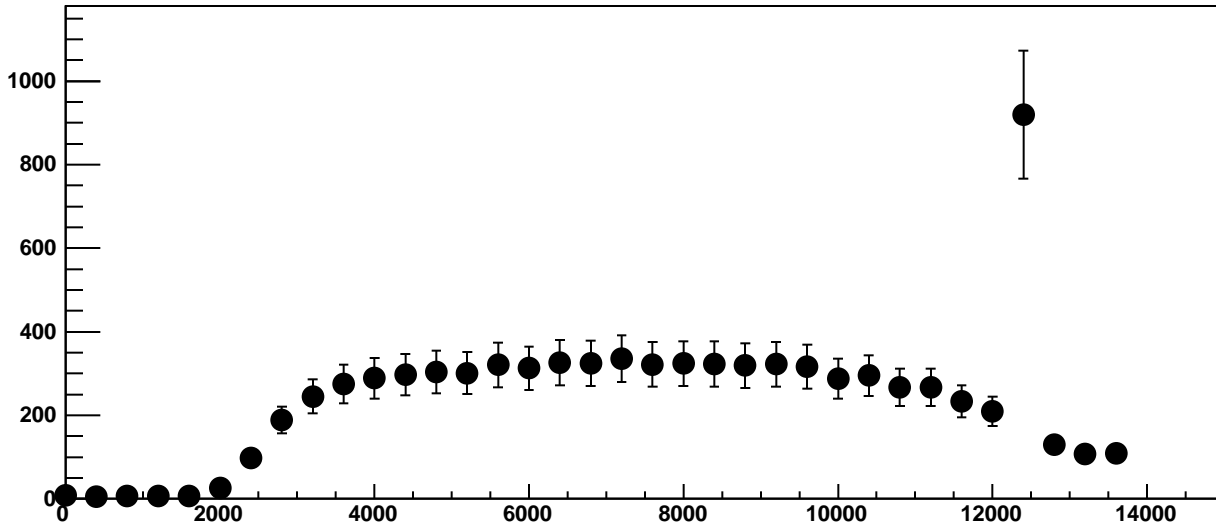


Chip 11, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC

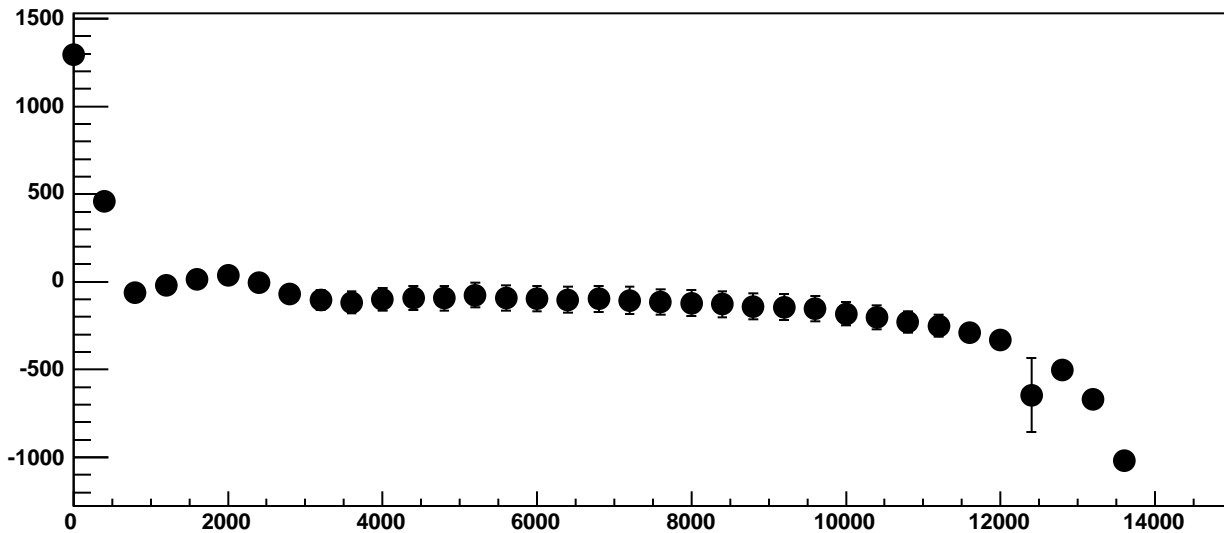


$\chi^2 / \text{ndf}$  352.4 / 23  
p0 -444.8 ± 3.551  
p1 2.137 ± 0.002296

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC

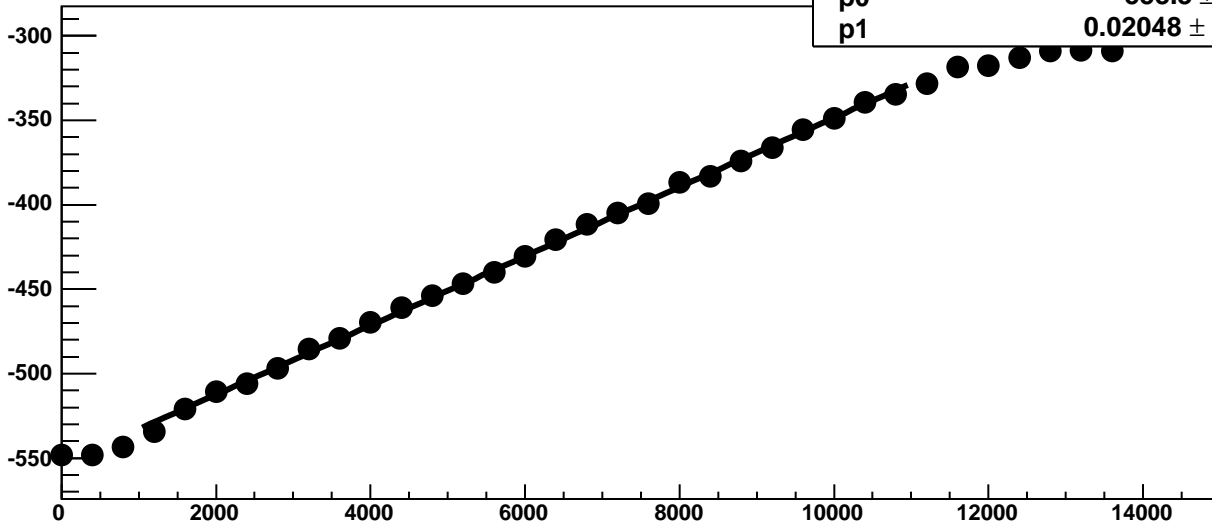


Chip 11, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC



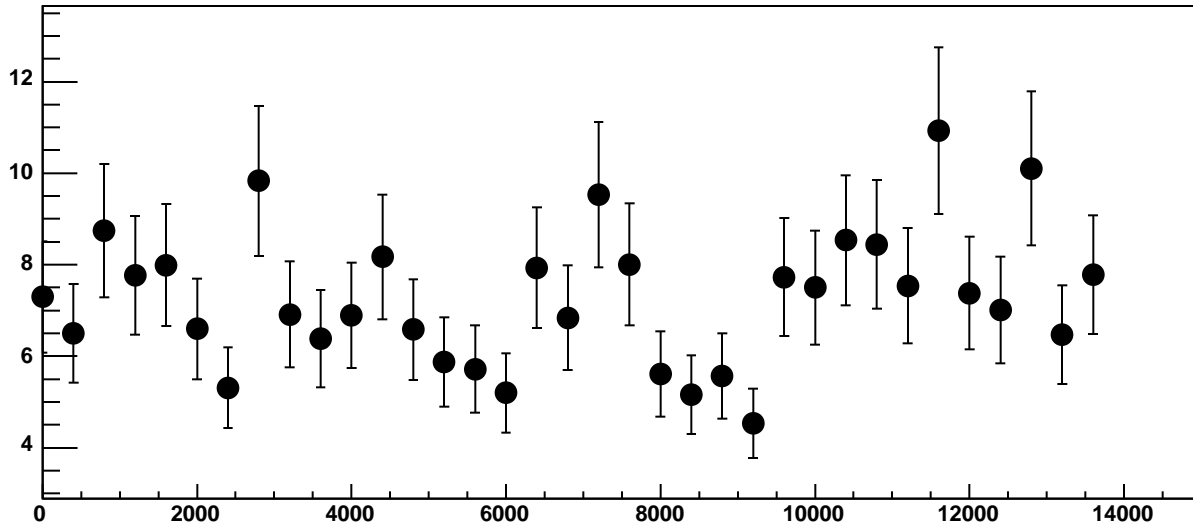


Chip 11, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

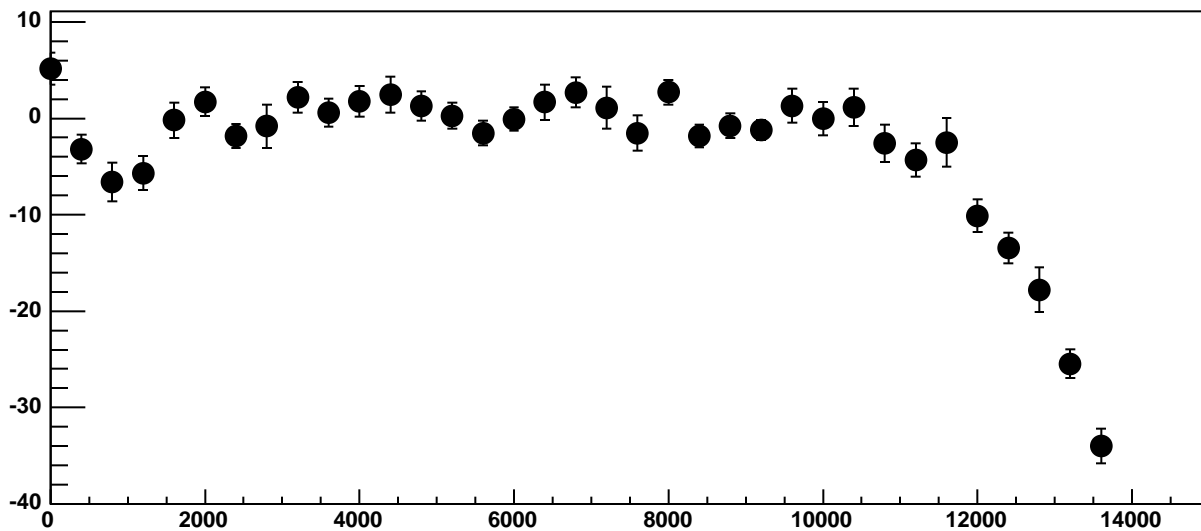


$\chi^2 / \text{ndf}$  36.86 / 23  
p0  $-553.3 \pm 0.7383$   
p1  $0.02048 \pm 0.00011$

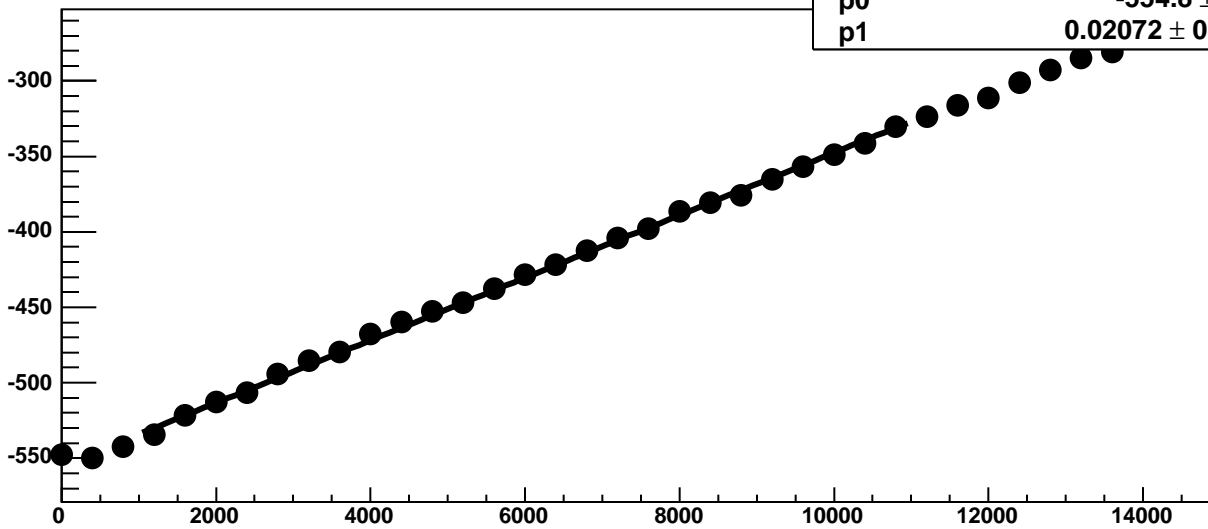
Chip 11, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

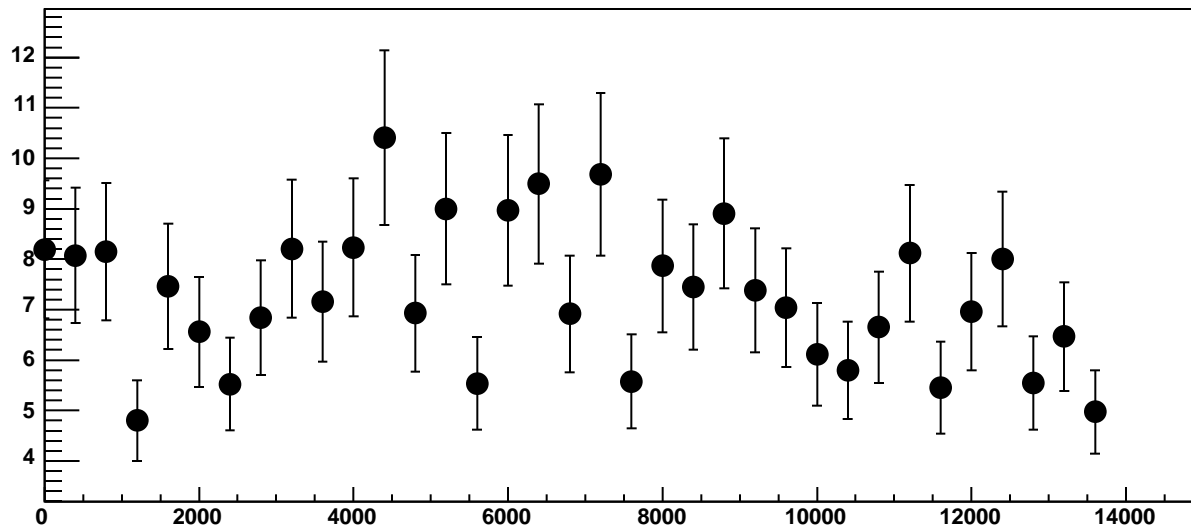


Chip 11, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

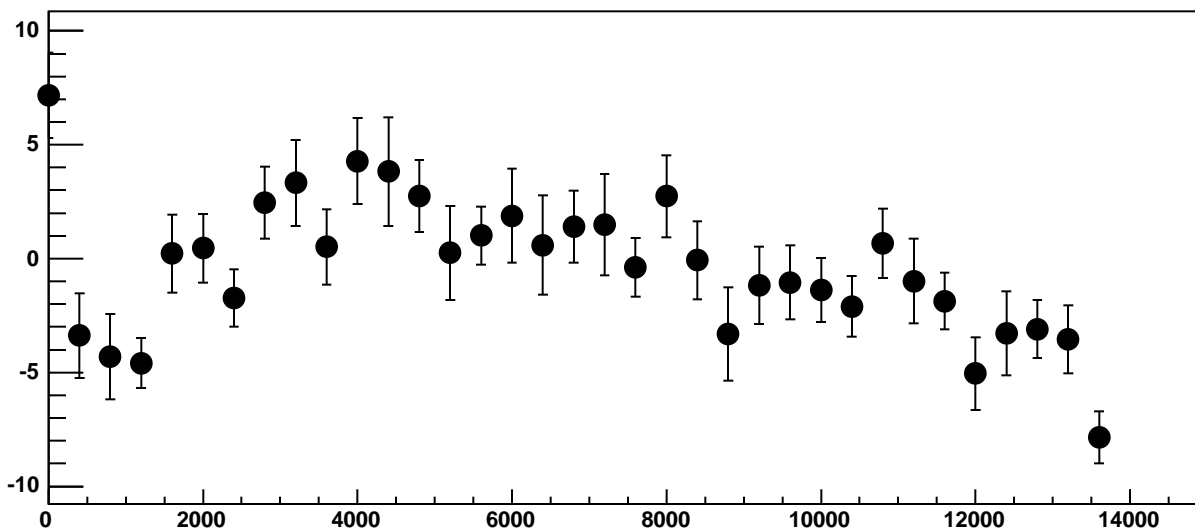


$\chi^2 / \text{ndf}$  48.07 / 23  
p0  $-554.8 \pm 0.6774$   
p1  $0.02072 \pm 0.000102$

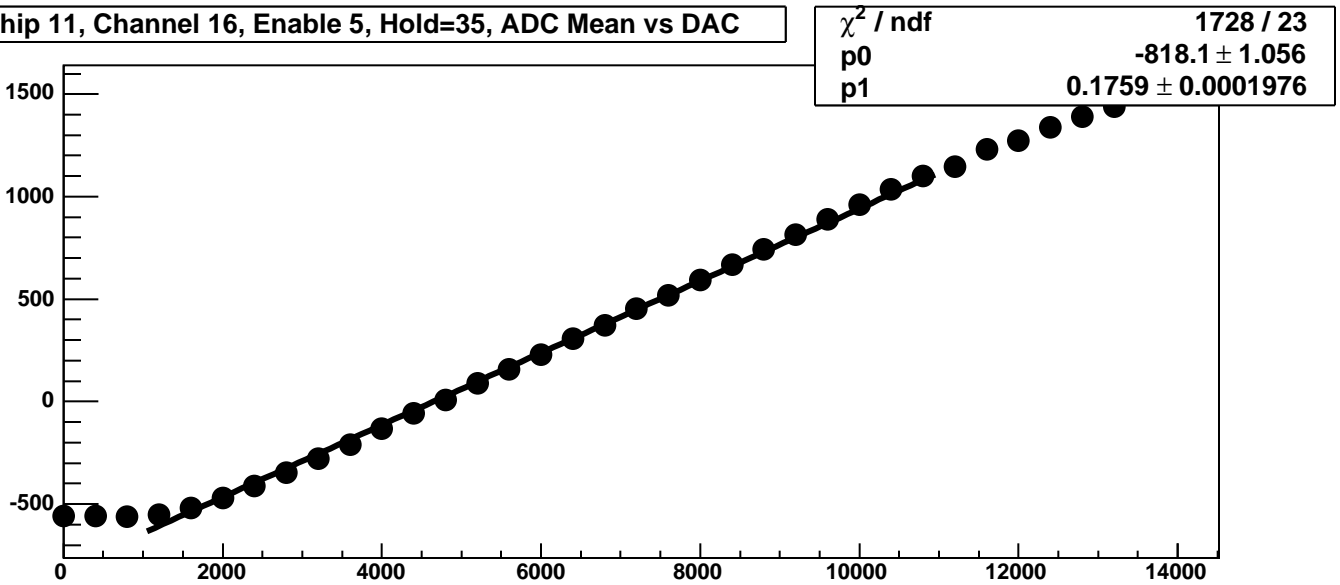
Chip 11, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



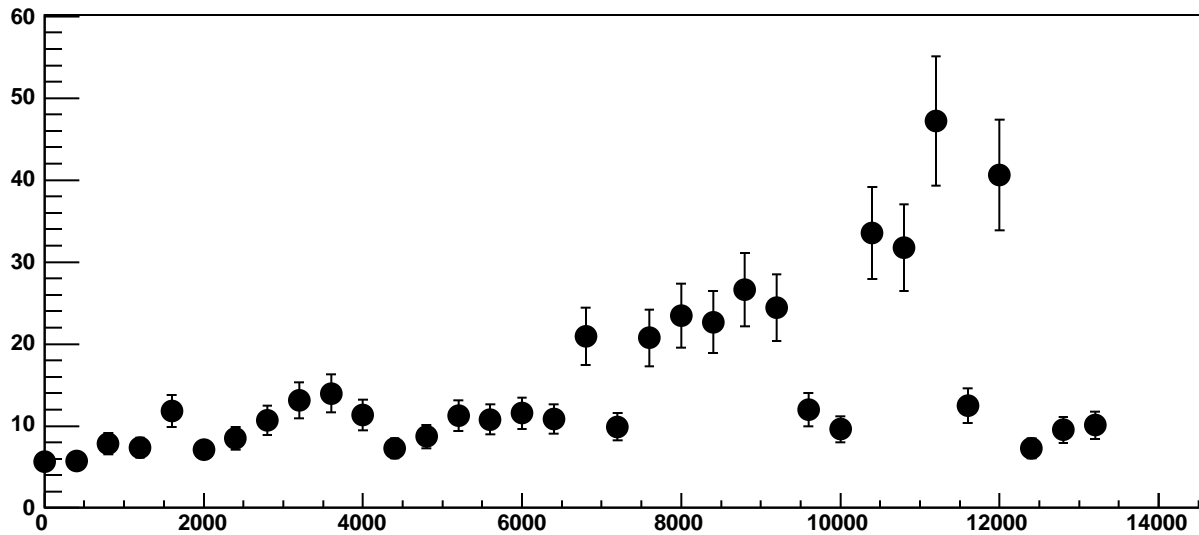
Chip 11, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



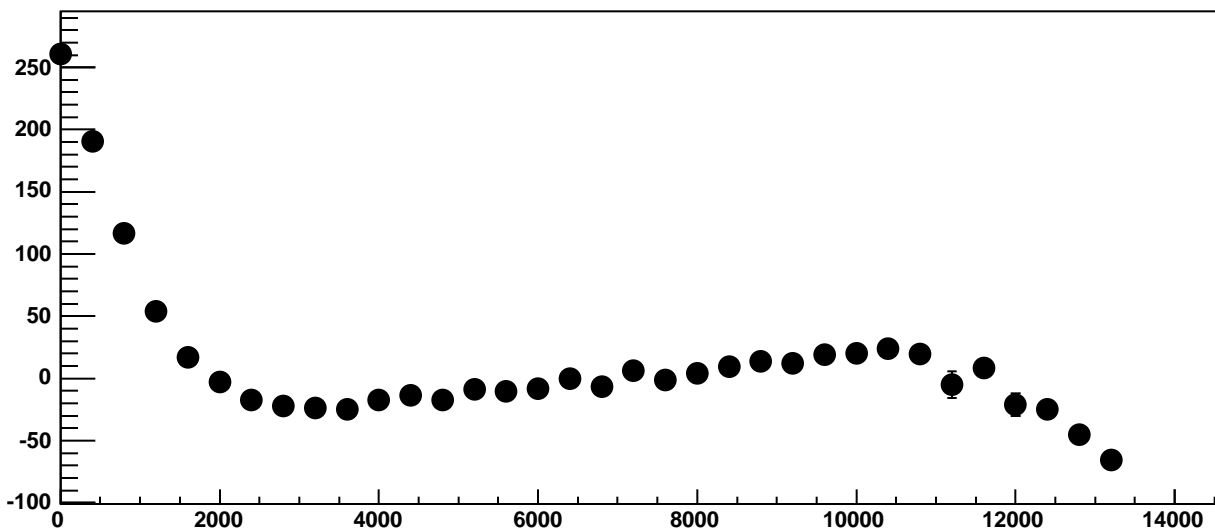
Chip 11, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC



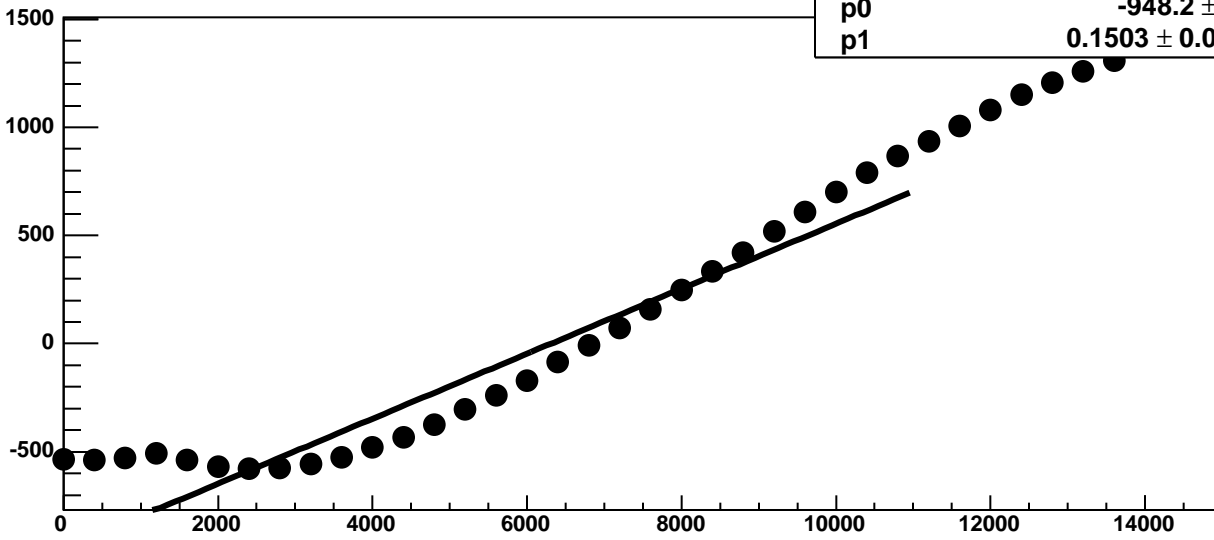
Chip 11, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC



Chip 11, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

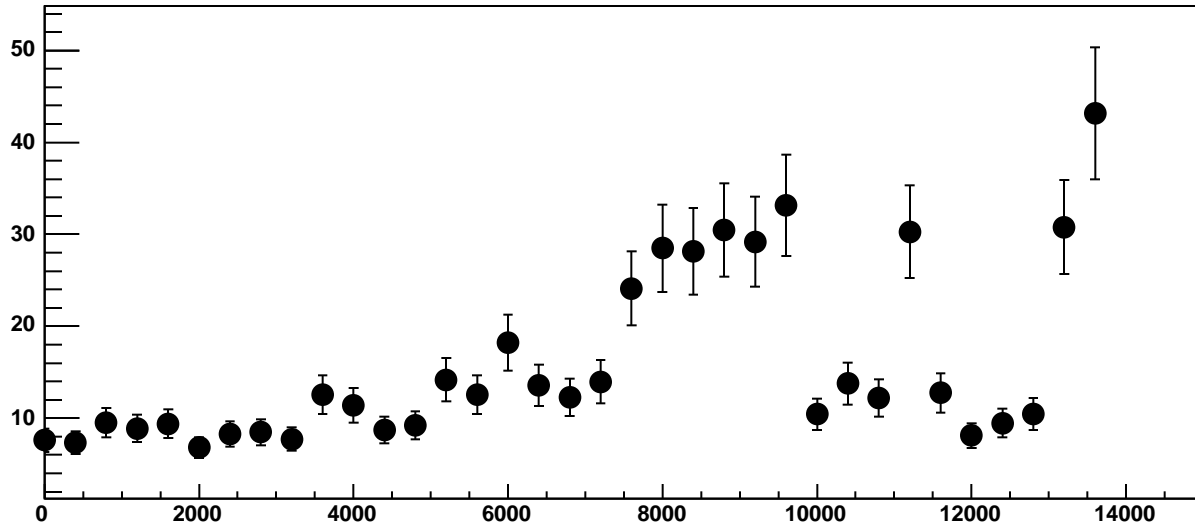


Chip 11, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

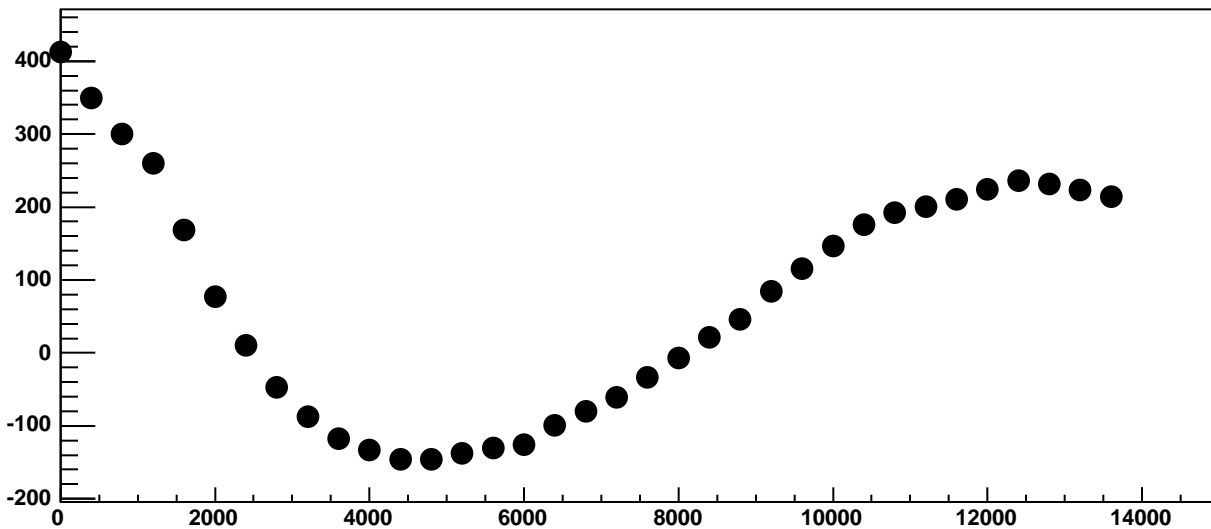


$\chi^2 / \text{ndf}$  6.131e+04 / 23  
p0 -948.2 ± 0.9983  
p1 0.1503 ± 0.0001894

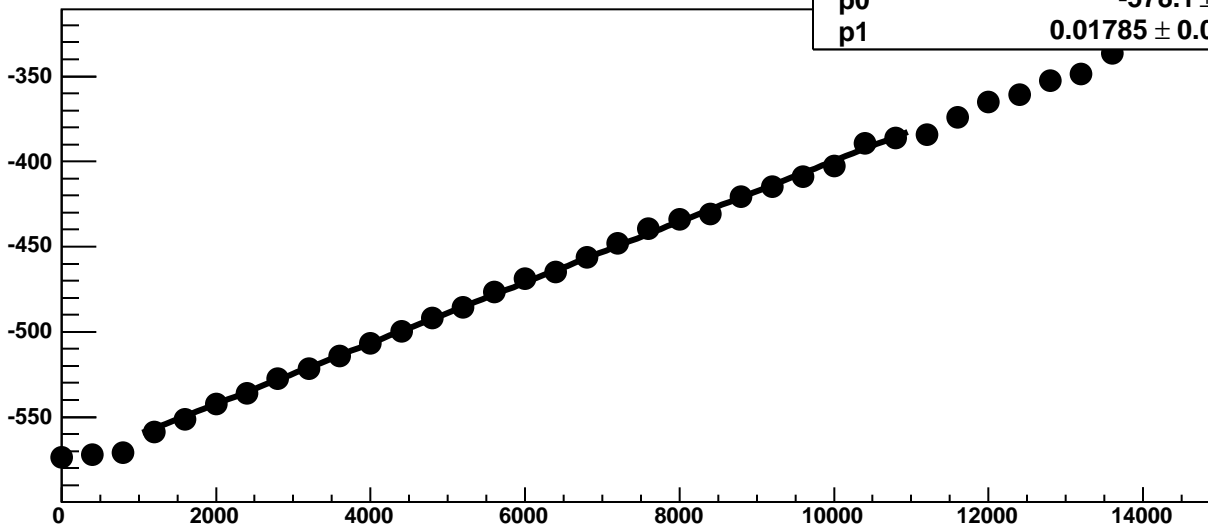
Chip 11, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

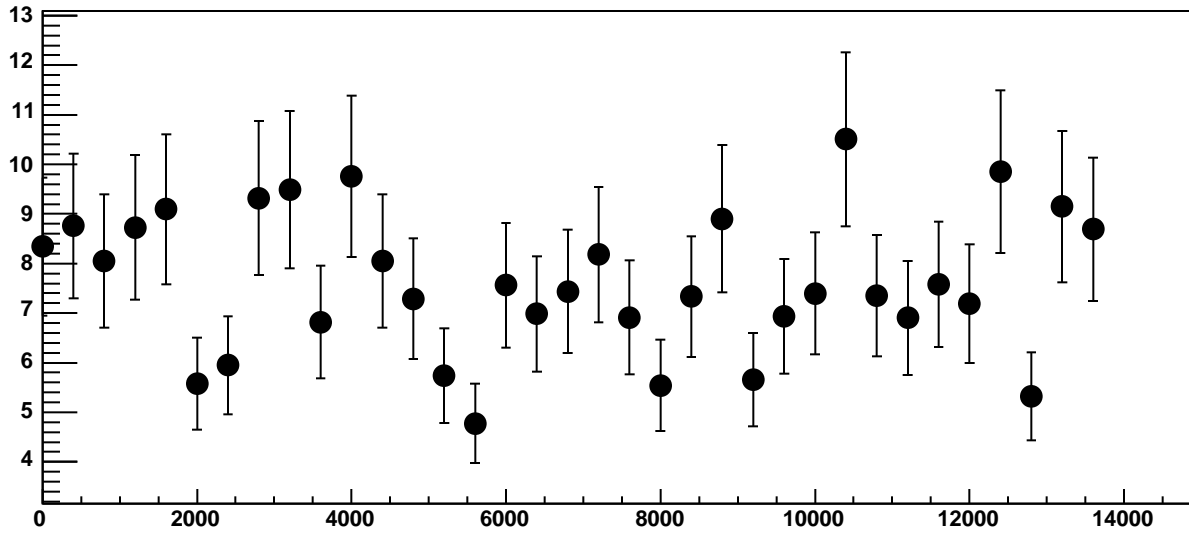


Chip 11, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

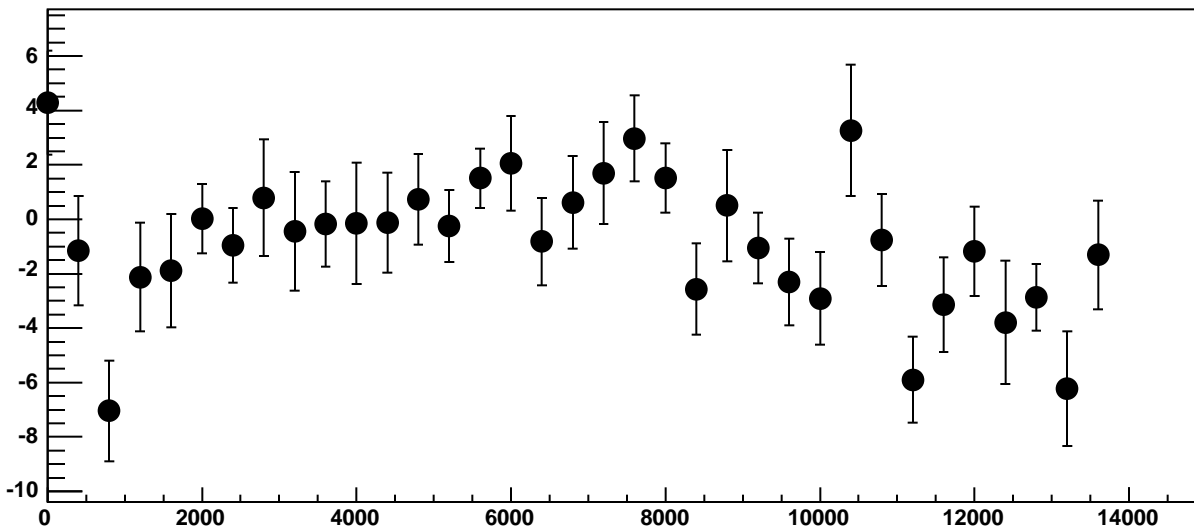


$\chi^2 / \text{ndf}$  22.44 / 23  
p0  $-578.1 \pm 0.7818$   
p1  $0.01785 \pm 0.0001178$

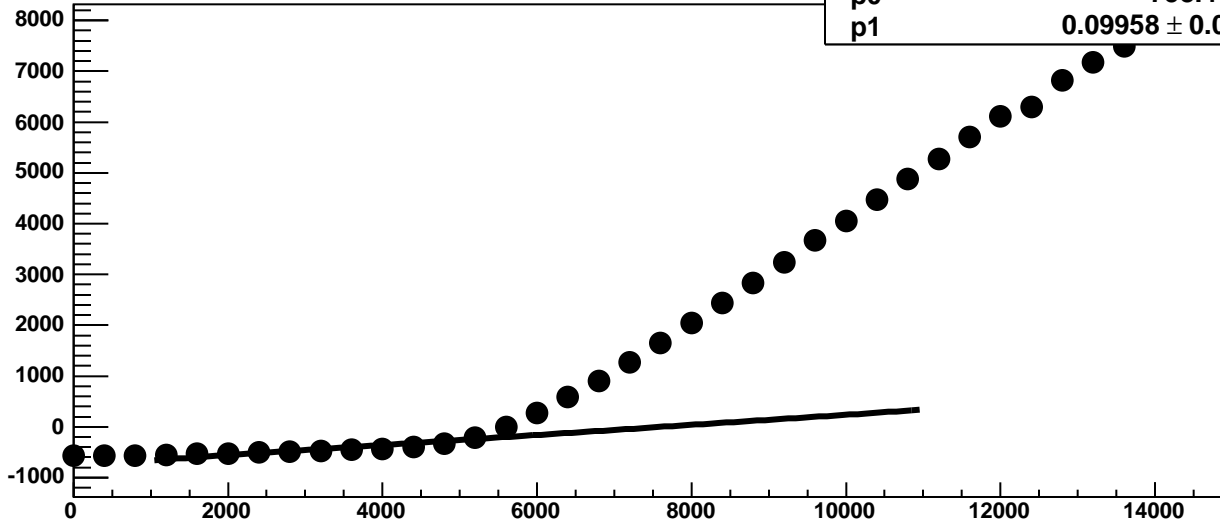
Chip 11, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC



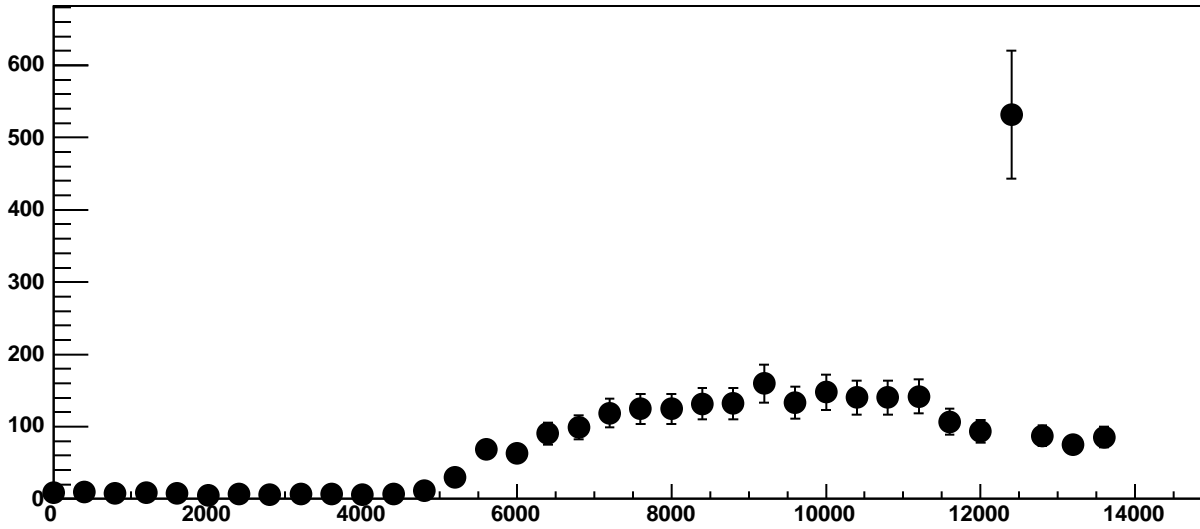
Chip 11, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC



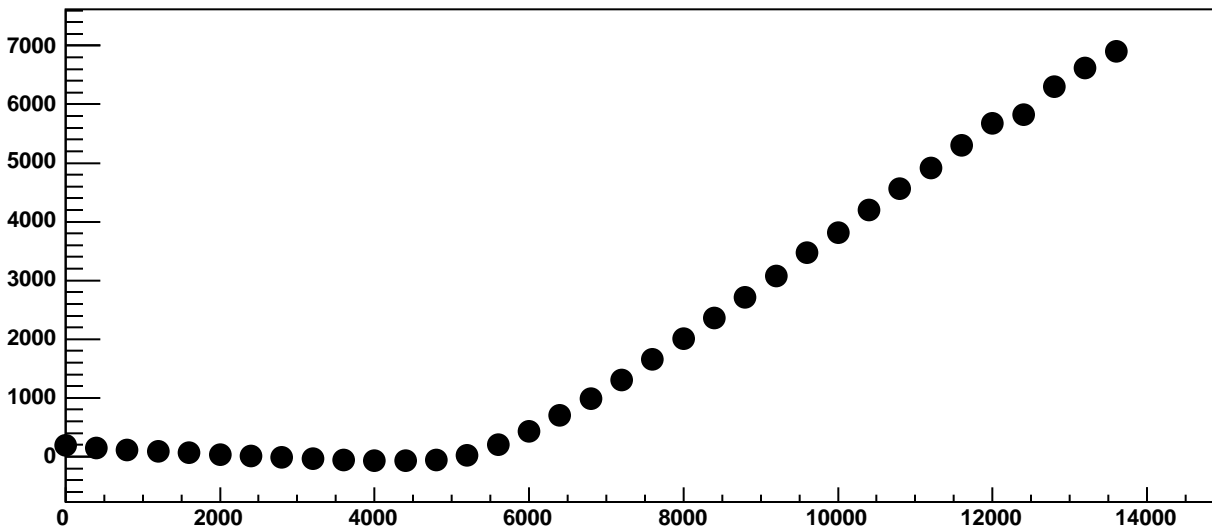
Chip 11, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC



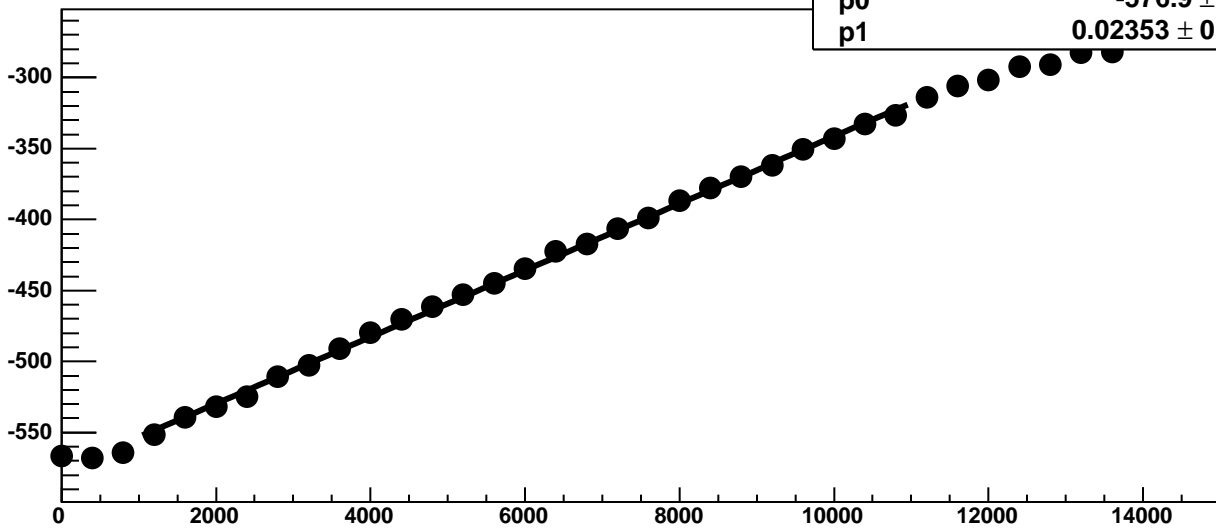
Chip 11, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC



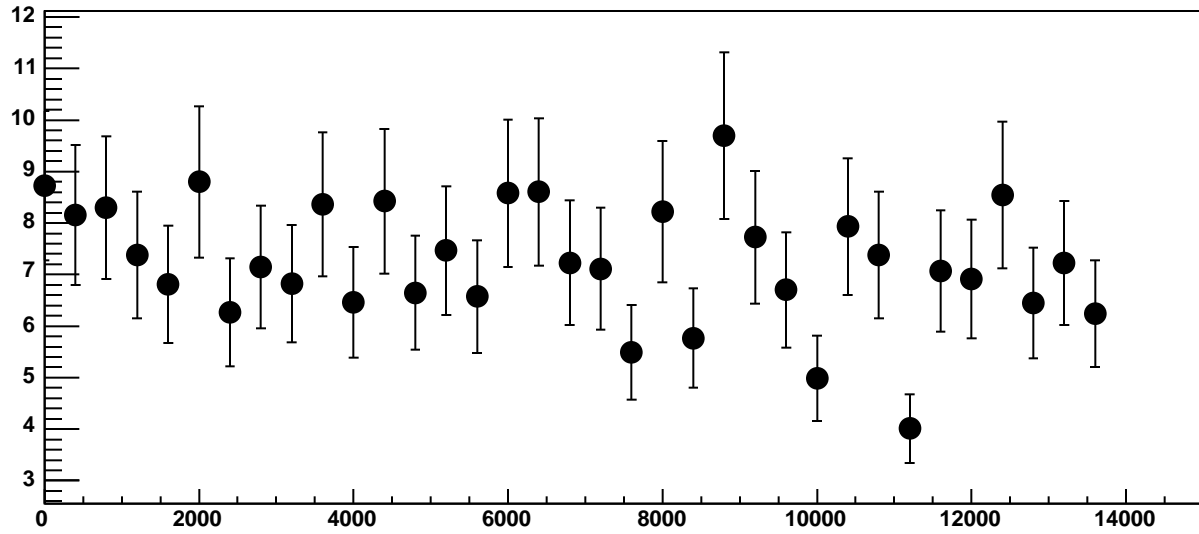
Chip 11, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC



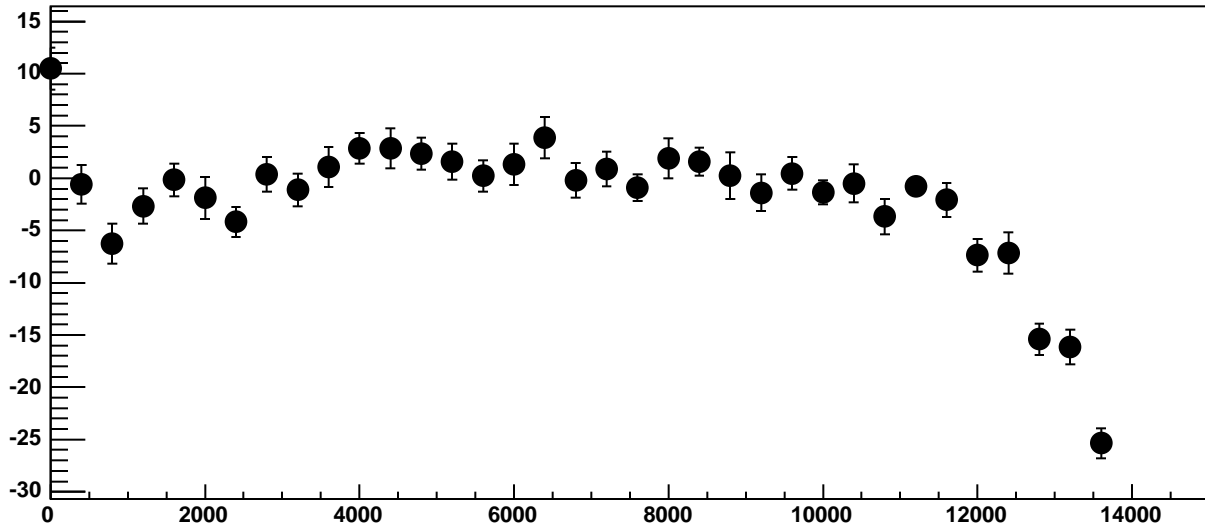
Chip 11, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



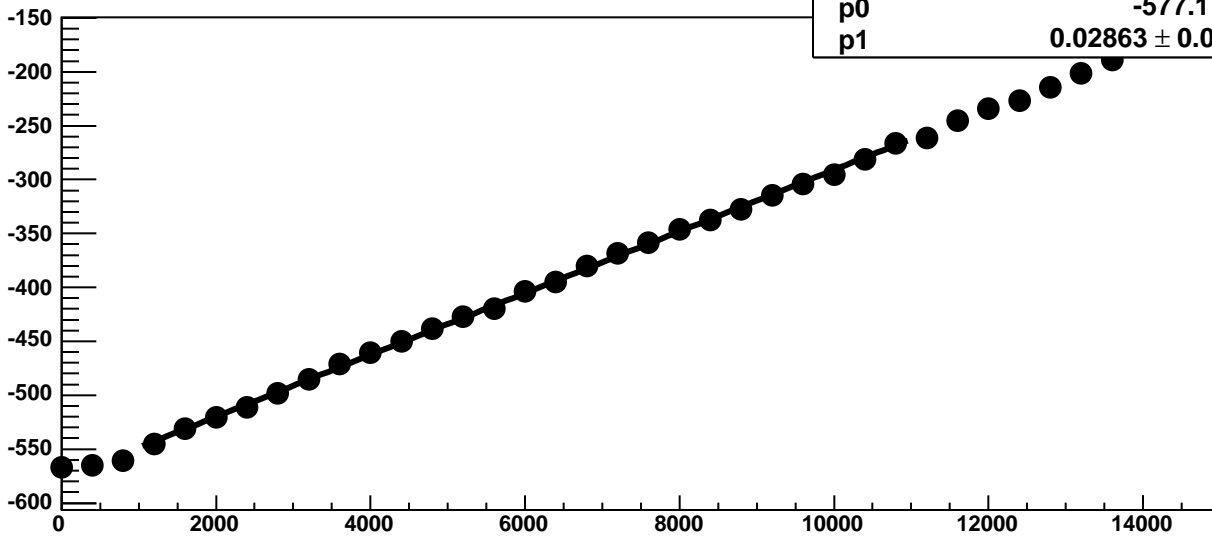
Chip 11, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

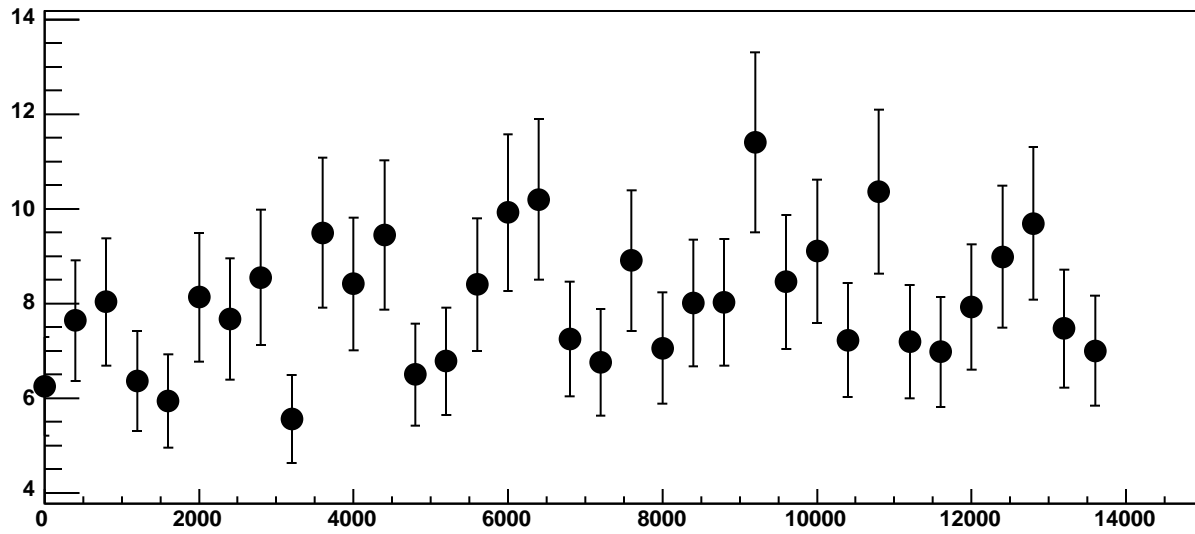


Chip 11, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



$\chi^2 / \text{ndf}$  29.76 / 23  
p0  $-577.1 \pm 0.769$   
p1  $0.02863 \pm 0.0001227$

Chip 11, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 11, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

