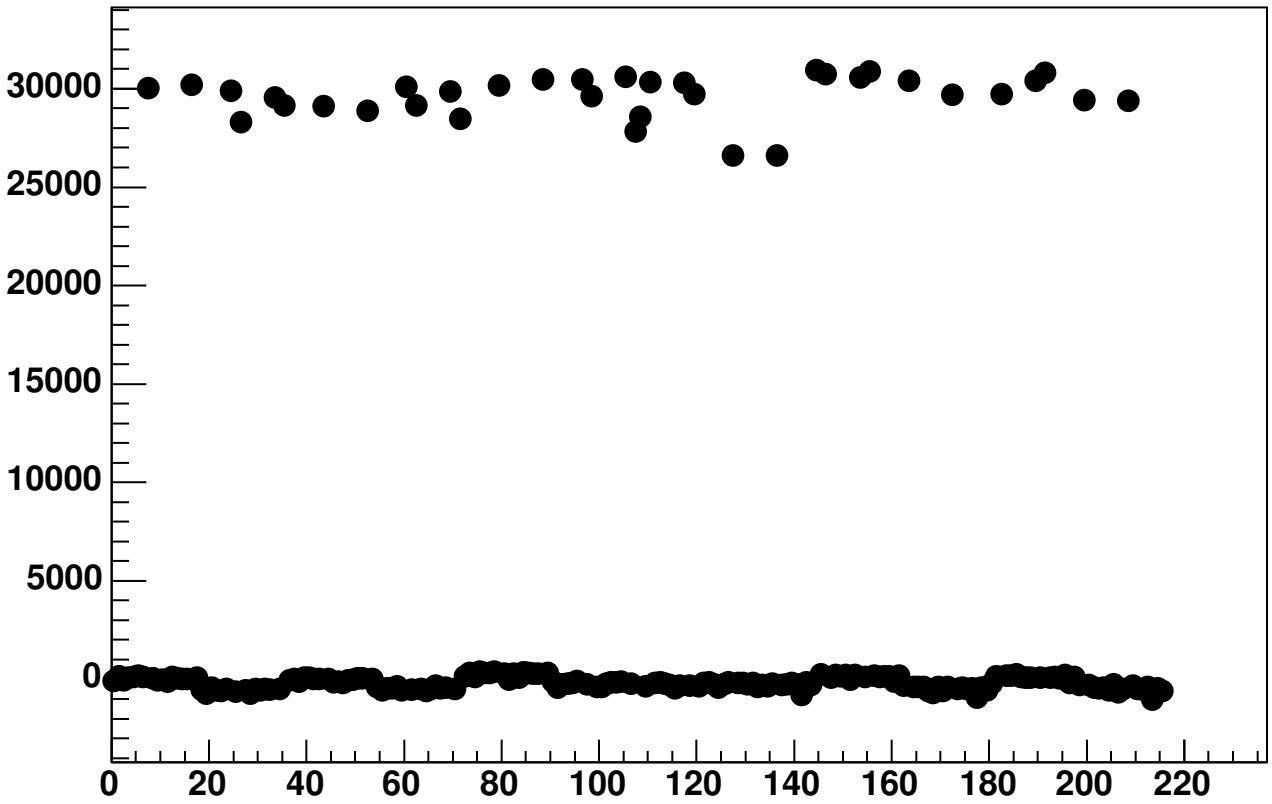
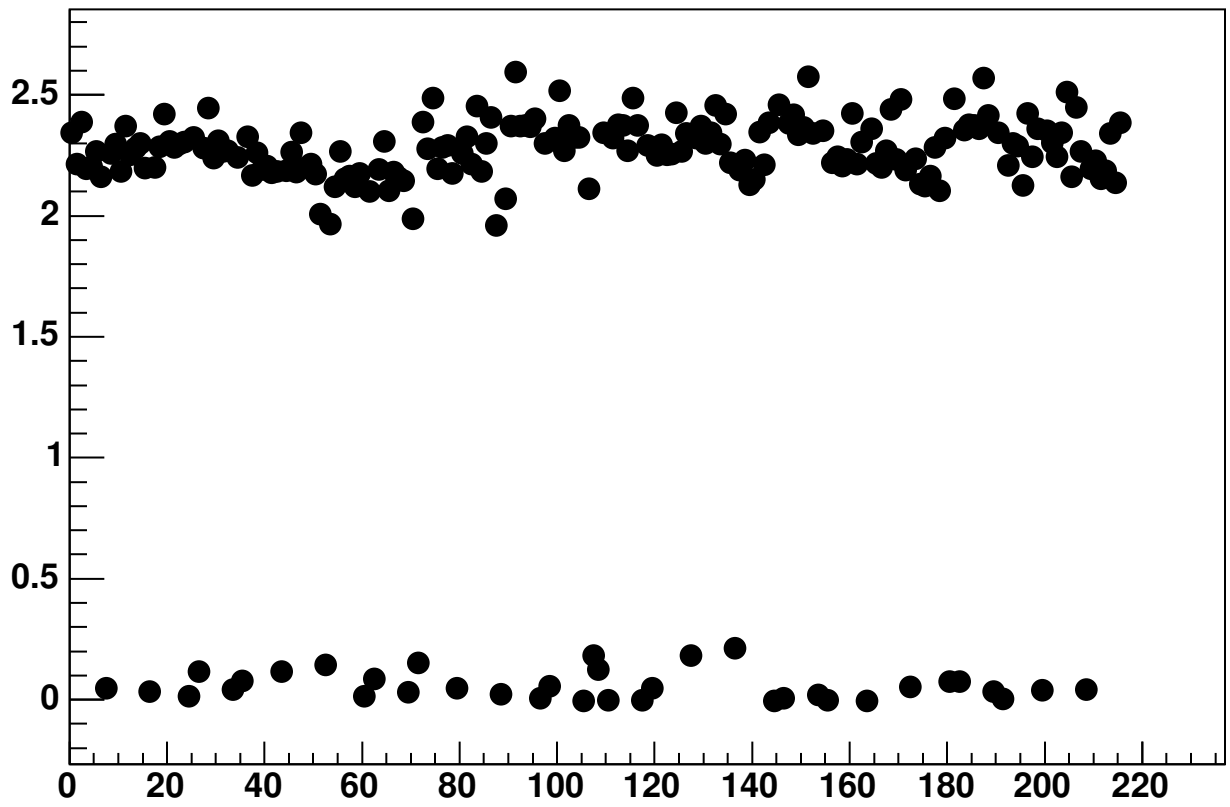
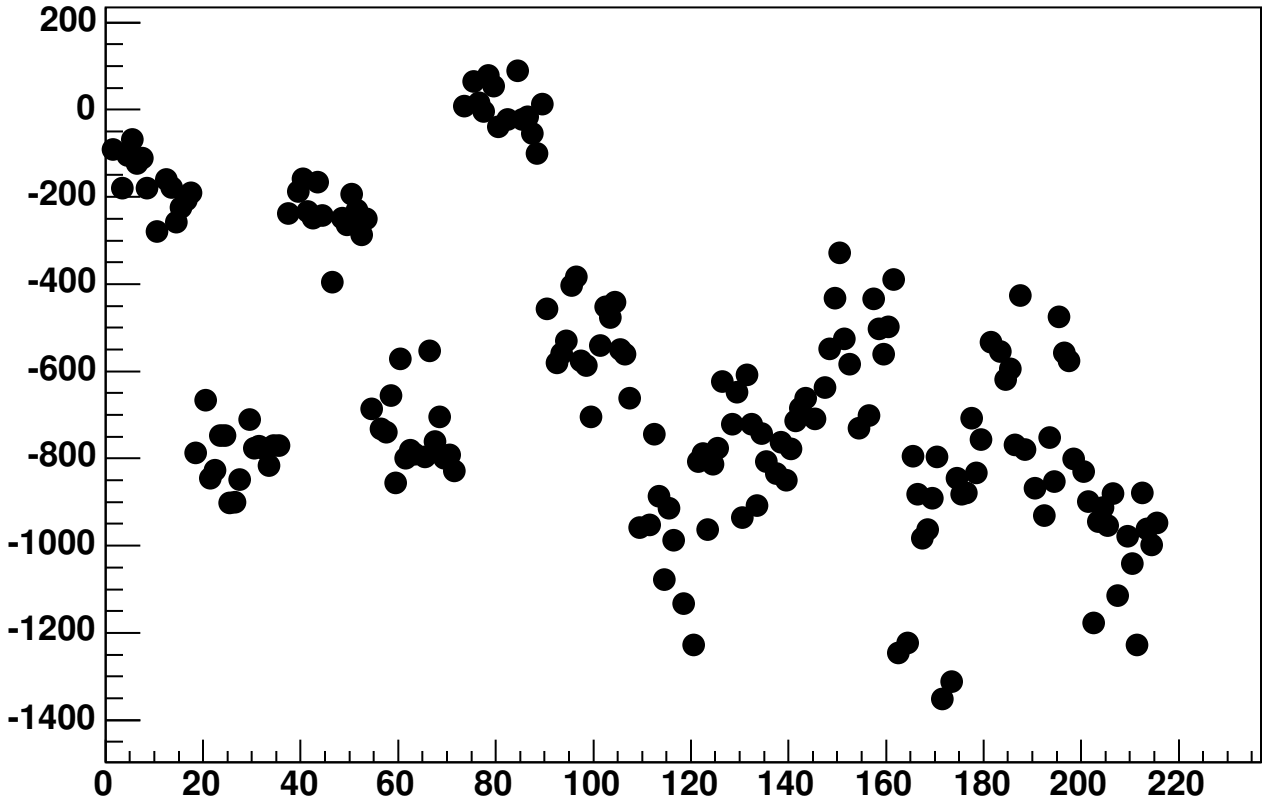
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



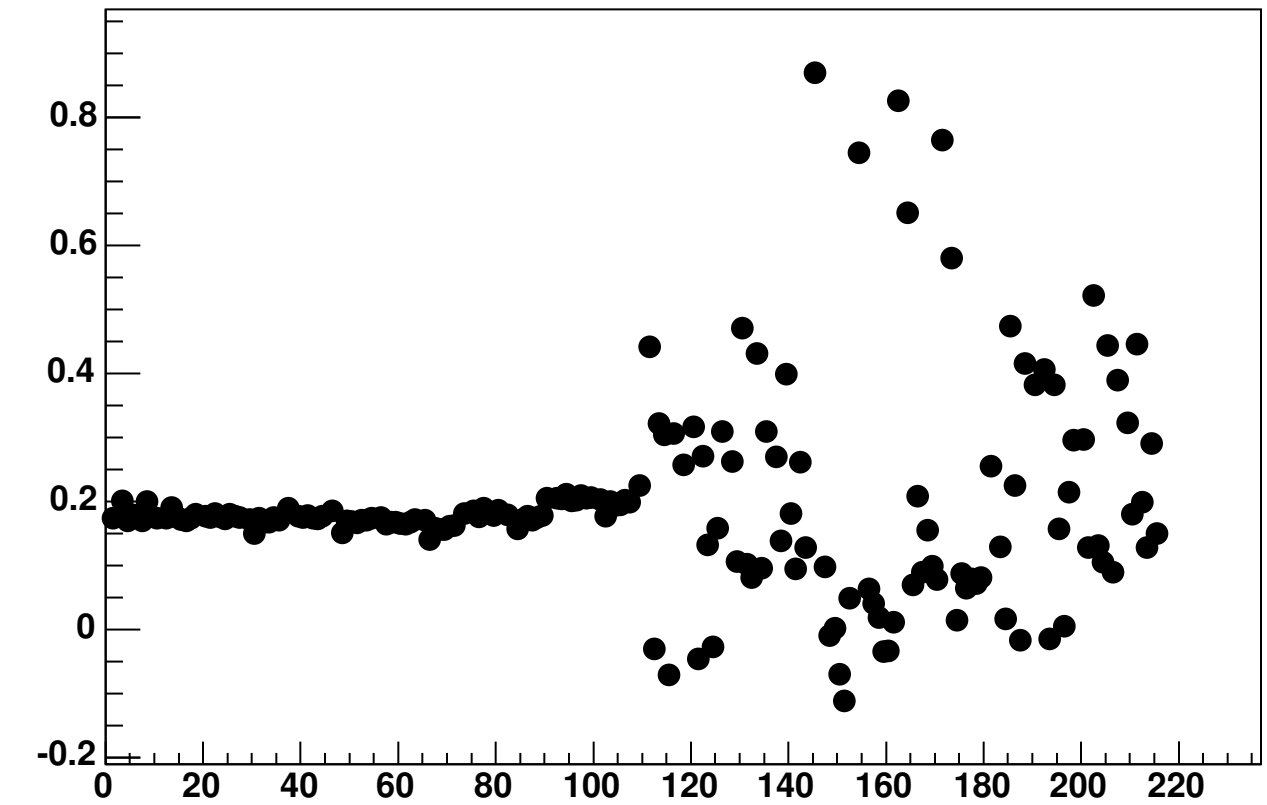
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



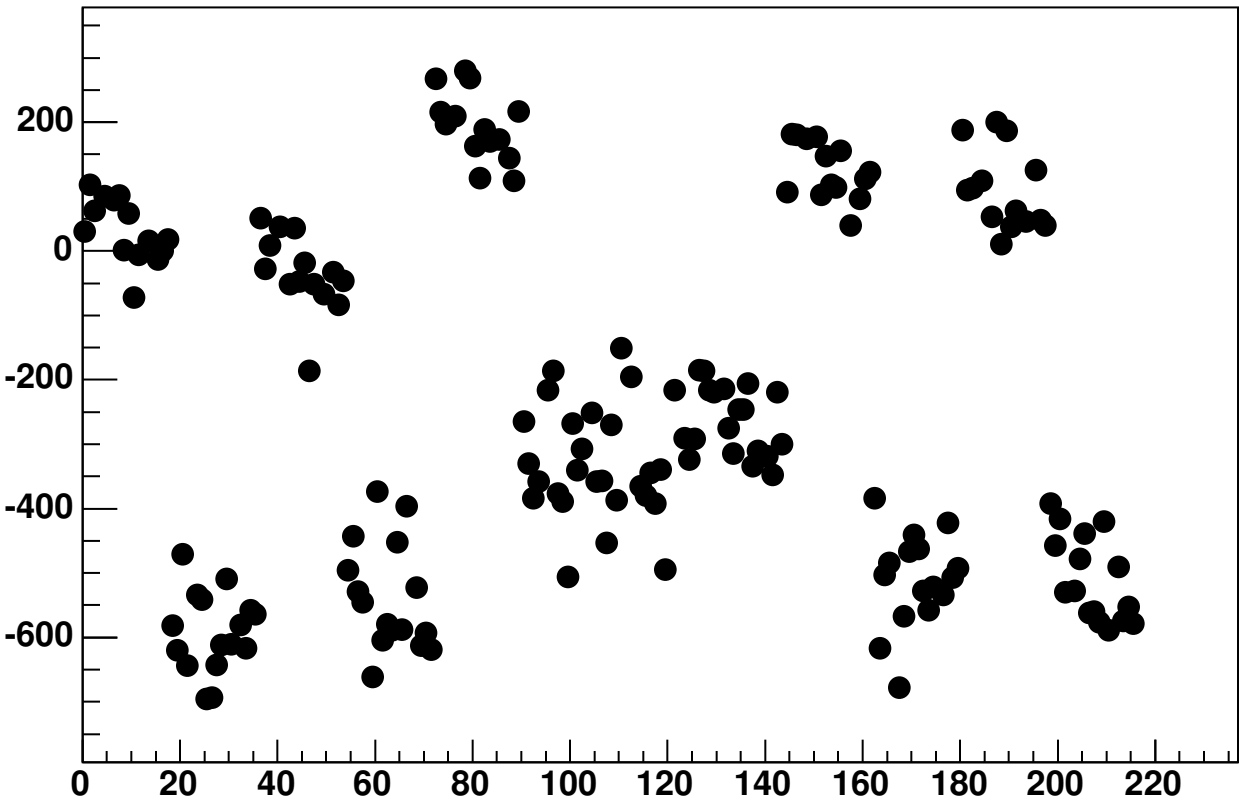
Disabled 0, Hold=35, Fit Intercept vs 18*Chip+Chan



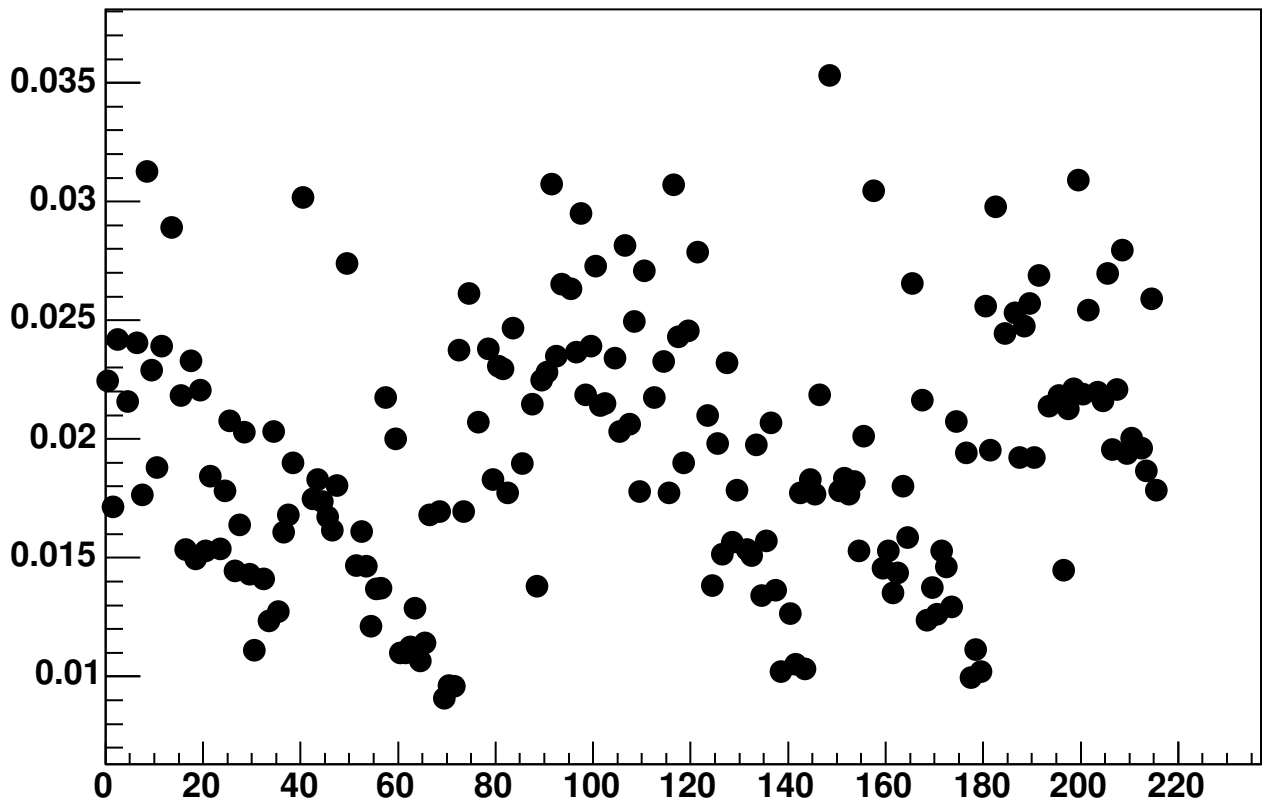
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



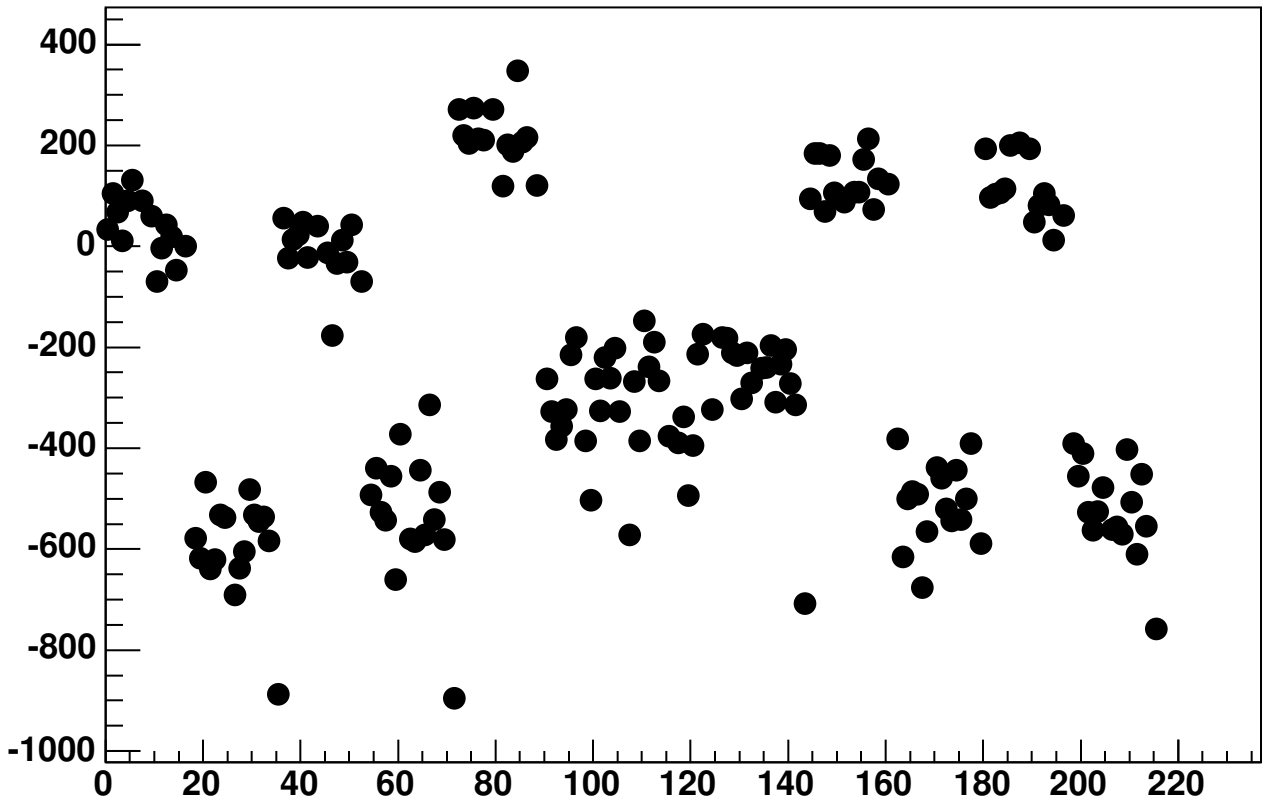
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



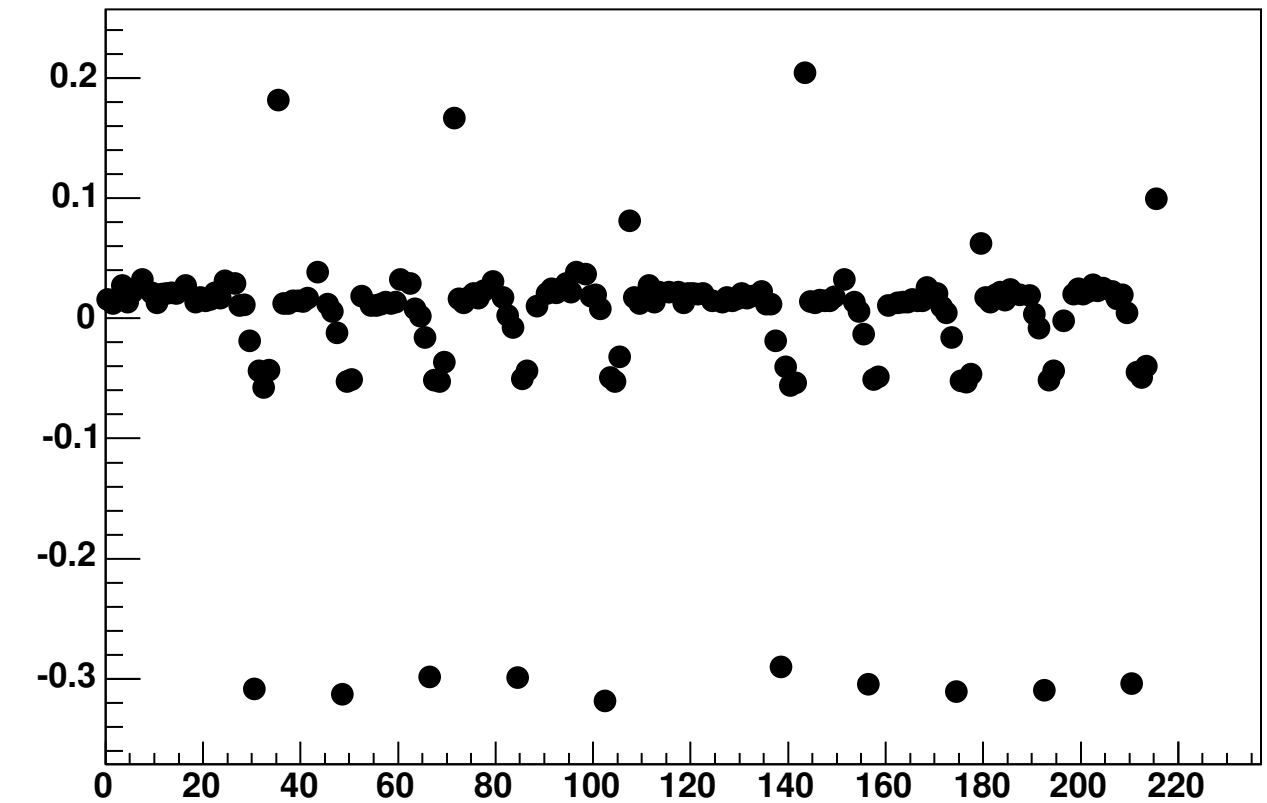
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



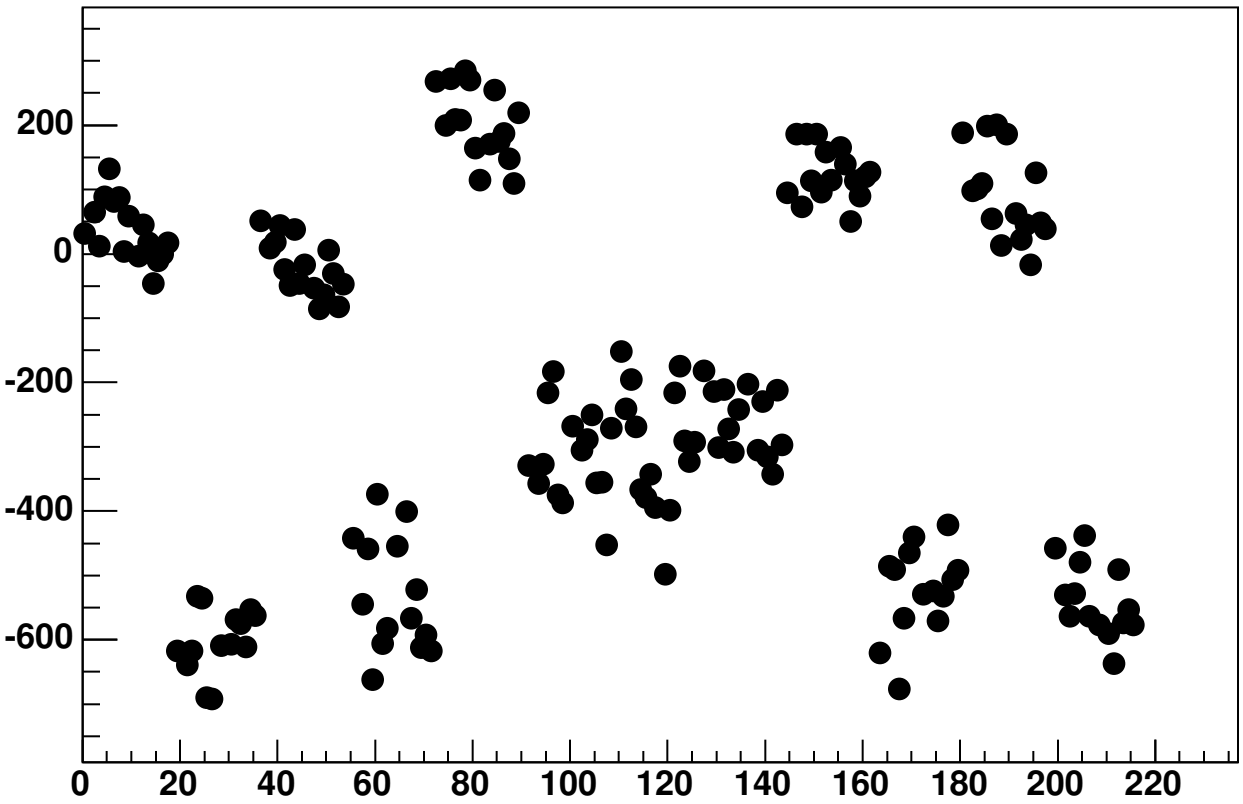
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



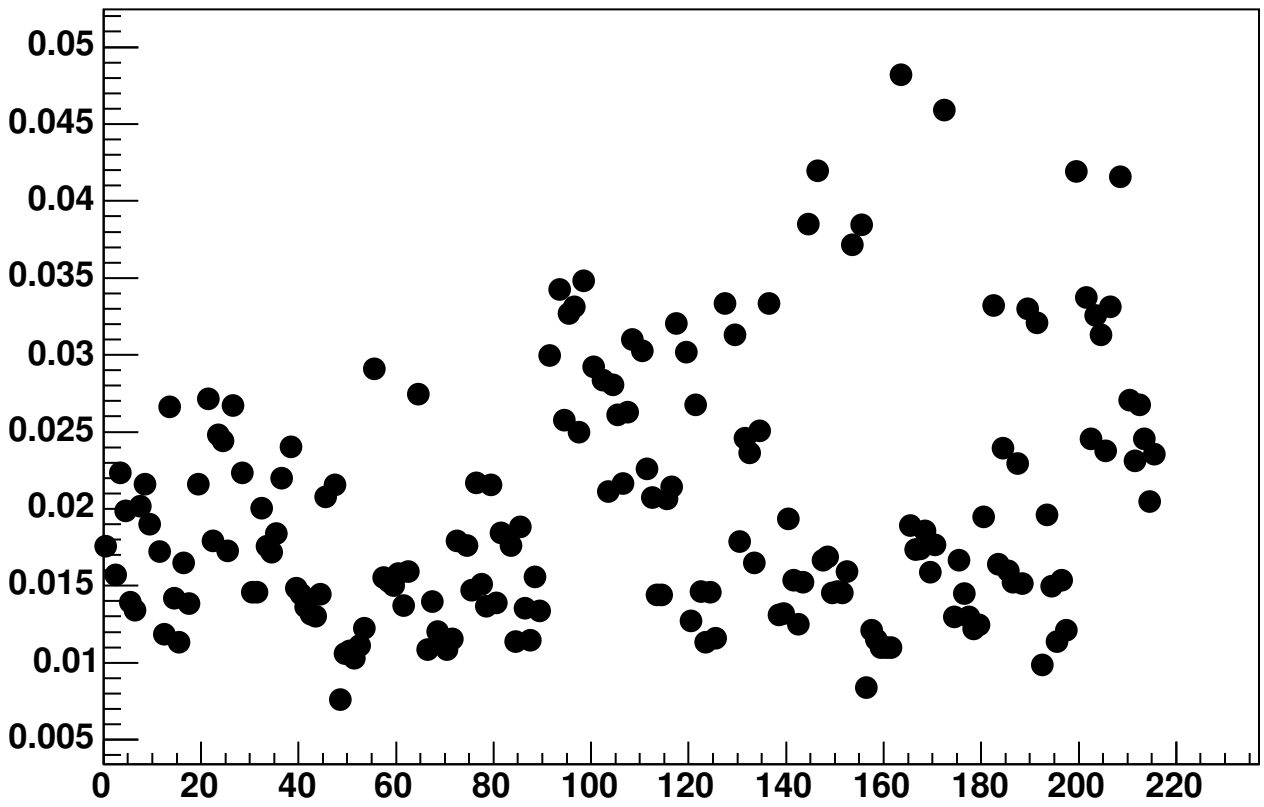
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



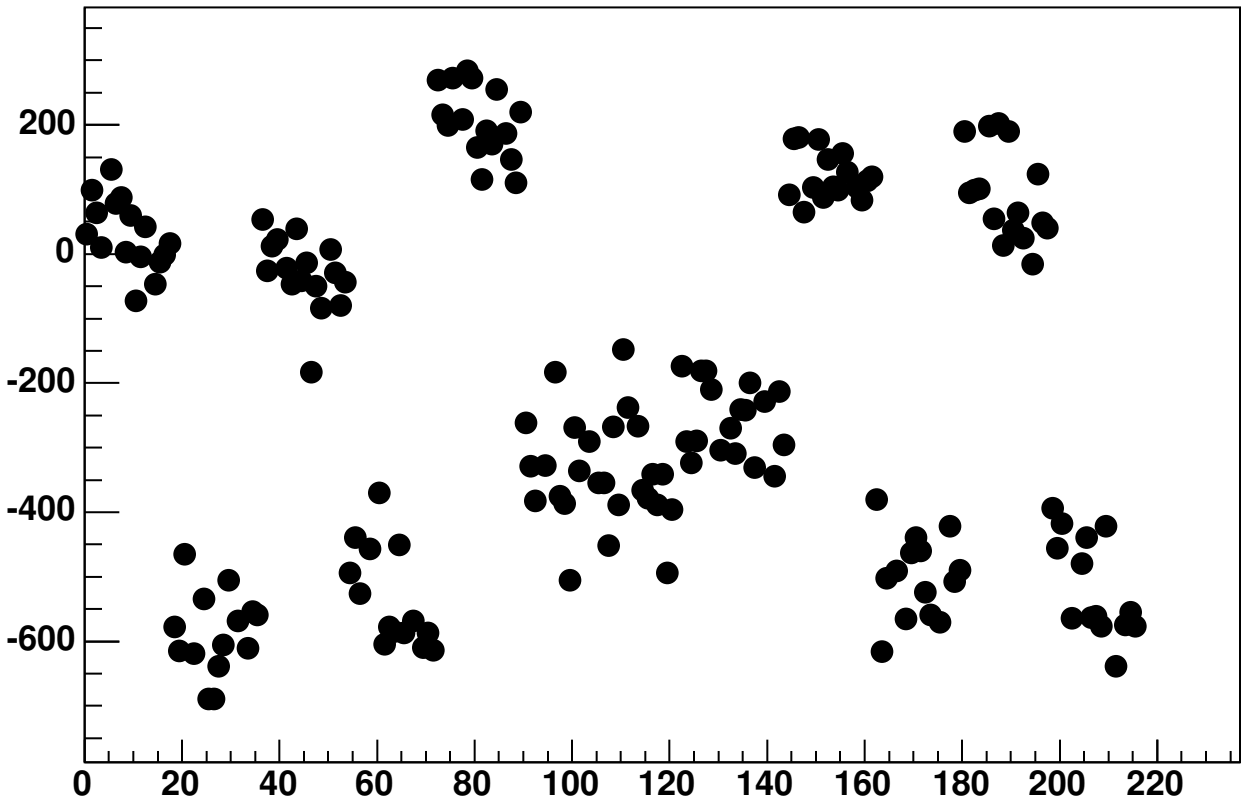
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



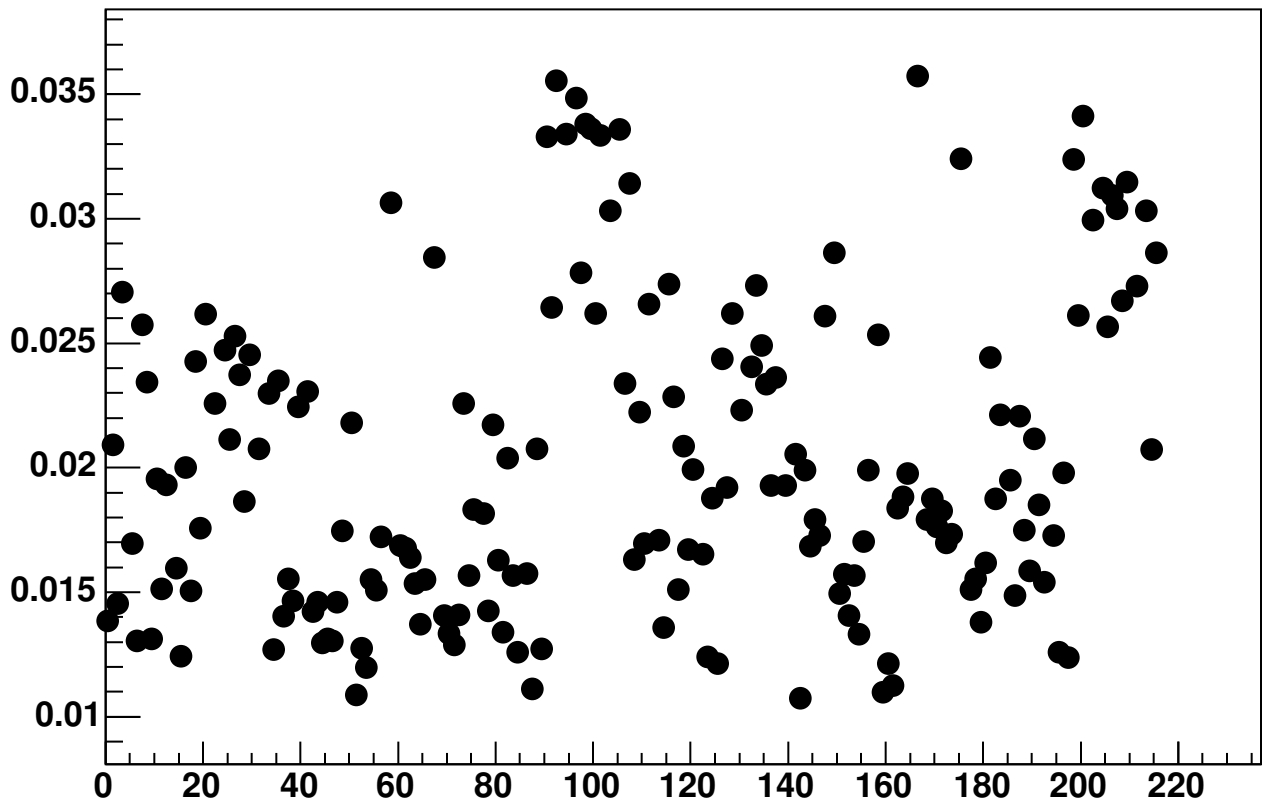
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



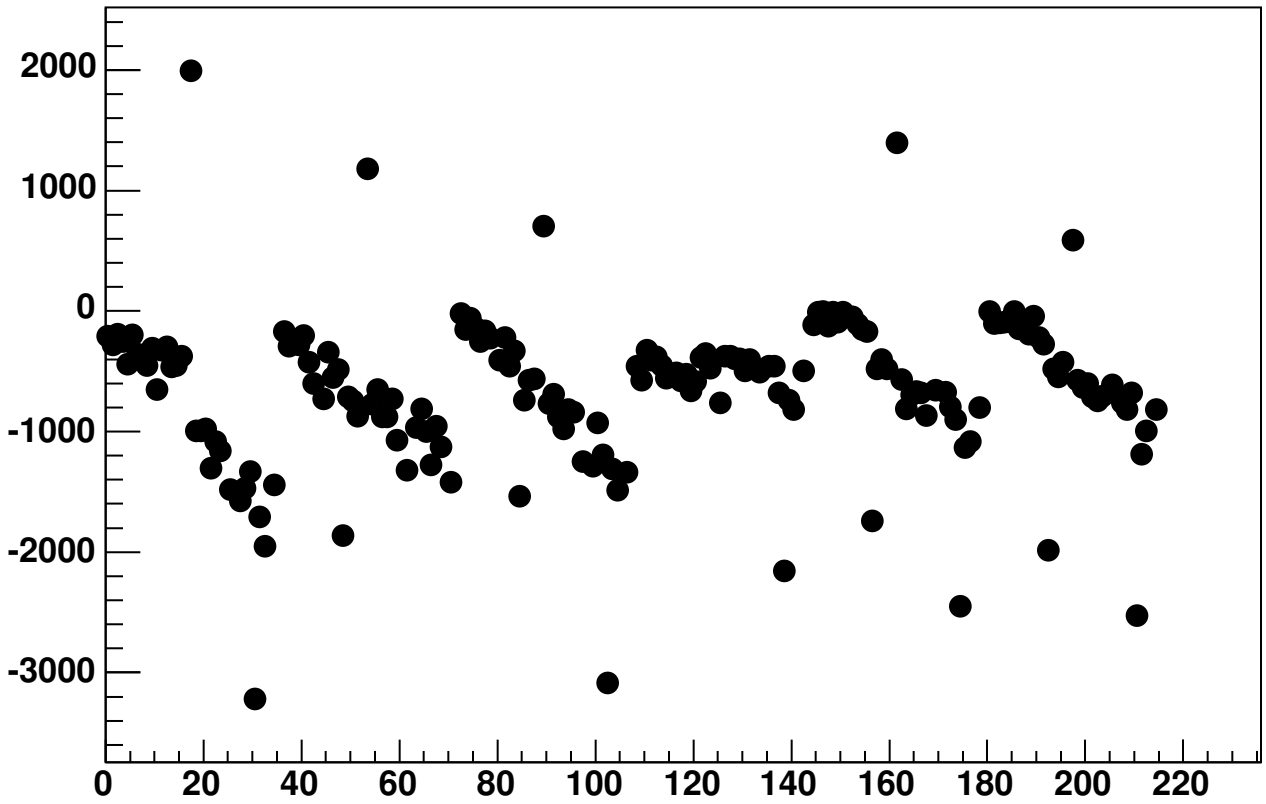
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



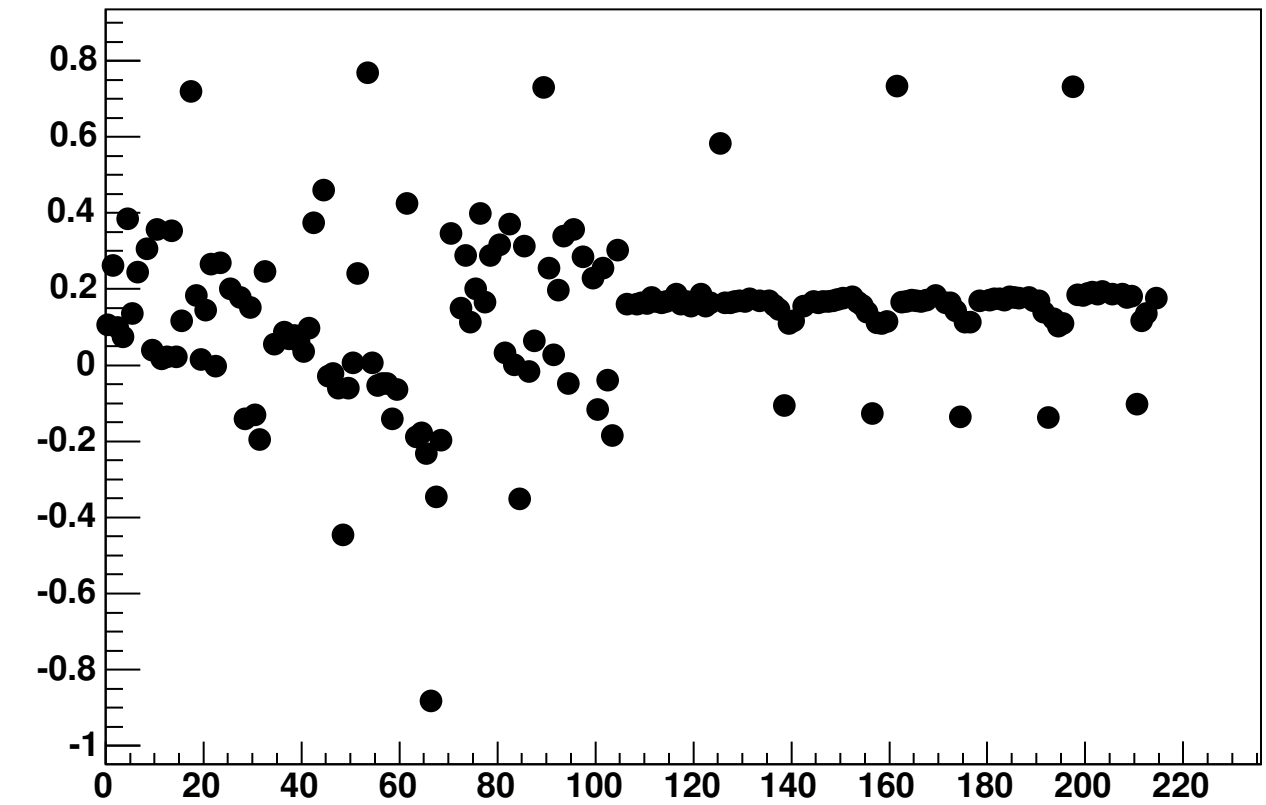
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



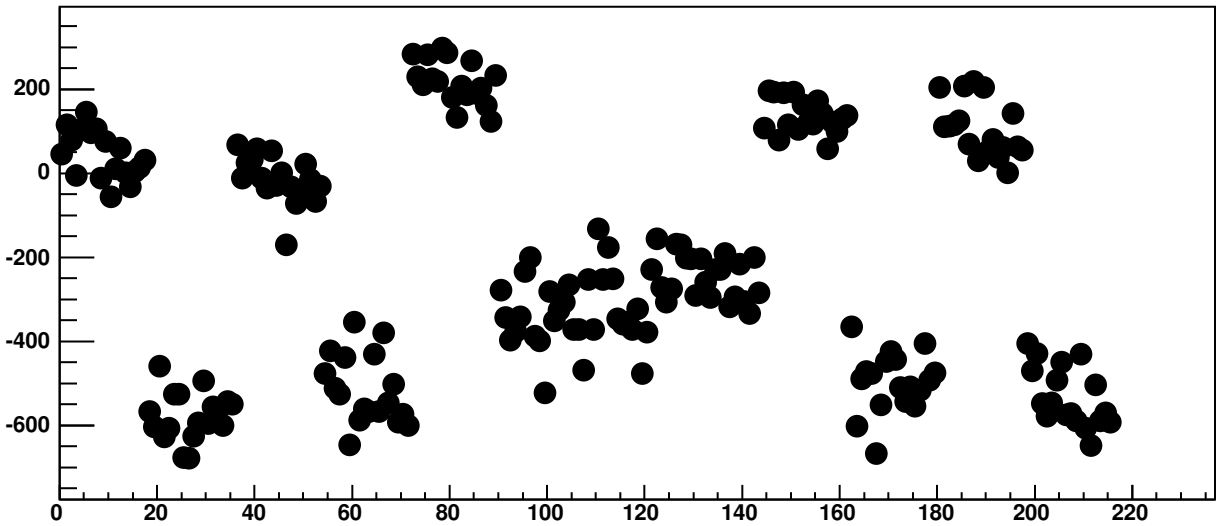
Disabled 5, Hold=35, Fit Intercept vs 18*Chip+Chan



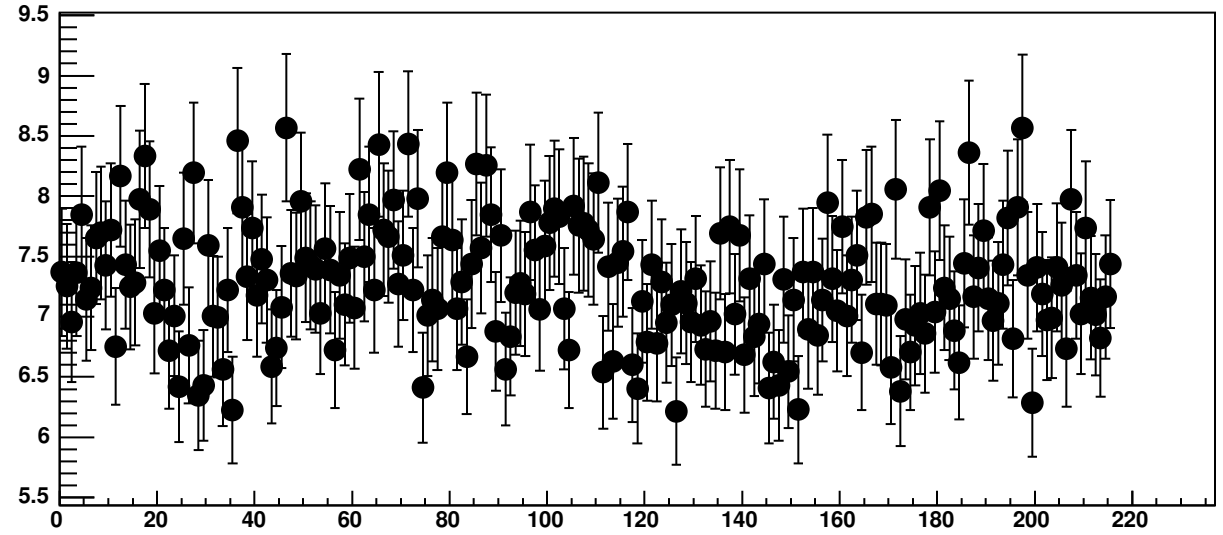
Disabled 5, Hold=35, Fit Slope vs 18*Chip+Chan



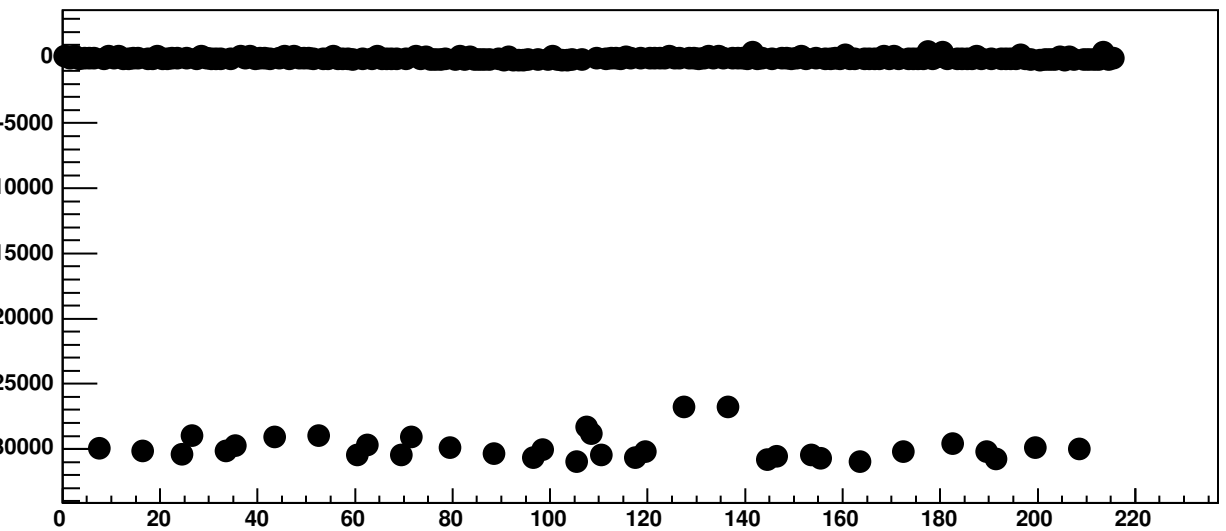
Enables off, ADC Mean vs 18*Chip+Chan



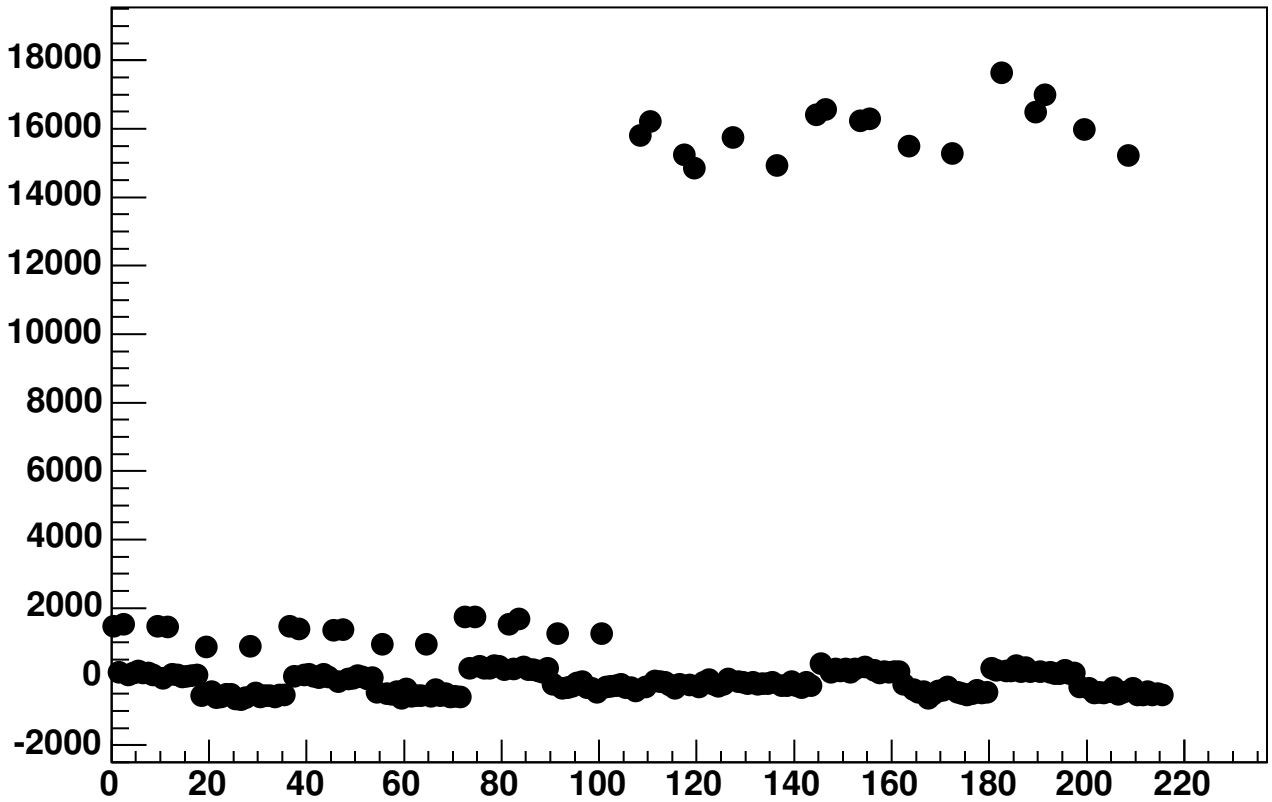
Enables off, ADC Noise vs 18*Chip+Chan



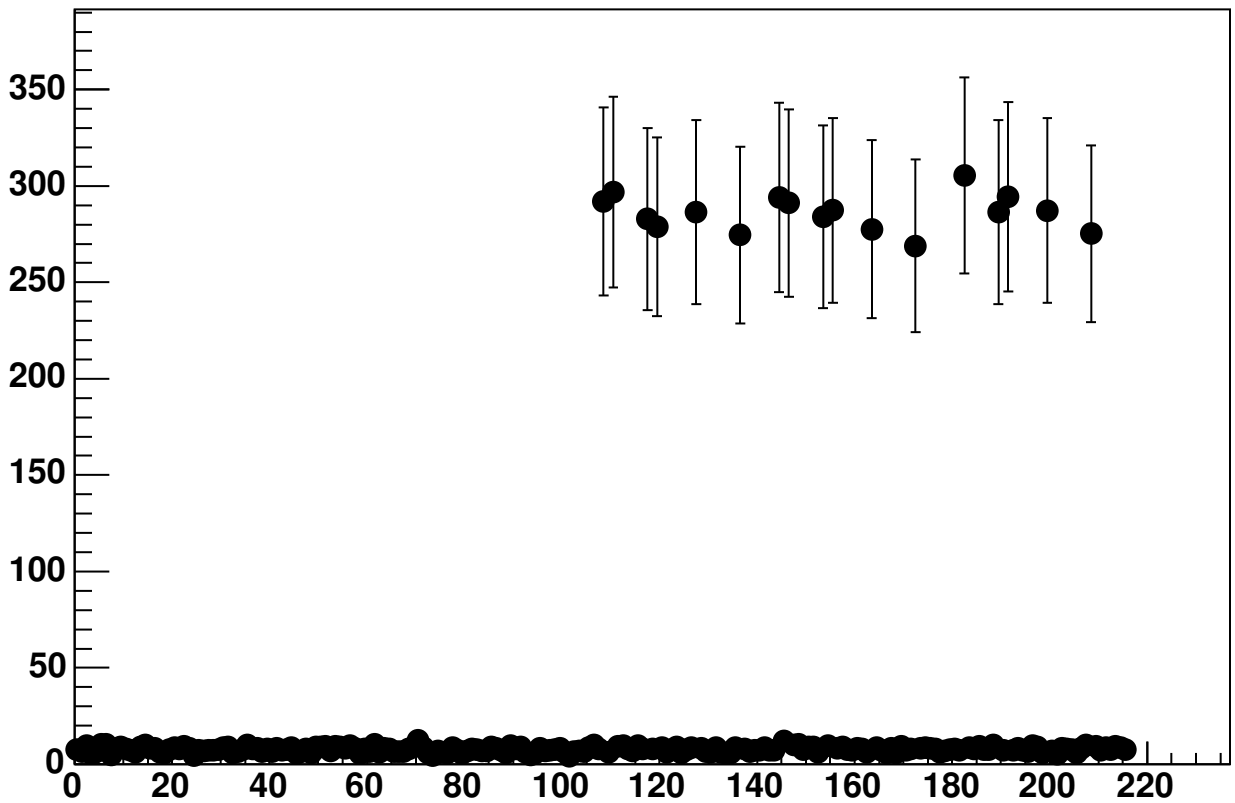
Enables off, ADC Residuals vs 18*Chip+Chan



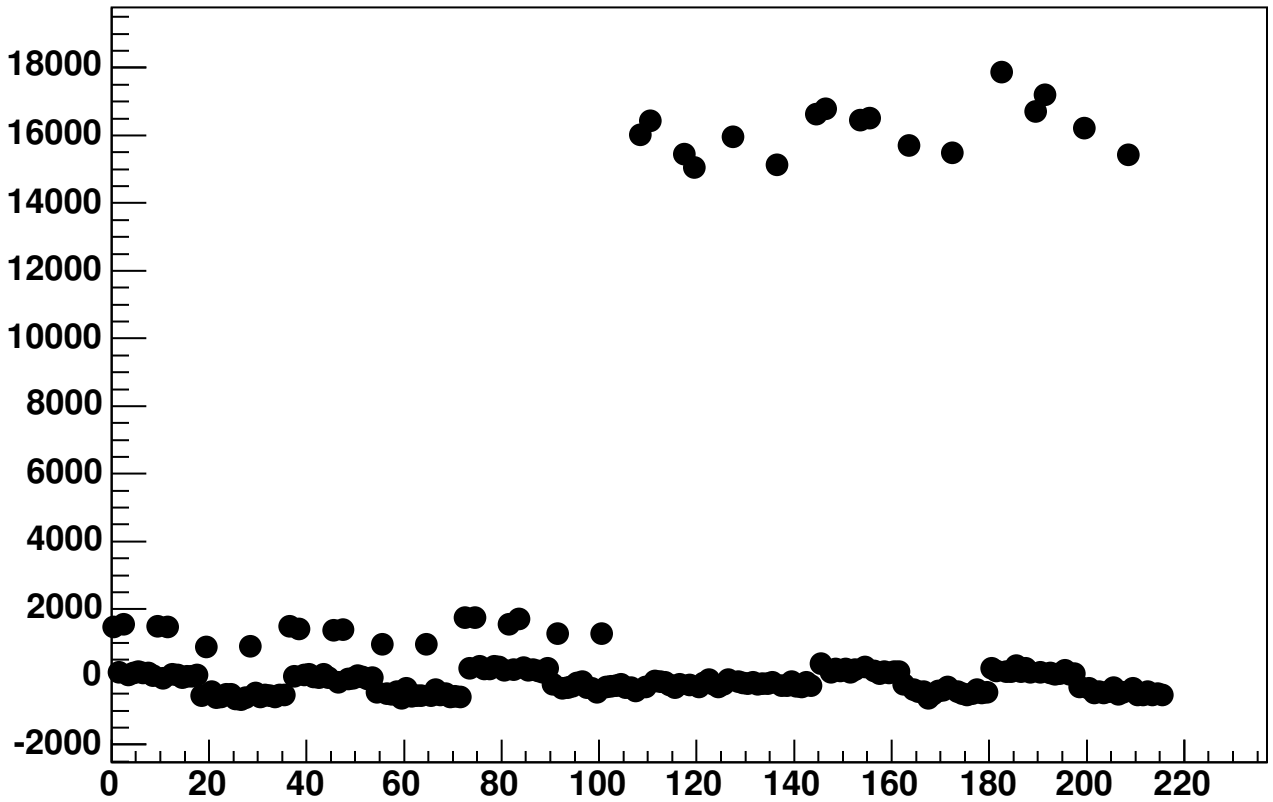
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



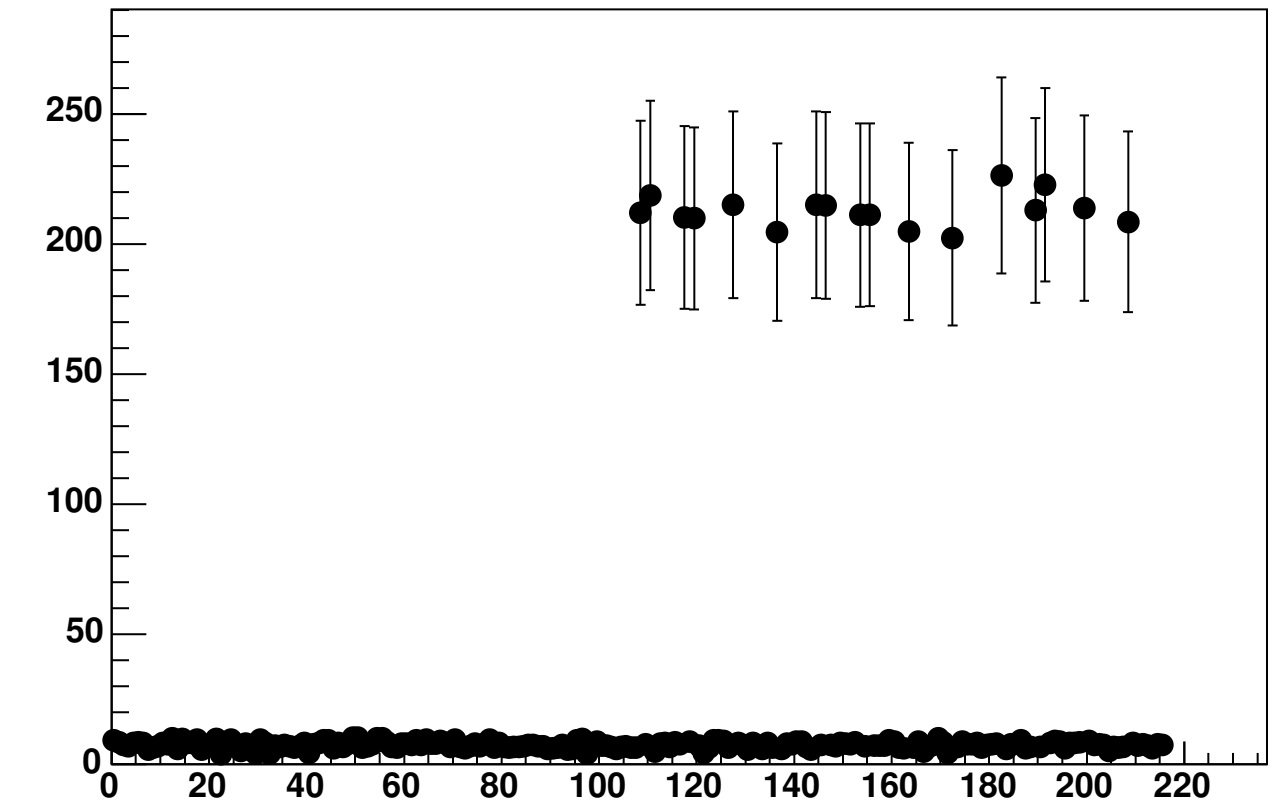
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



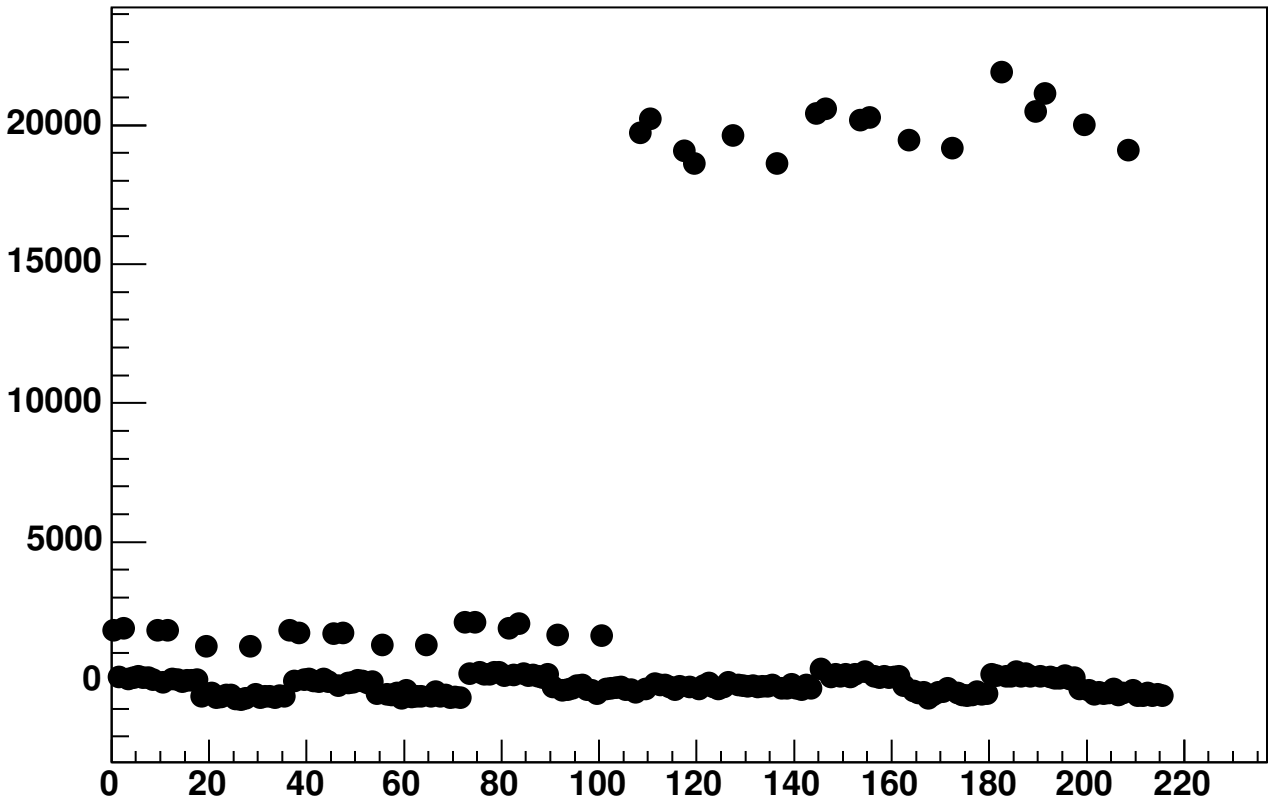
Enable 0, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



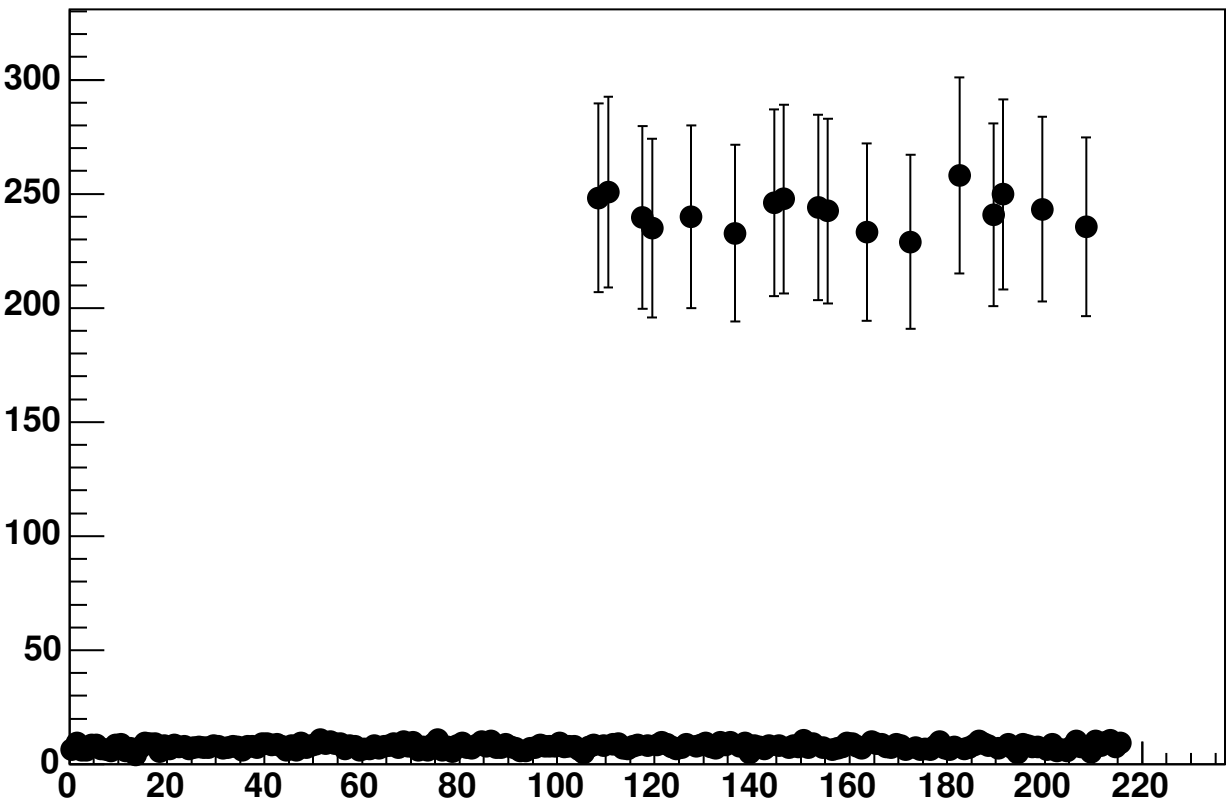
Enable 0, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



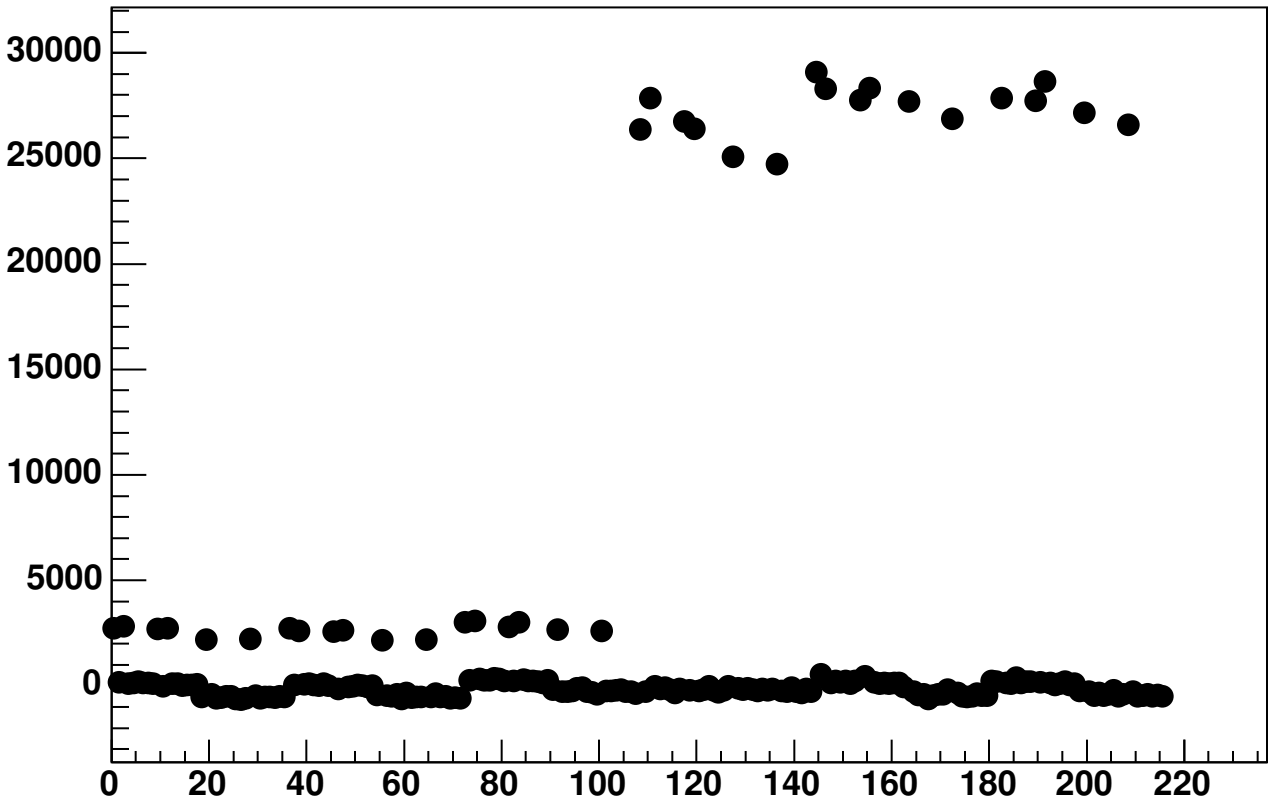
Enable 0, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



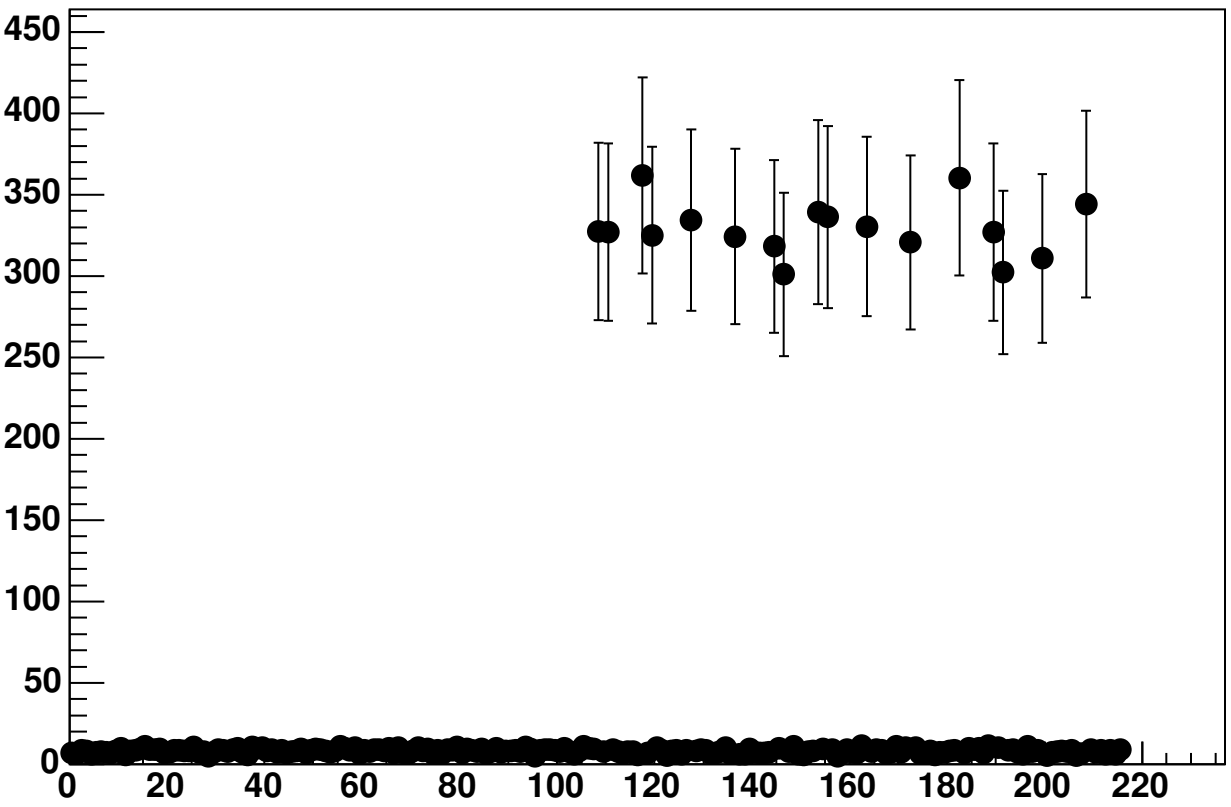
Enable 0, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



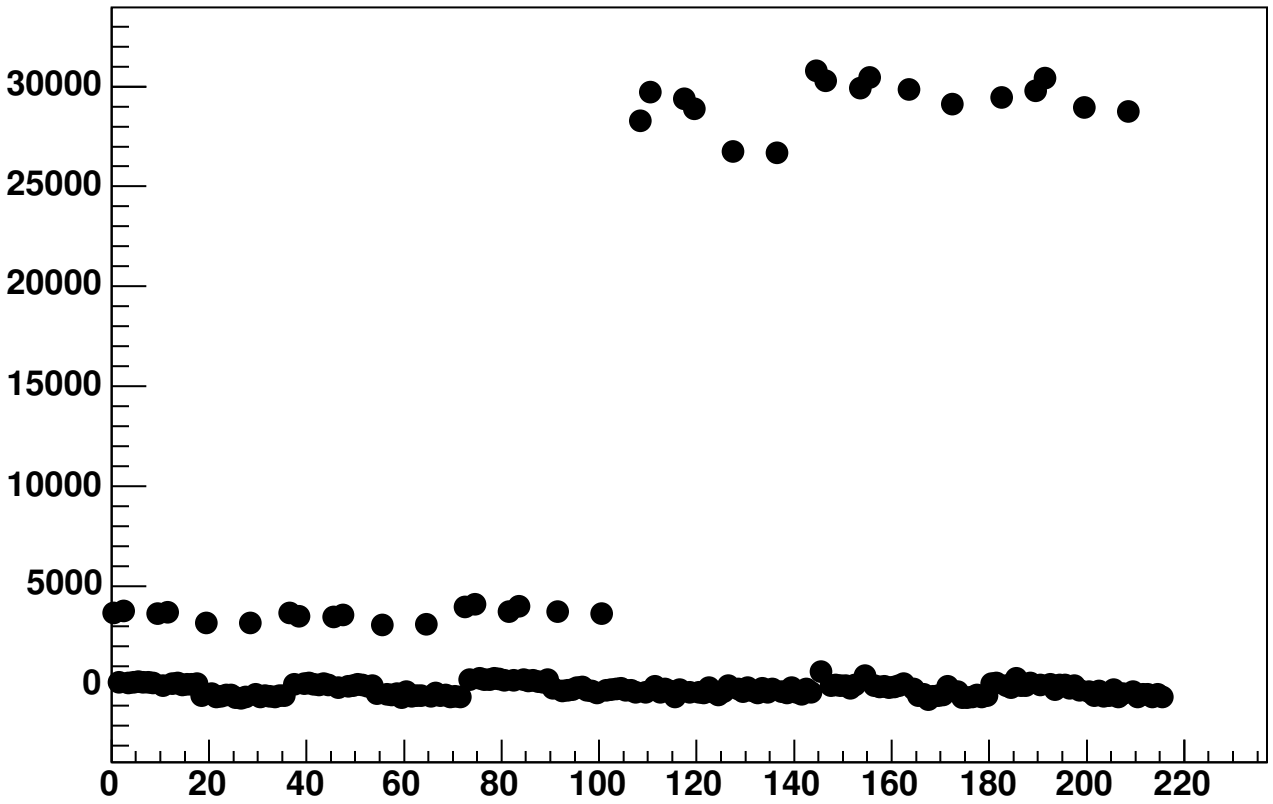
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



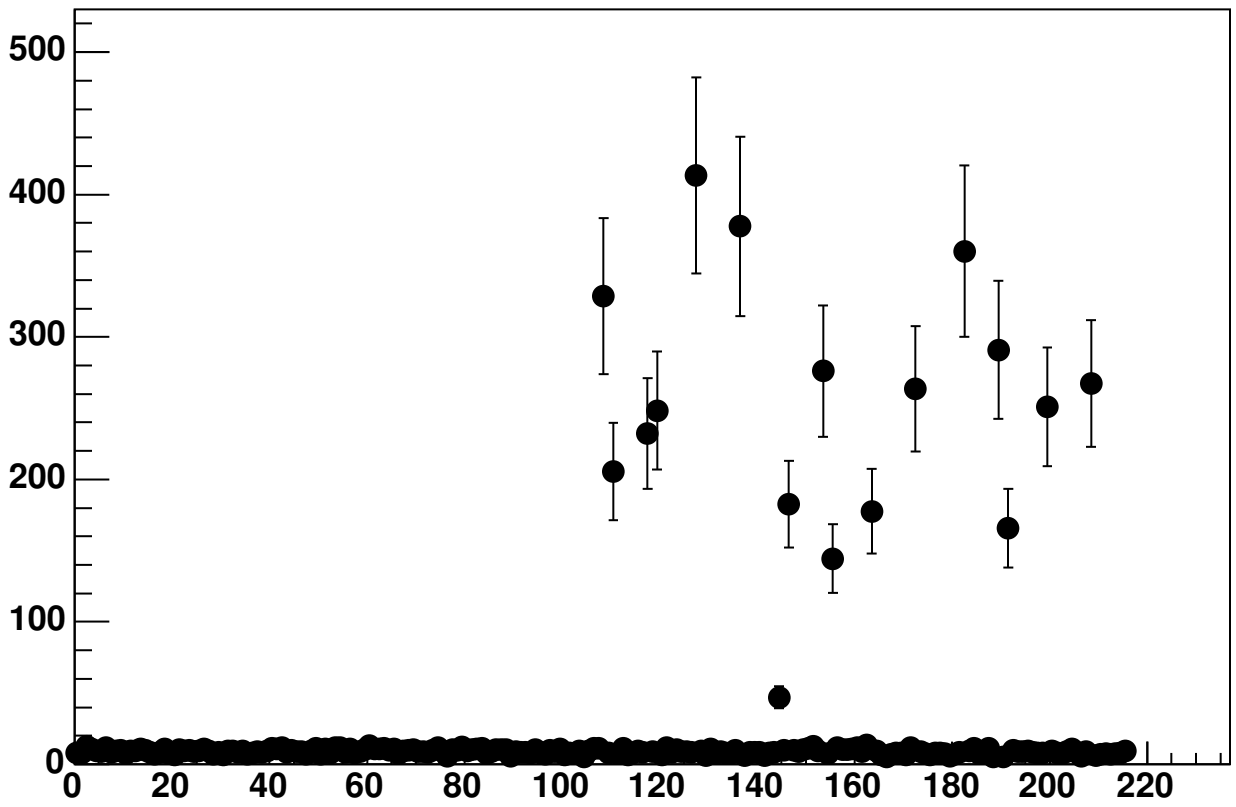
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



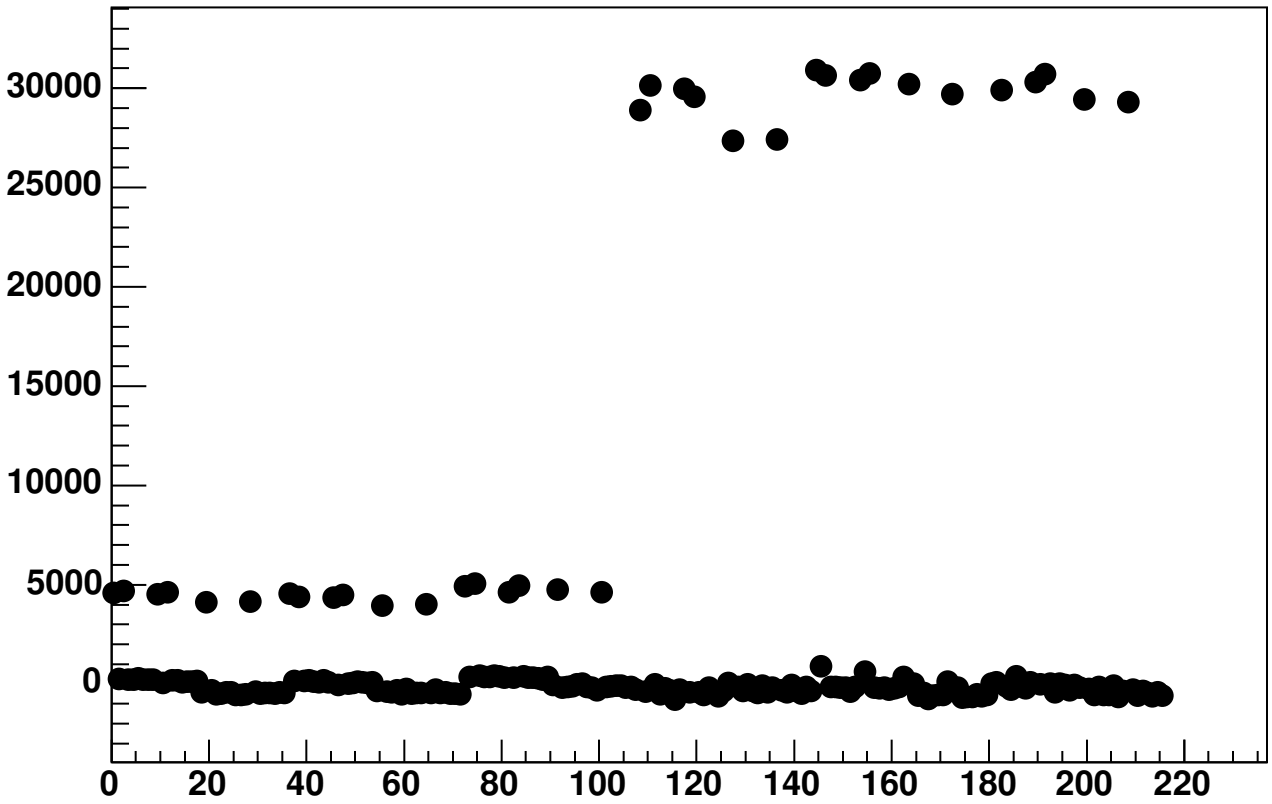
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



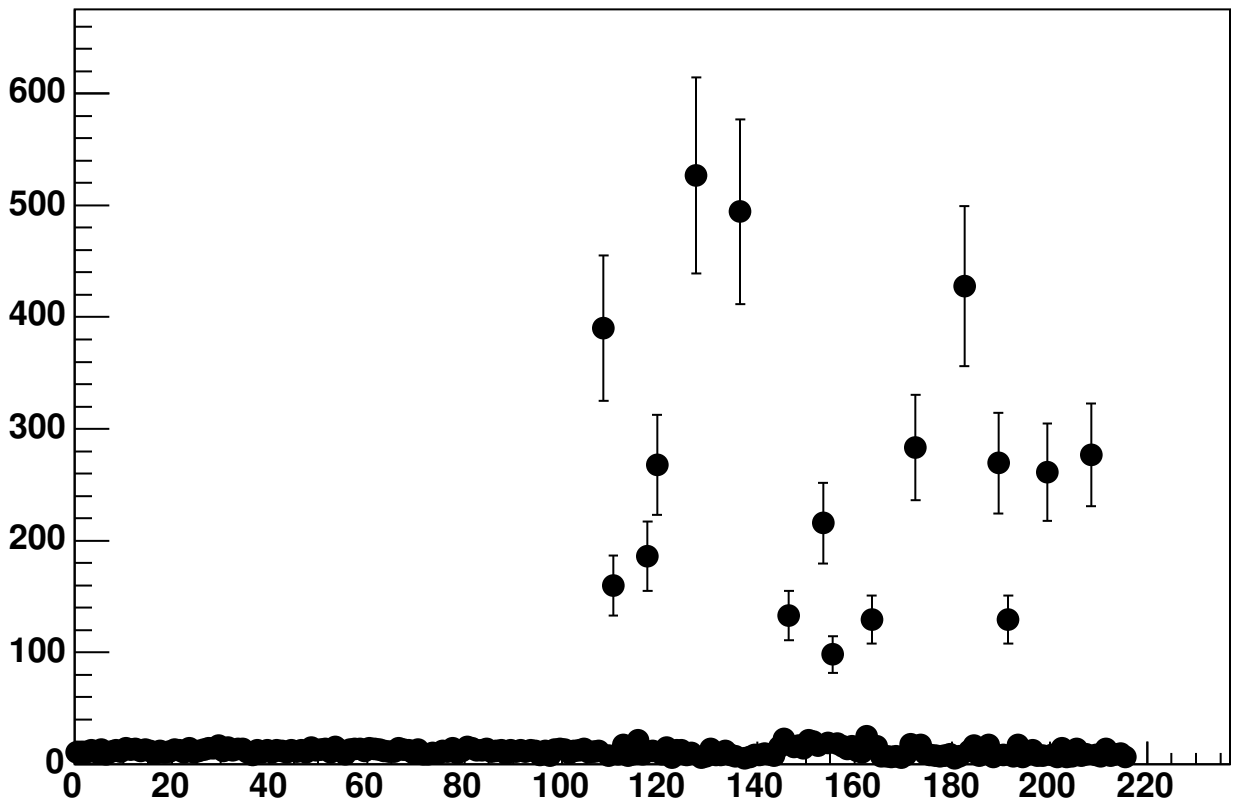
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



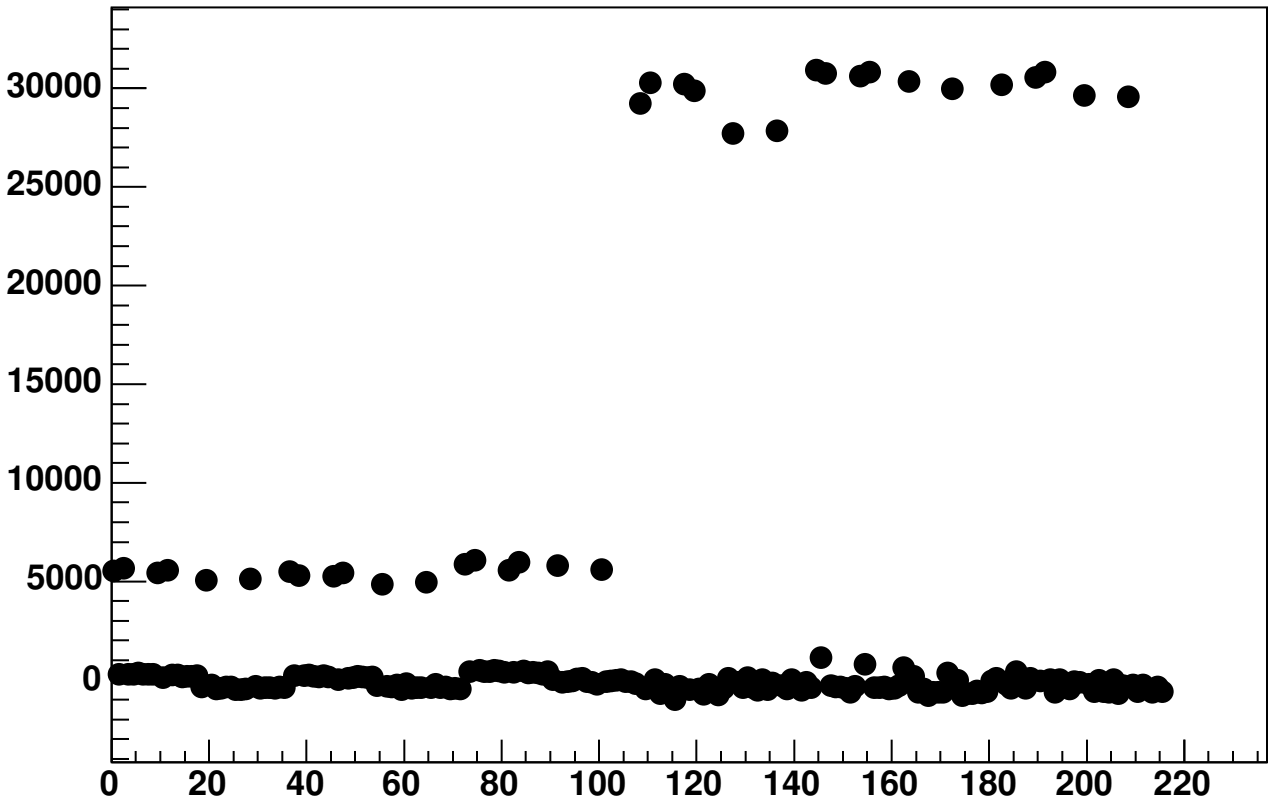
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



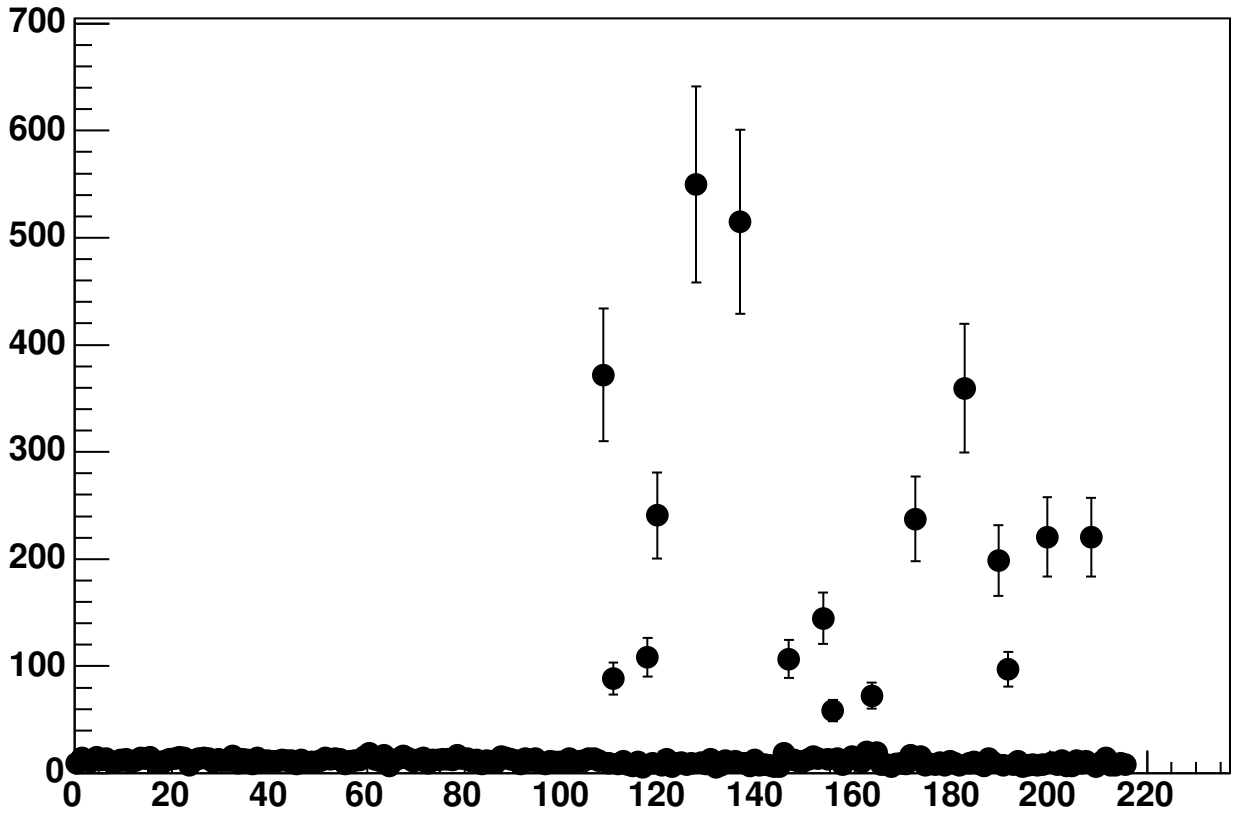
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



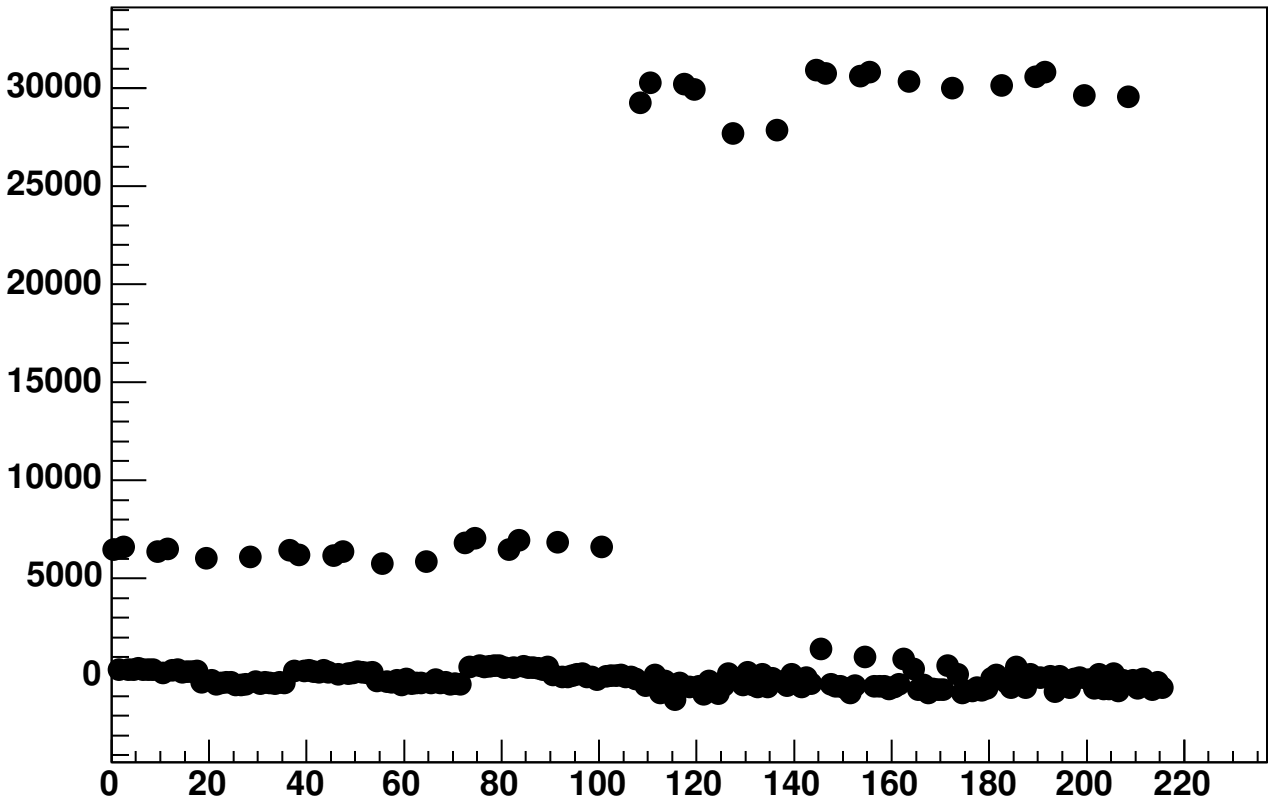
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



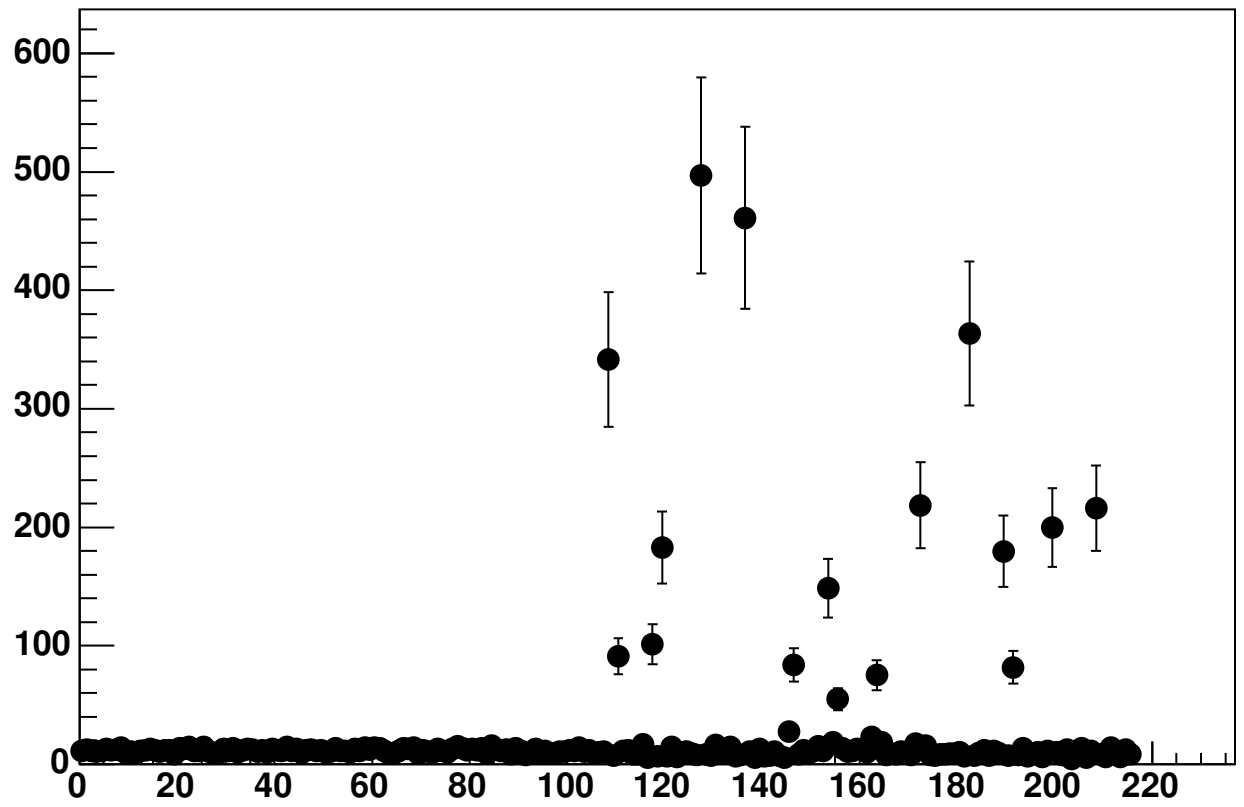
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



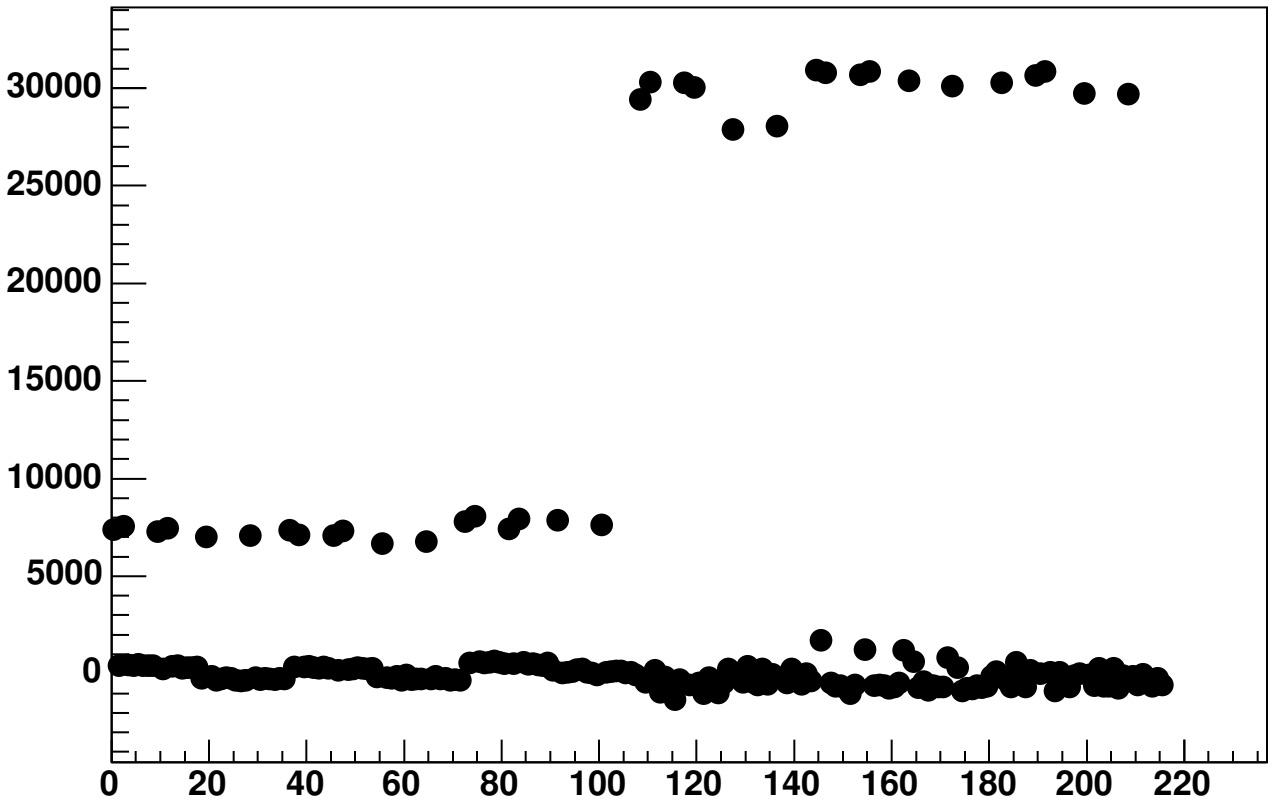
Enable 0, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



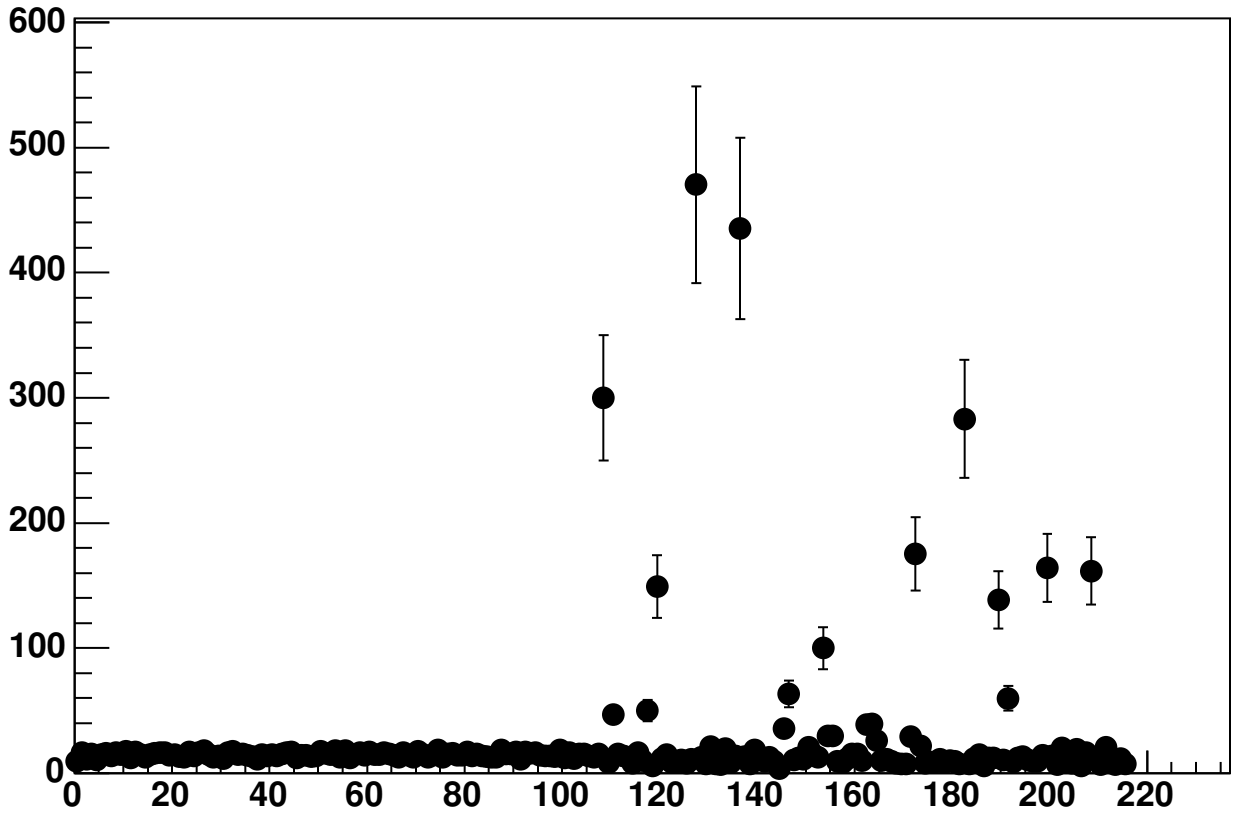
Enable 0, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



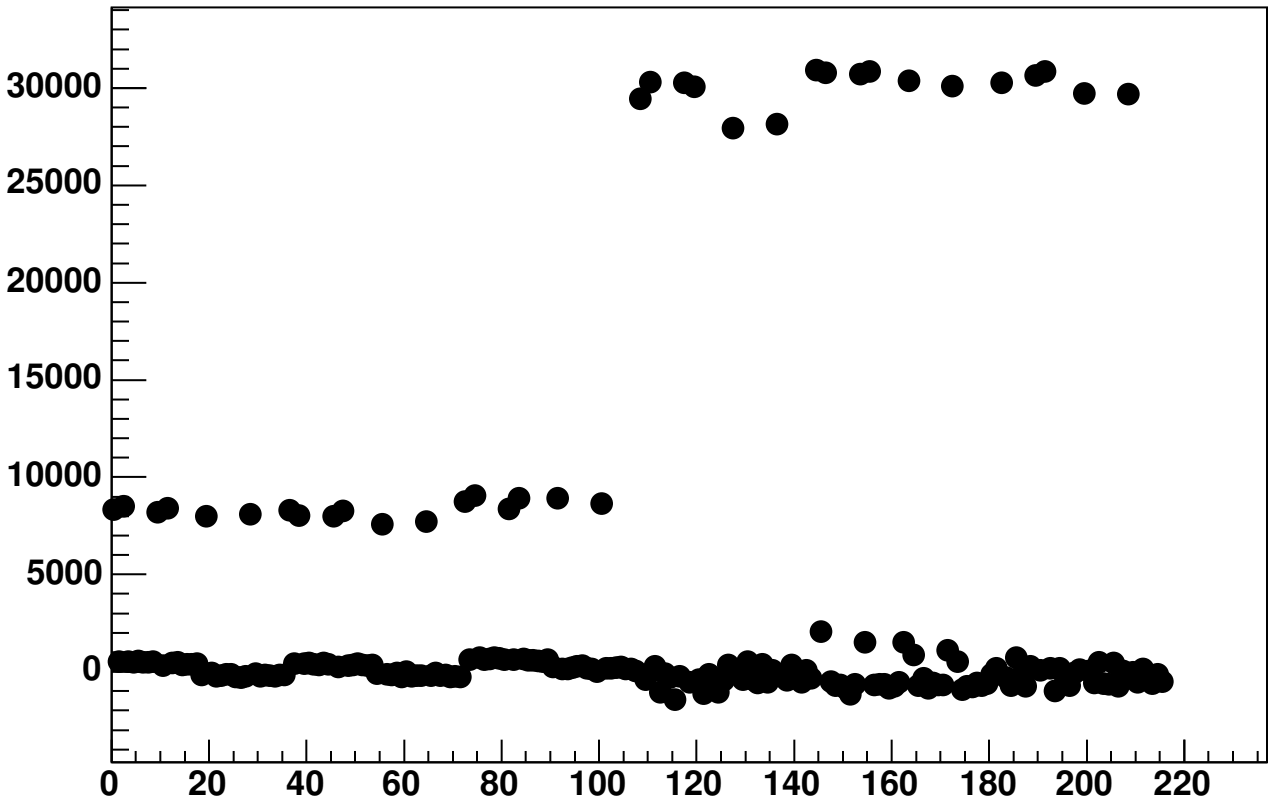
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



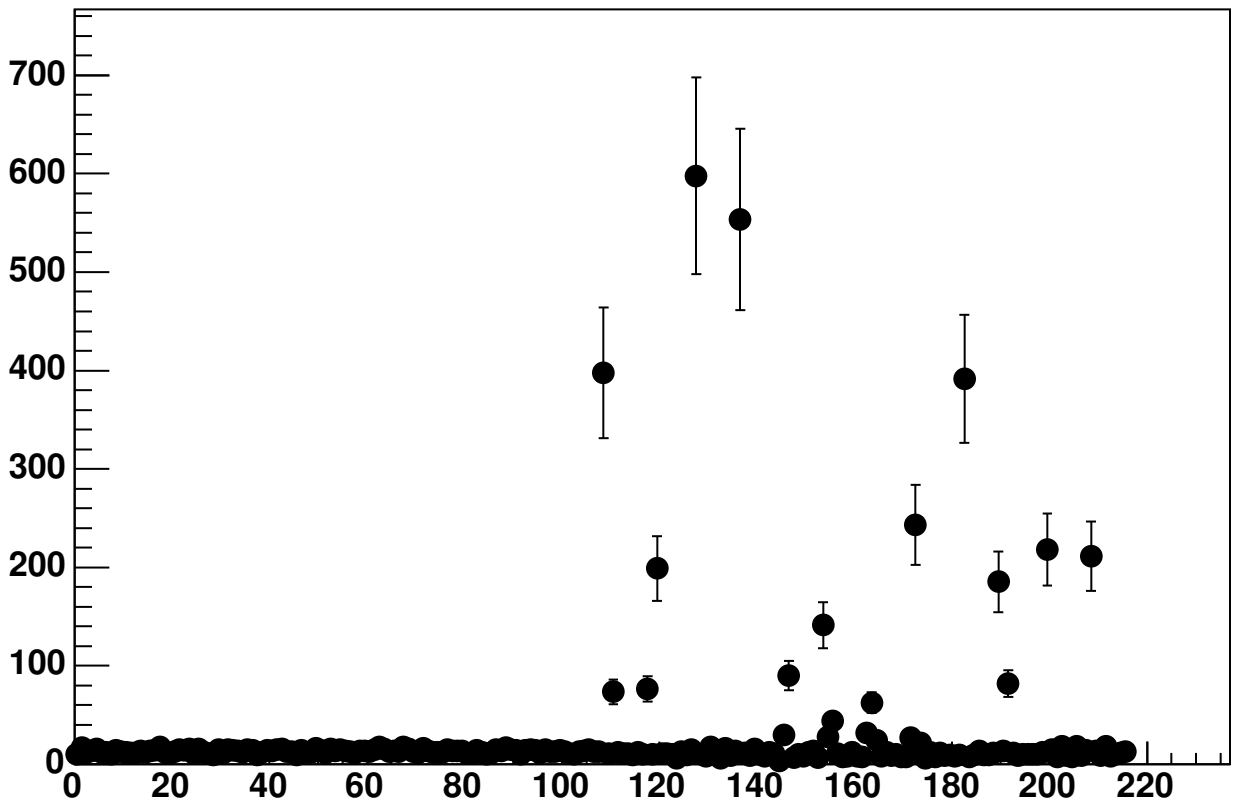
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



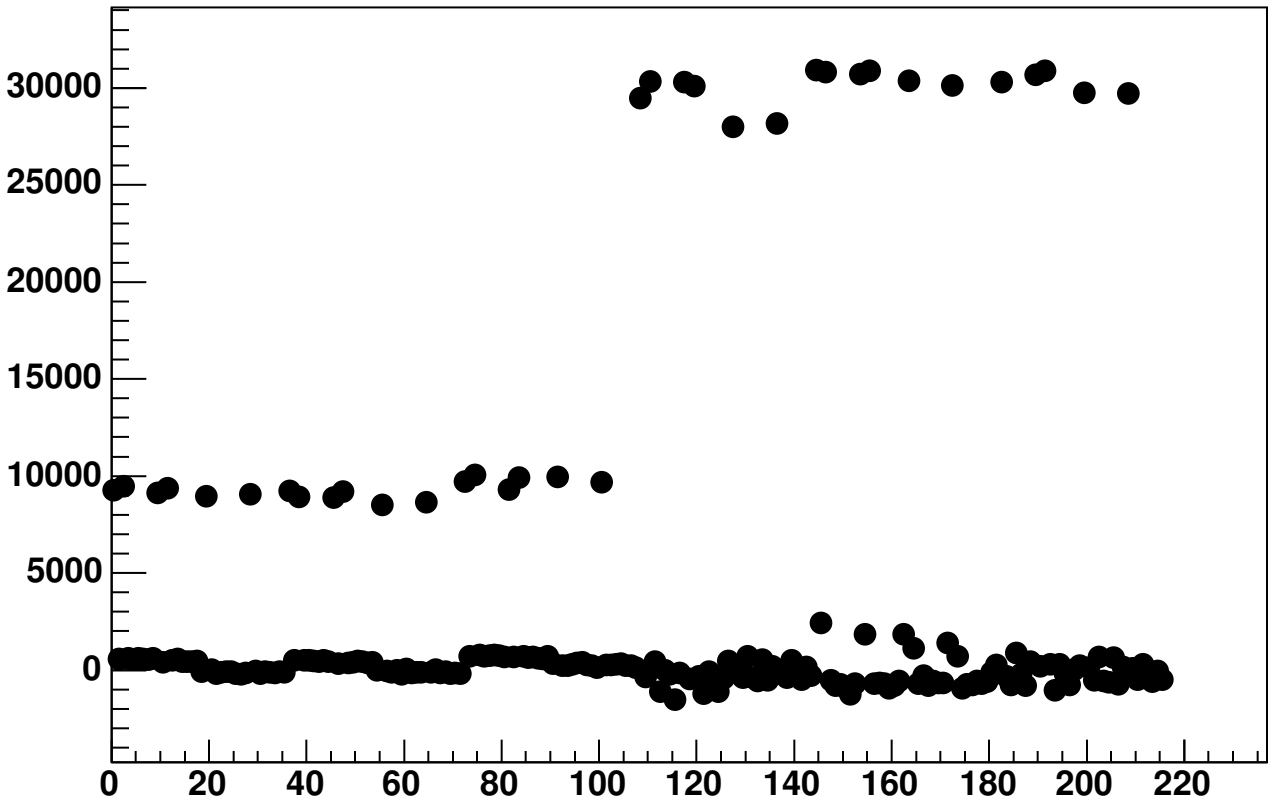
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



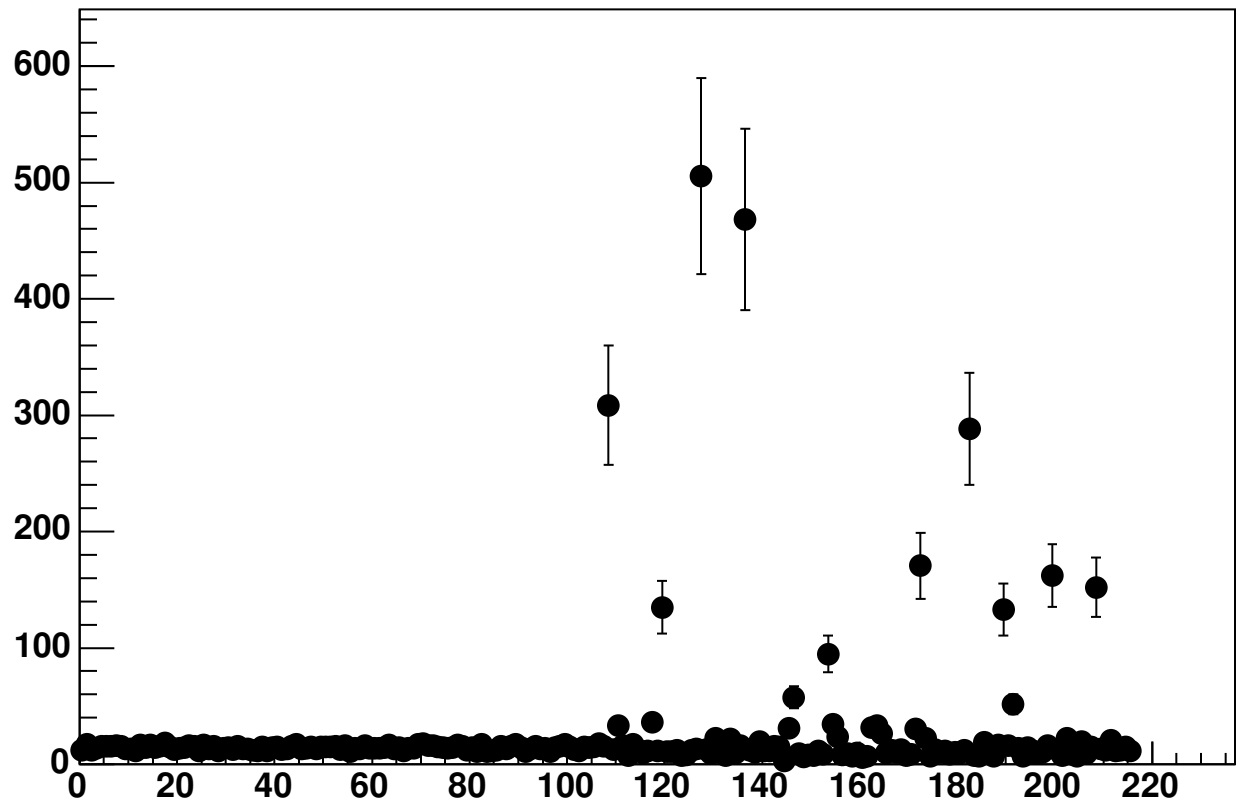
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



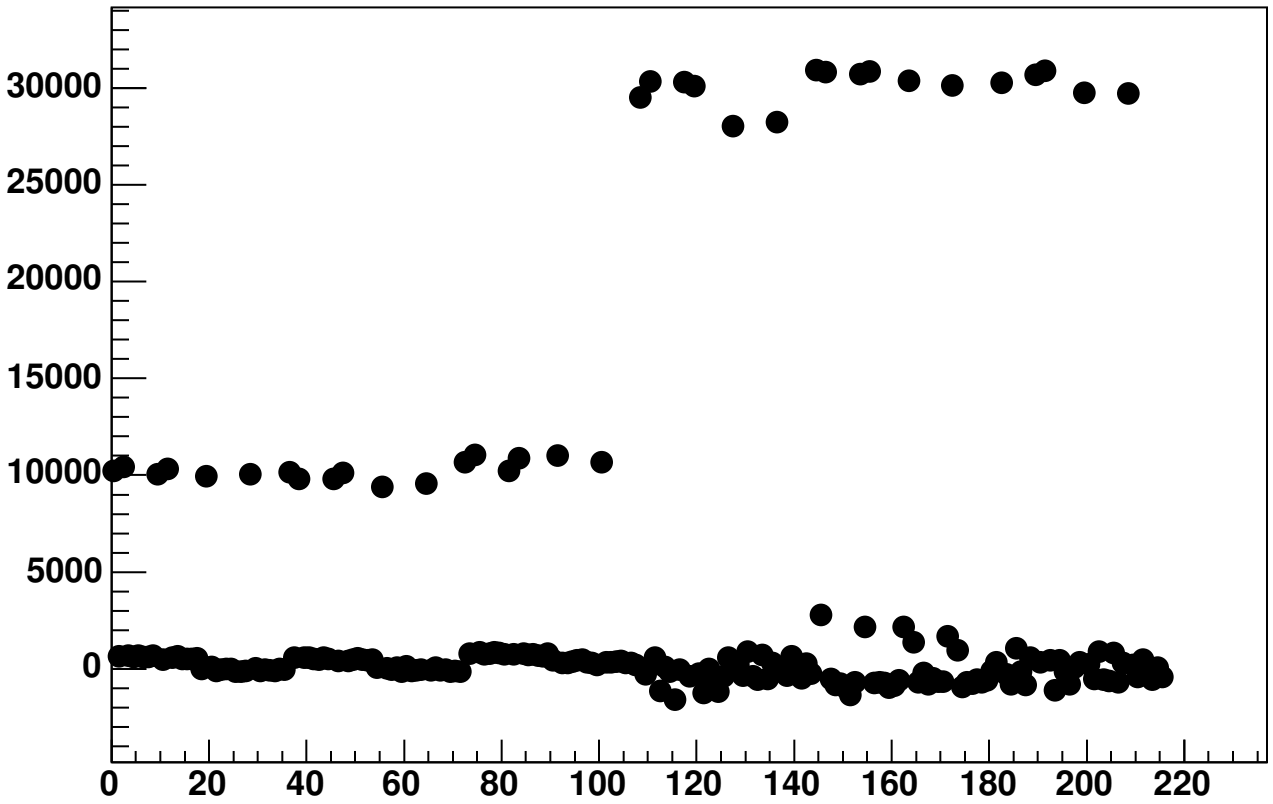
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



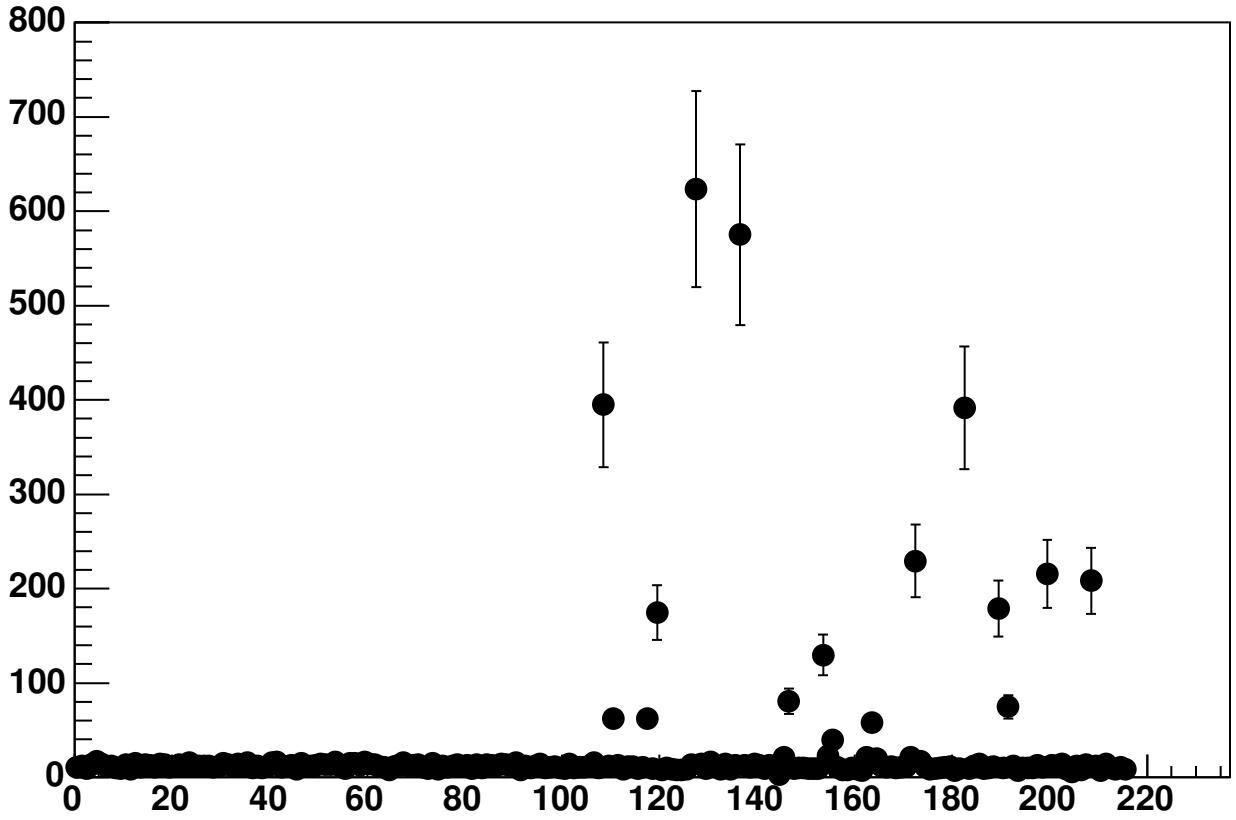
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



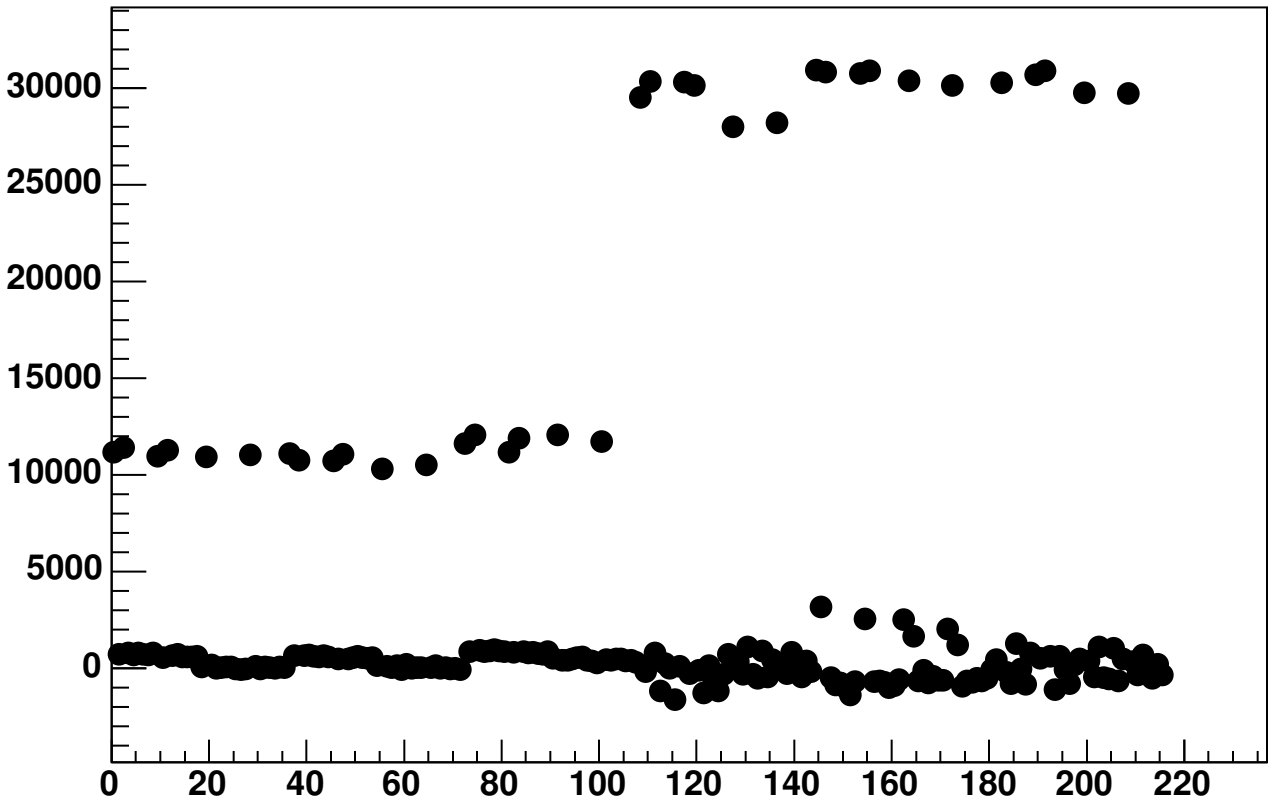
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



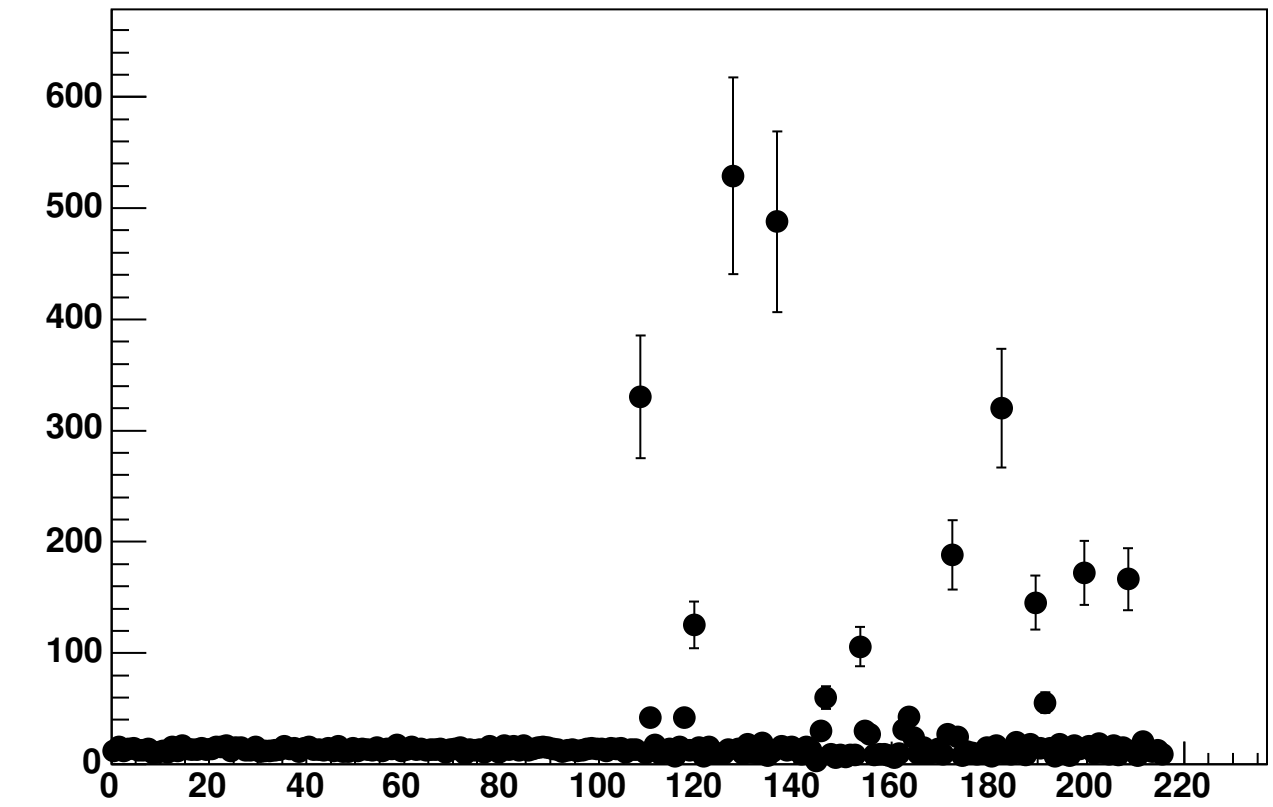
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



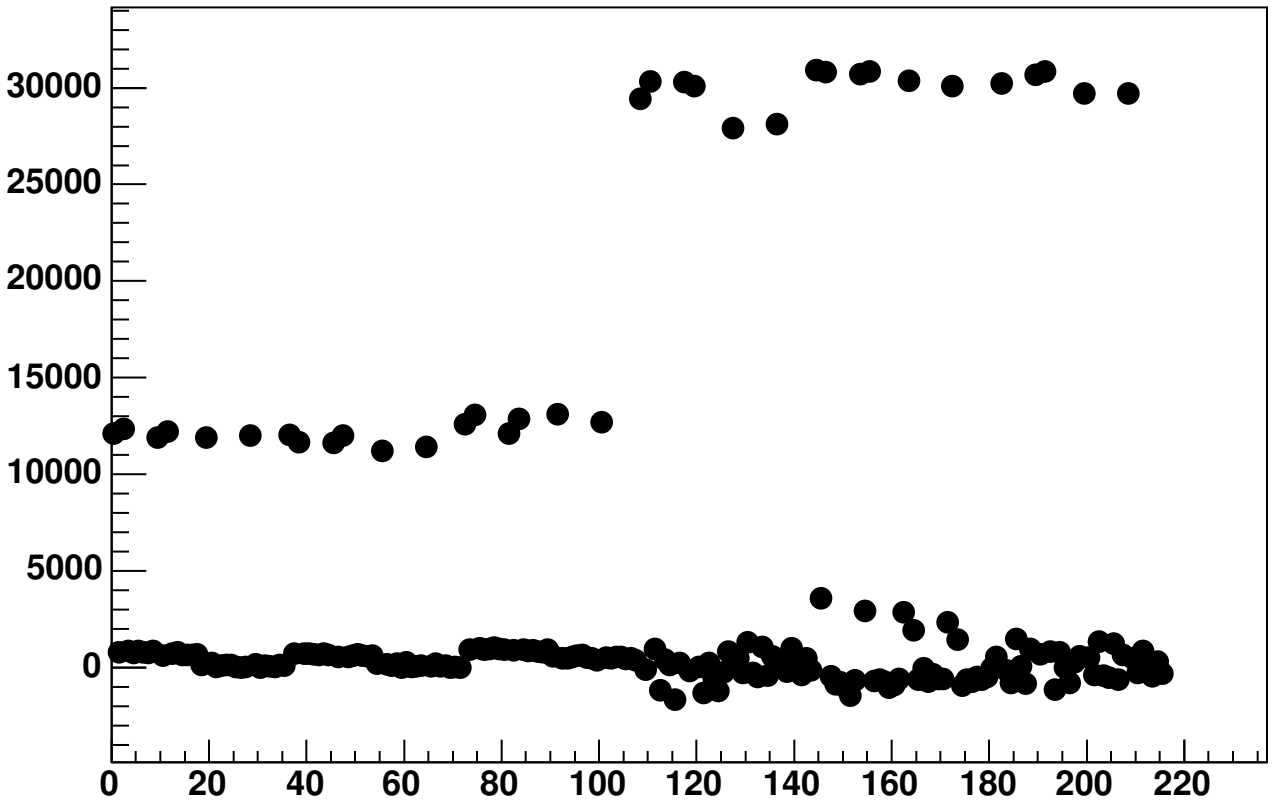
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



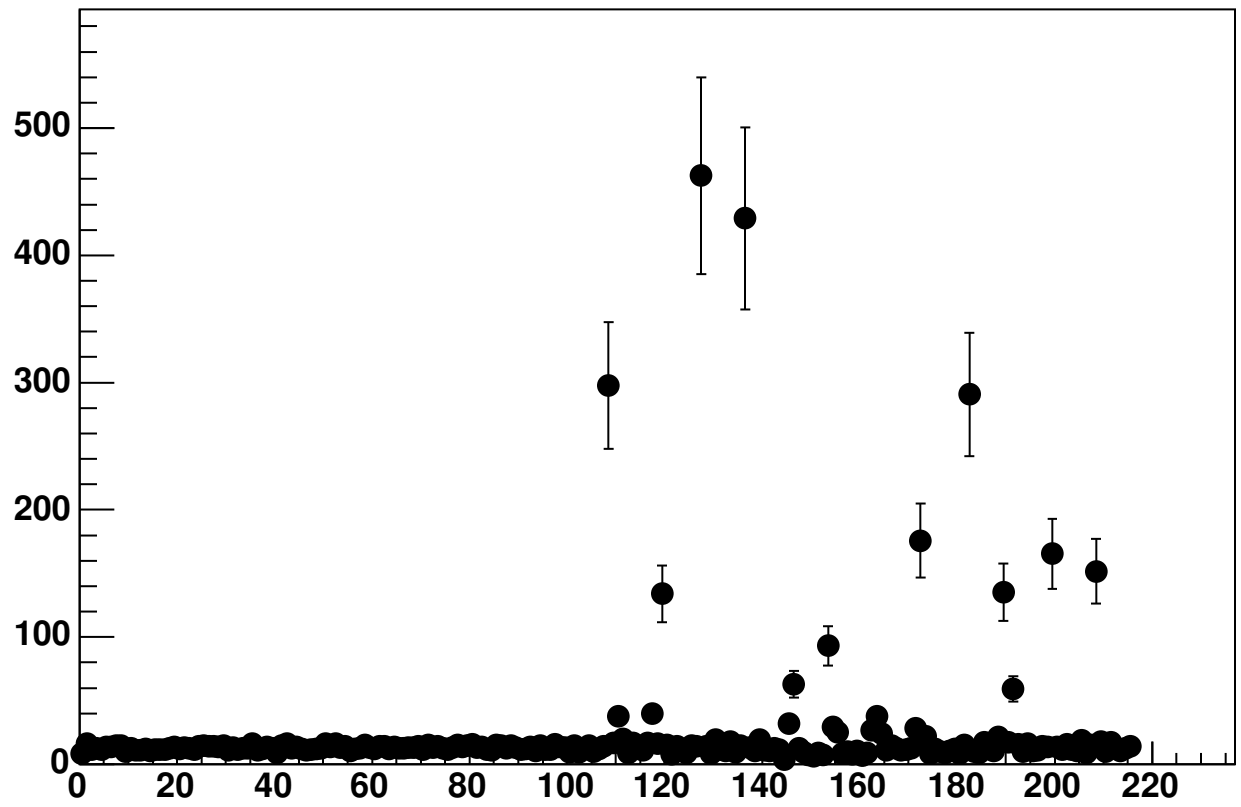
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



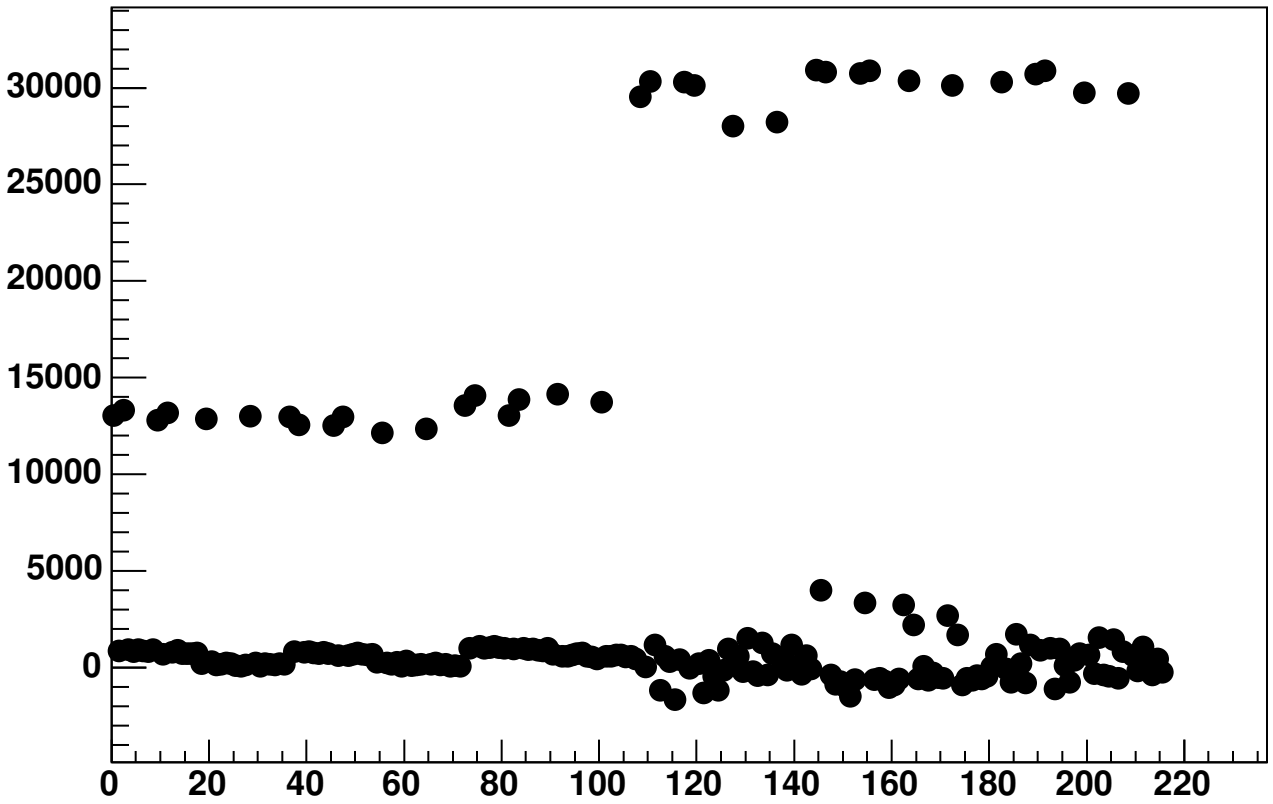
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



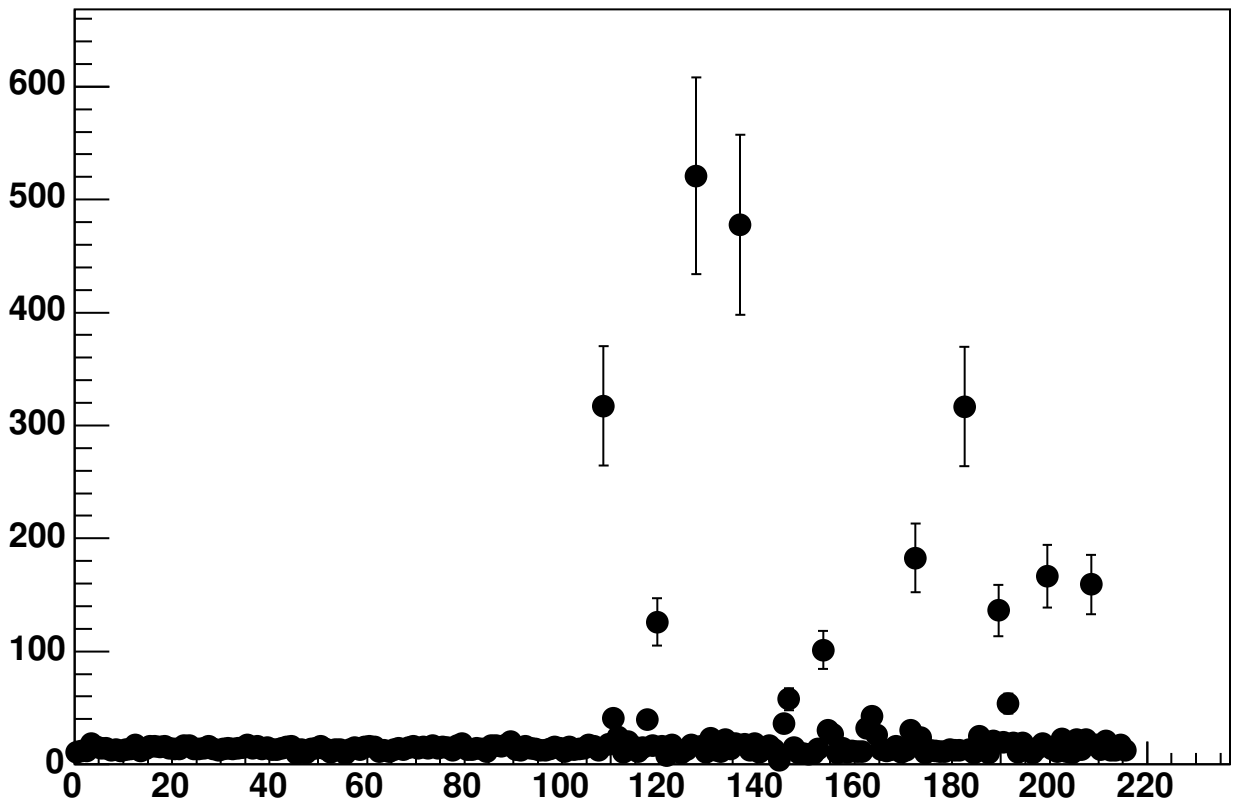
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



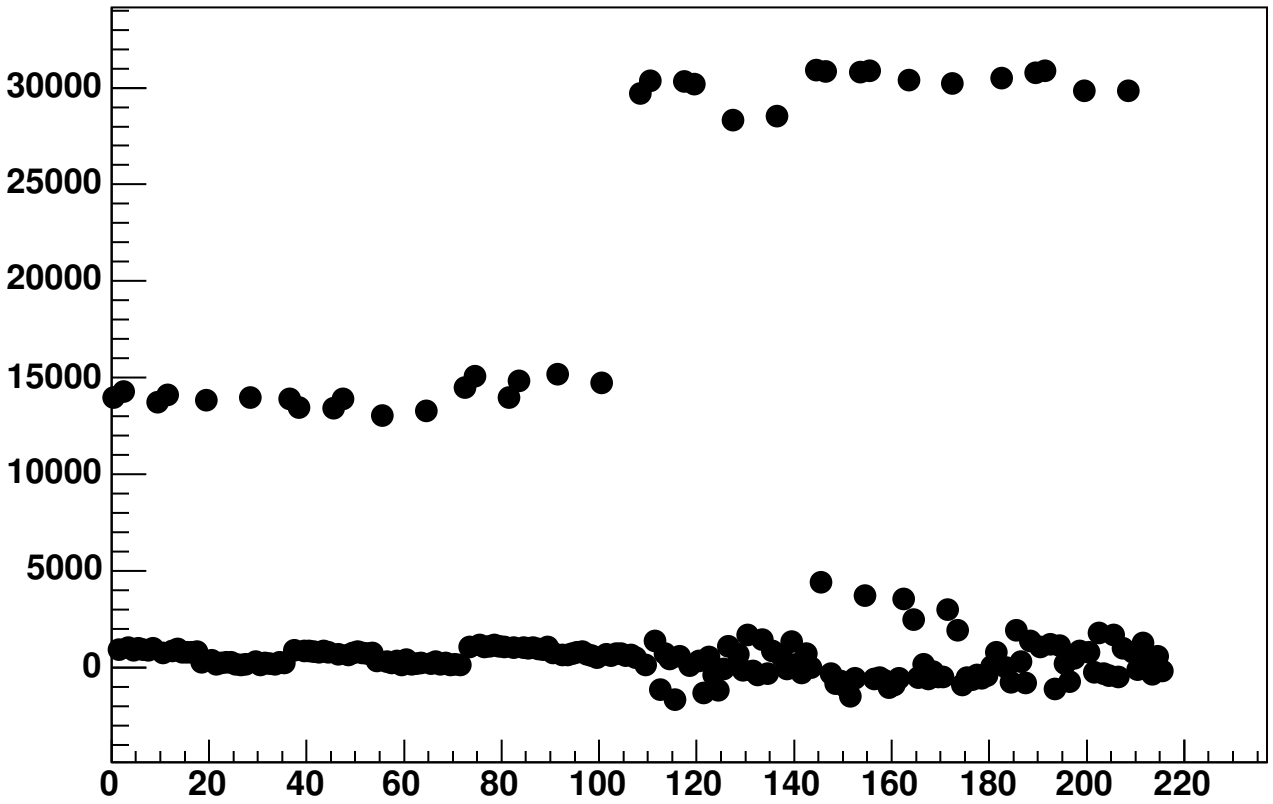
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



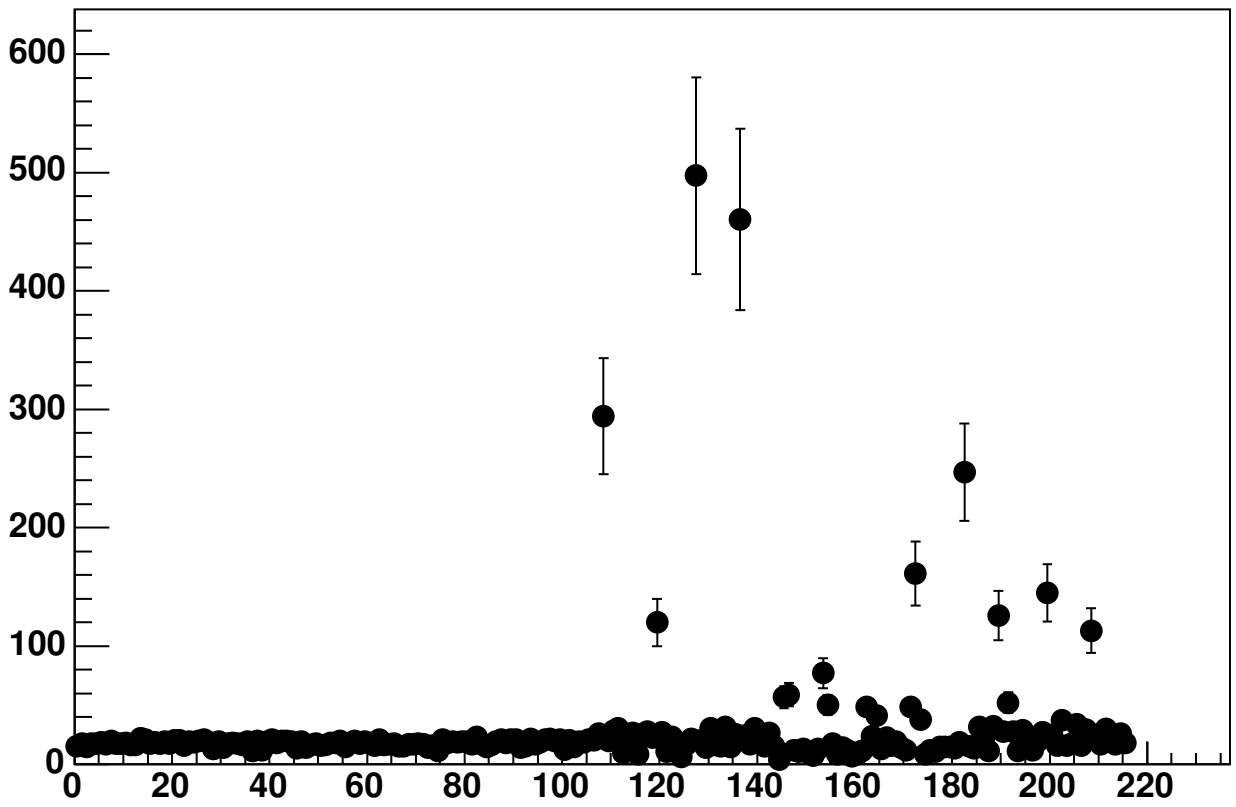
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



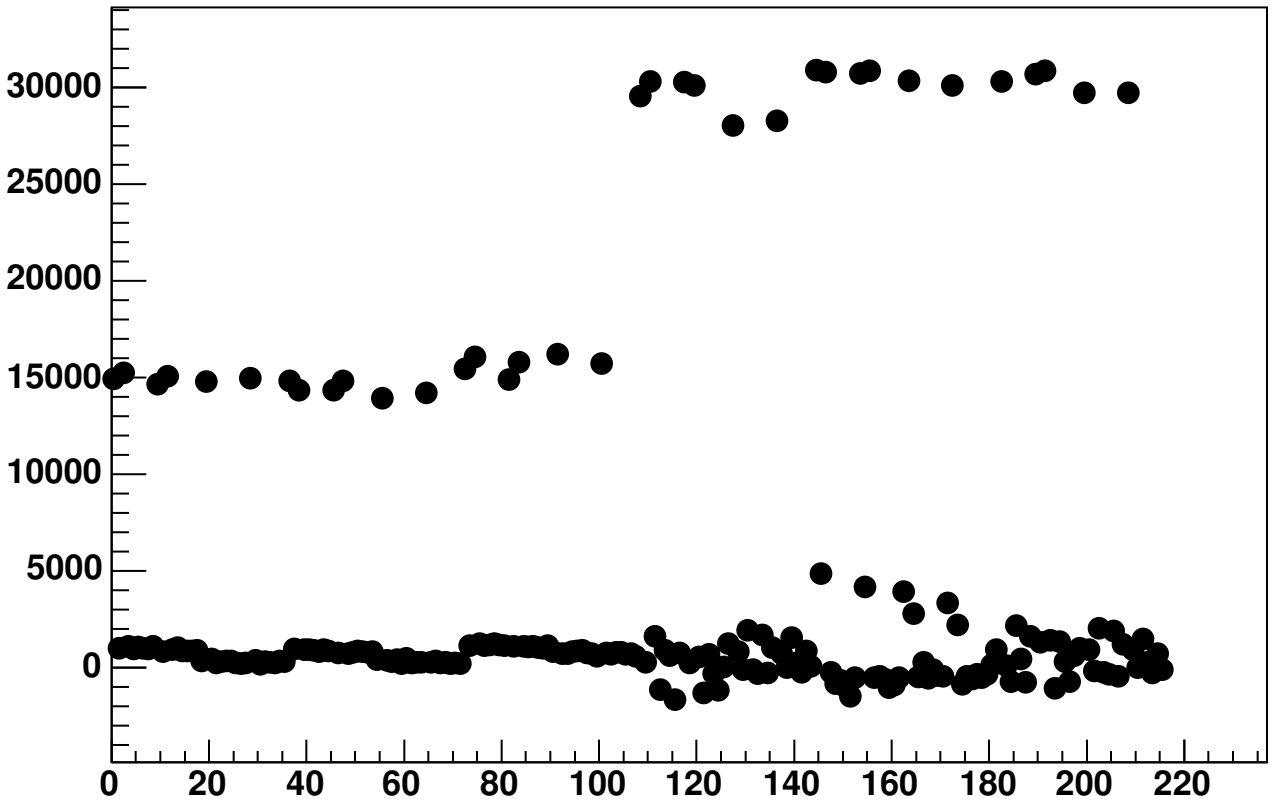
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



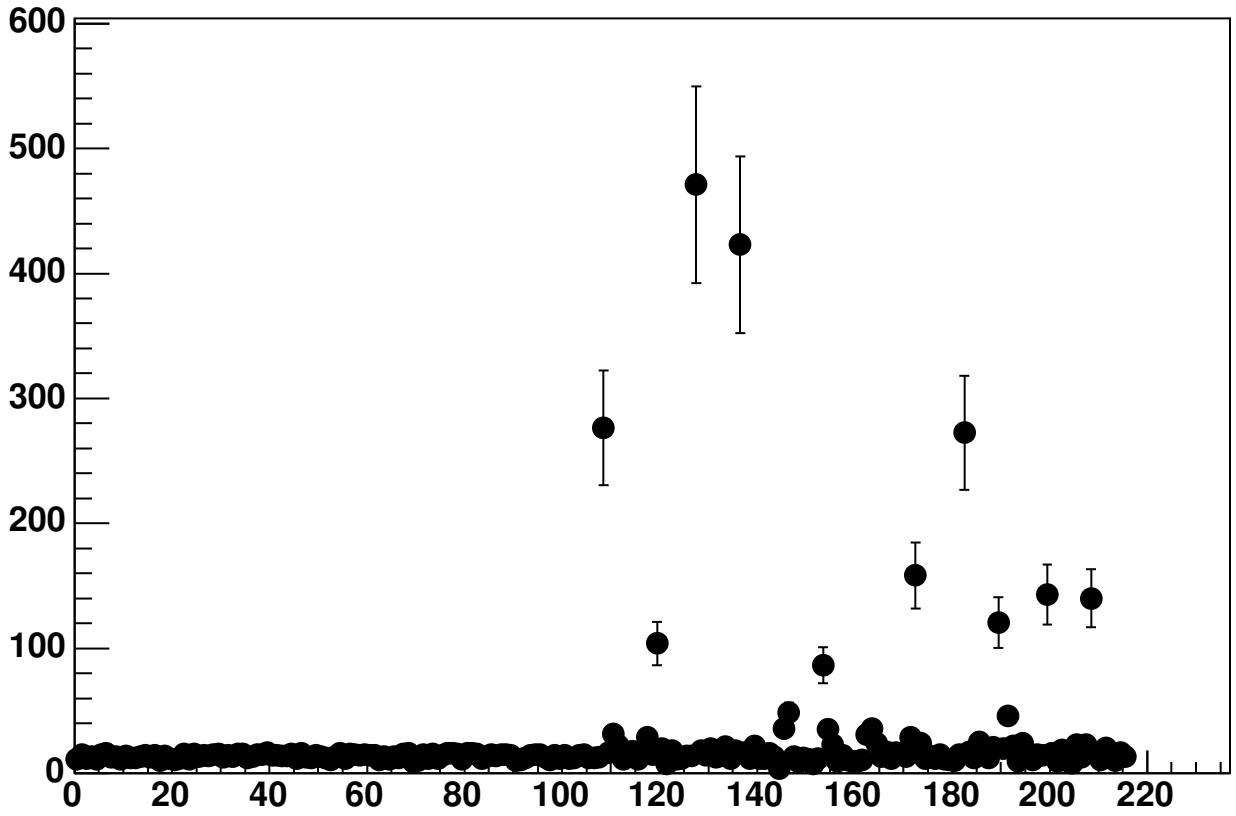
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



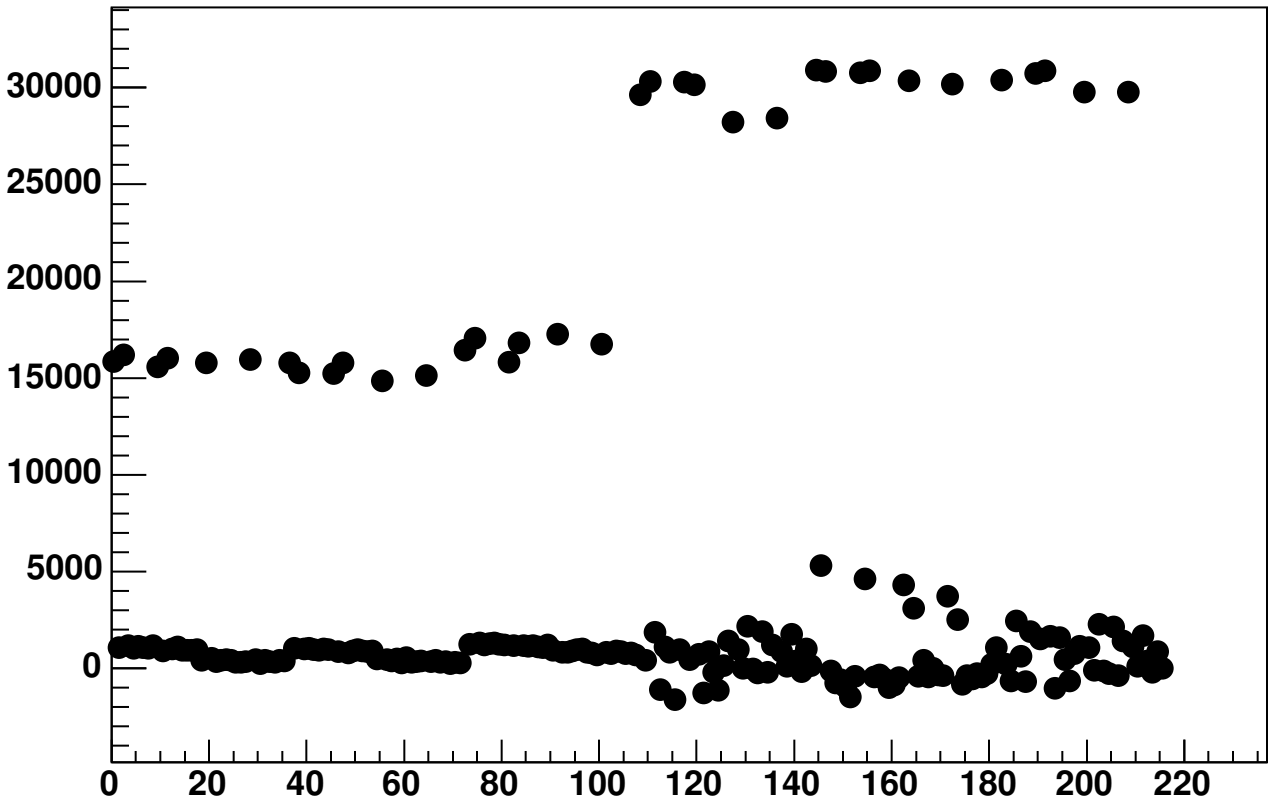
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



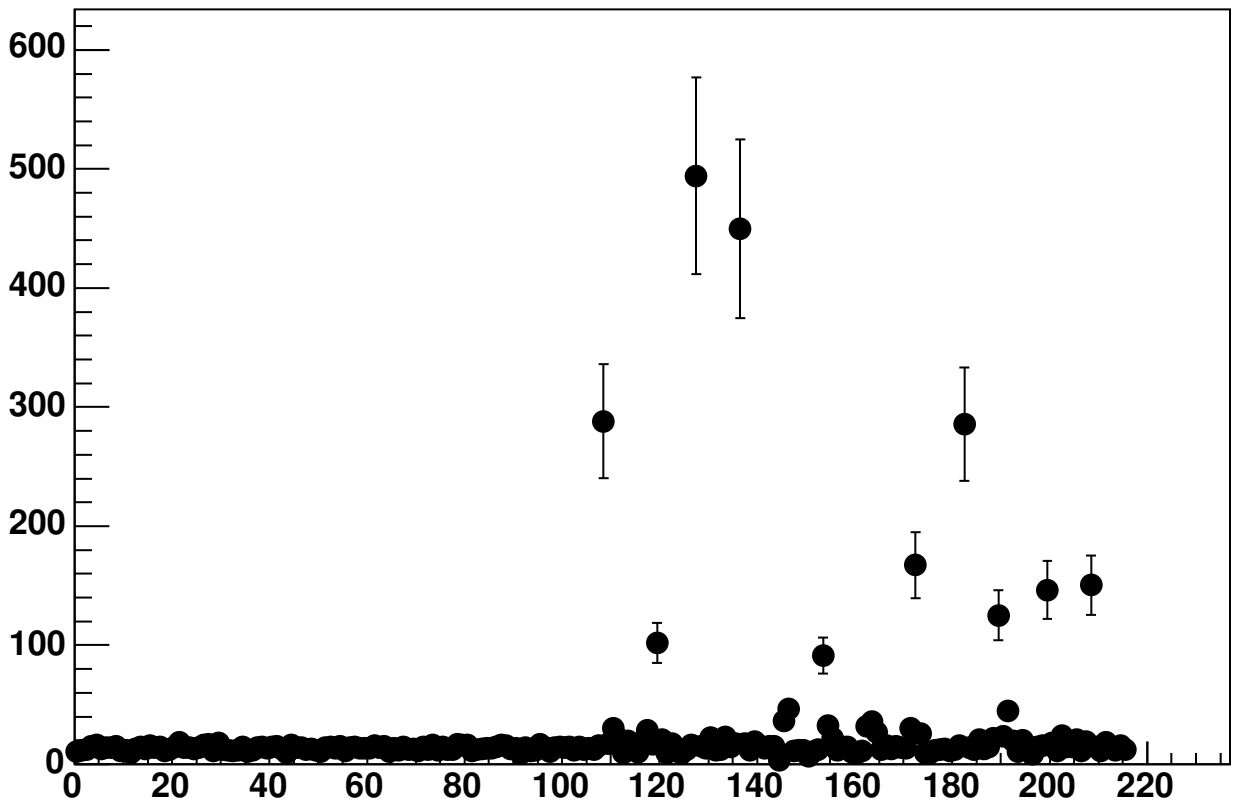
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



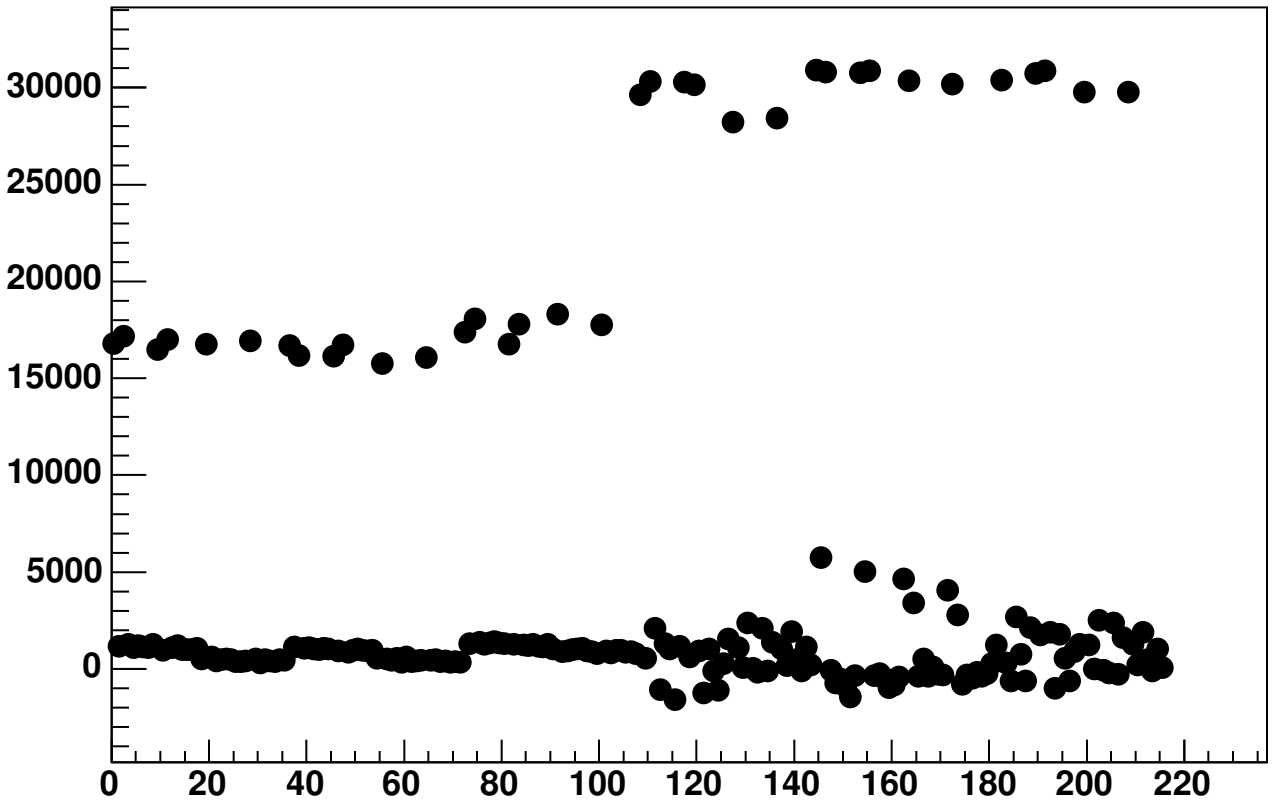
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



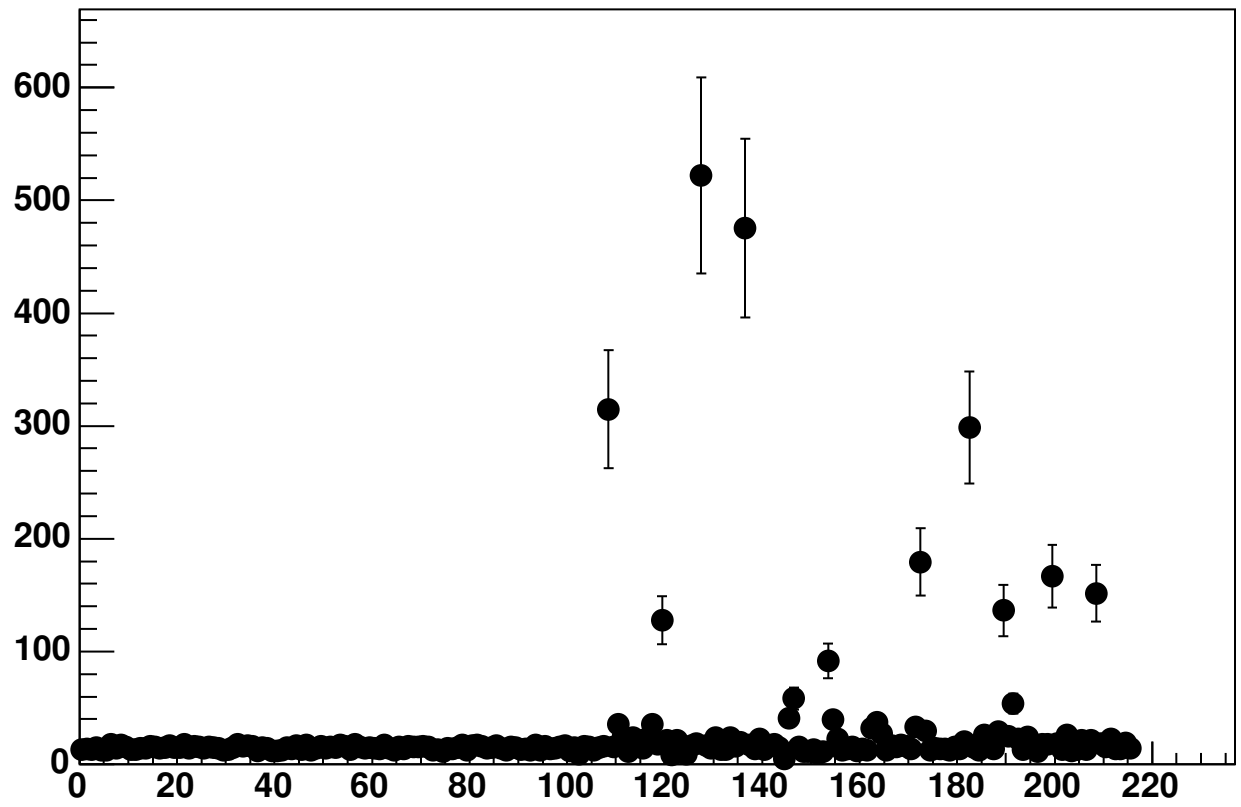
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



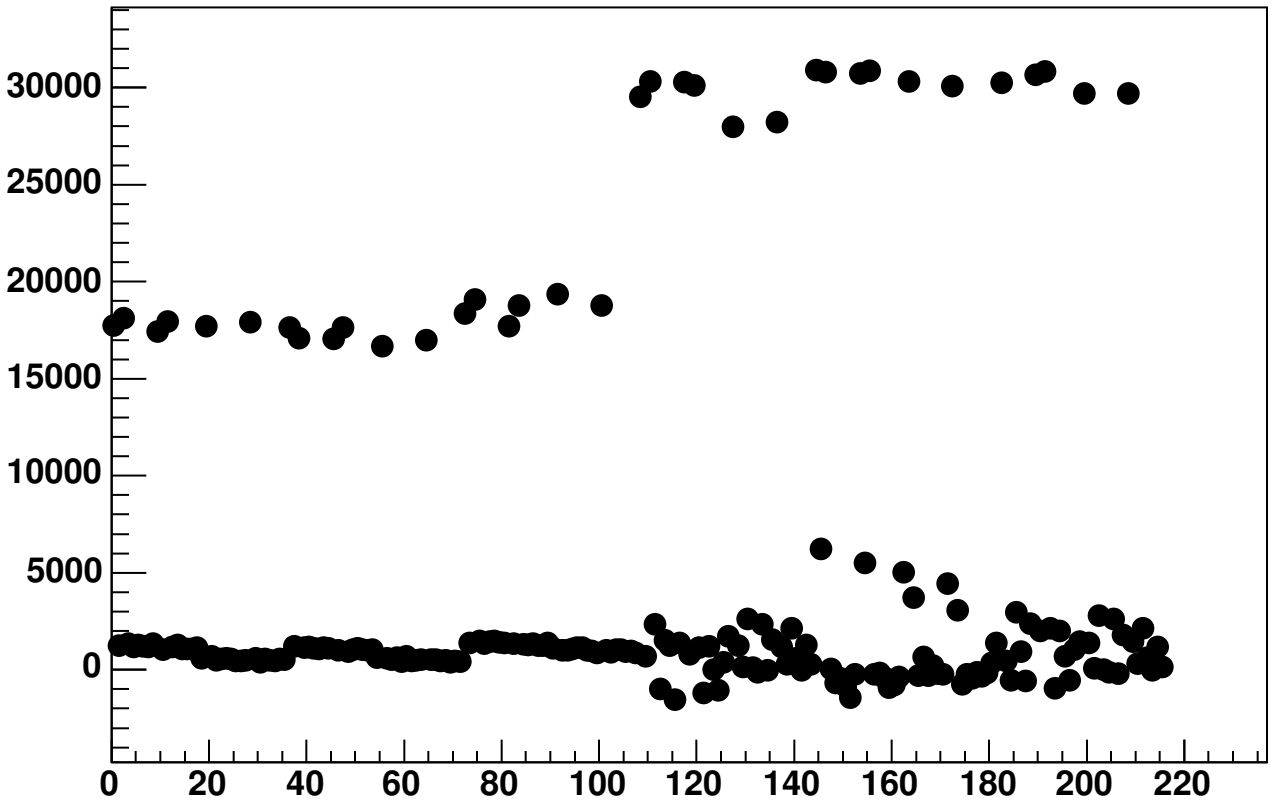
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



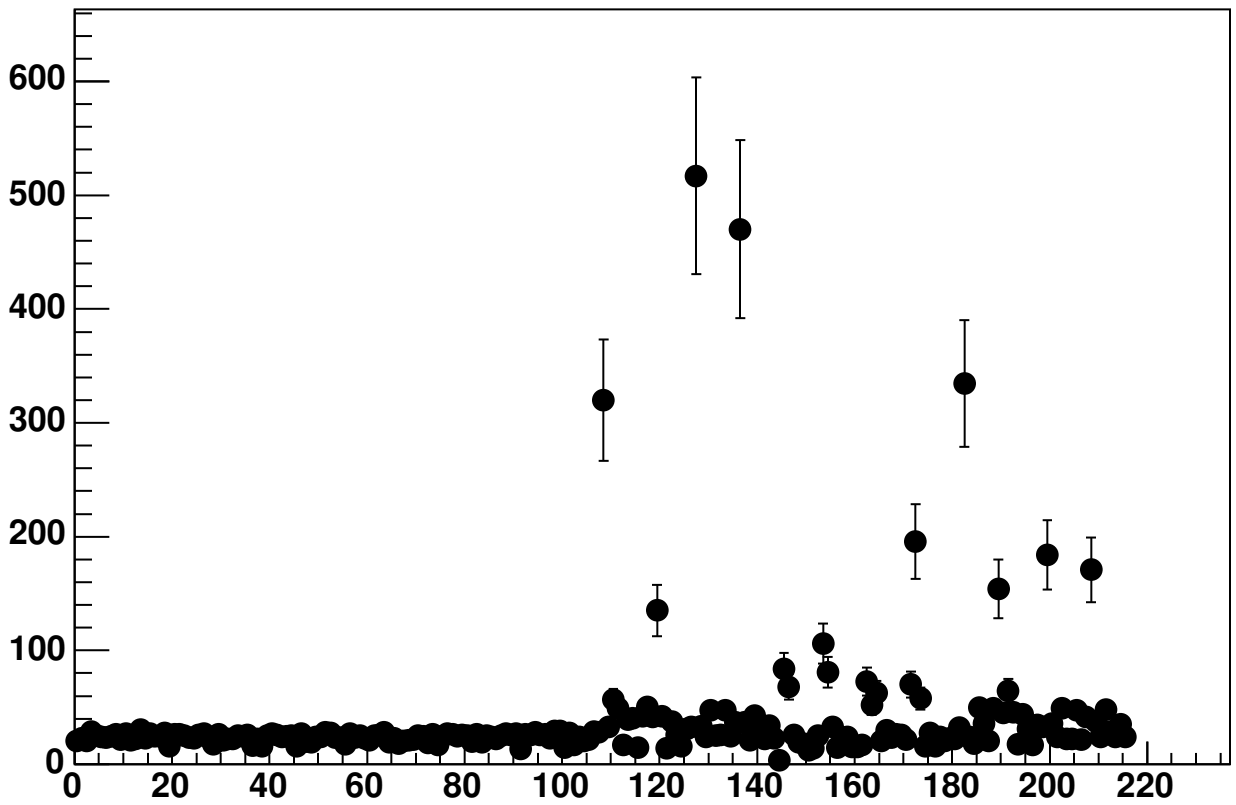
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



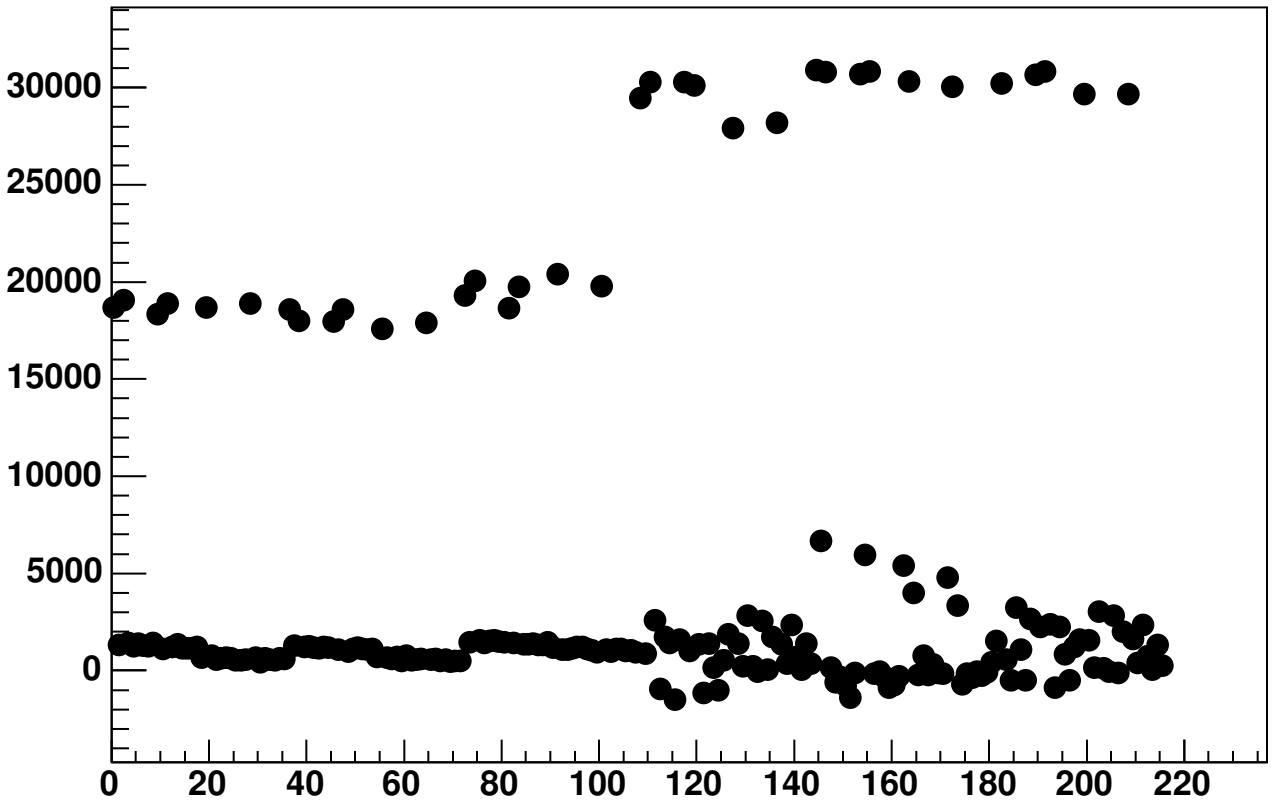
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



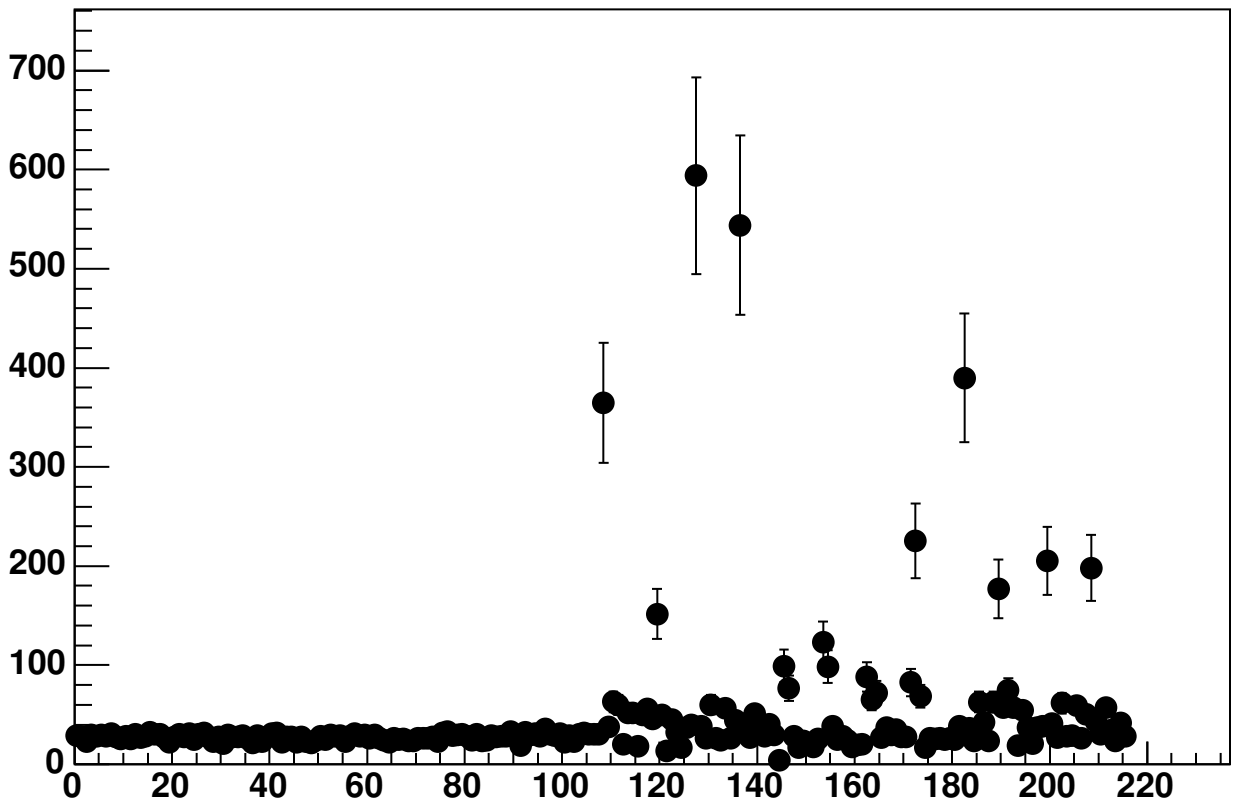
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



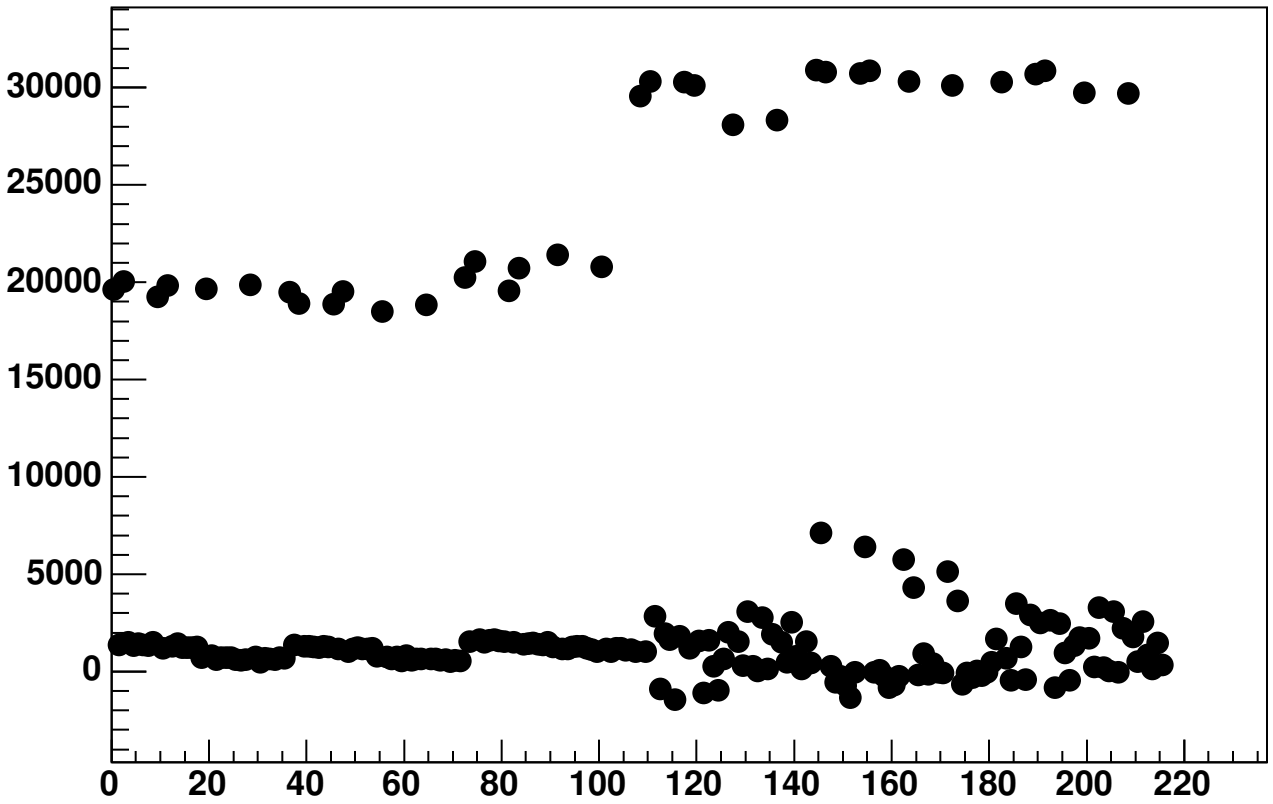
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



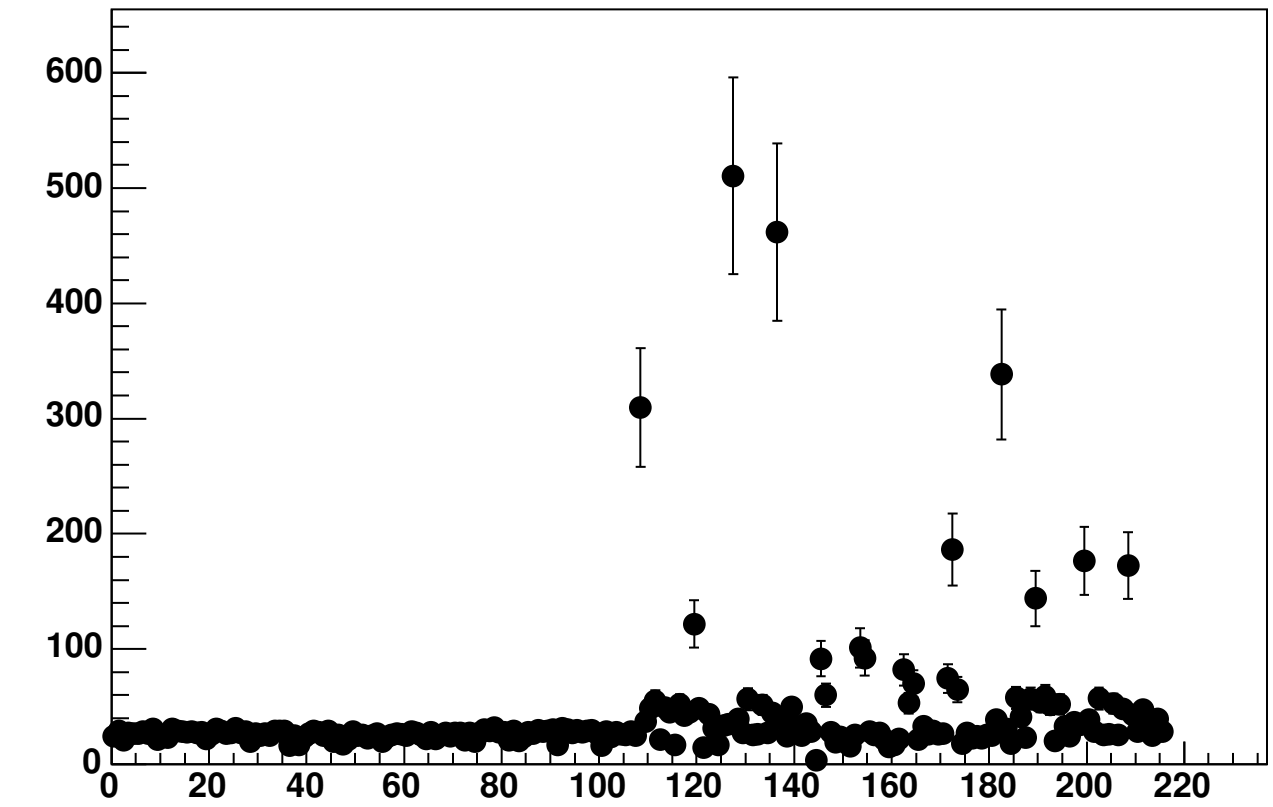
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



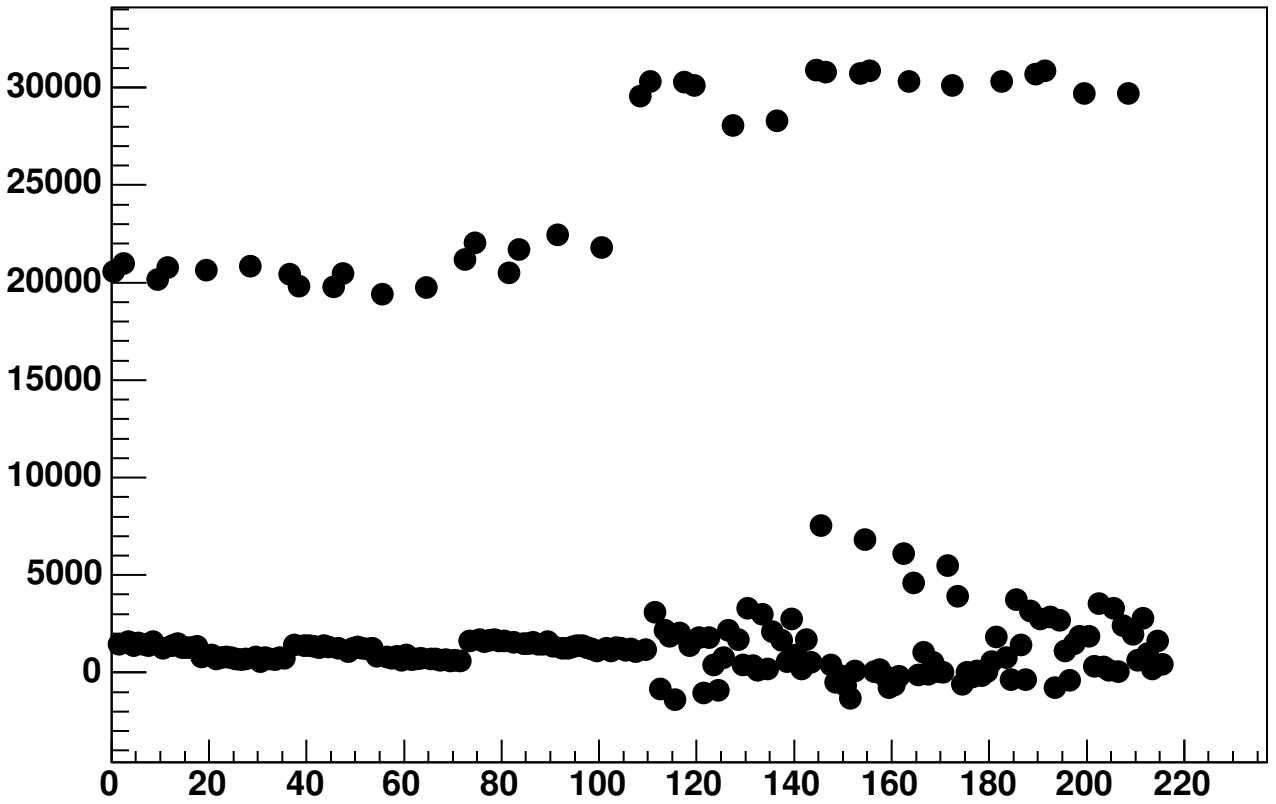
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



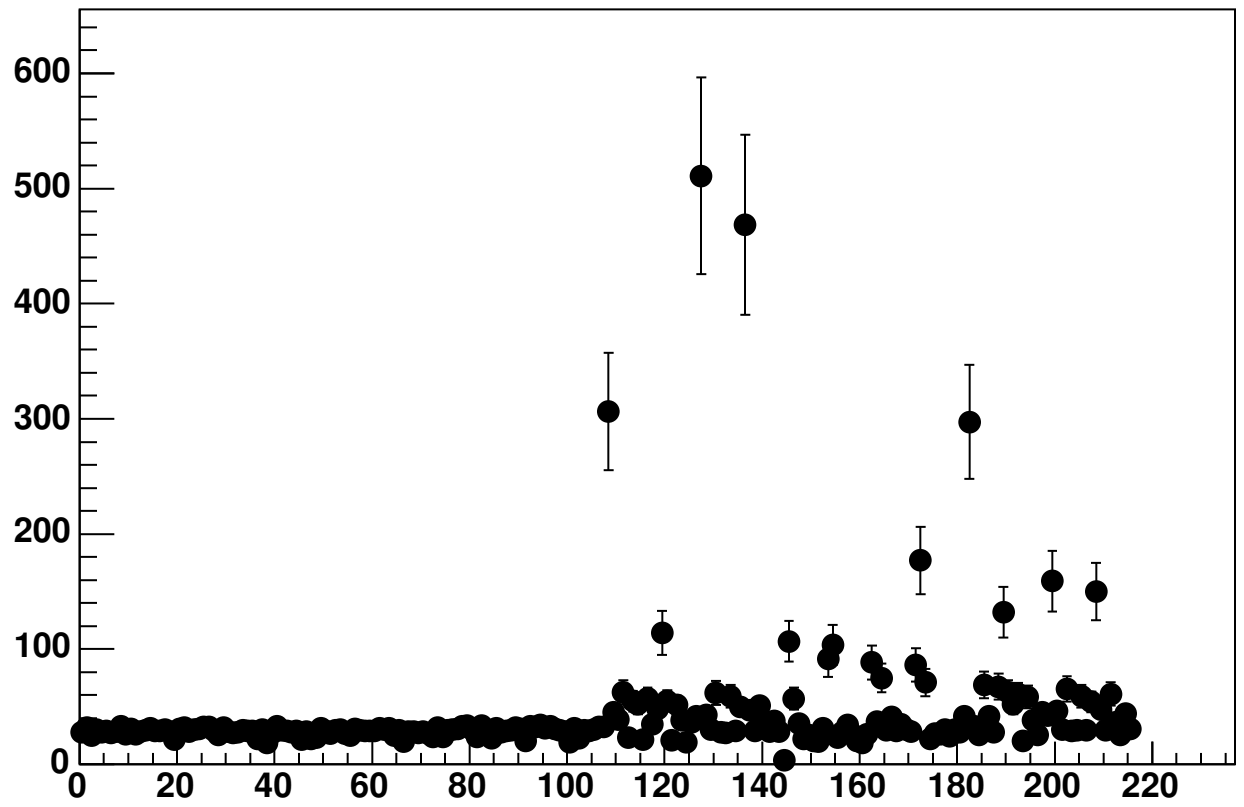
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



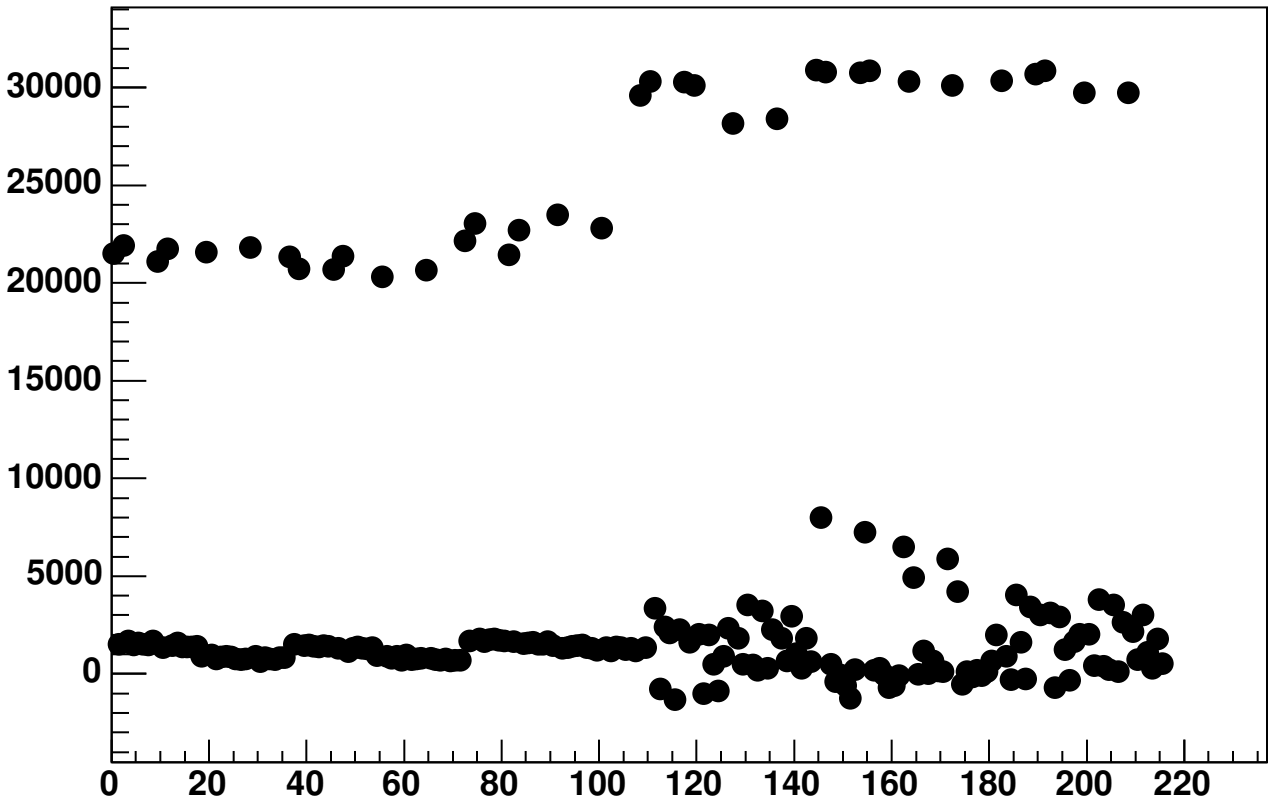
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



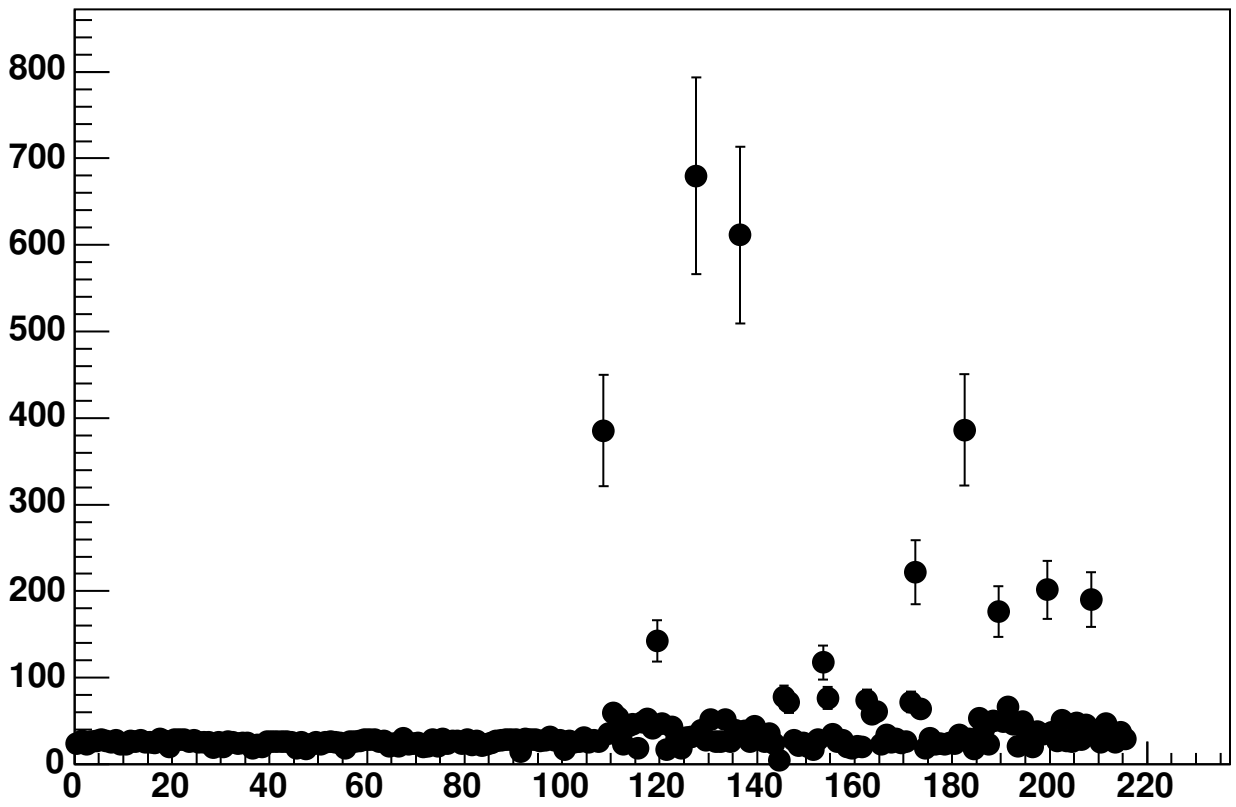
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



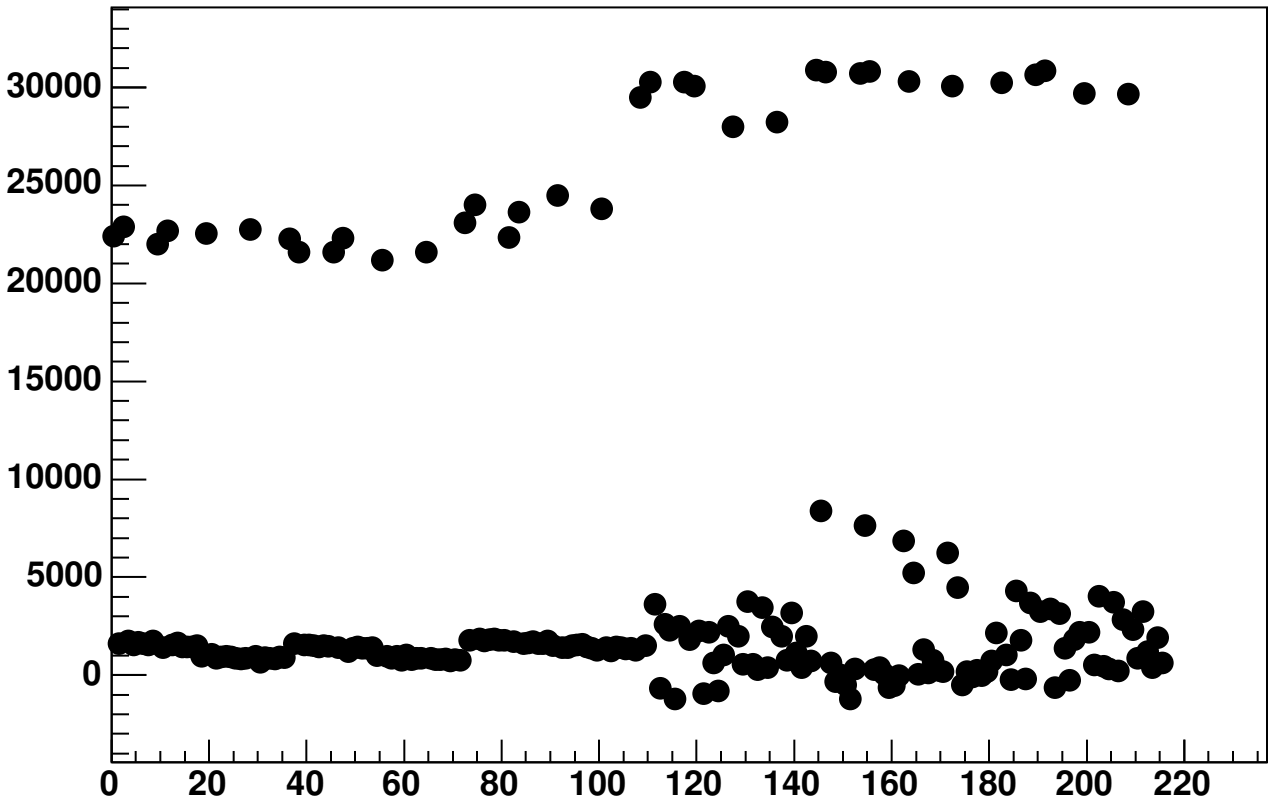
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



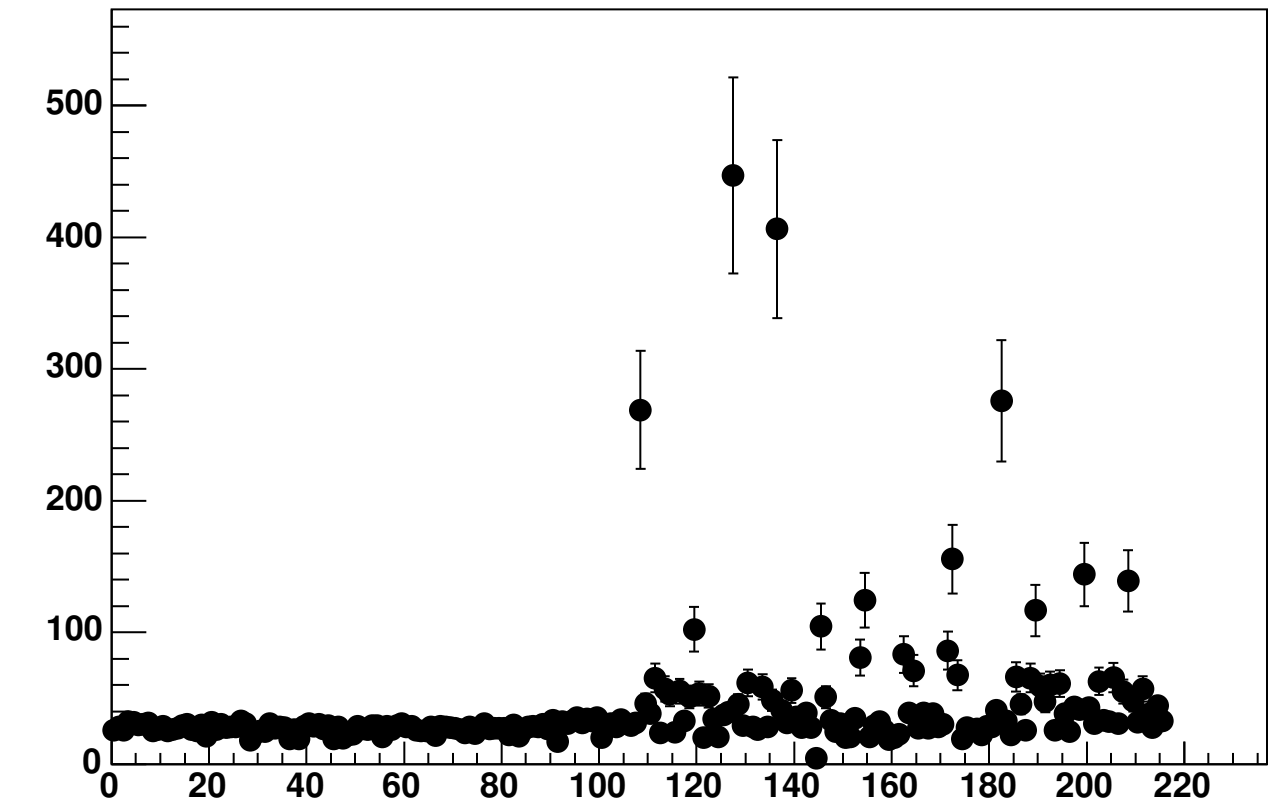
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



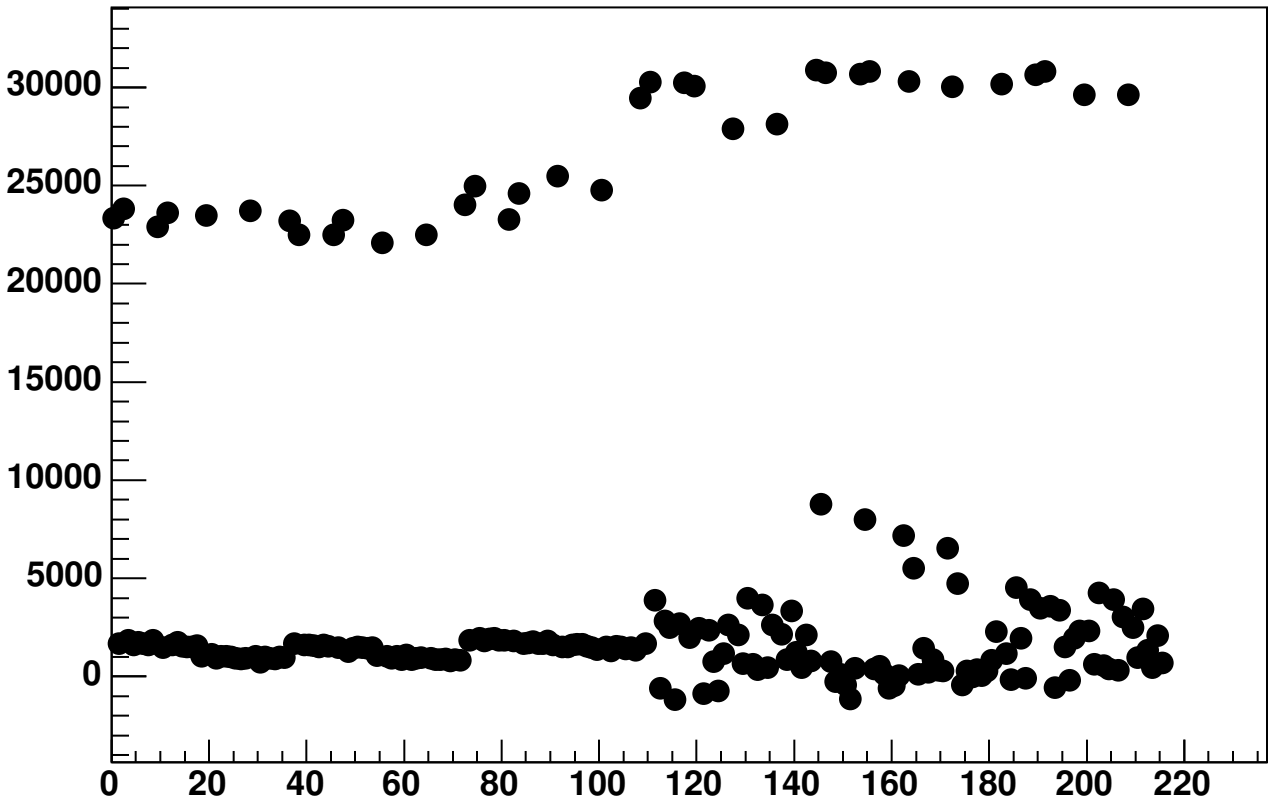
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



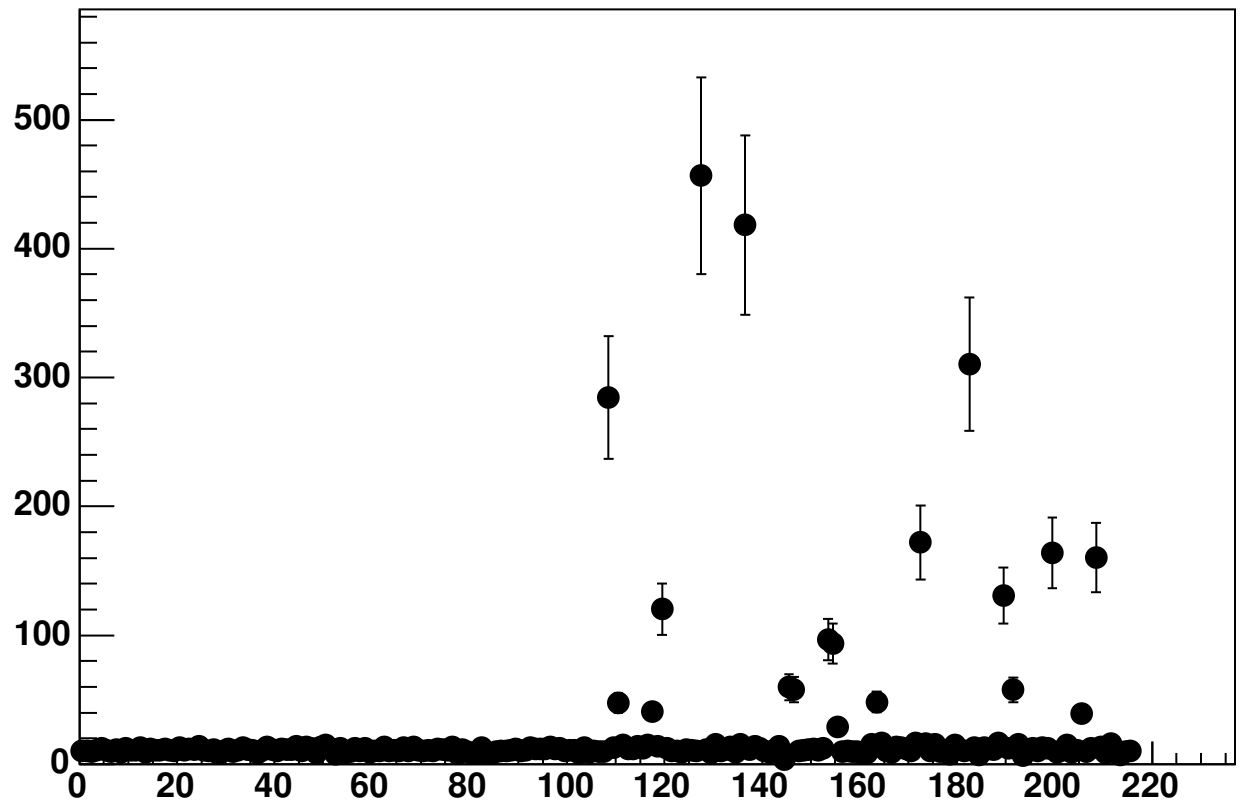
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



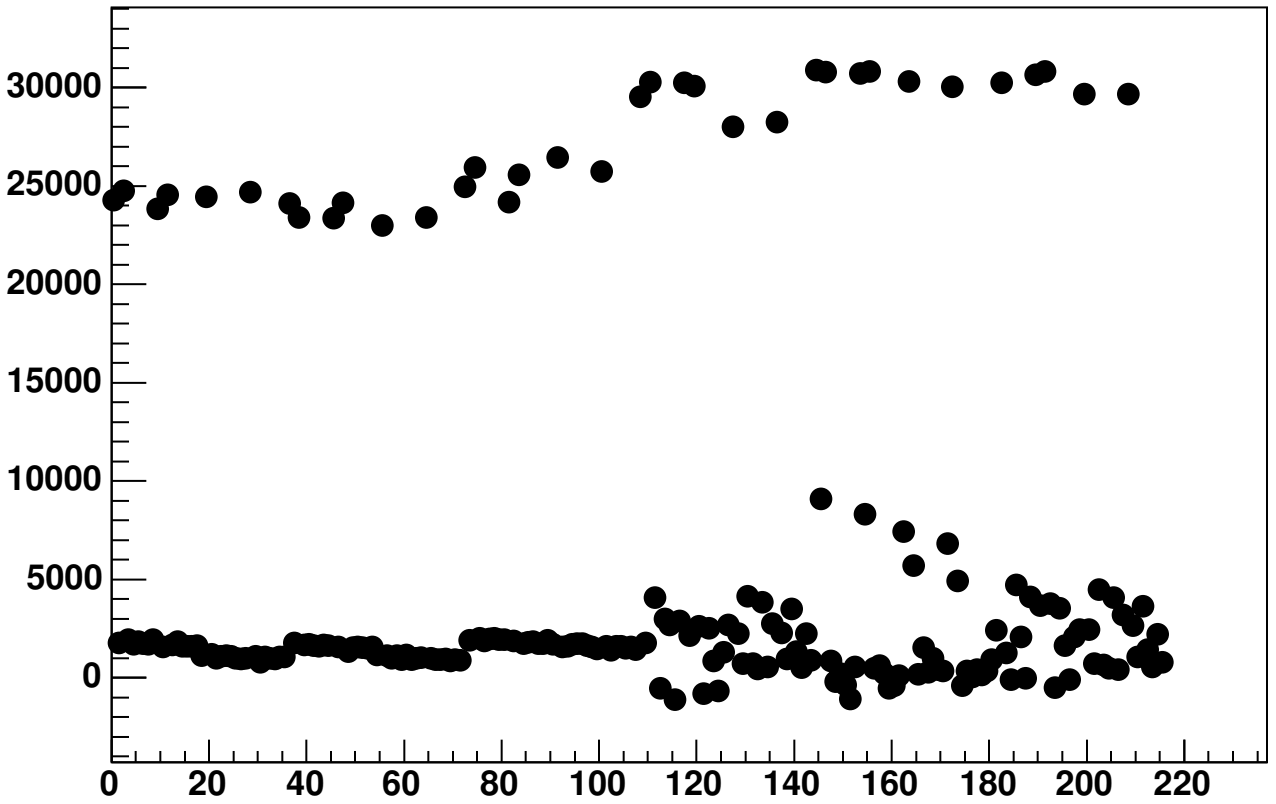
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



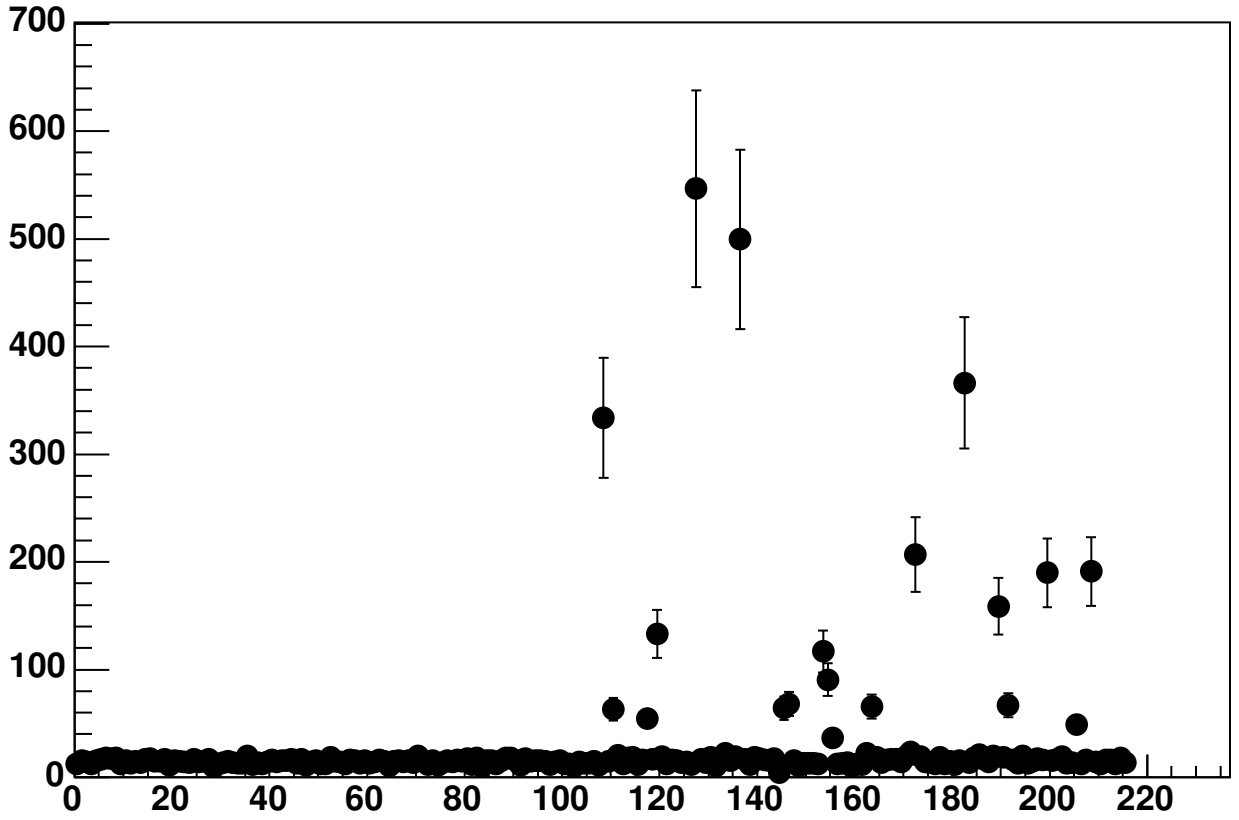
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



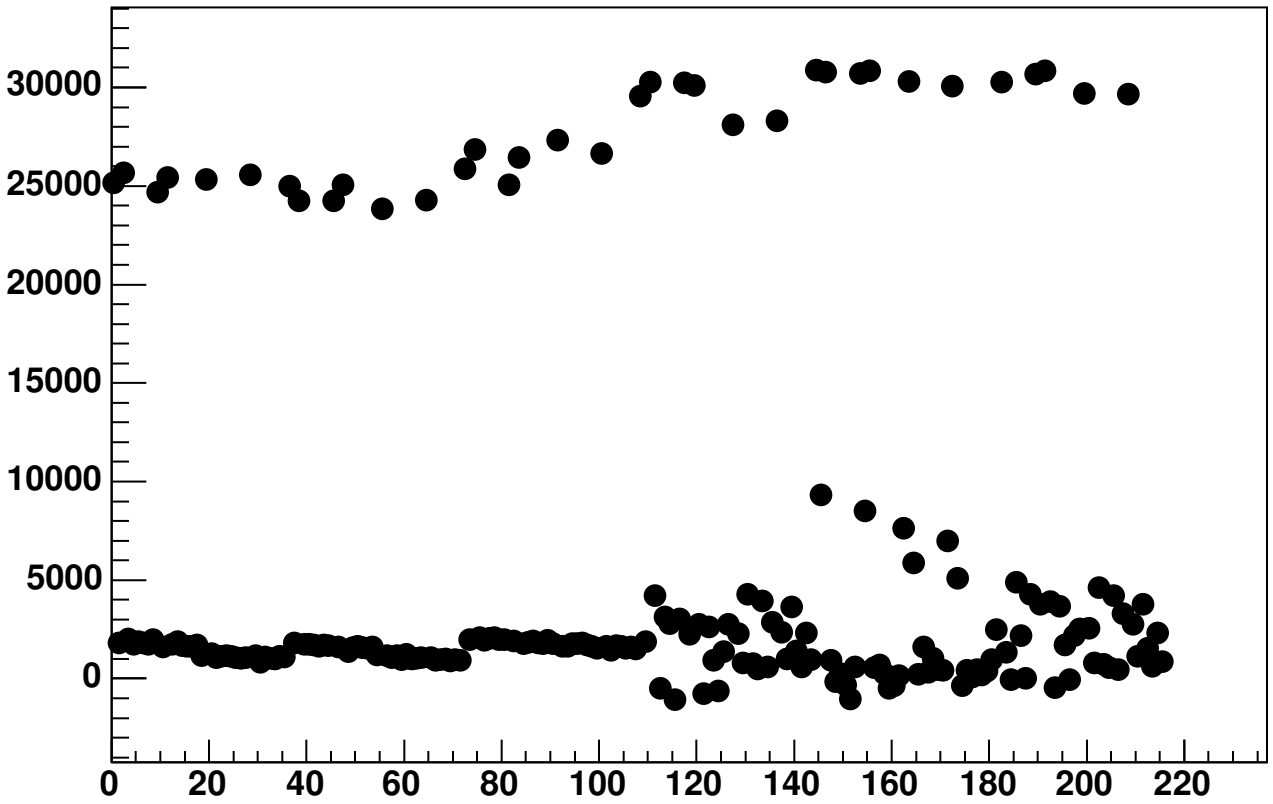
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



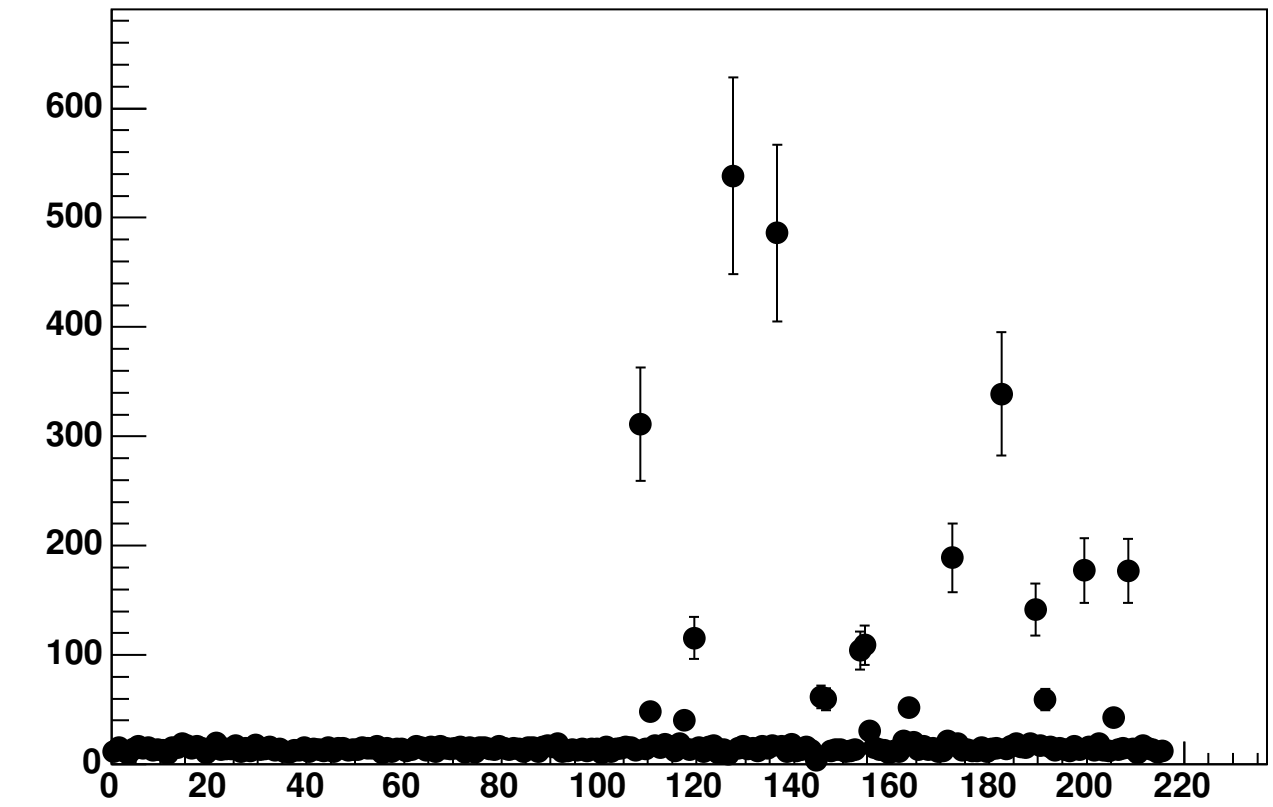
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



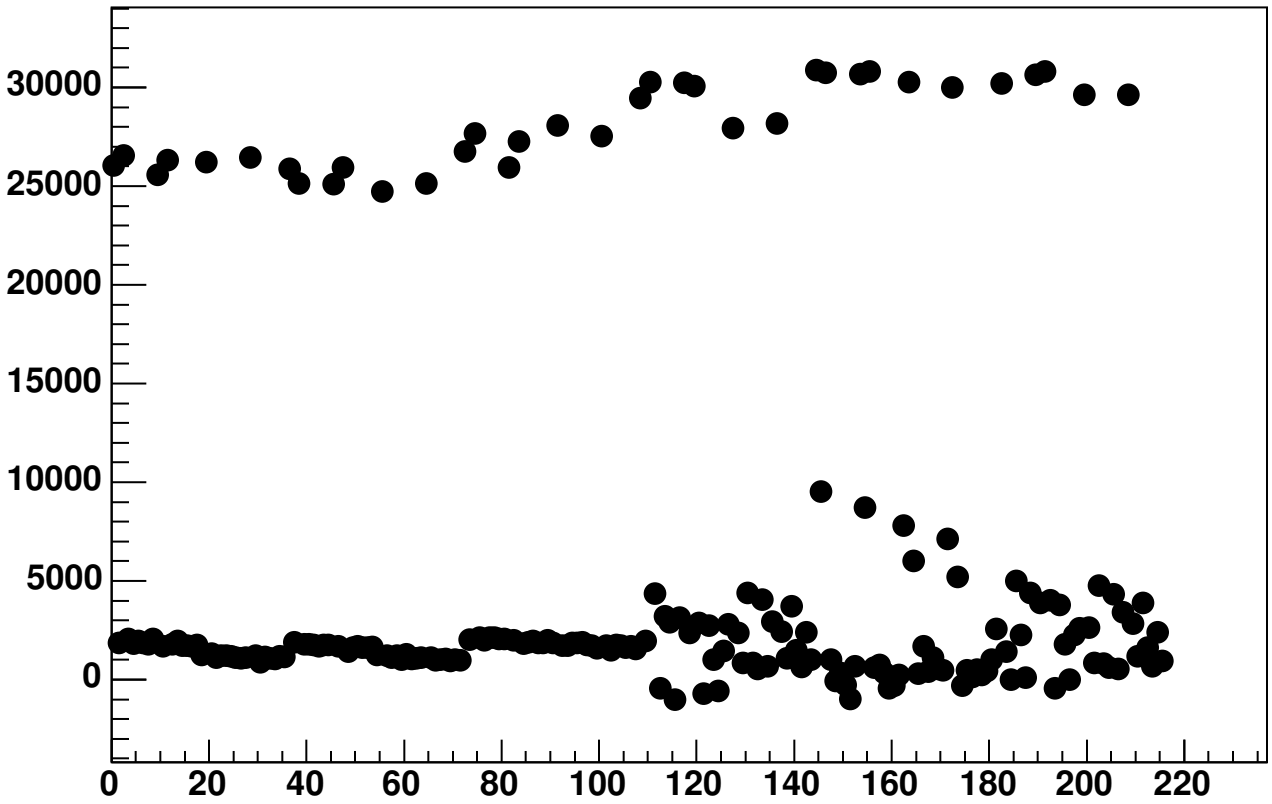
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



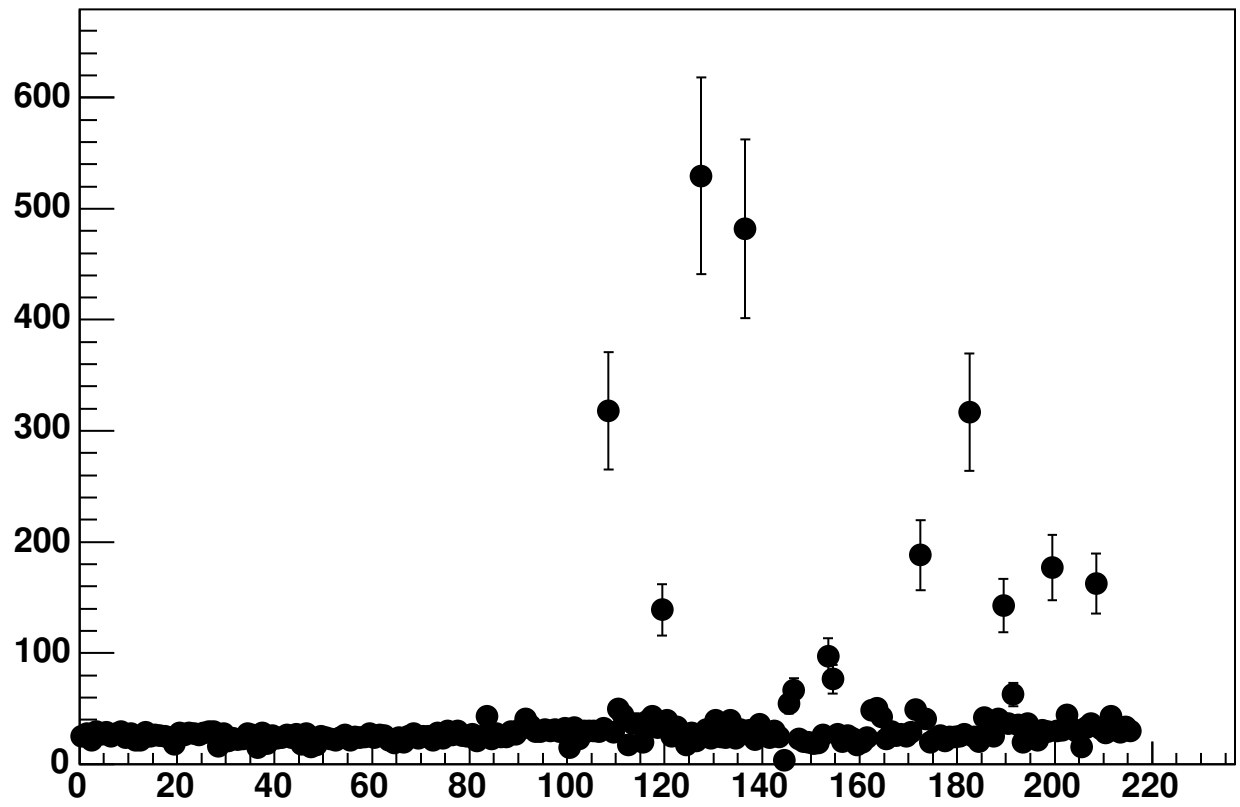
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



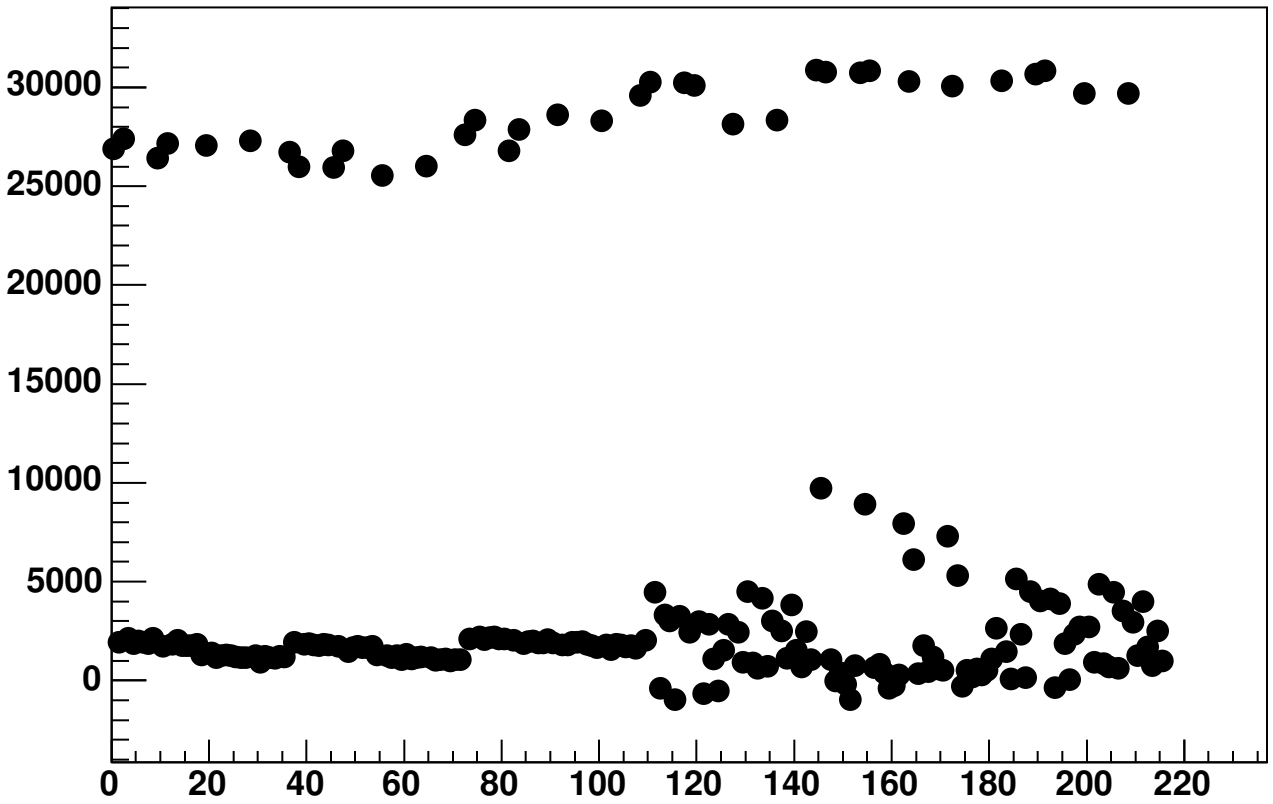
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



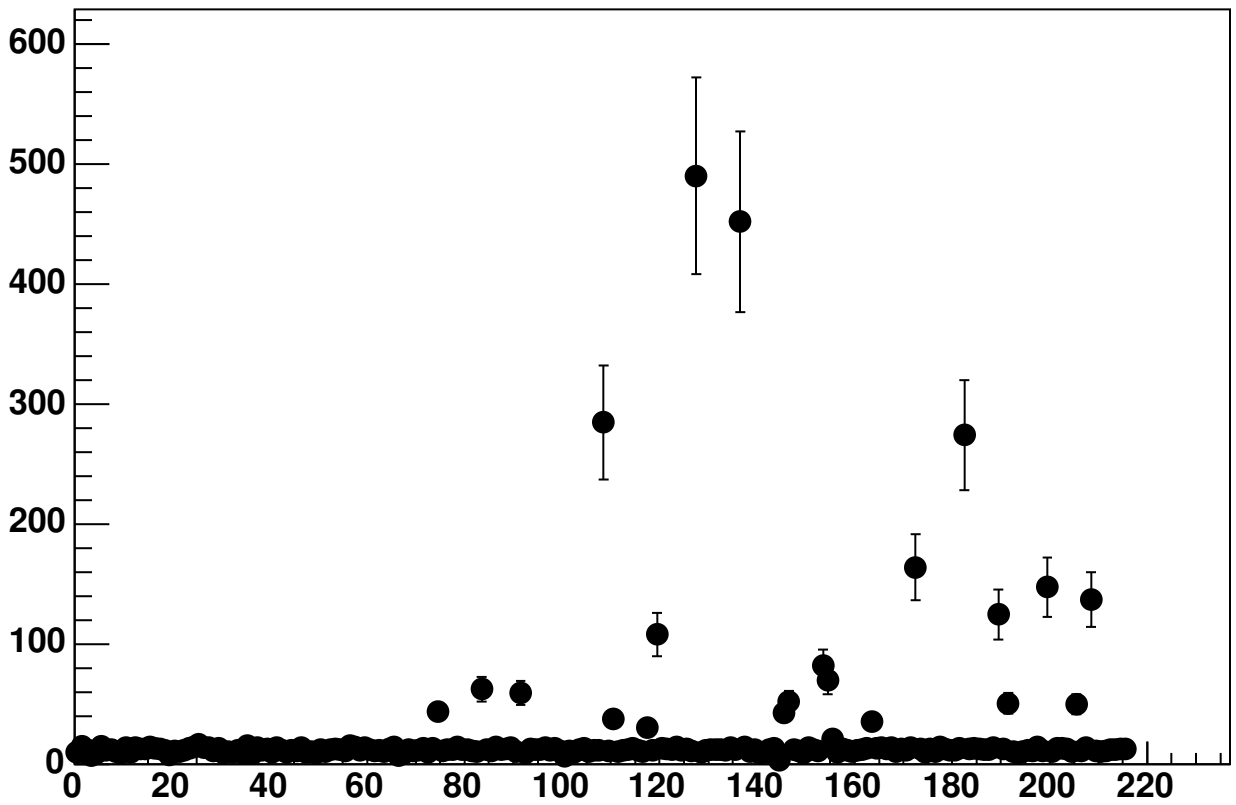
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



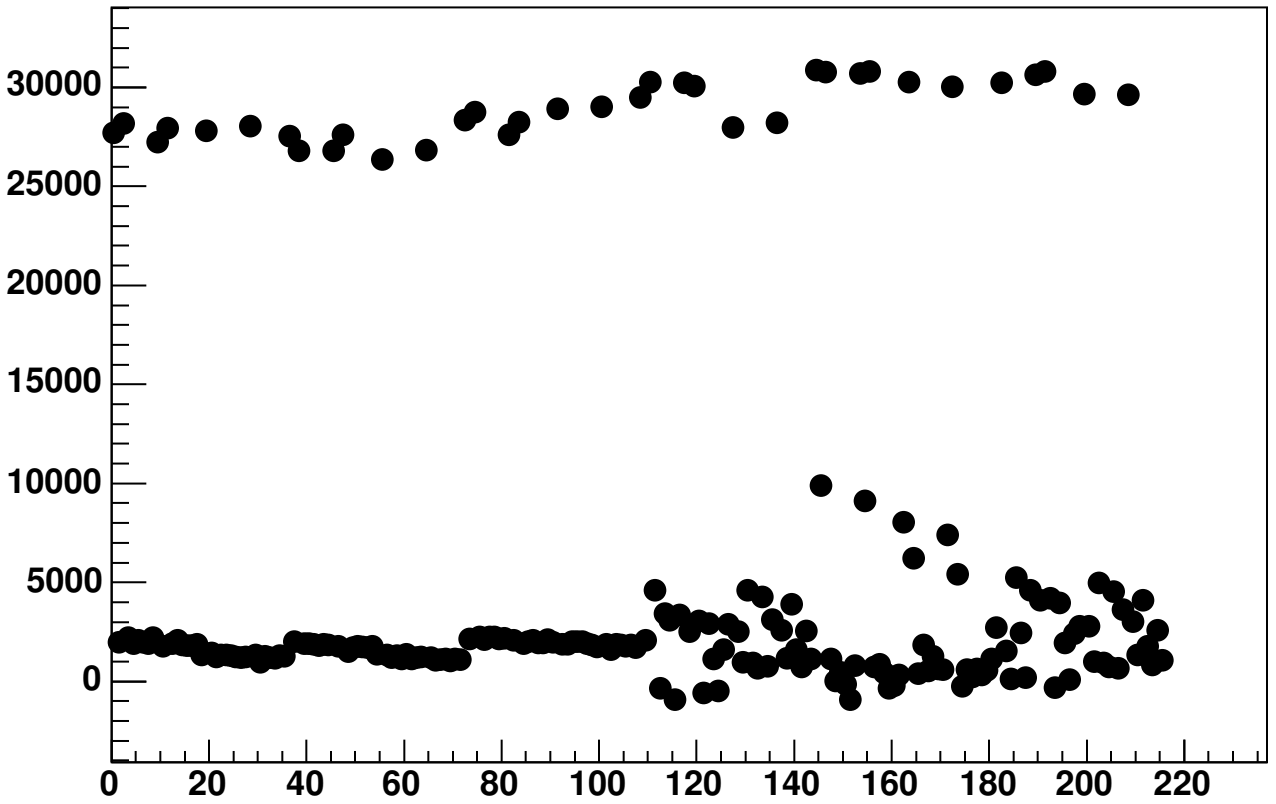
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



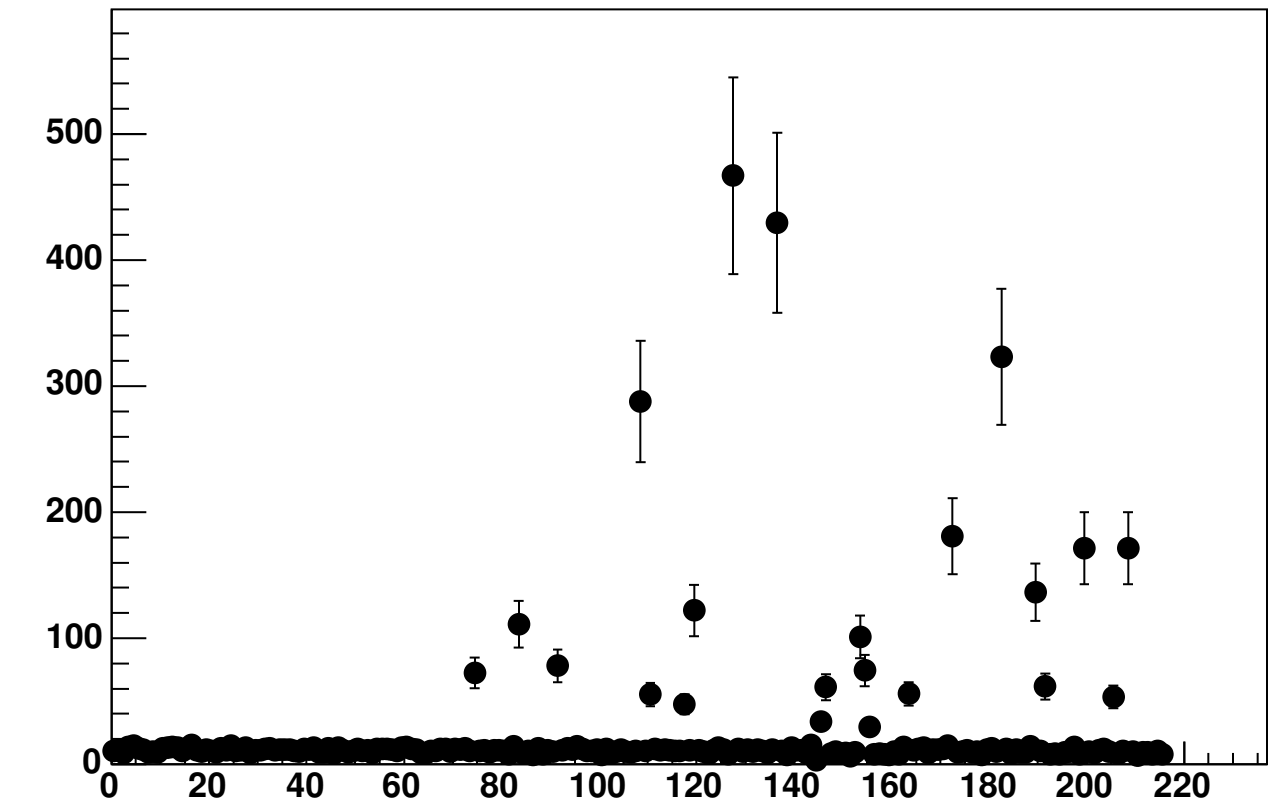
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



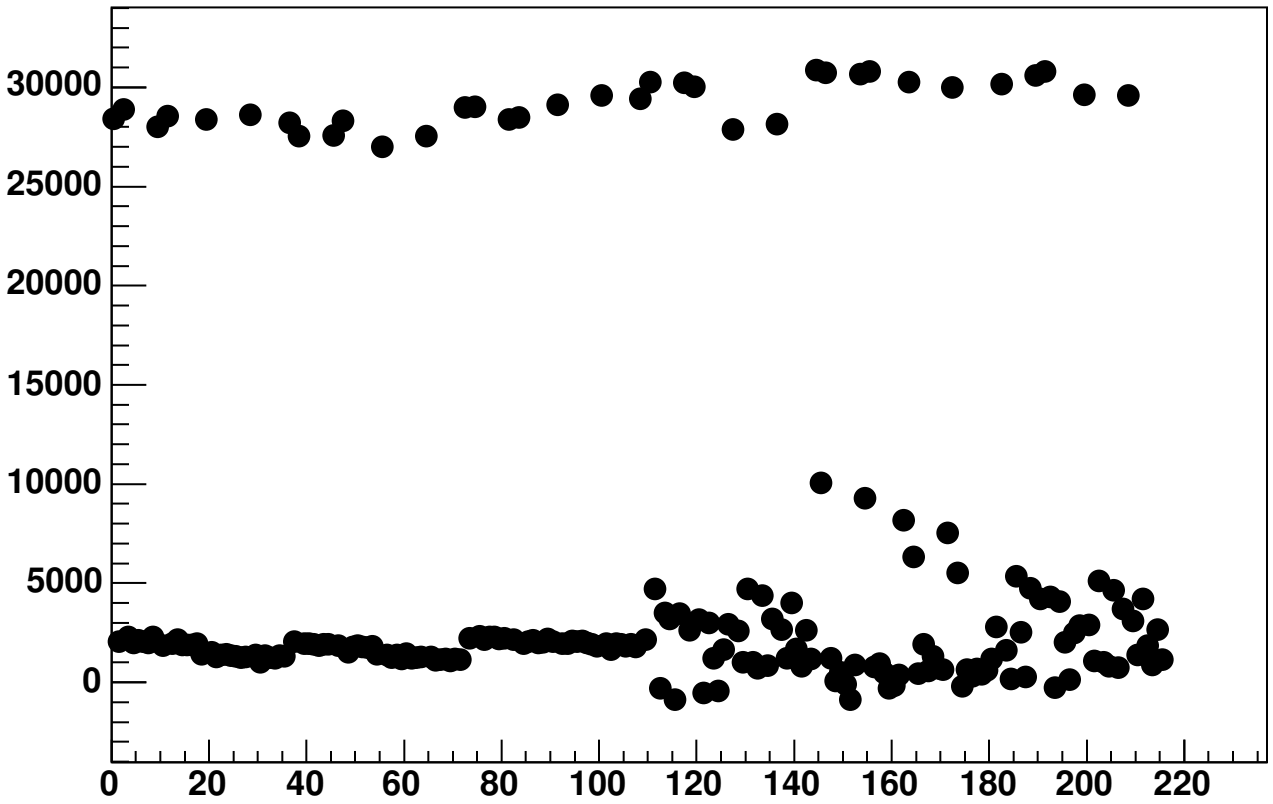
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



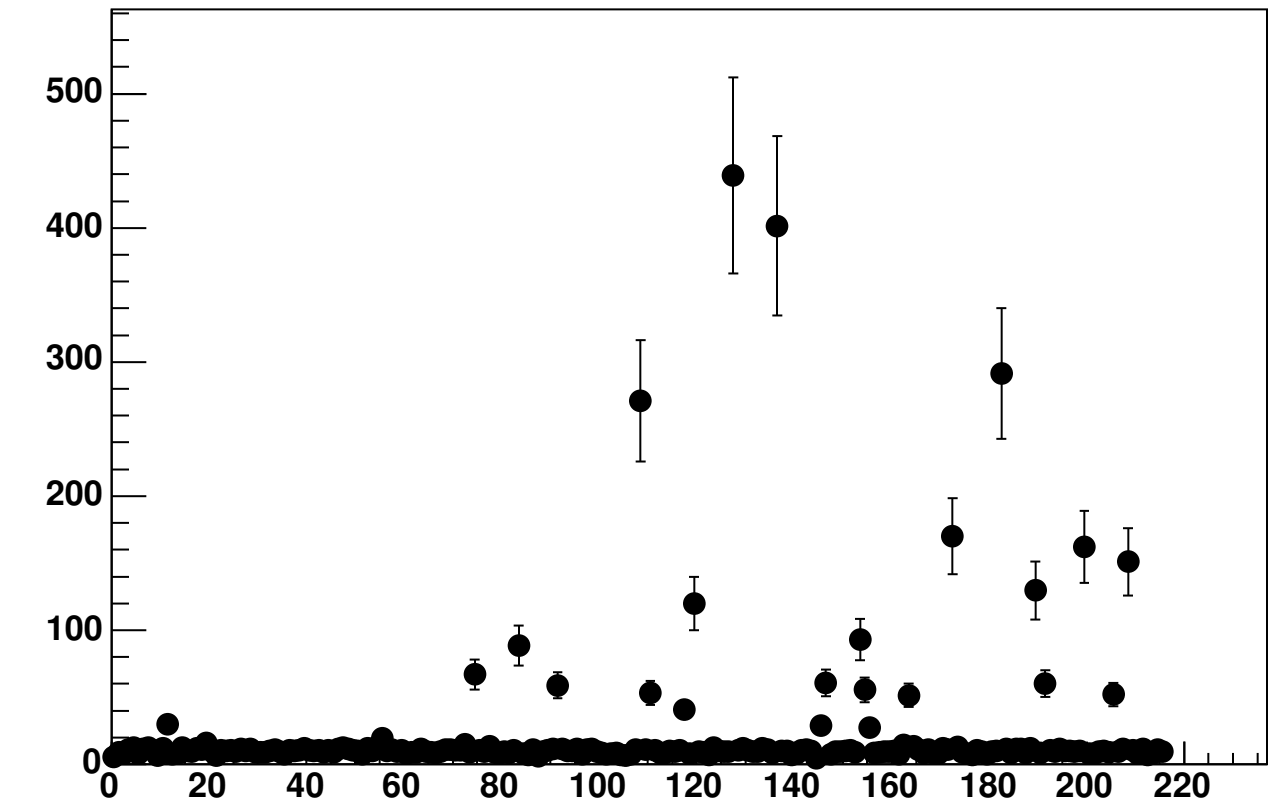
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



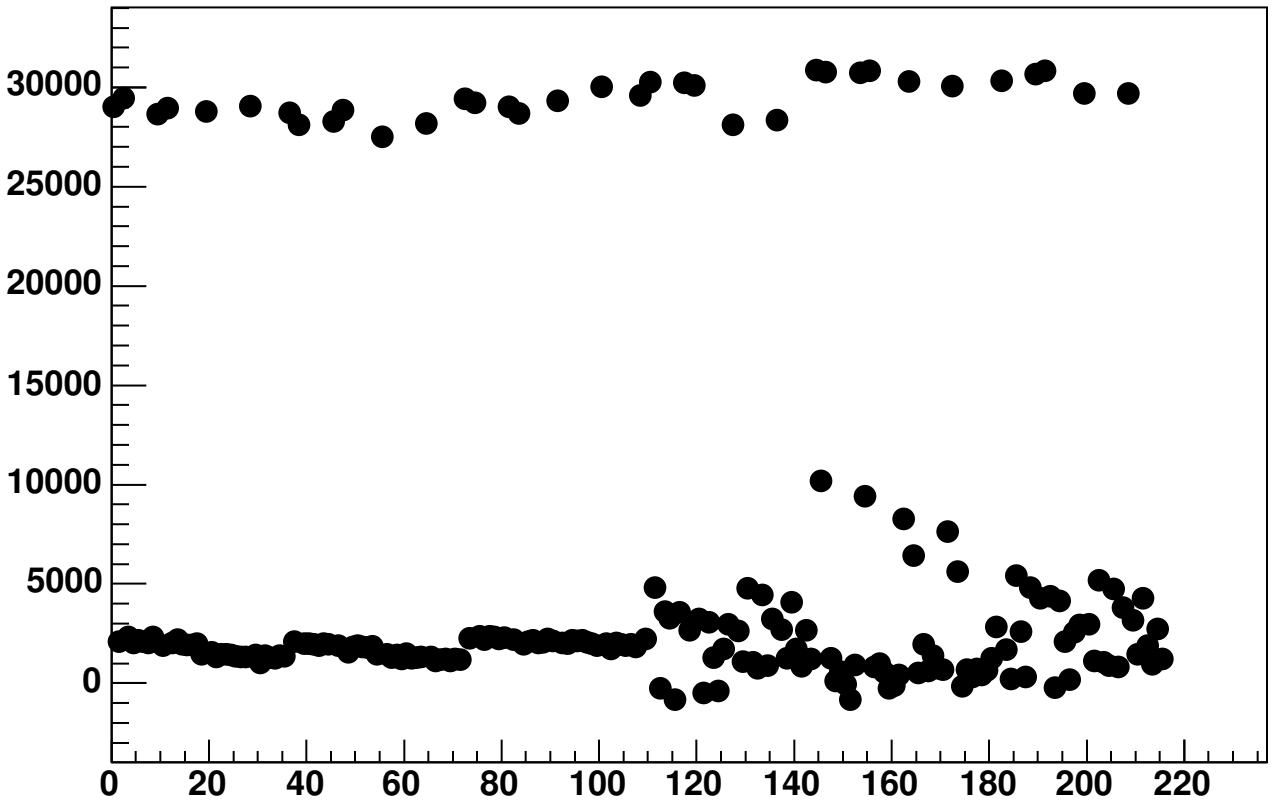
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



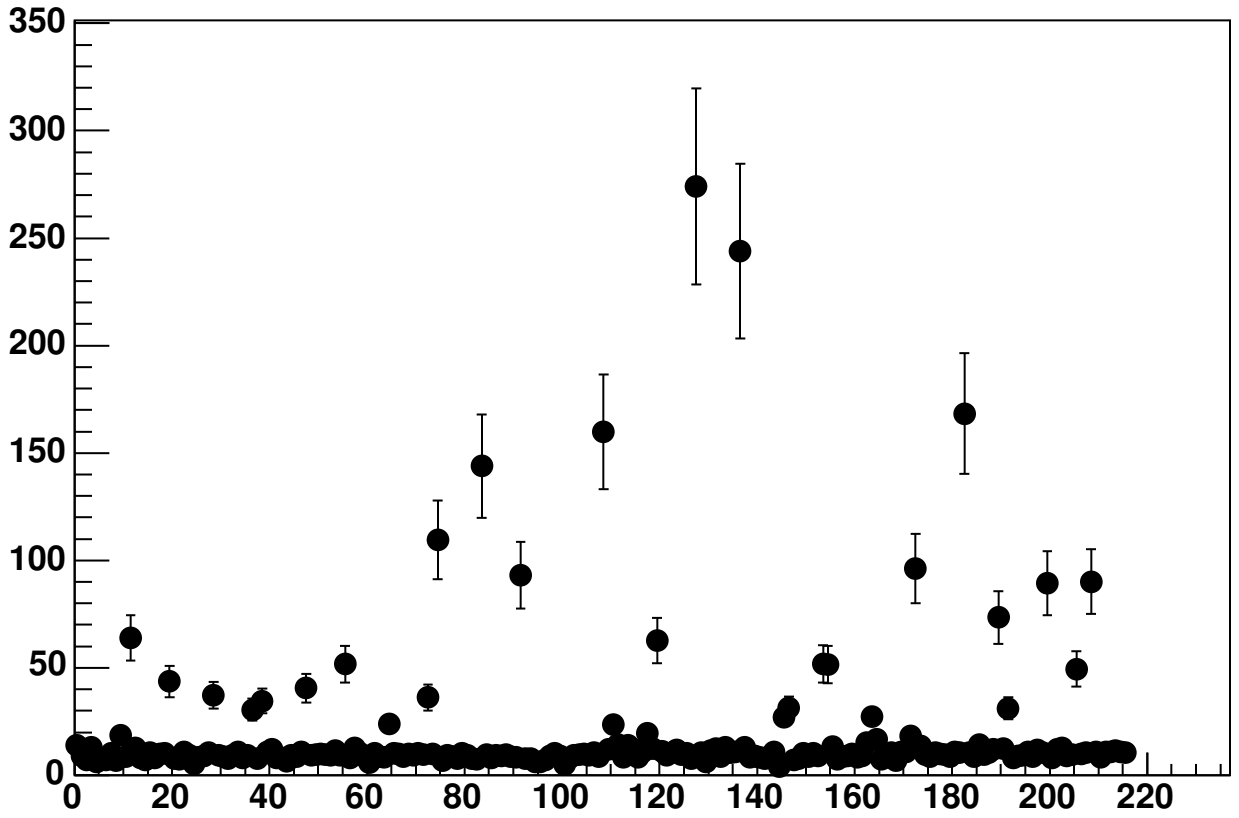
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



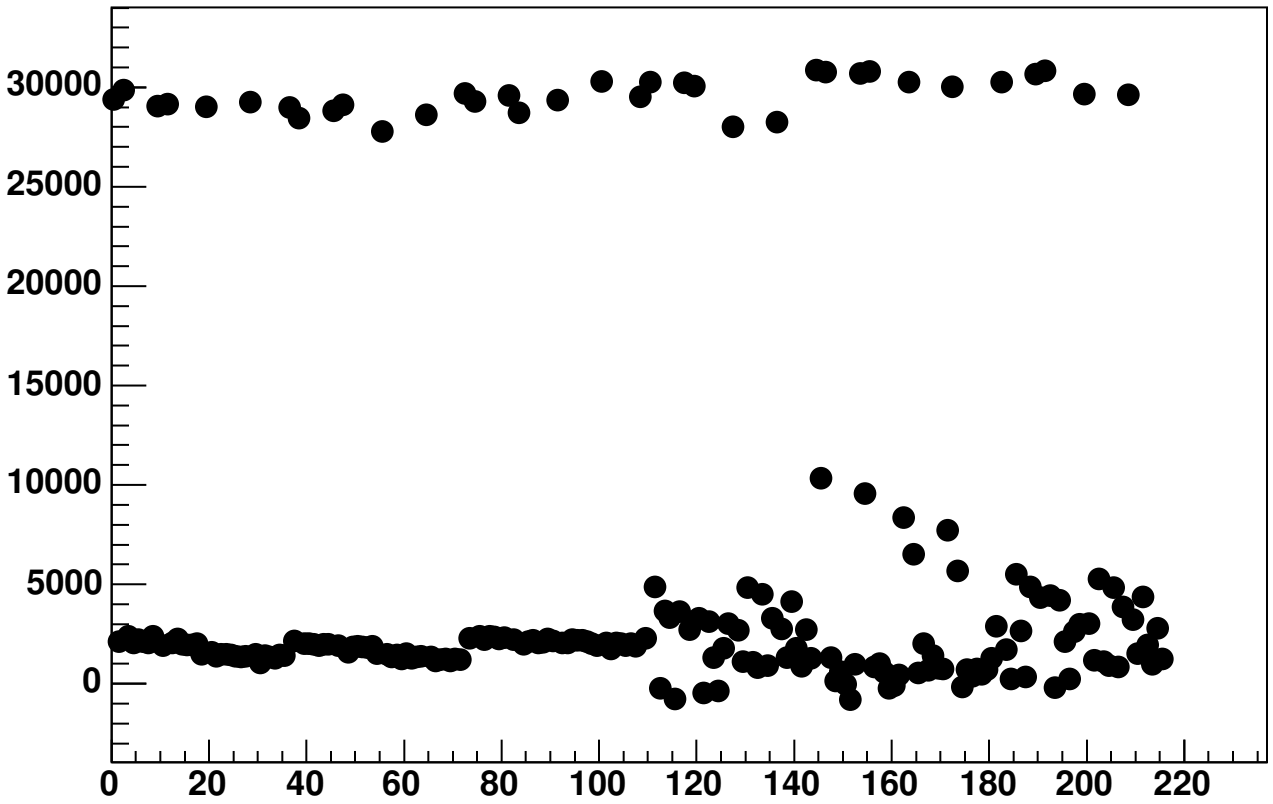
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



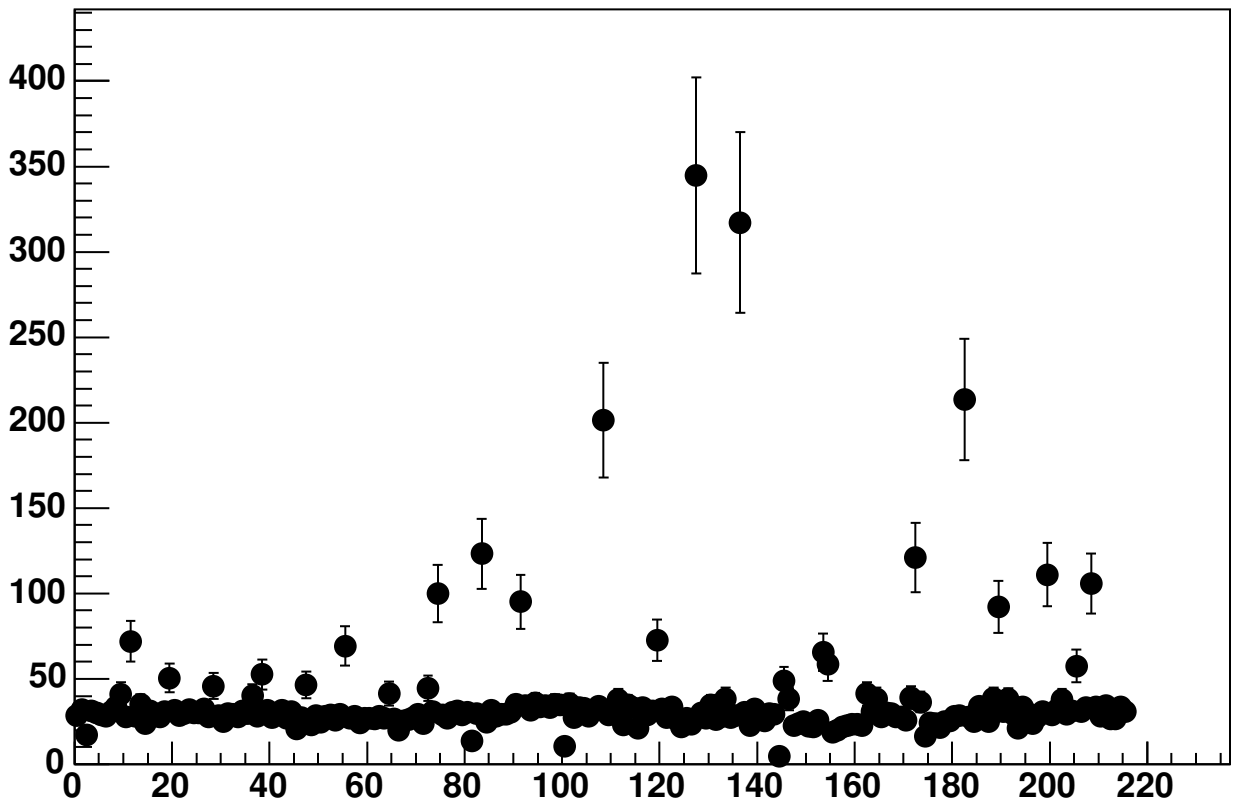
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



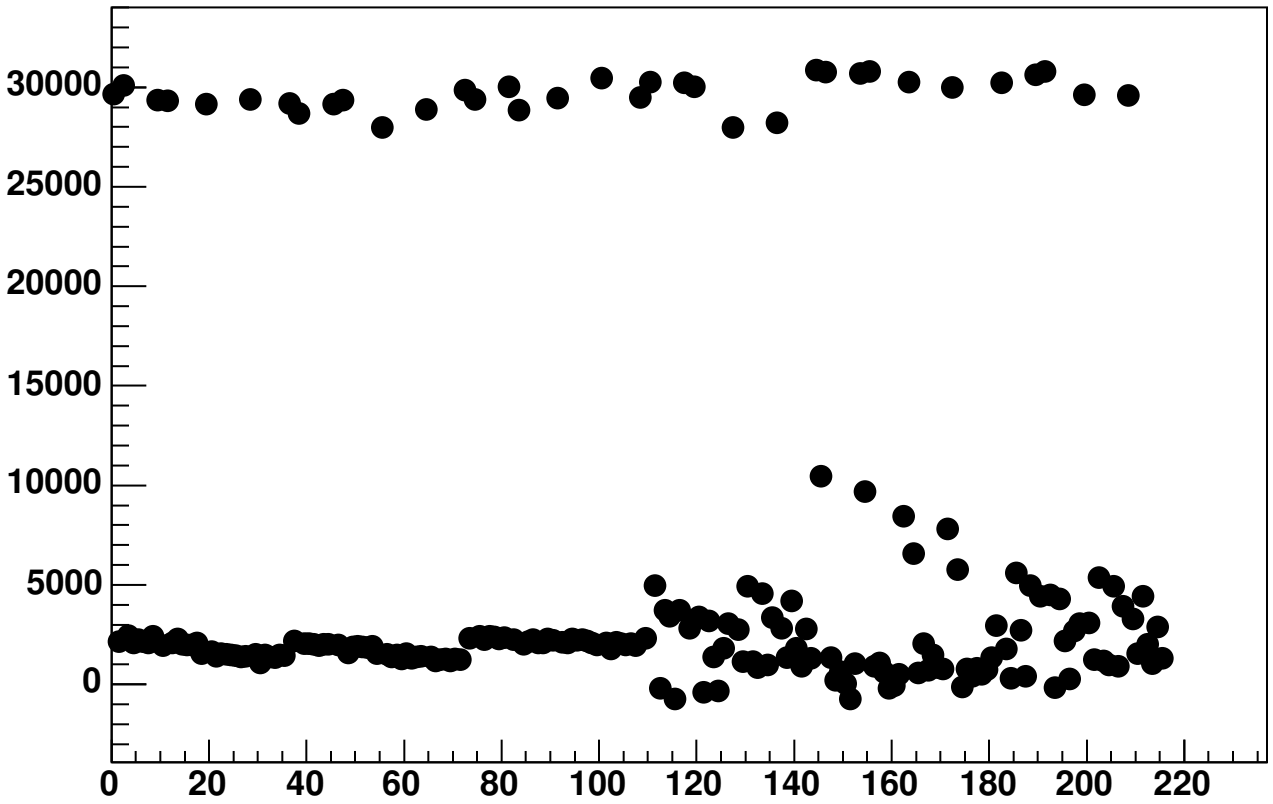
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



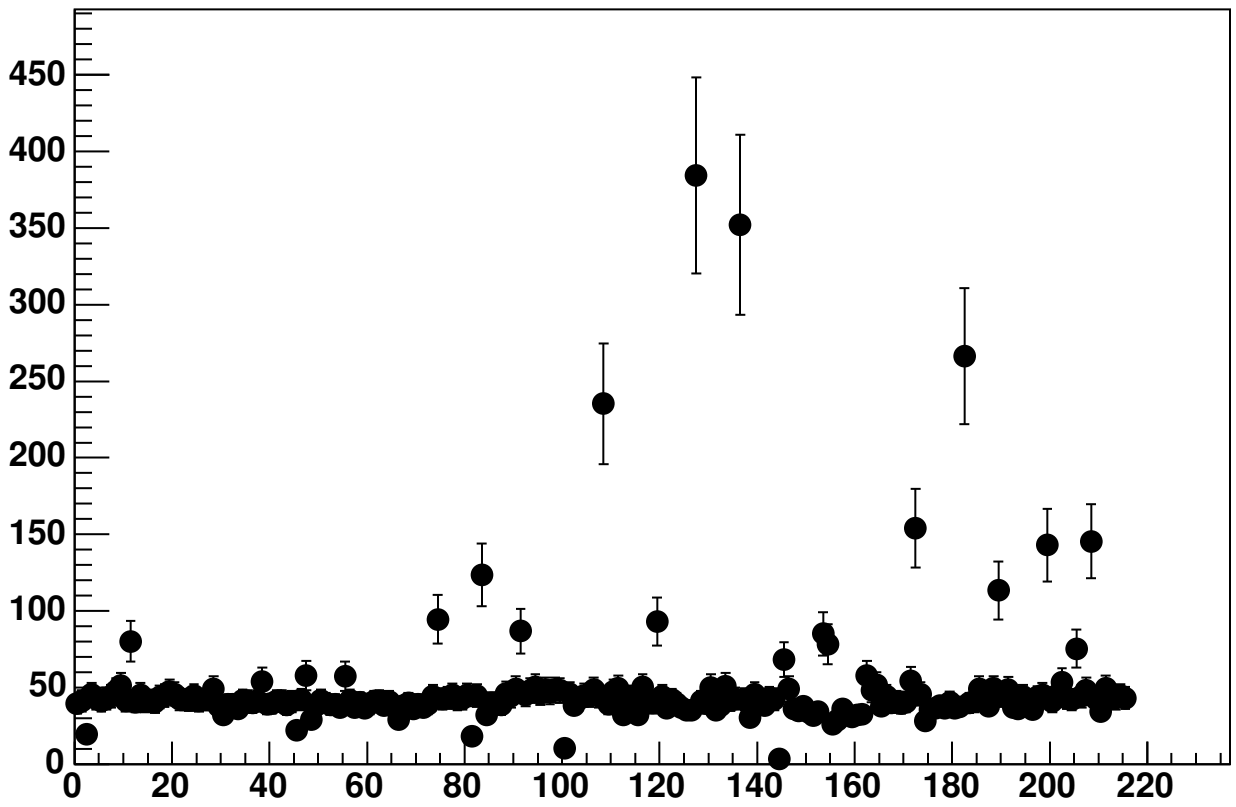
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



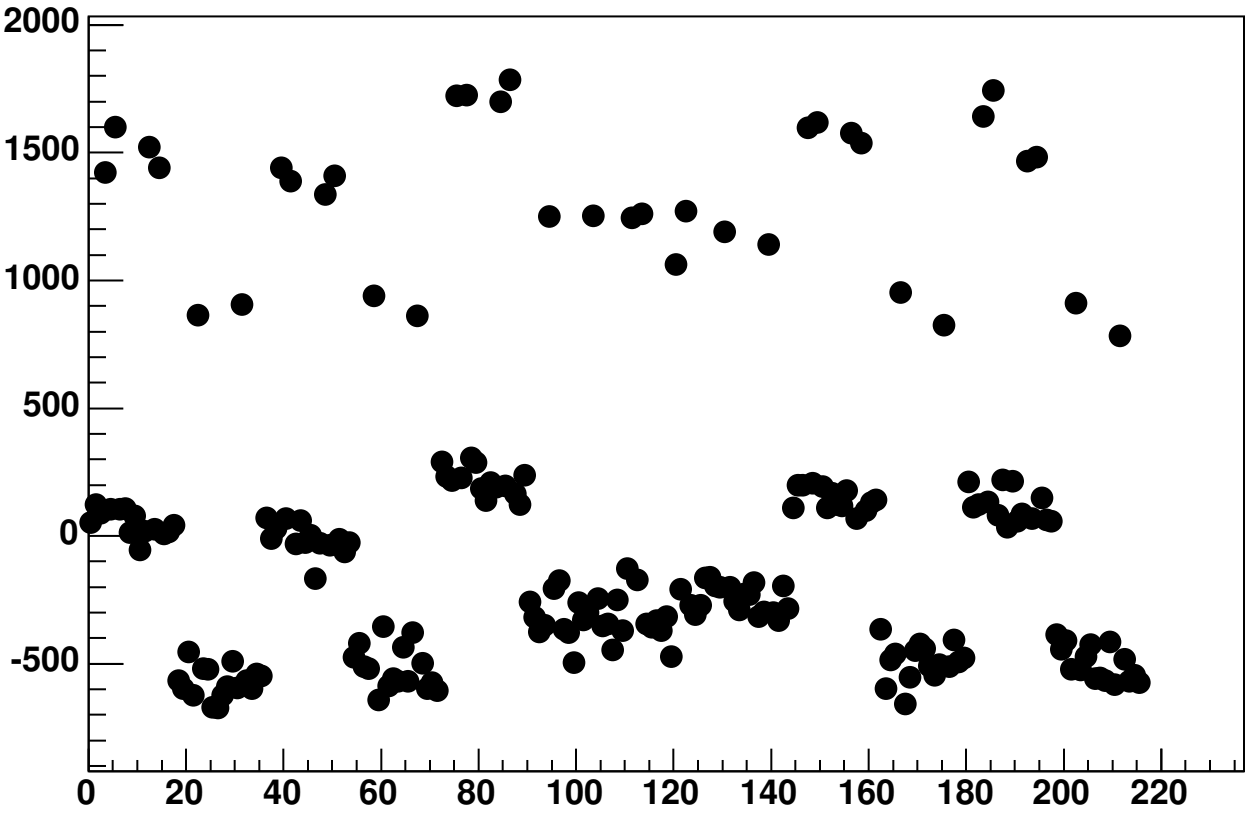
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



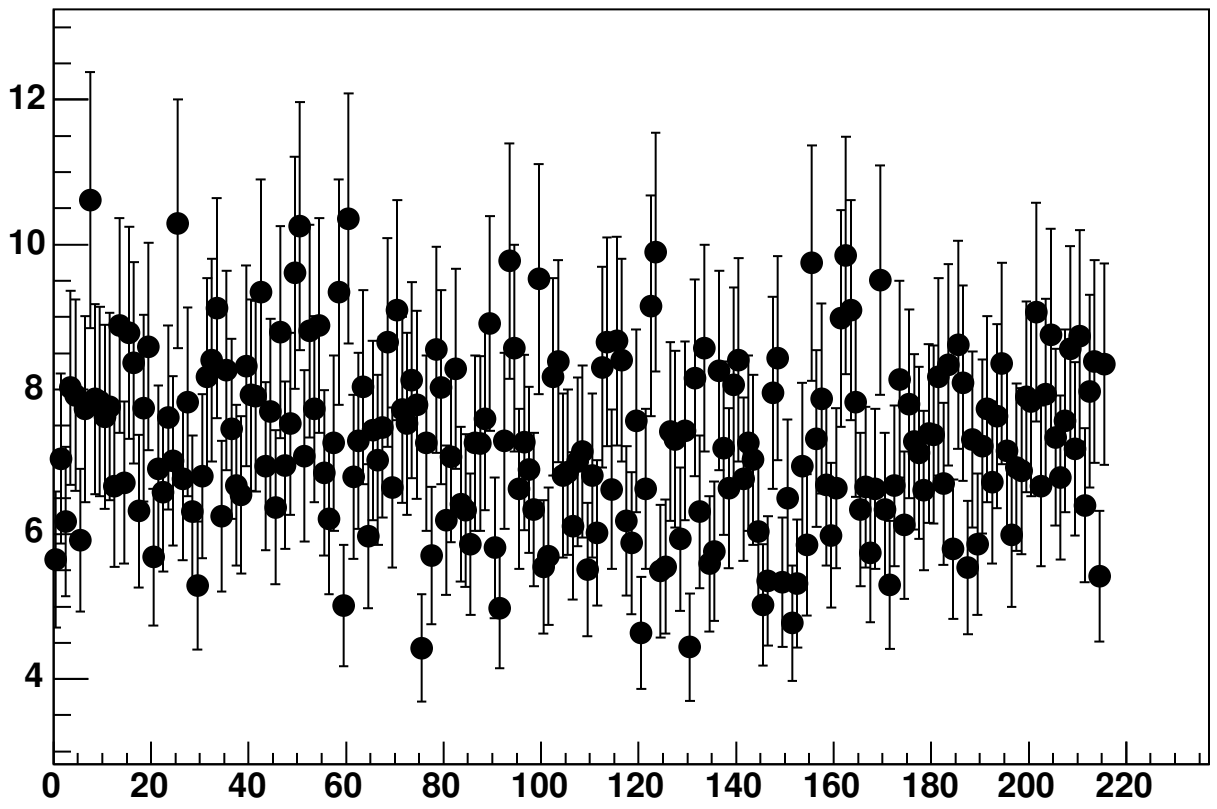
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



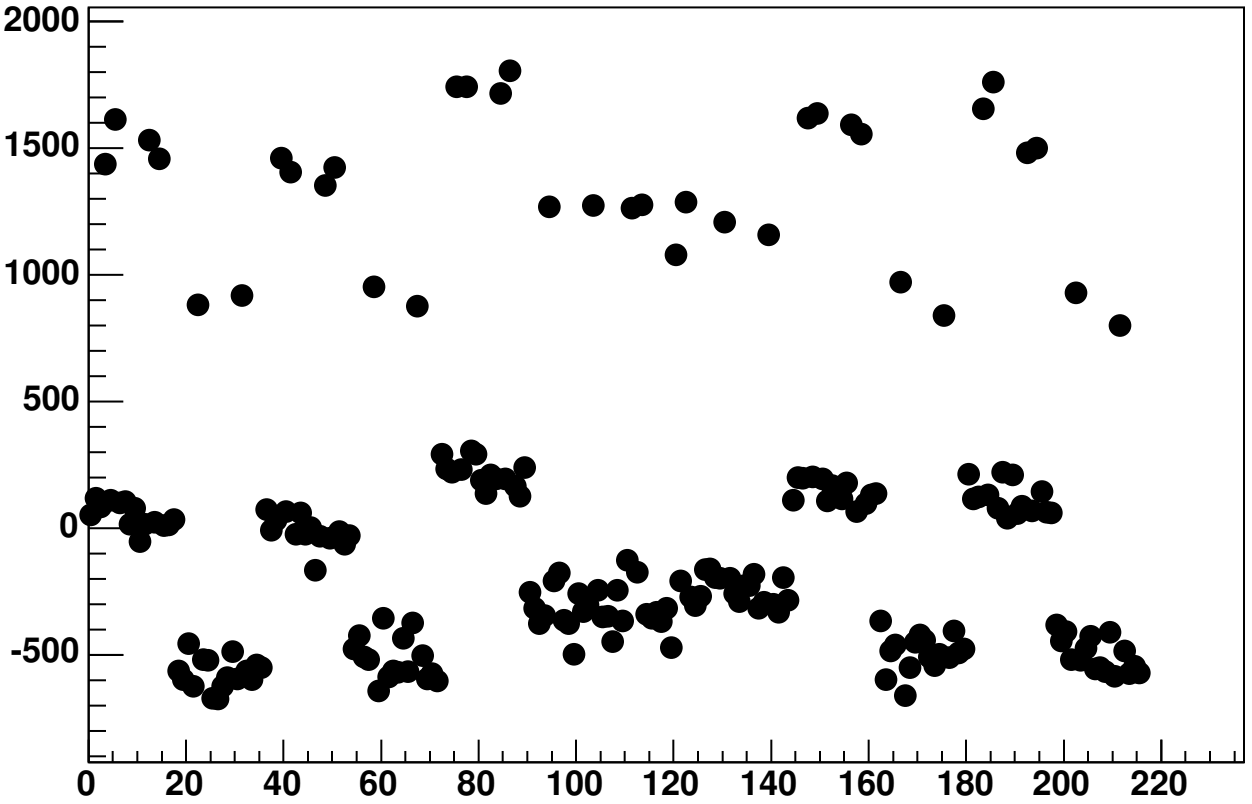
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



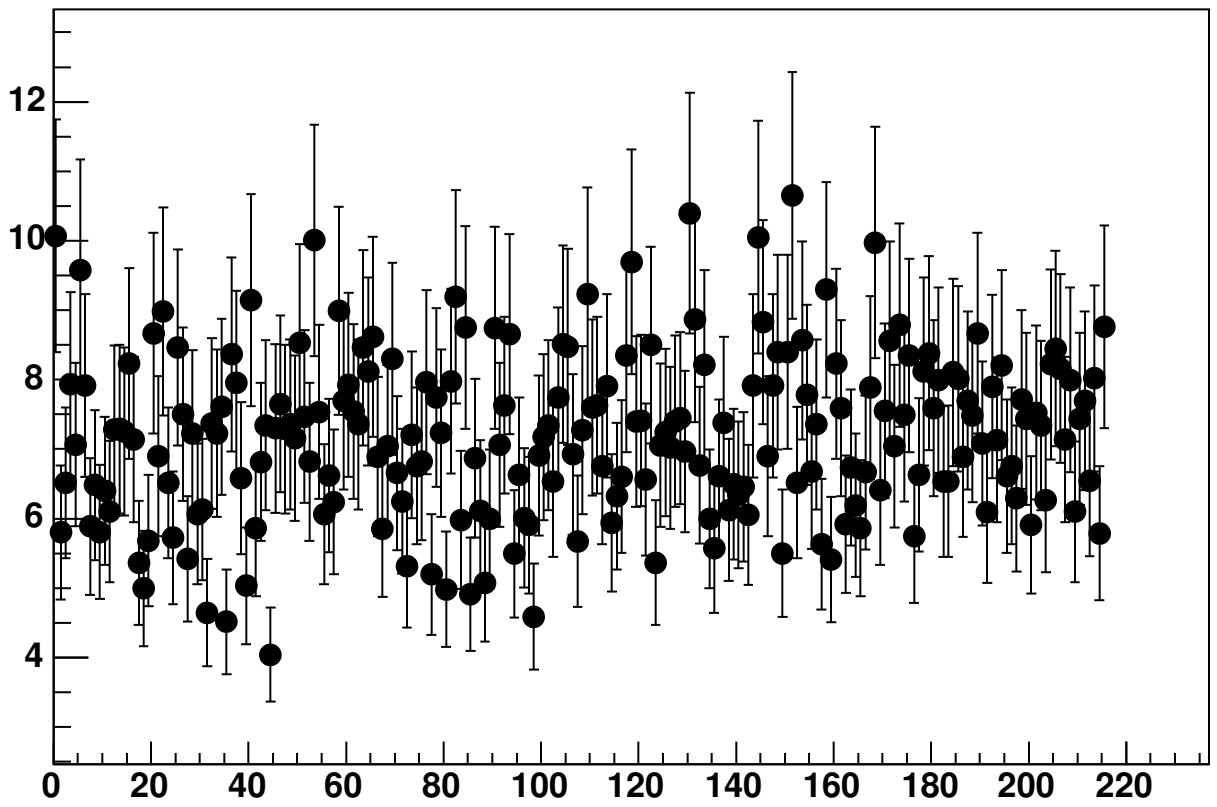
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



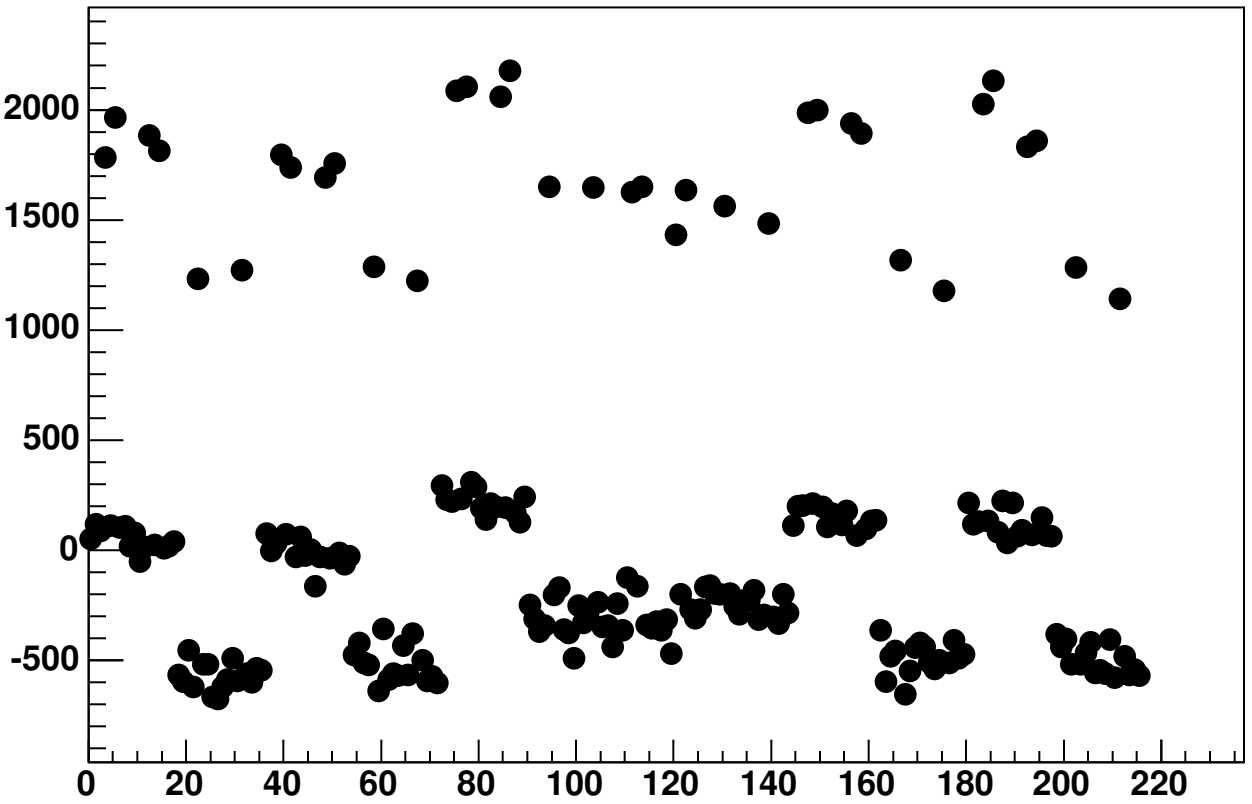
Enable 1, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



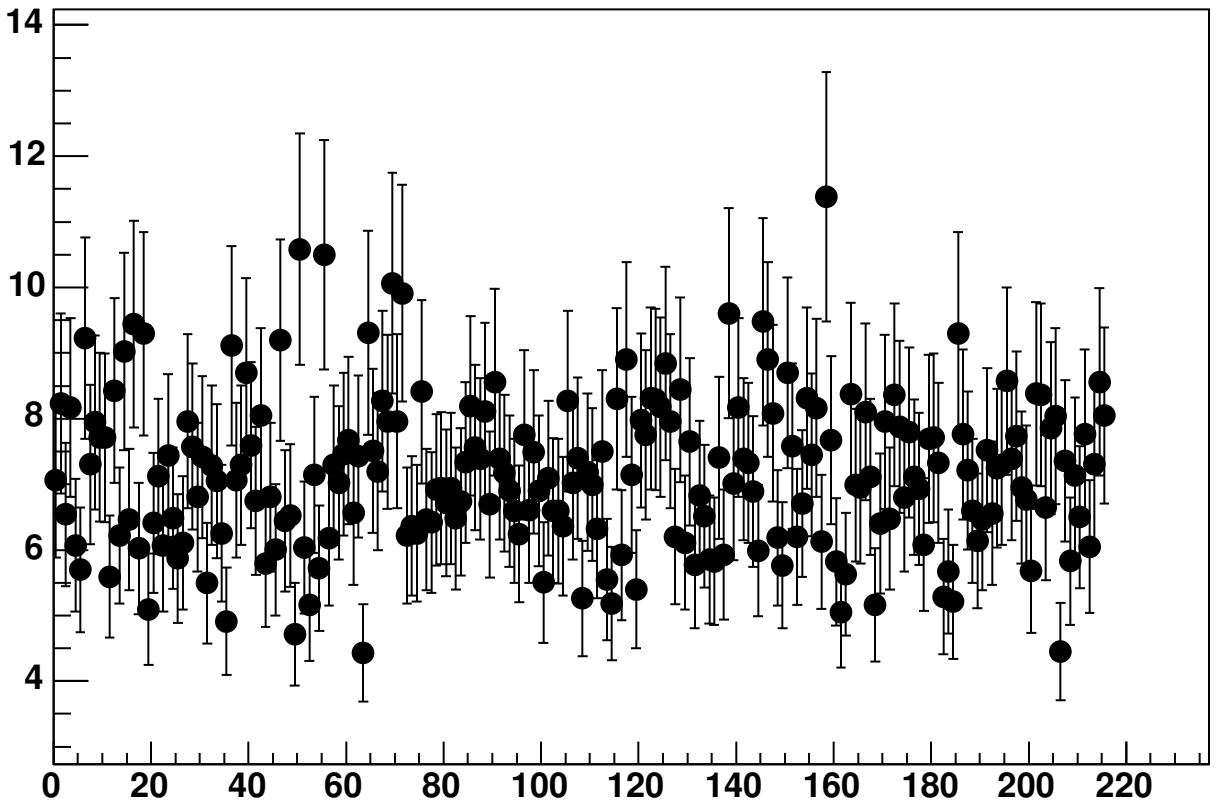
Enable 1, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



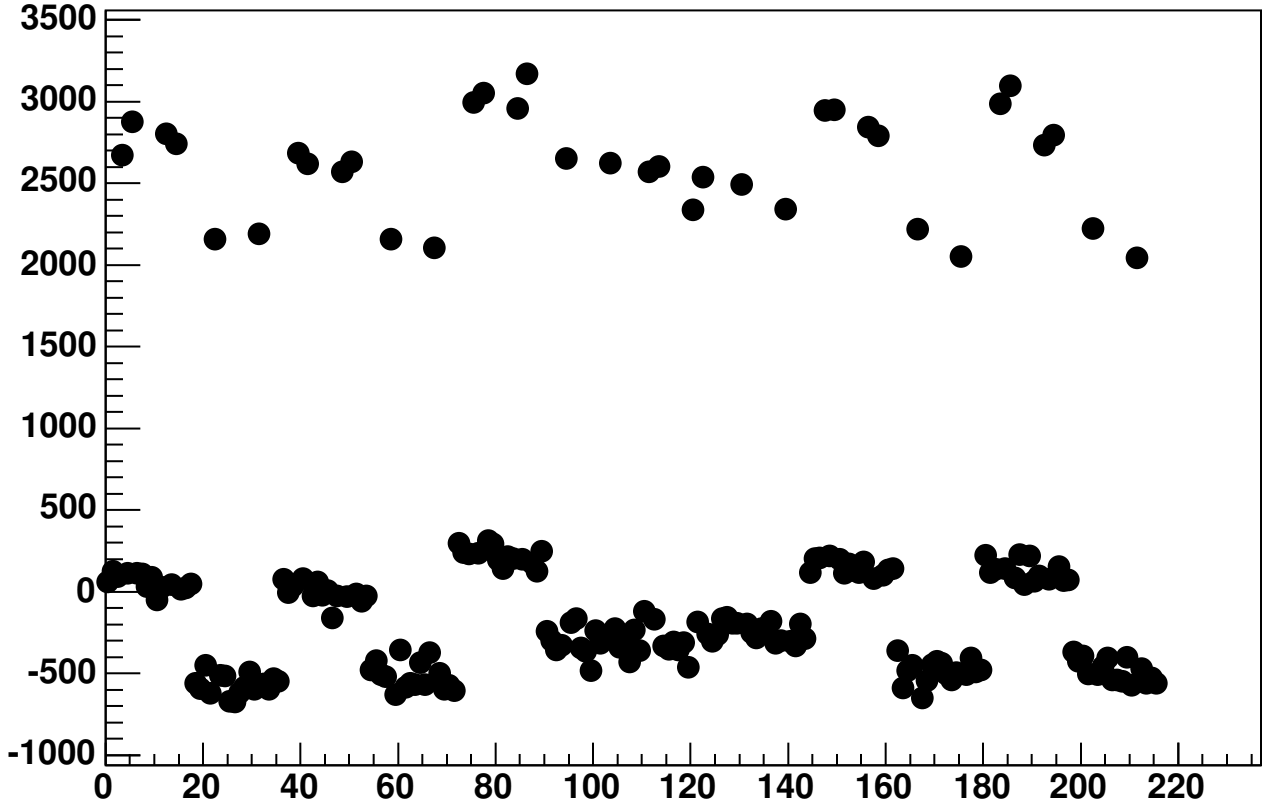
Enable 1, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



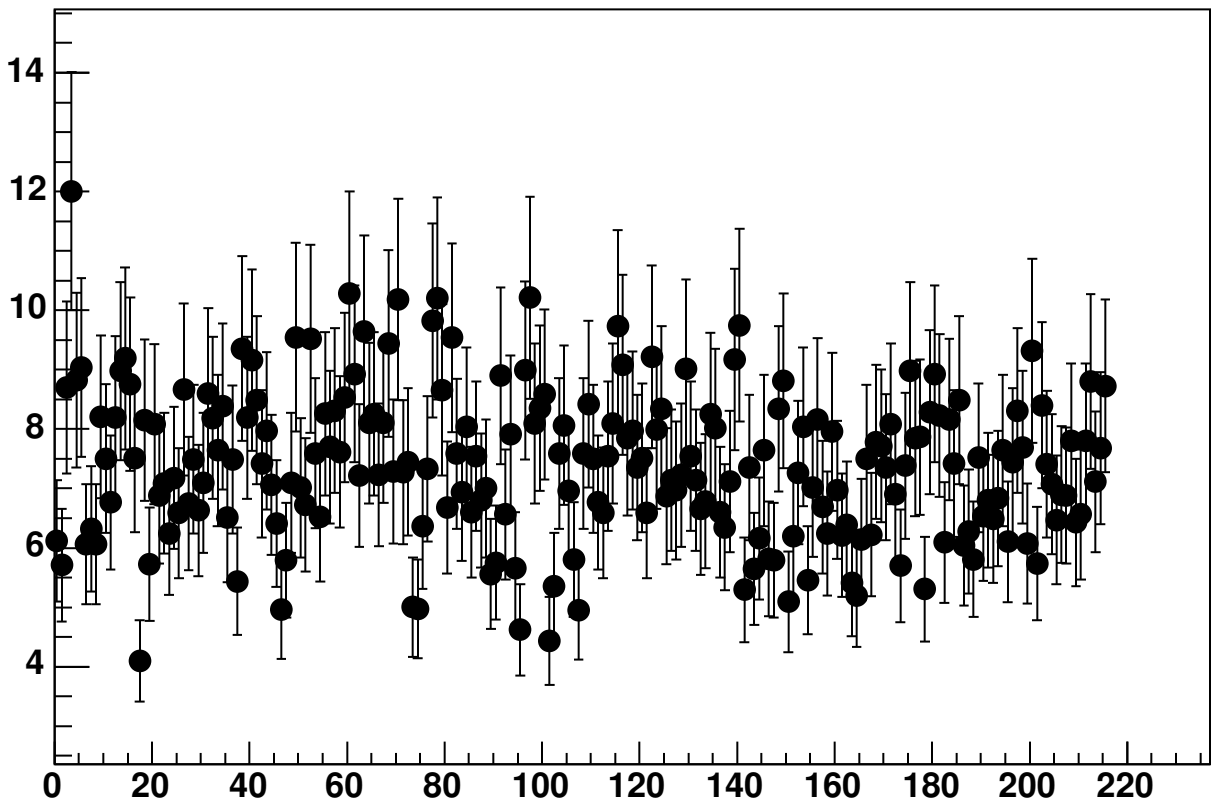
Enable 1, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



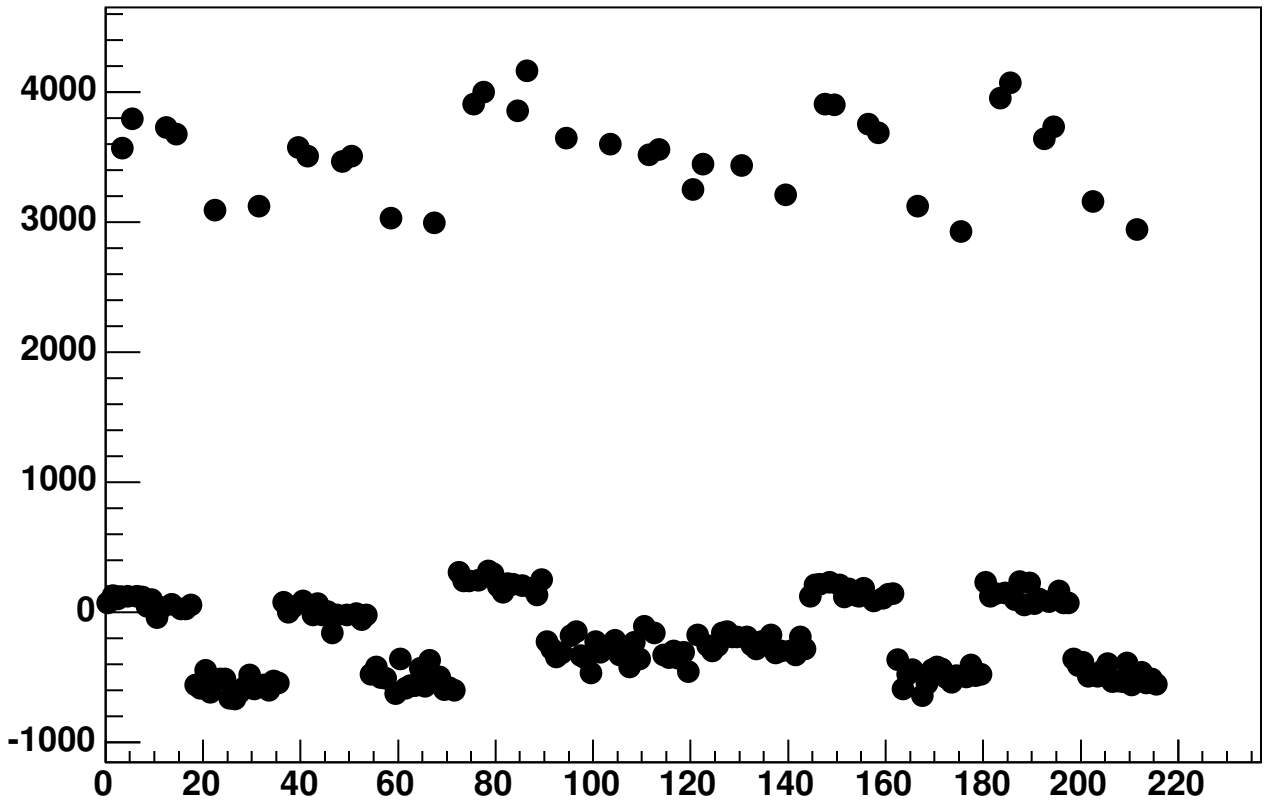
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



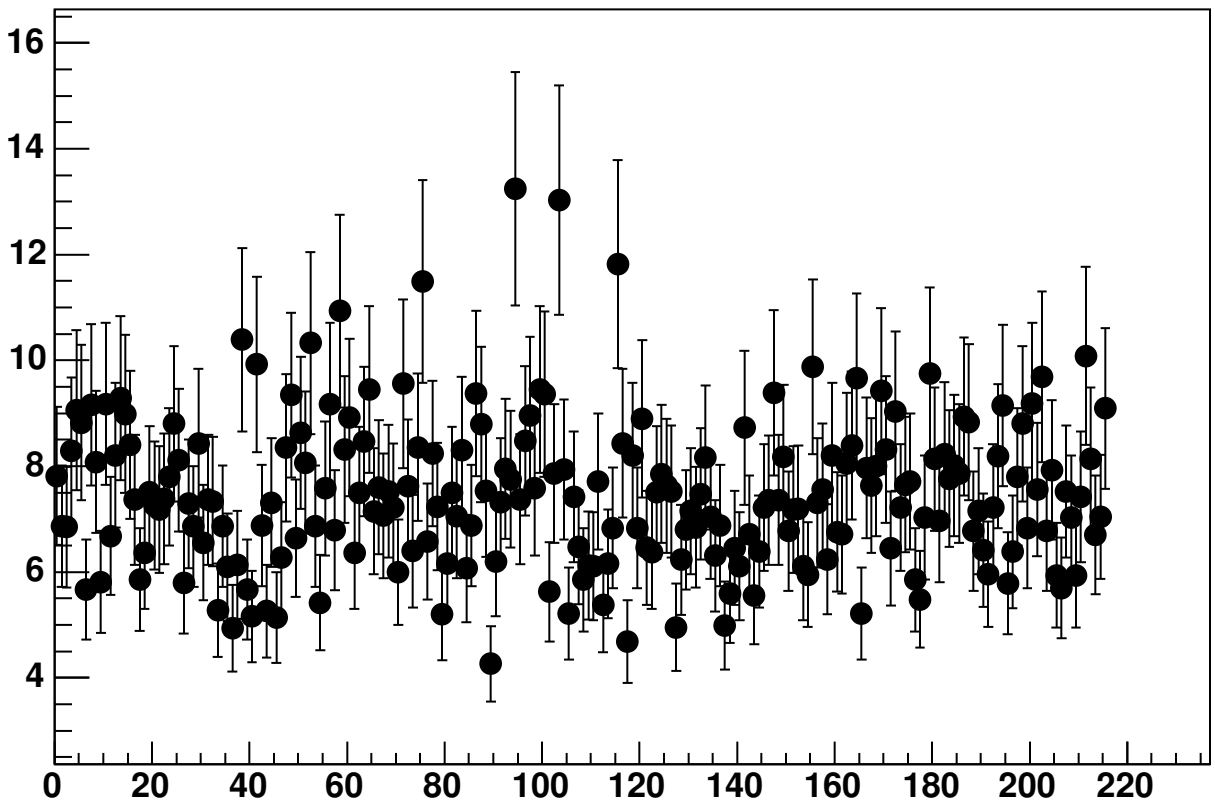
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



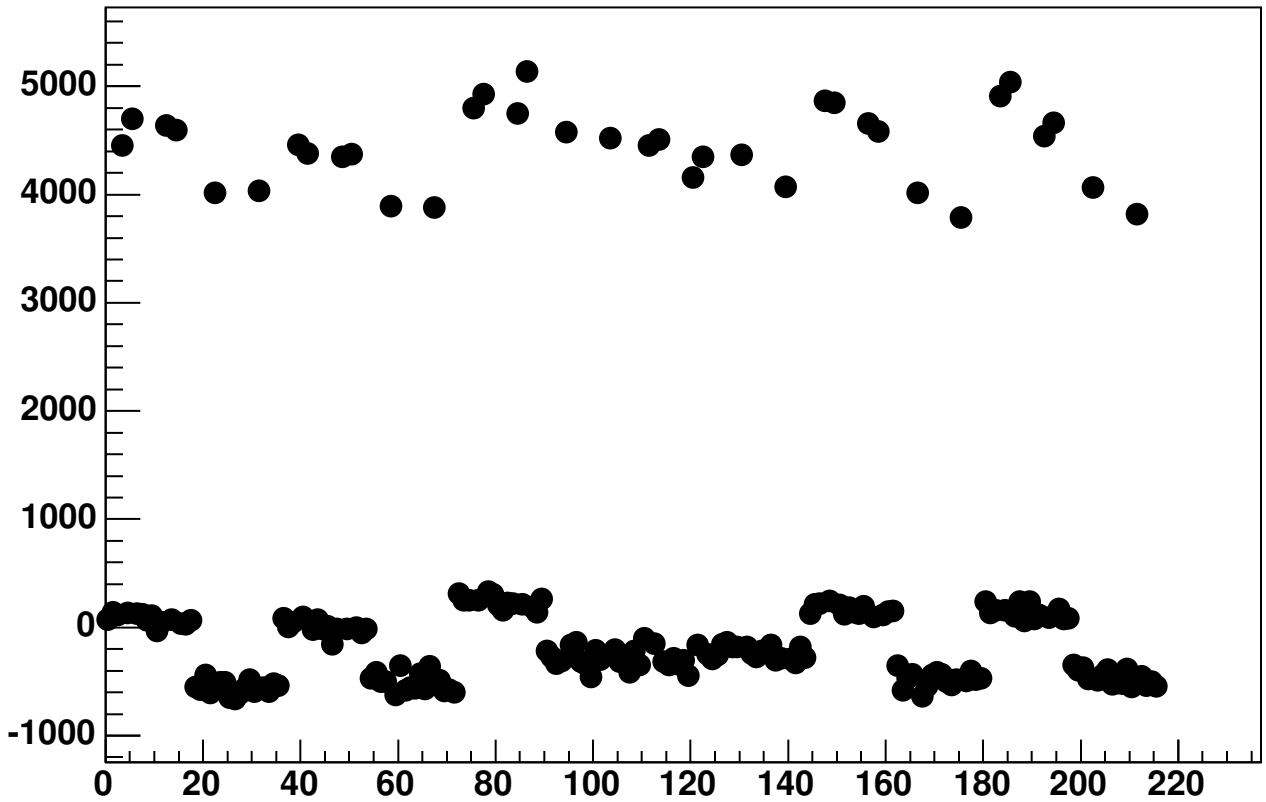
Enable 1, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



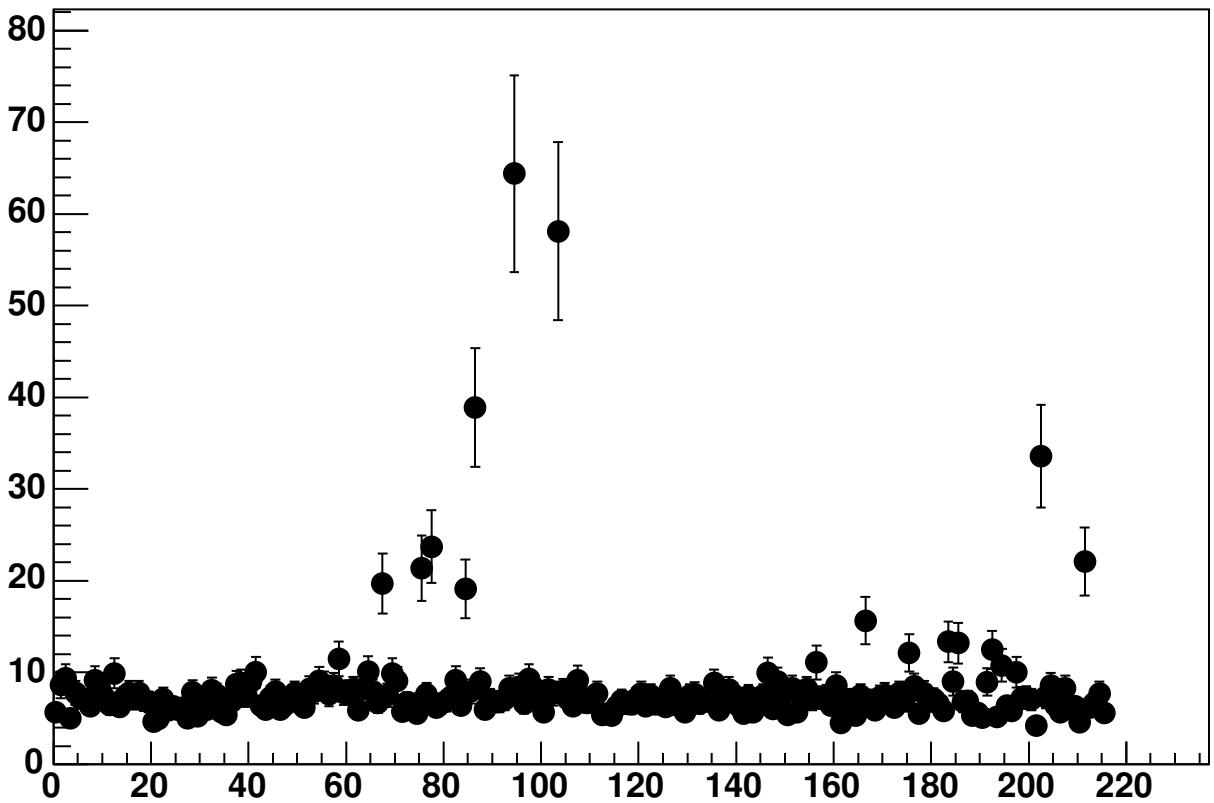
Enable 1, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



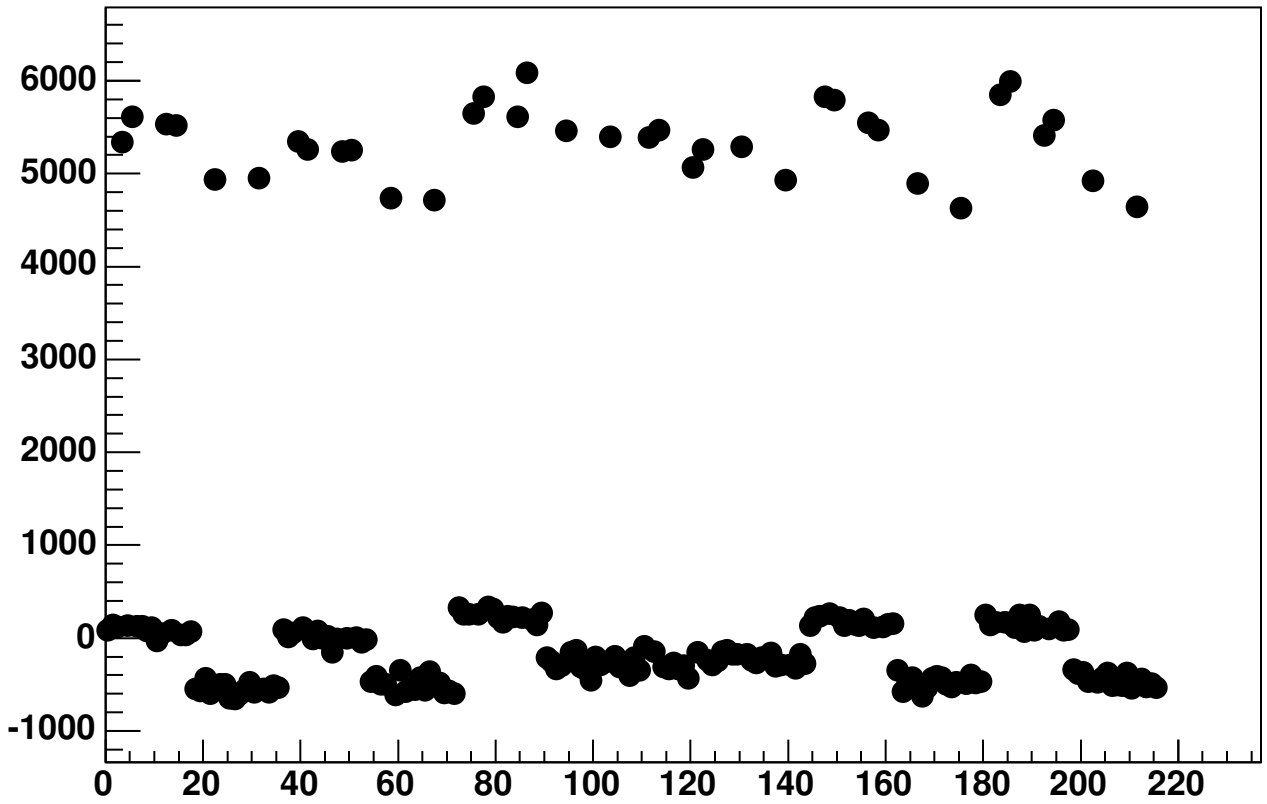
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



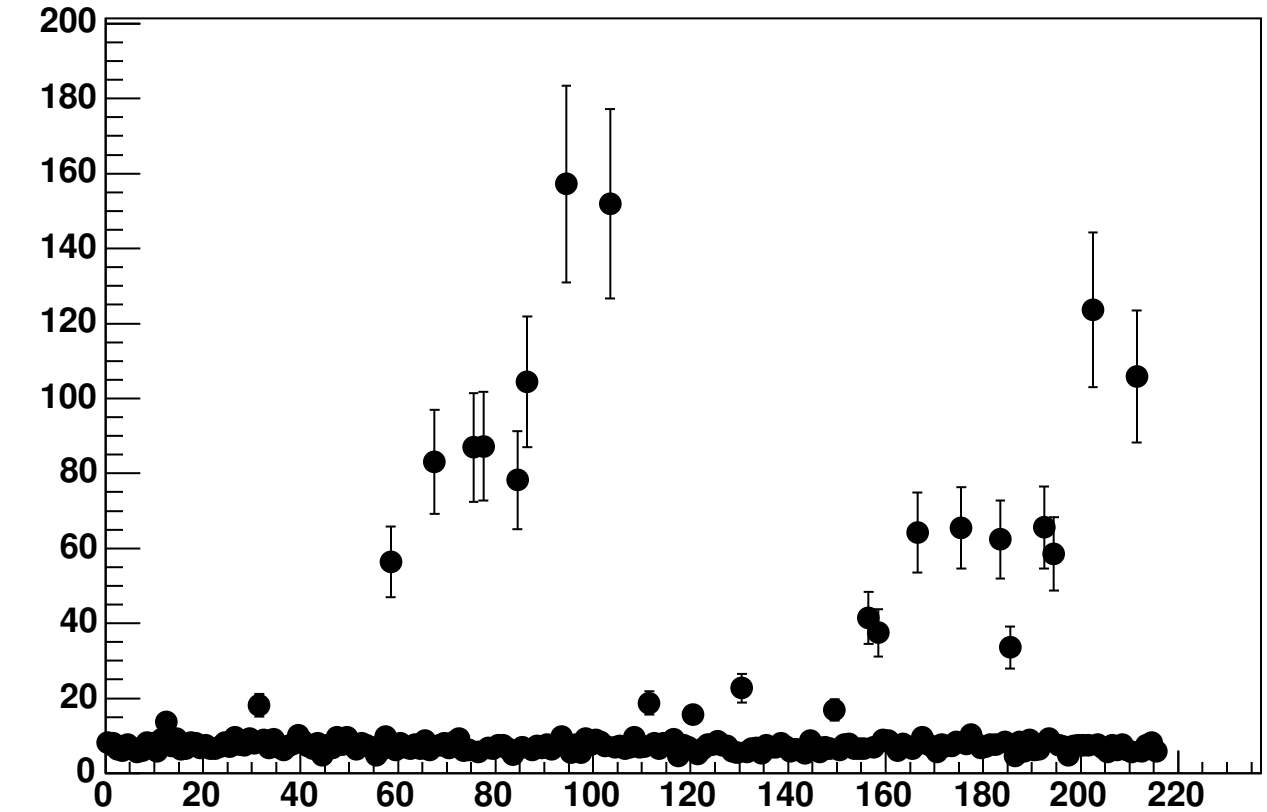
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



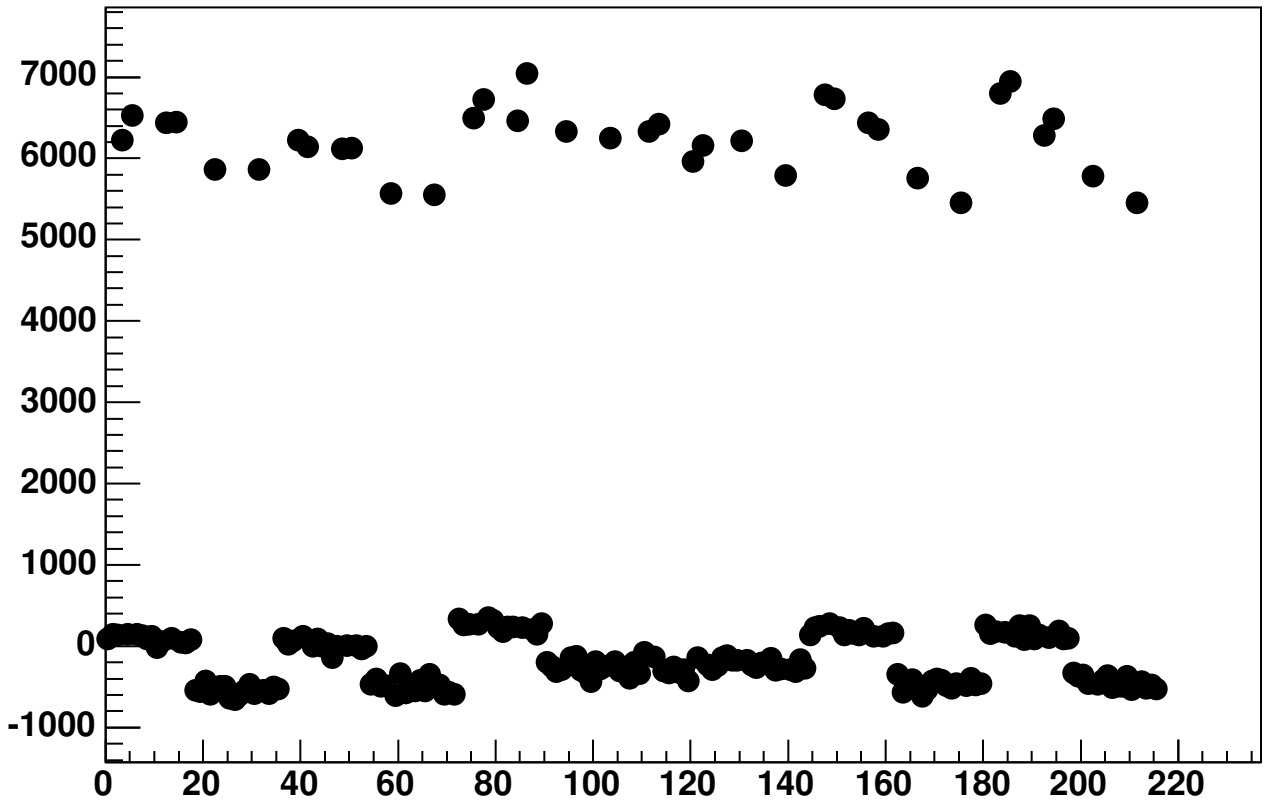
Enable 1, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



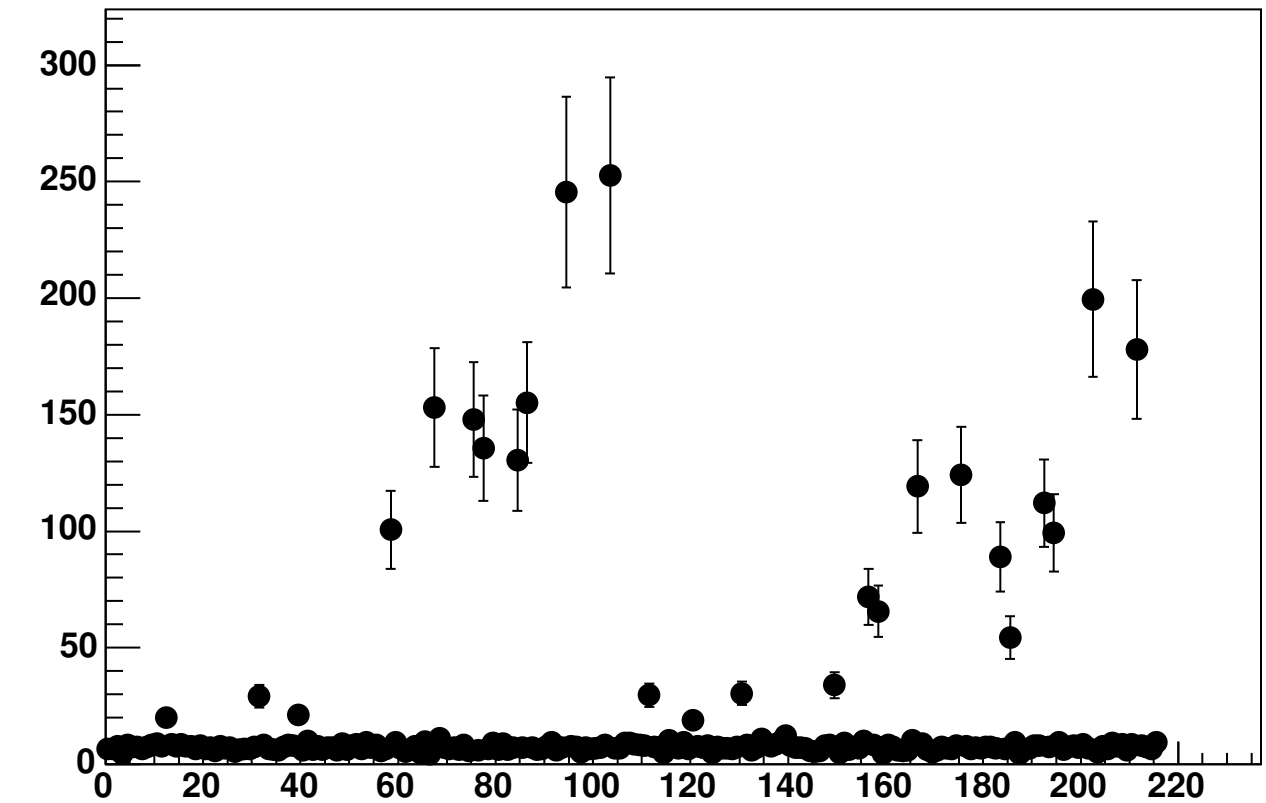
Enable 1, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



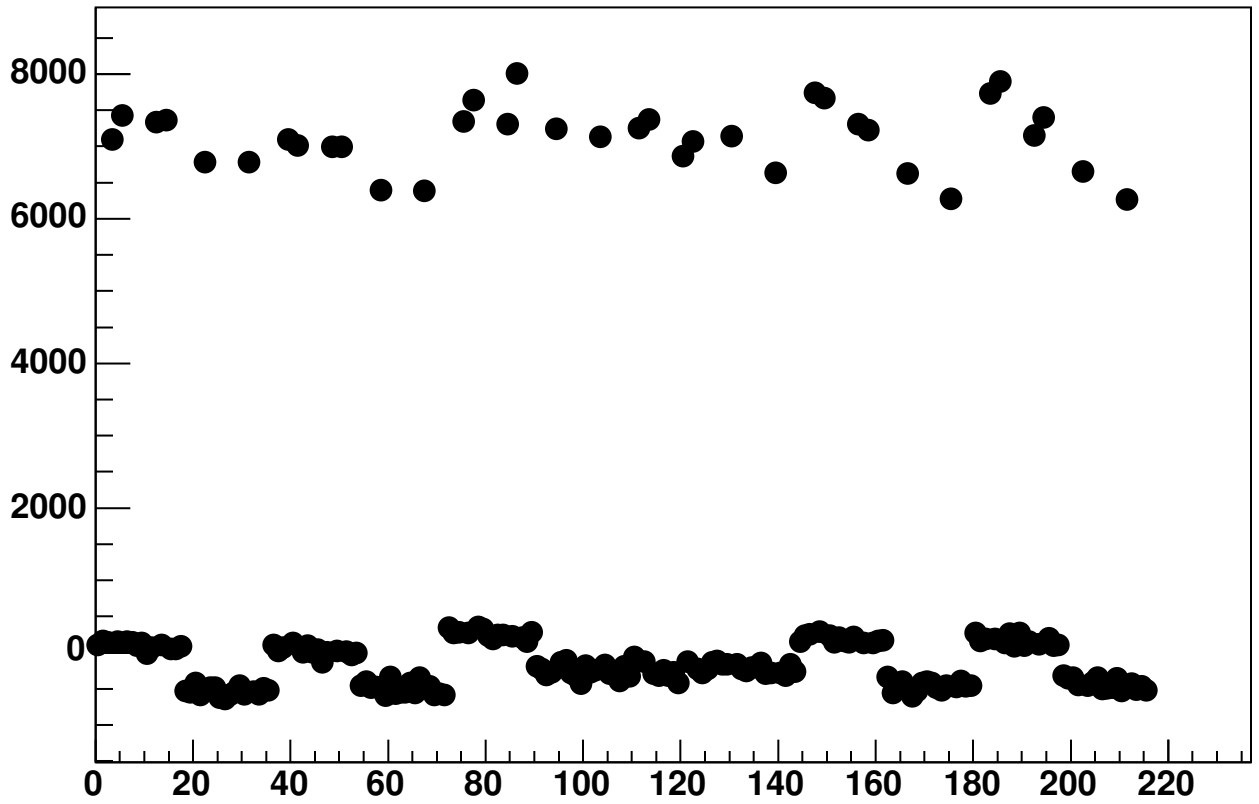
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



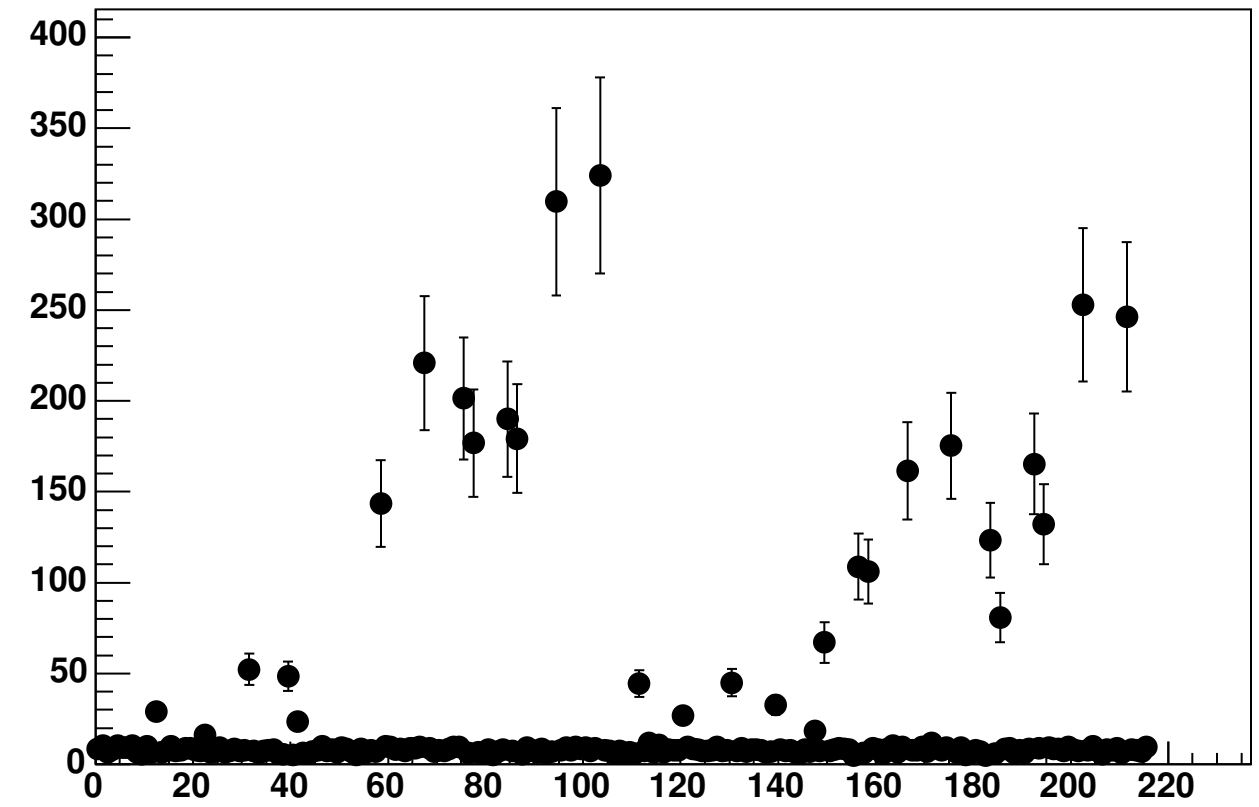
Enable 1, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



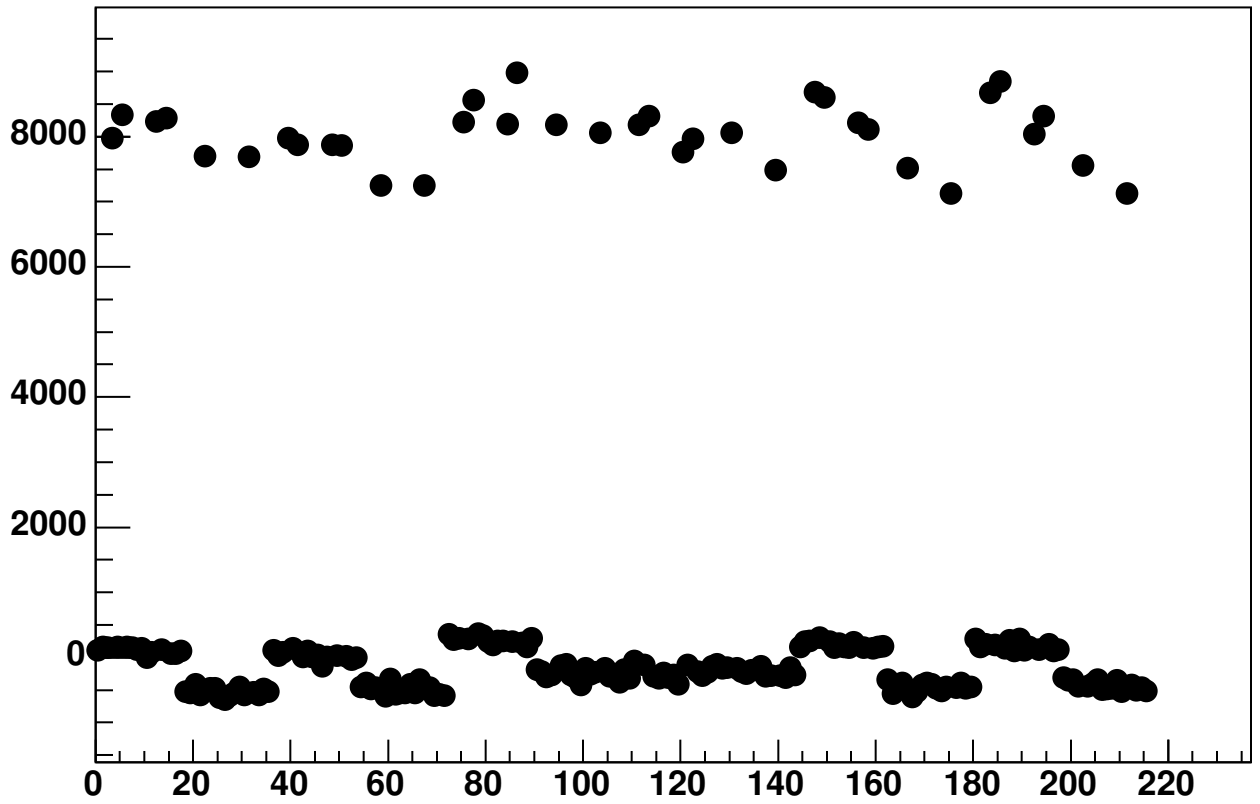
Enable 1, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



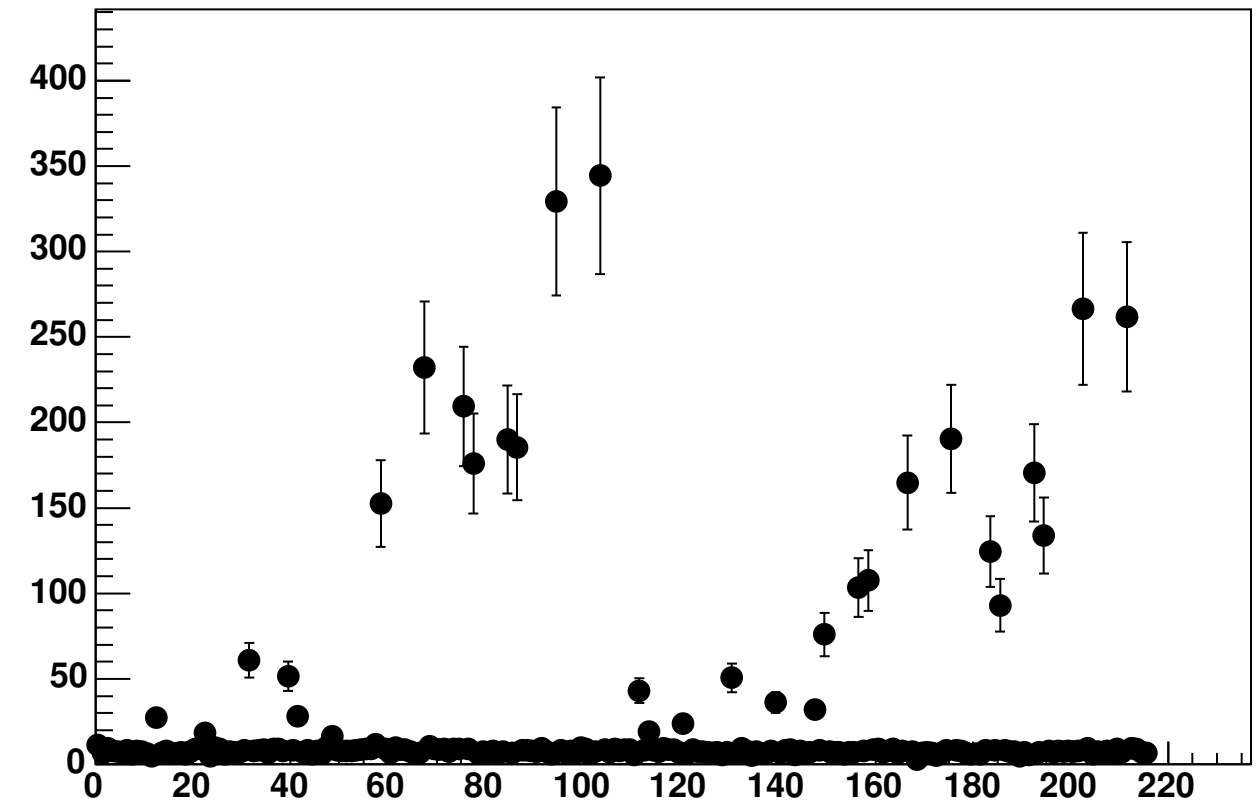
Enable 1, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



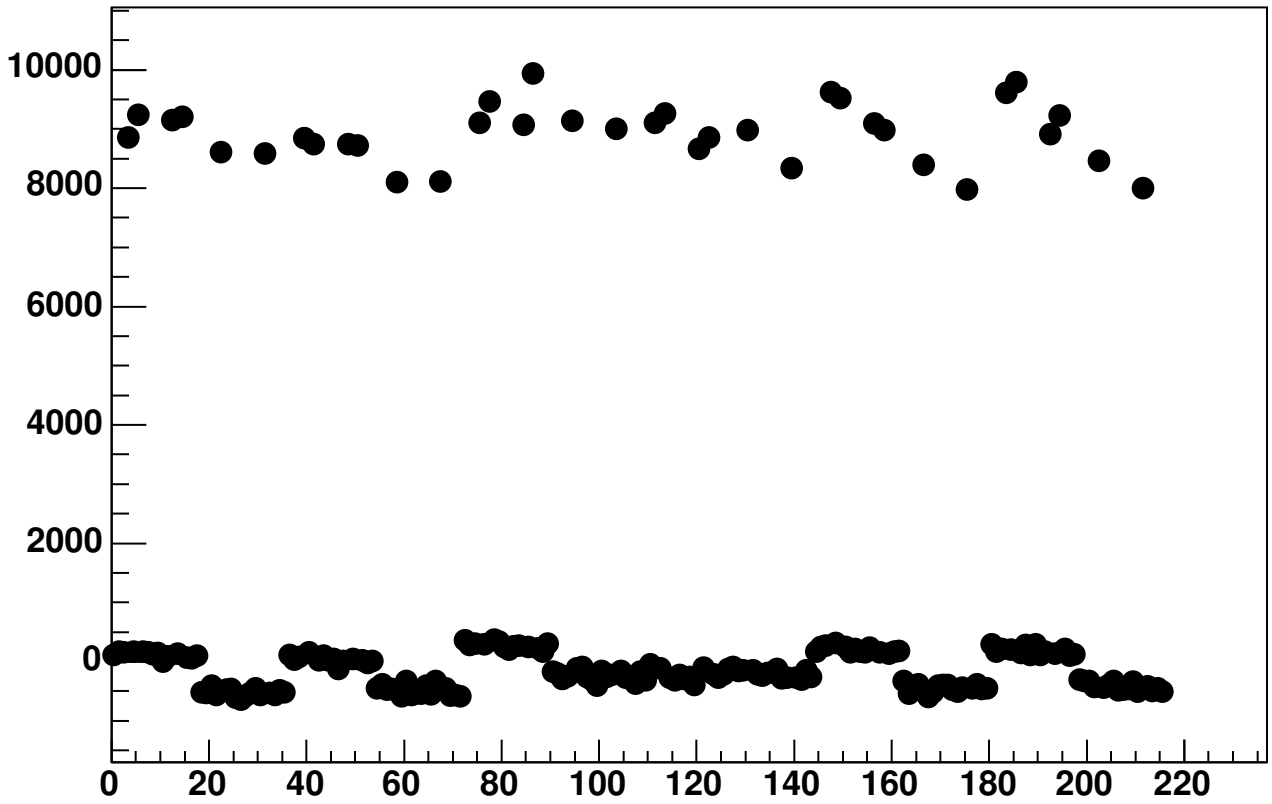
Enable 1, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



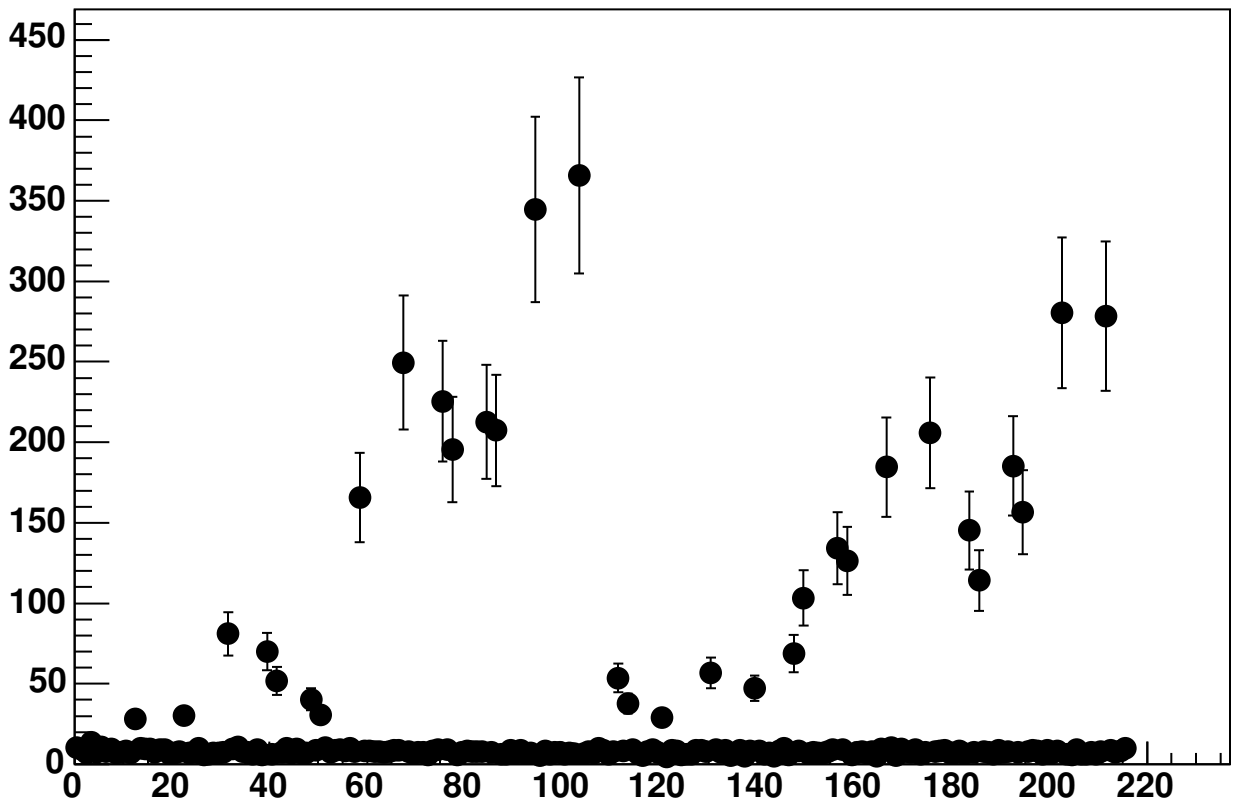
Enable 1, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



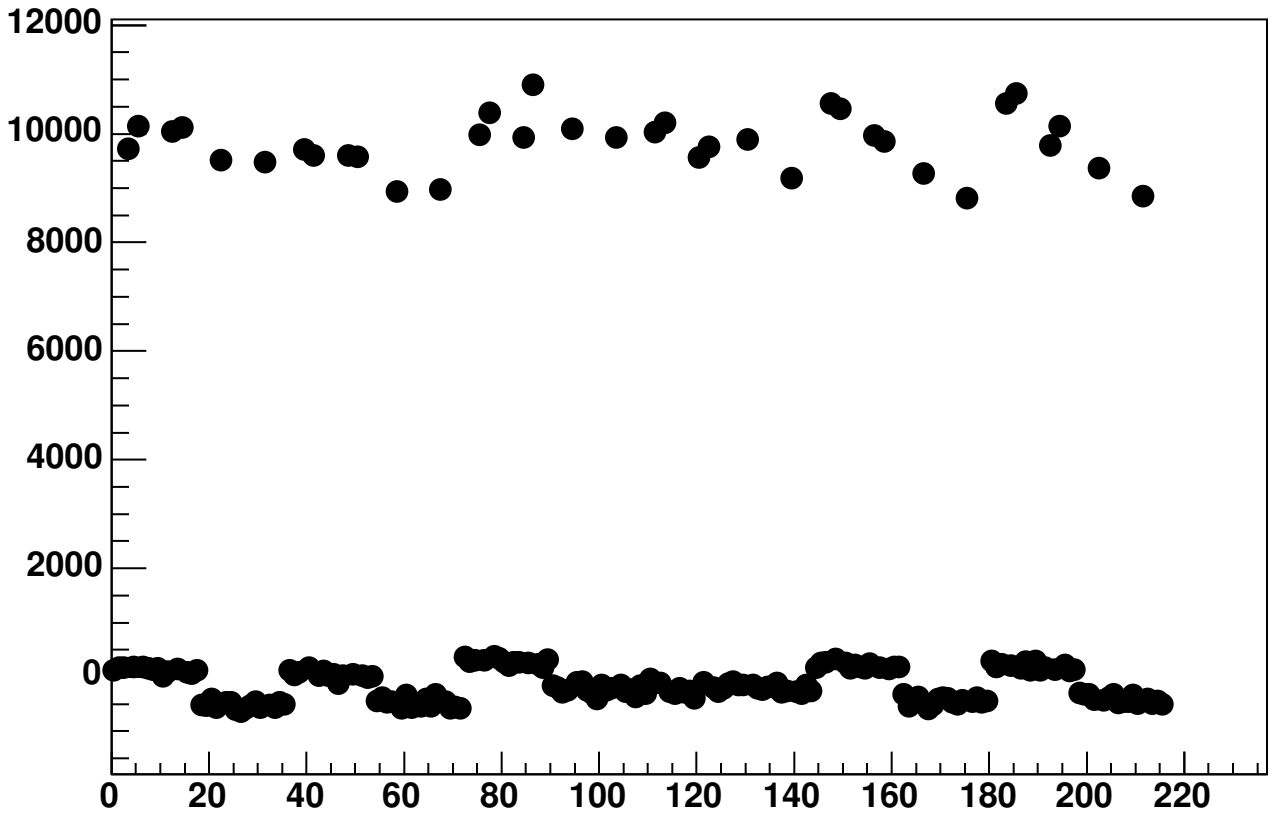
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



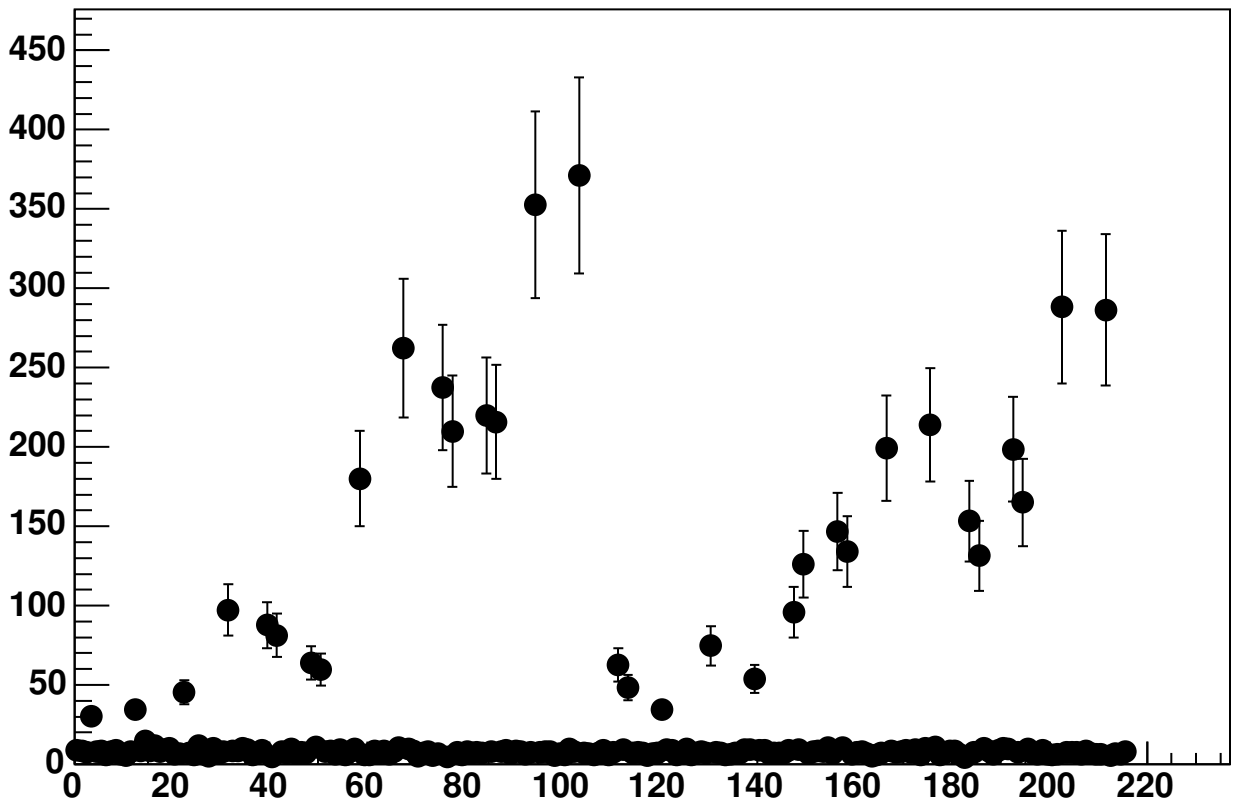
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



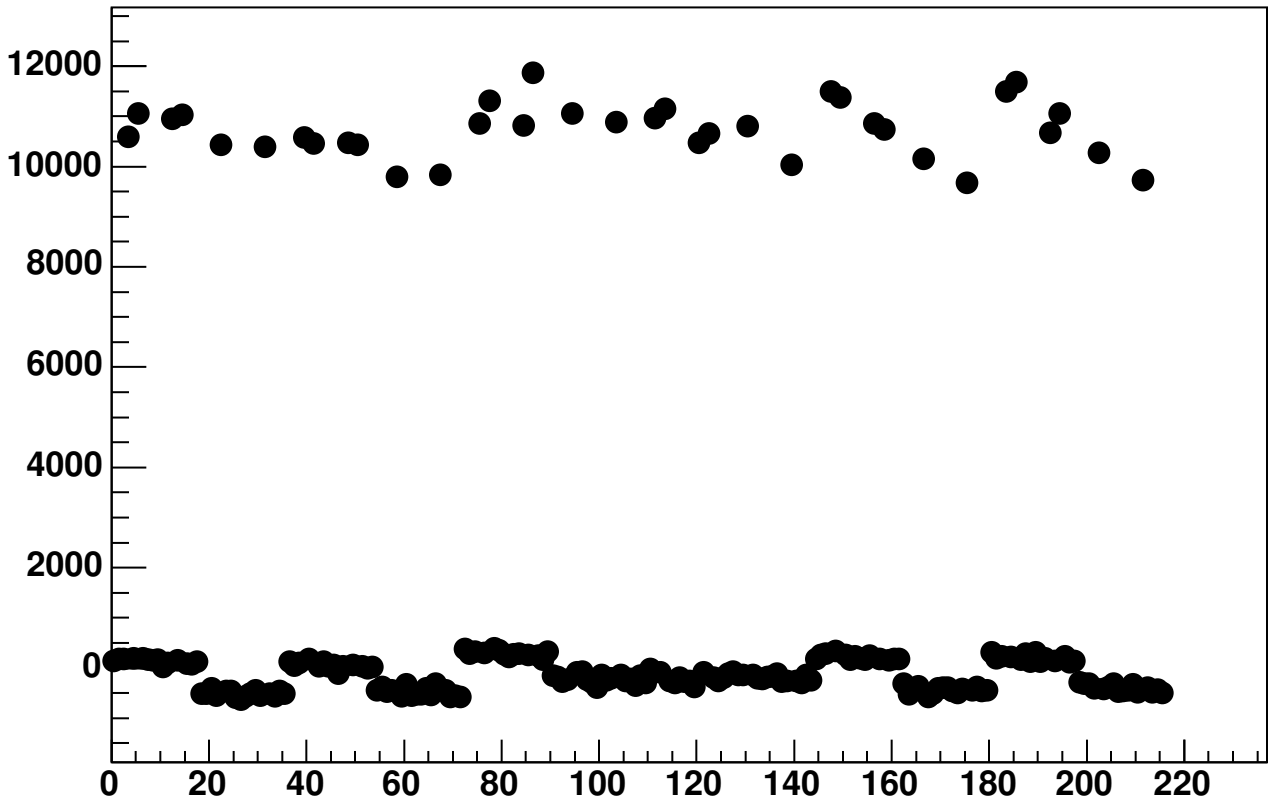
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



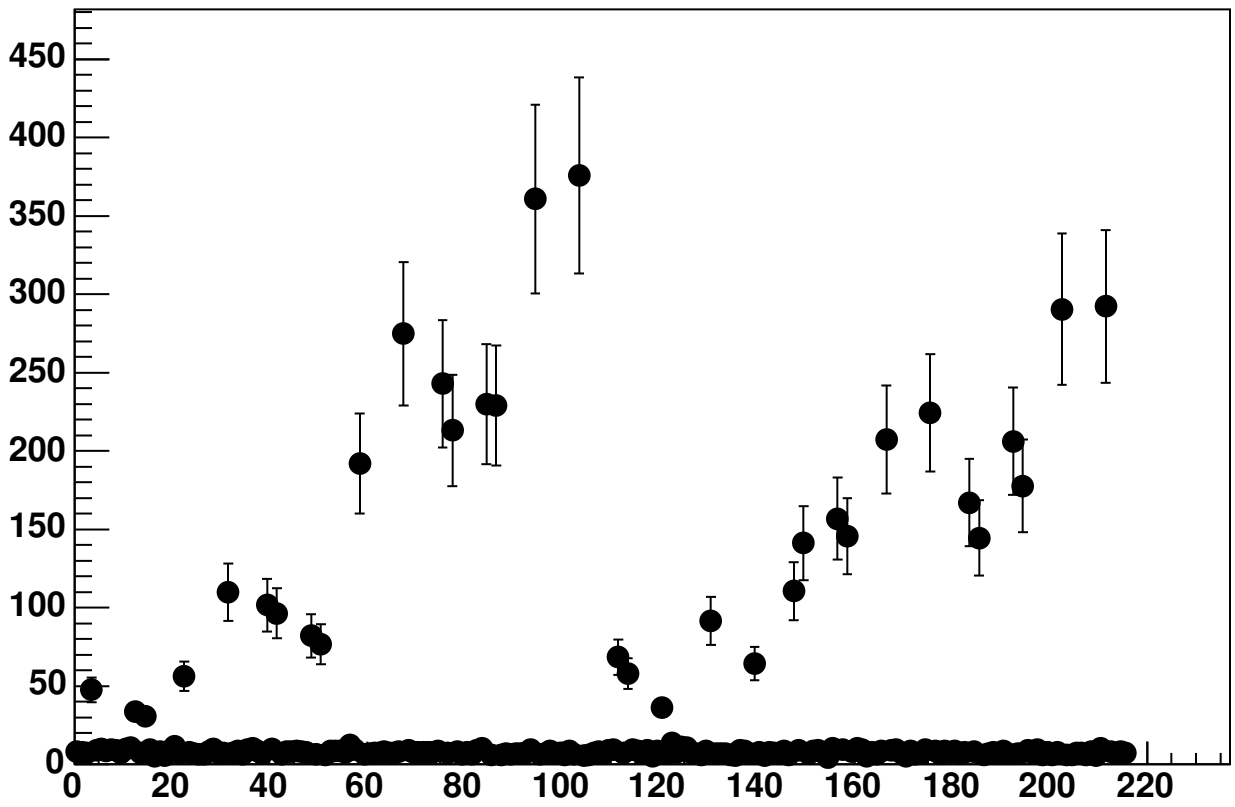
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



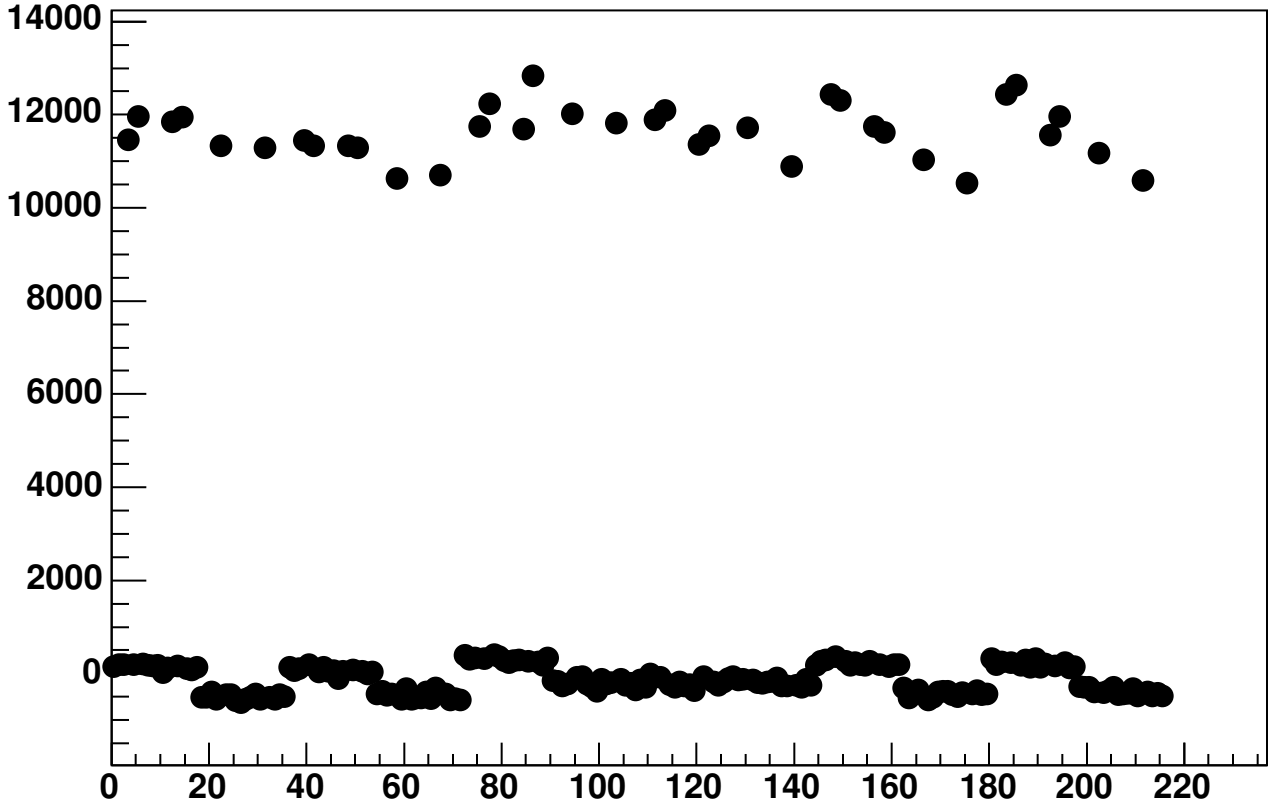
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



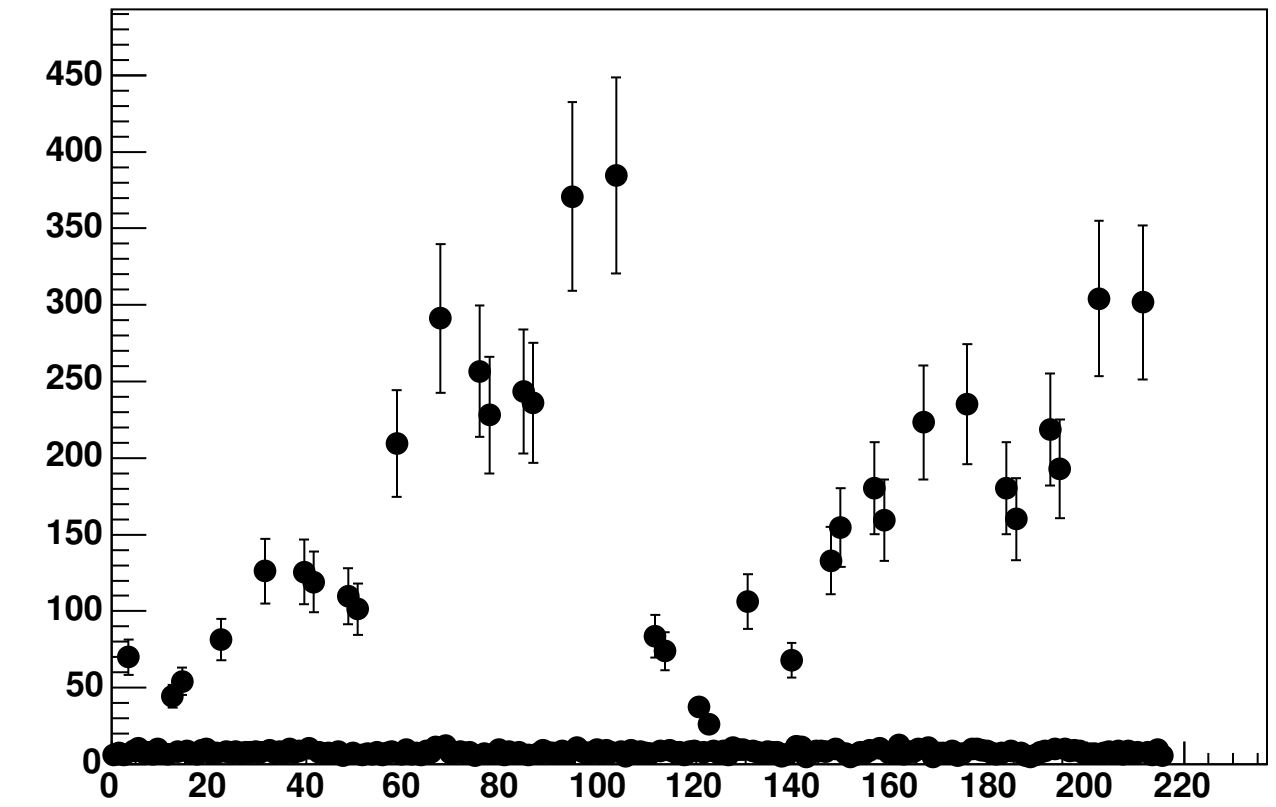
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



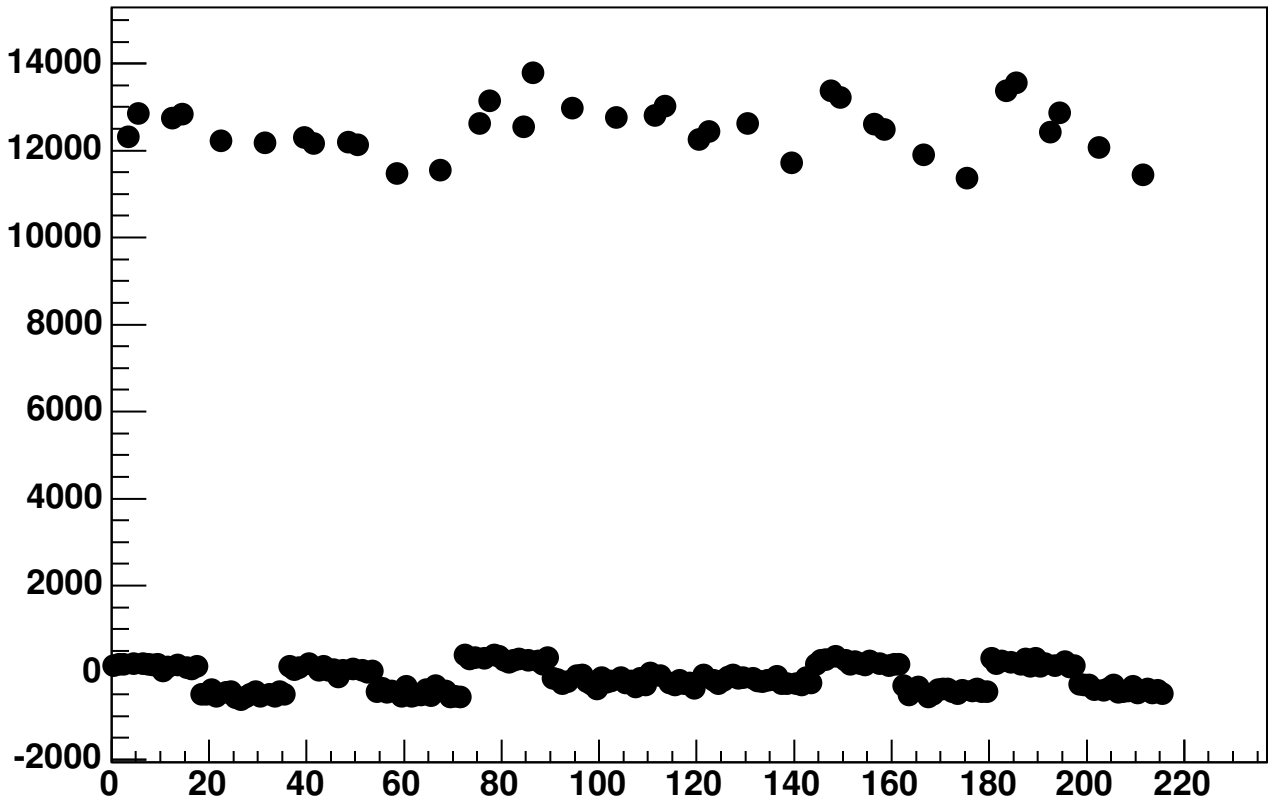
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



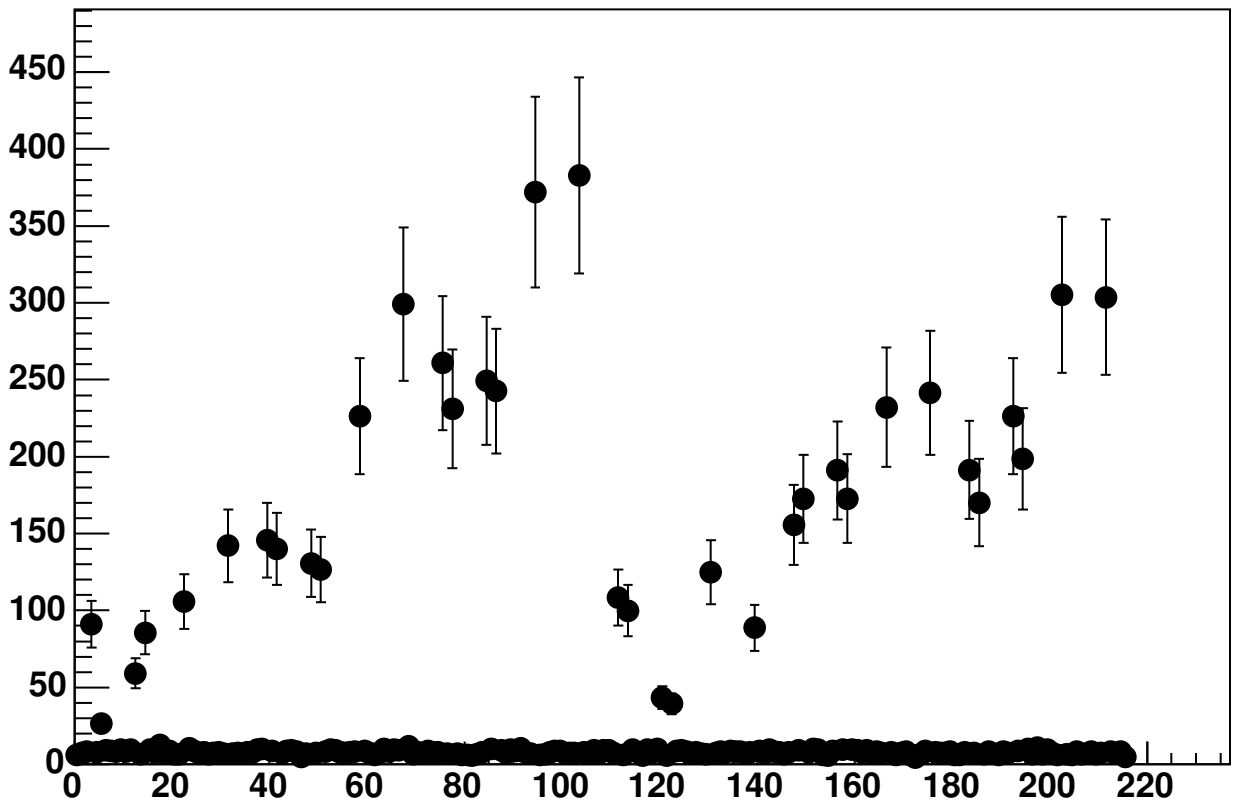
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



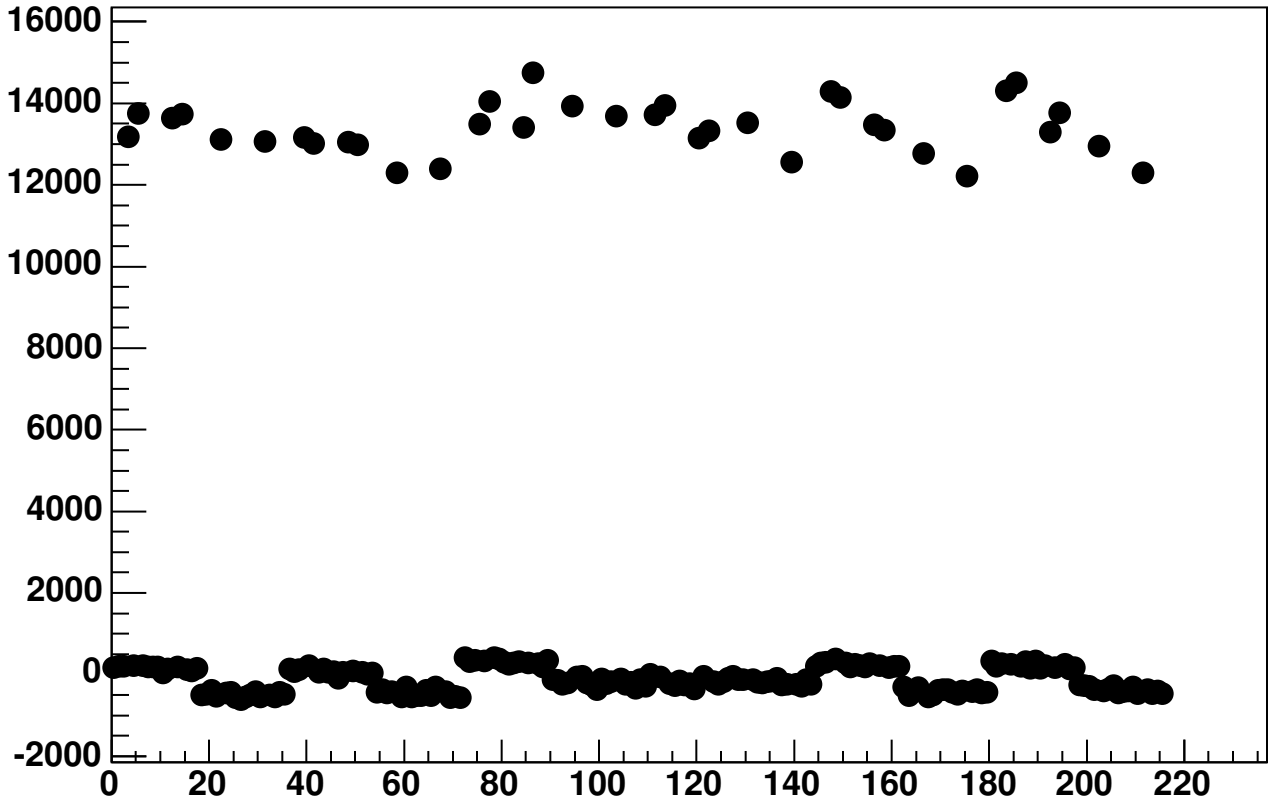
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



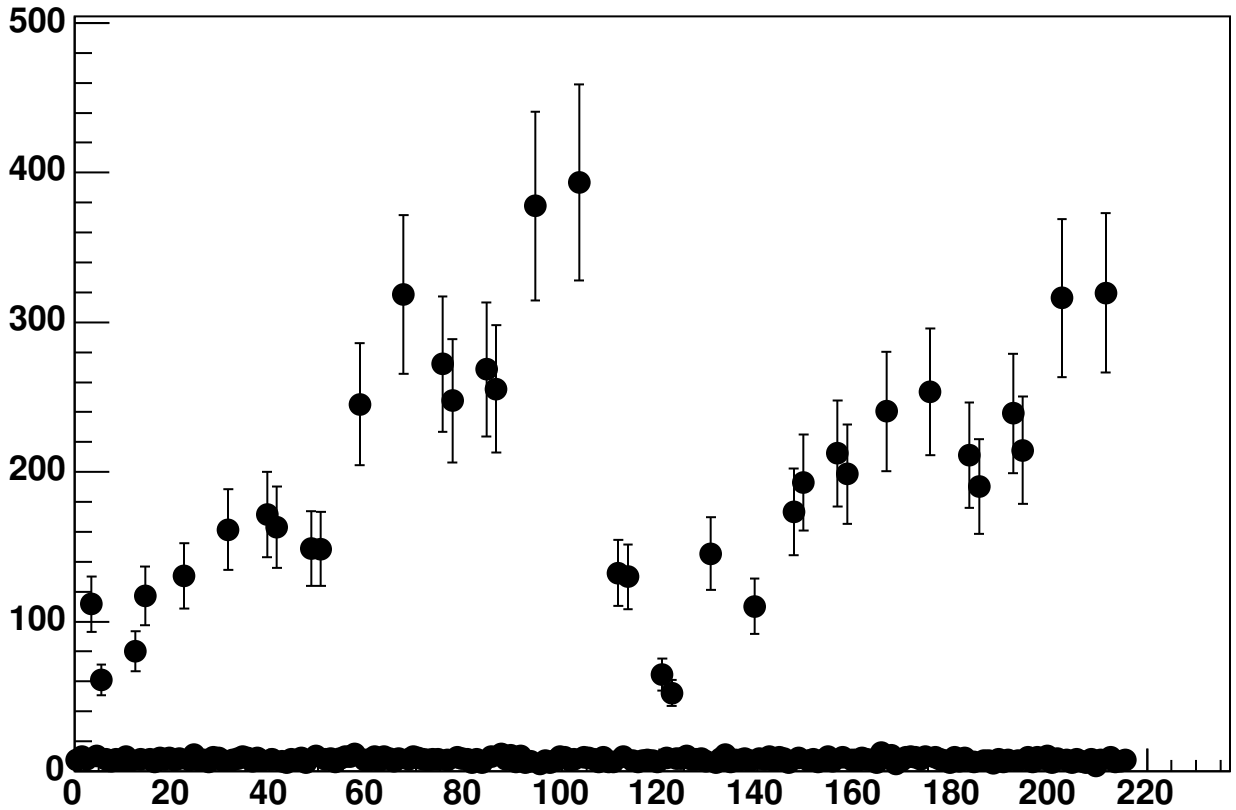
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



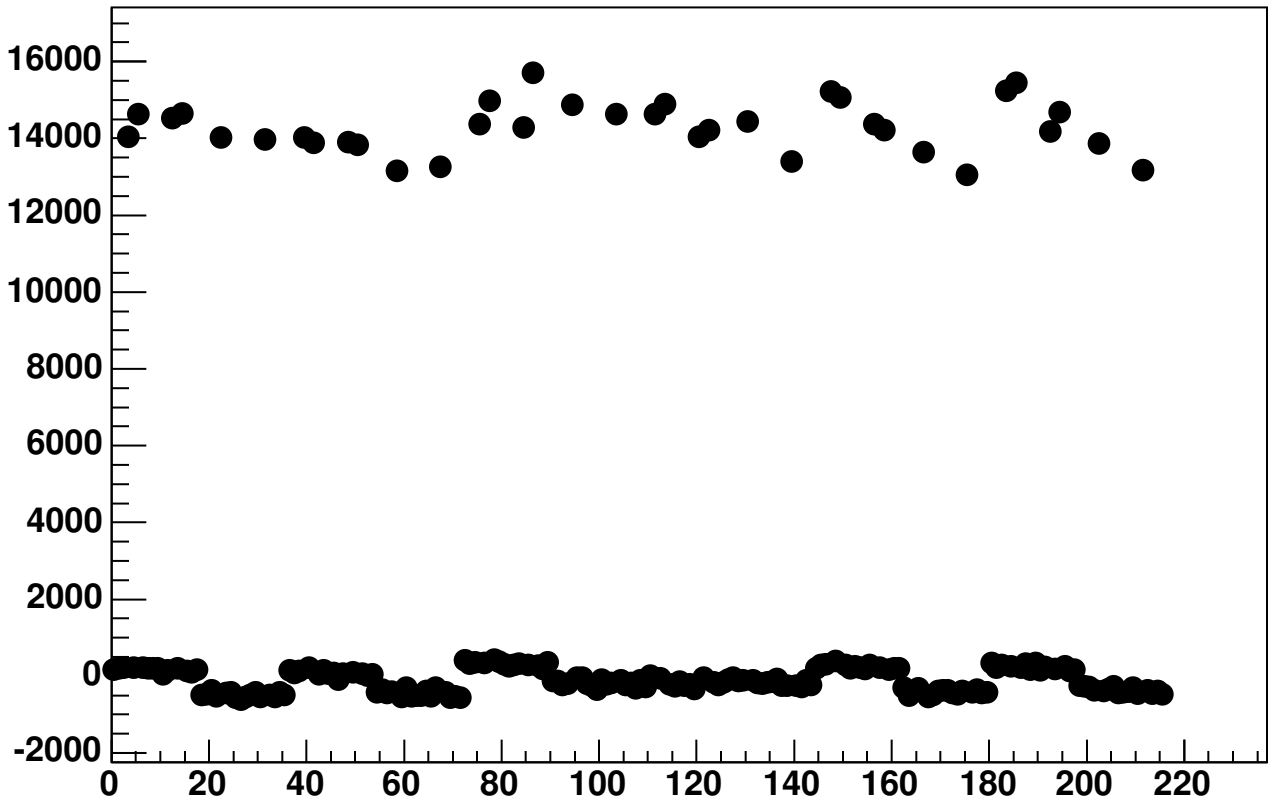
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



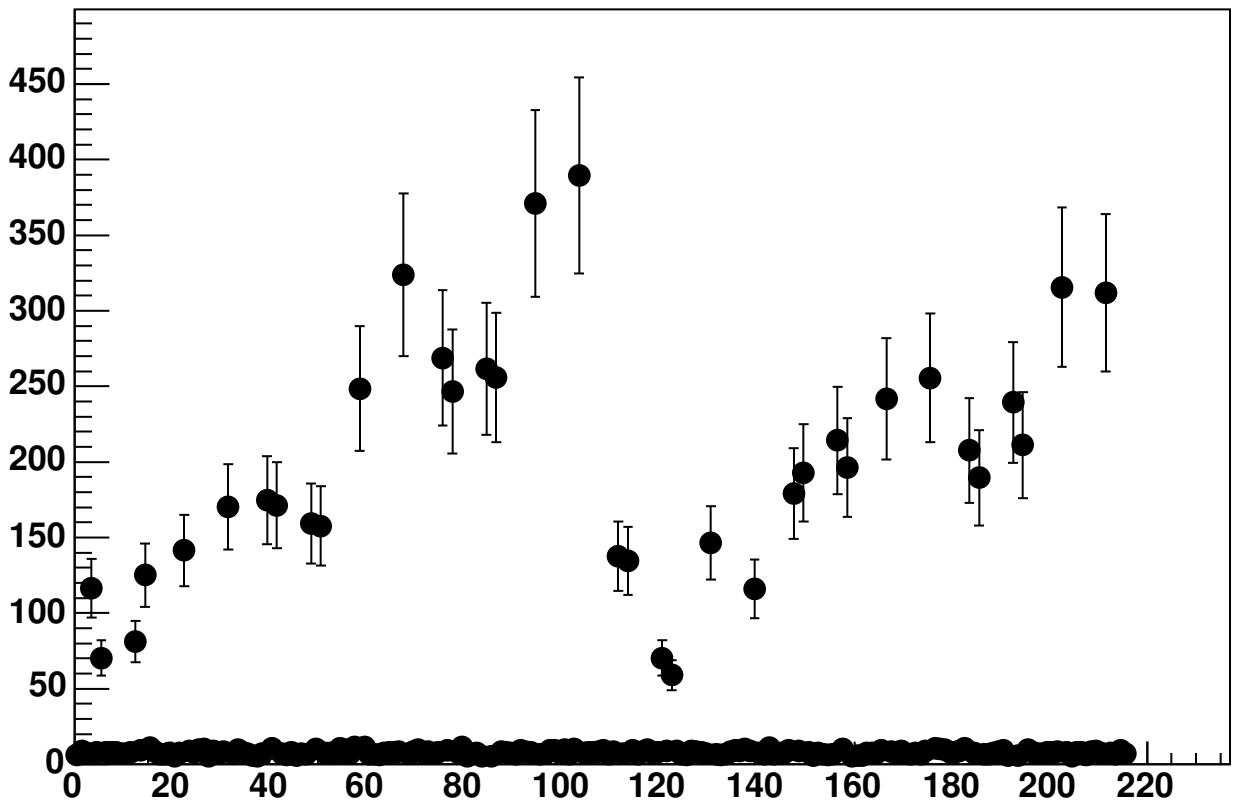
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



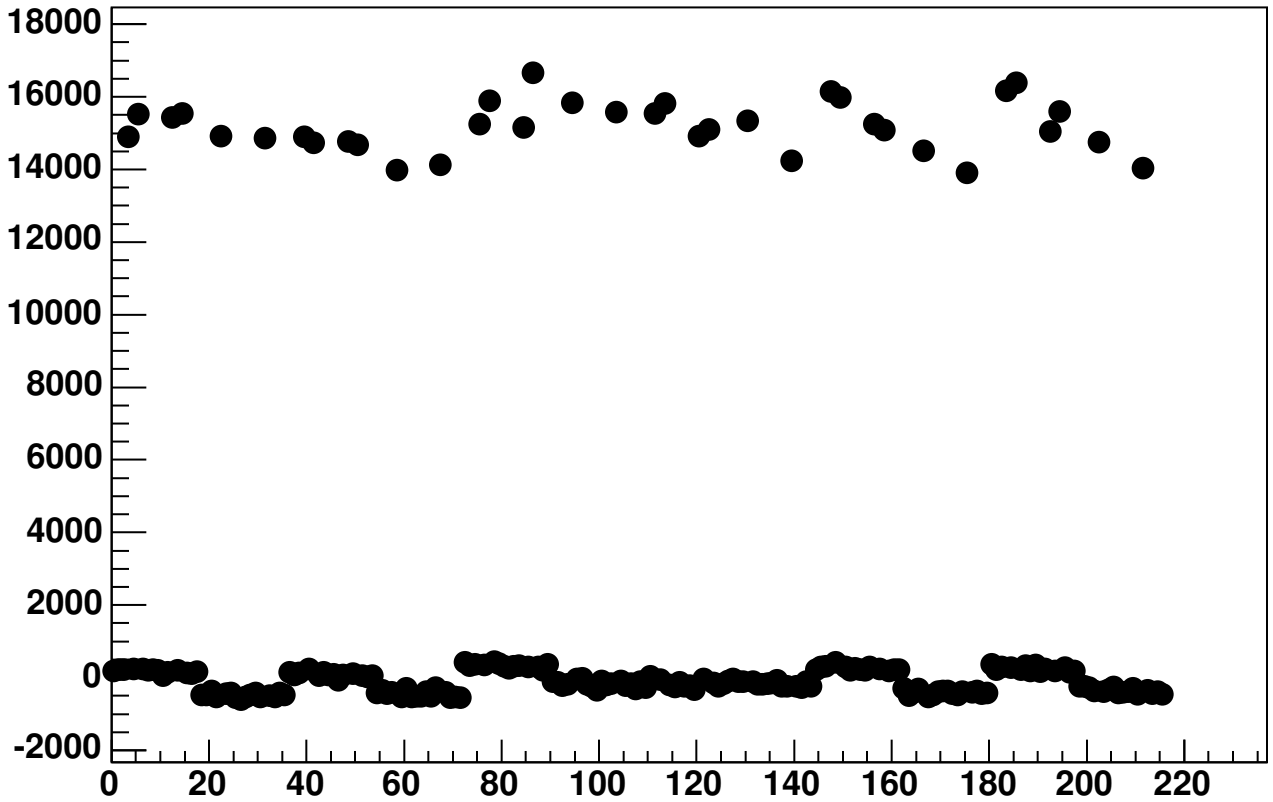
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



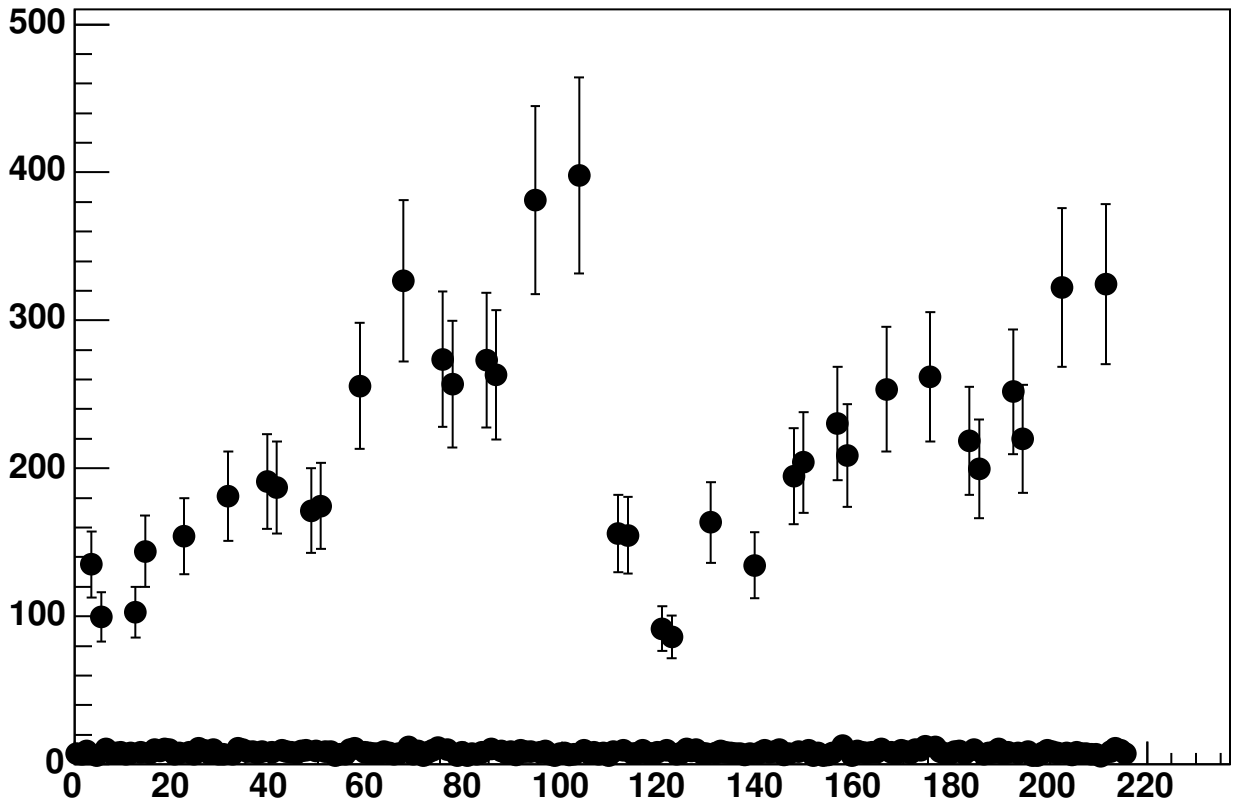
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



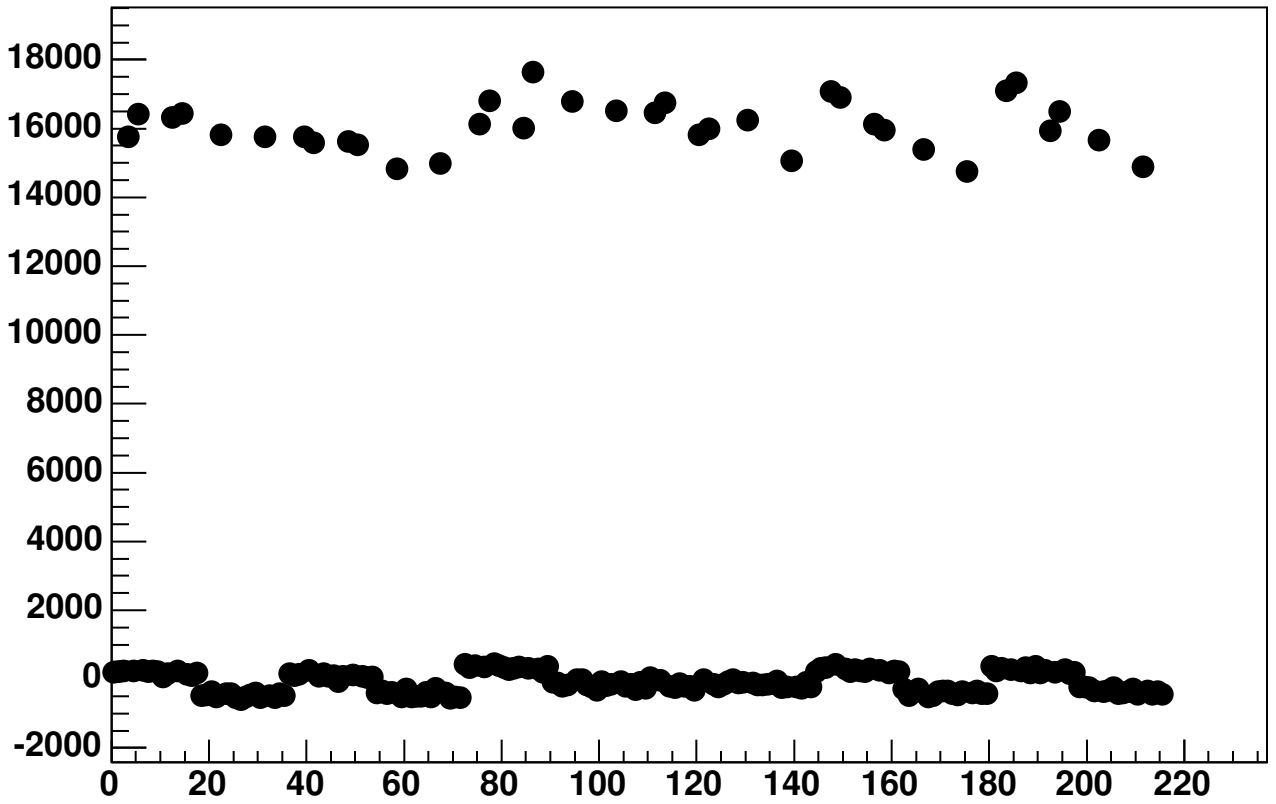
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



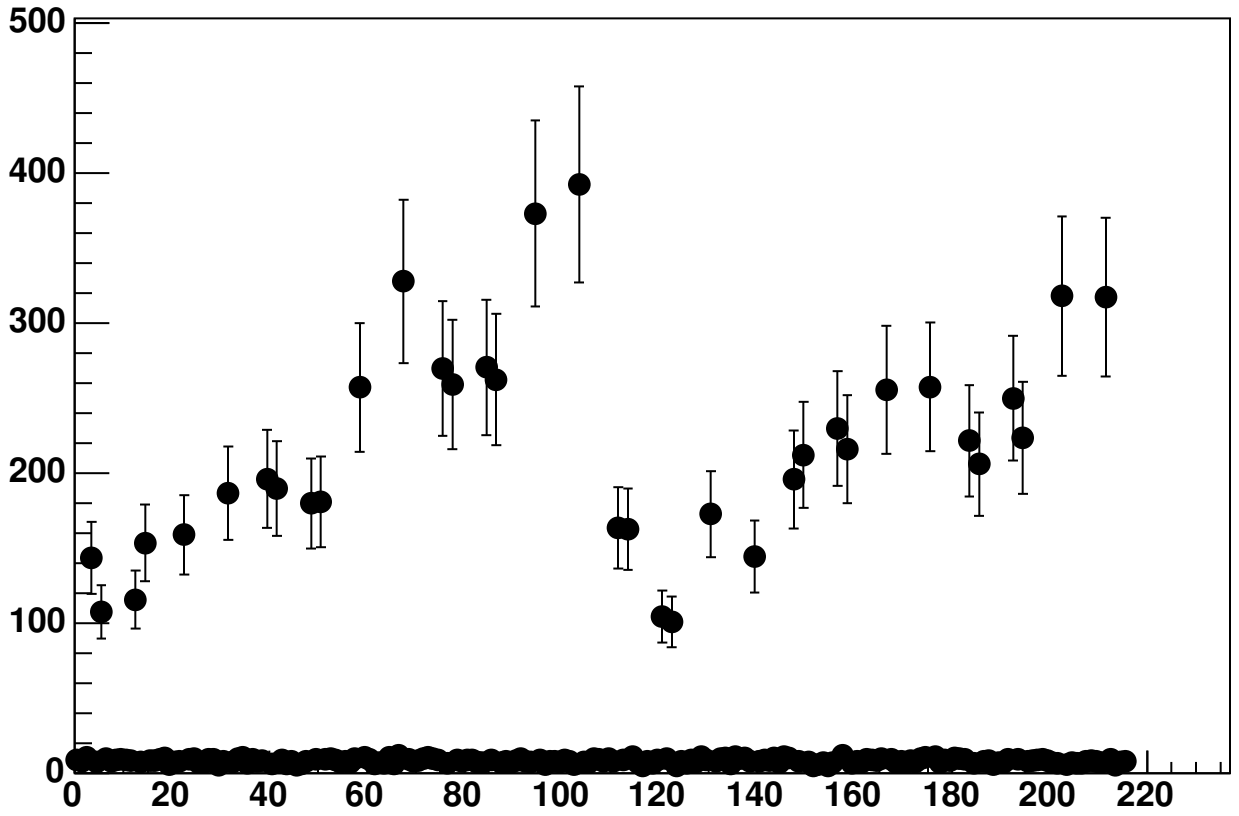
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



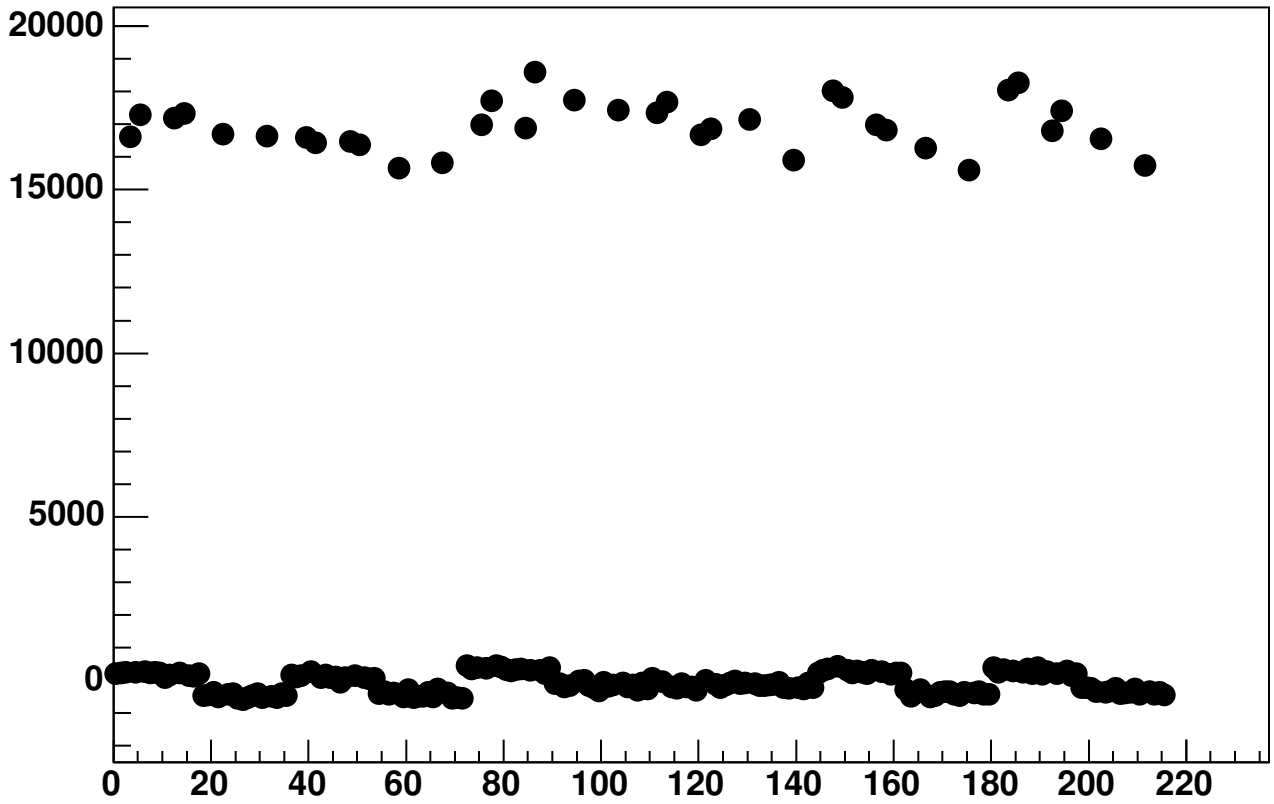
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



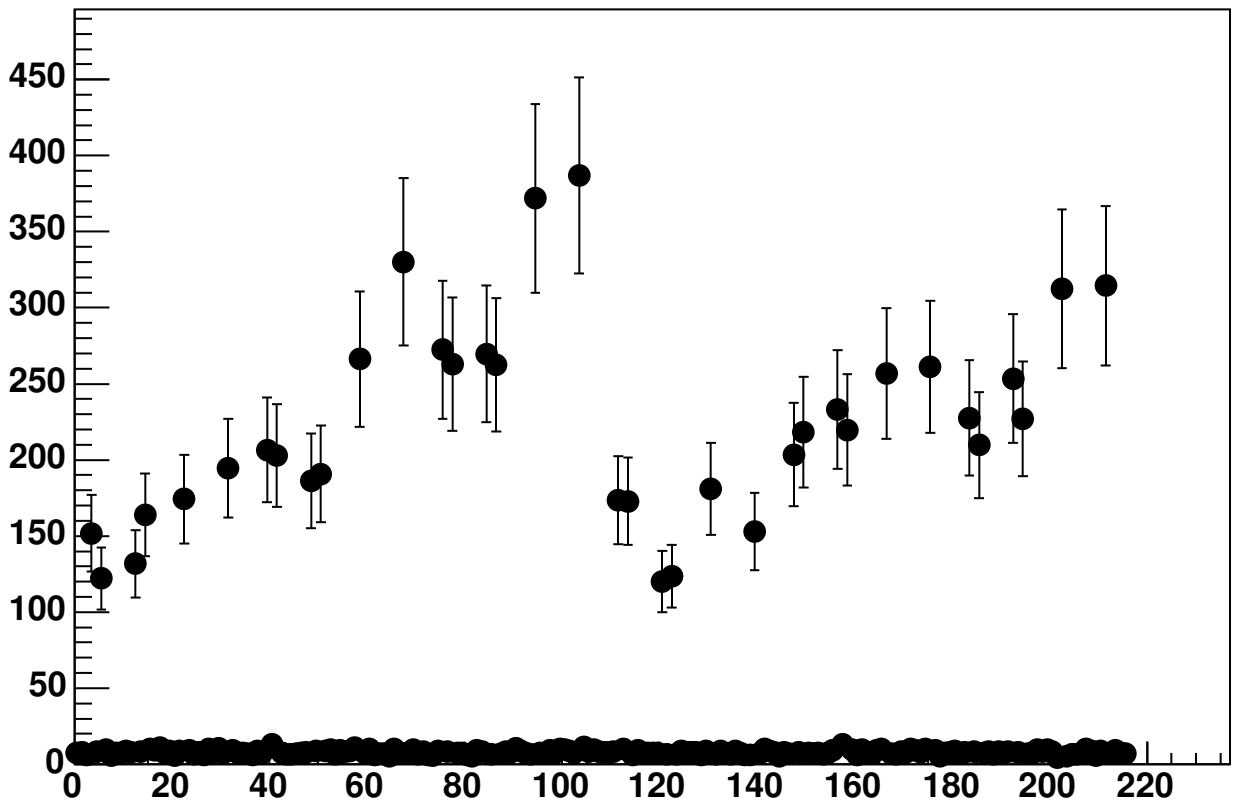
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



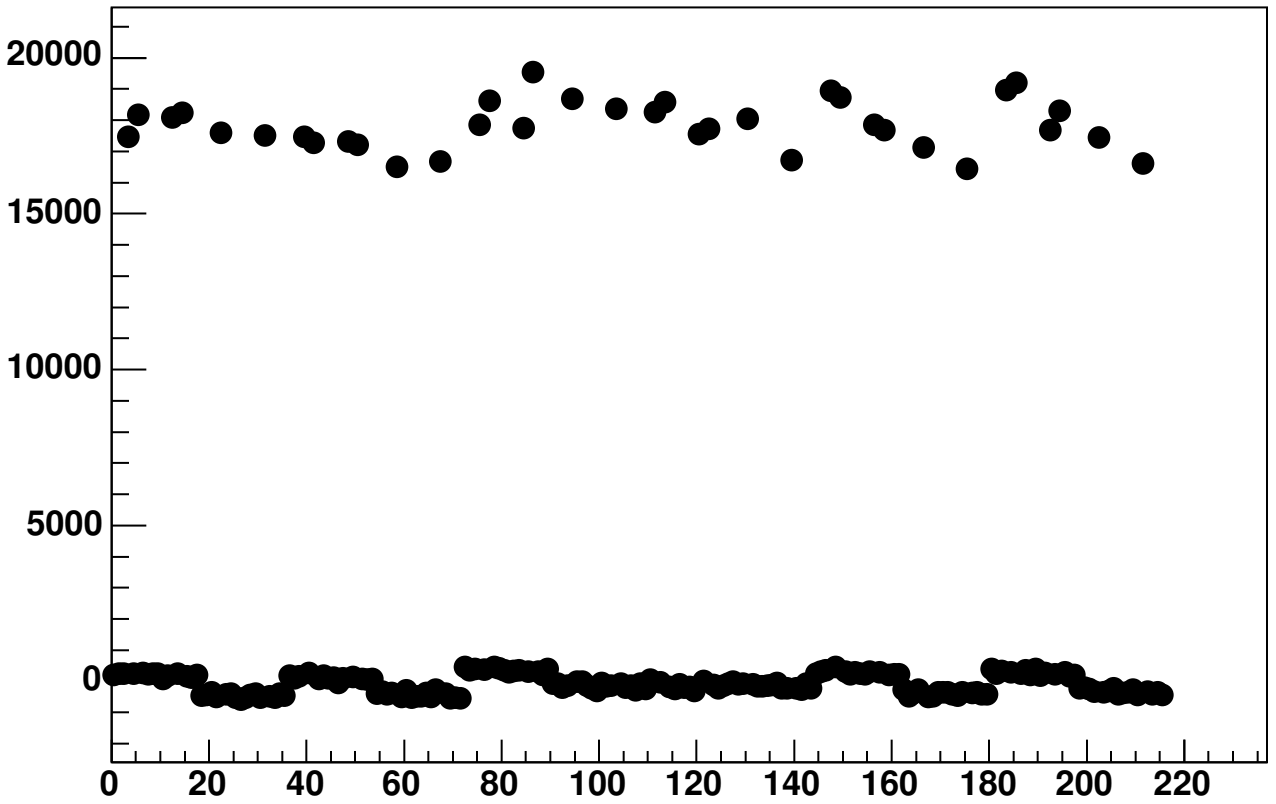
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



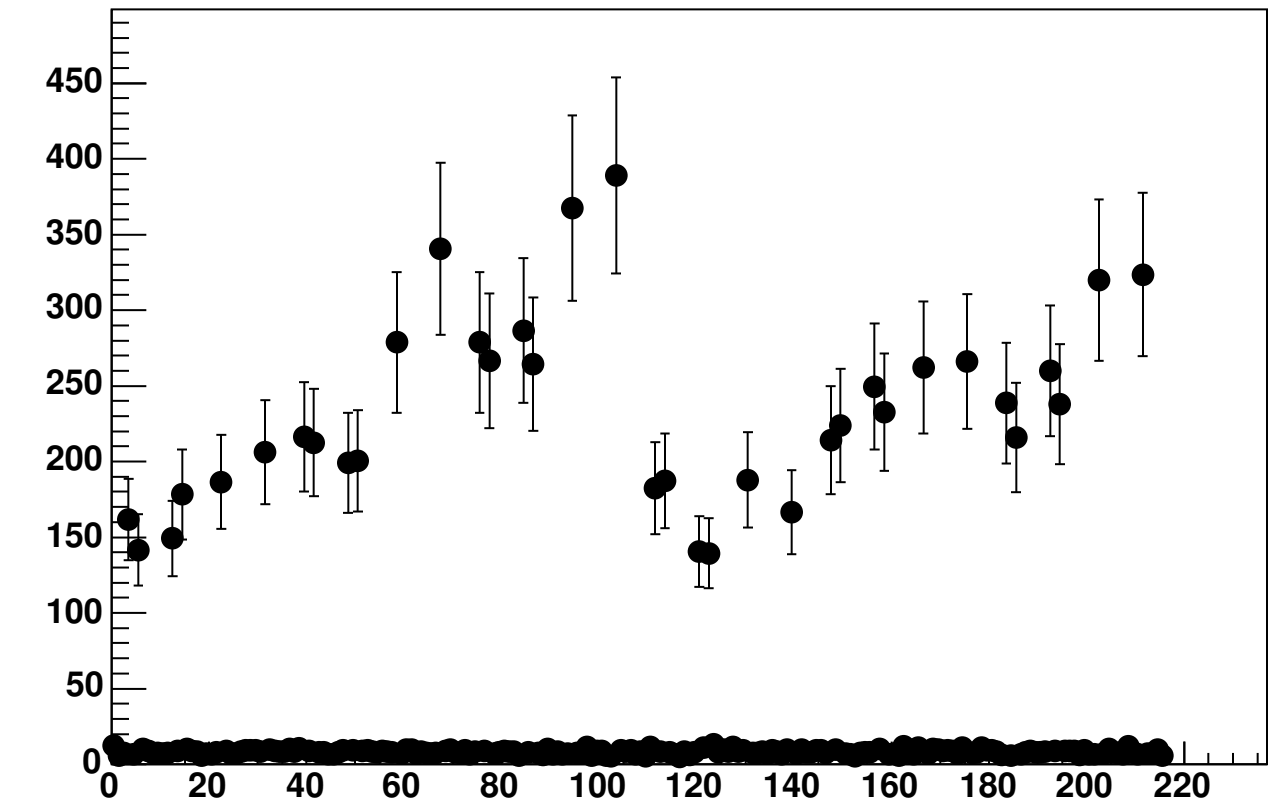
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



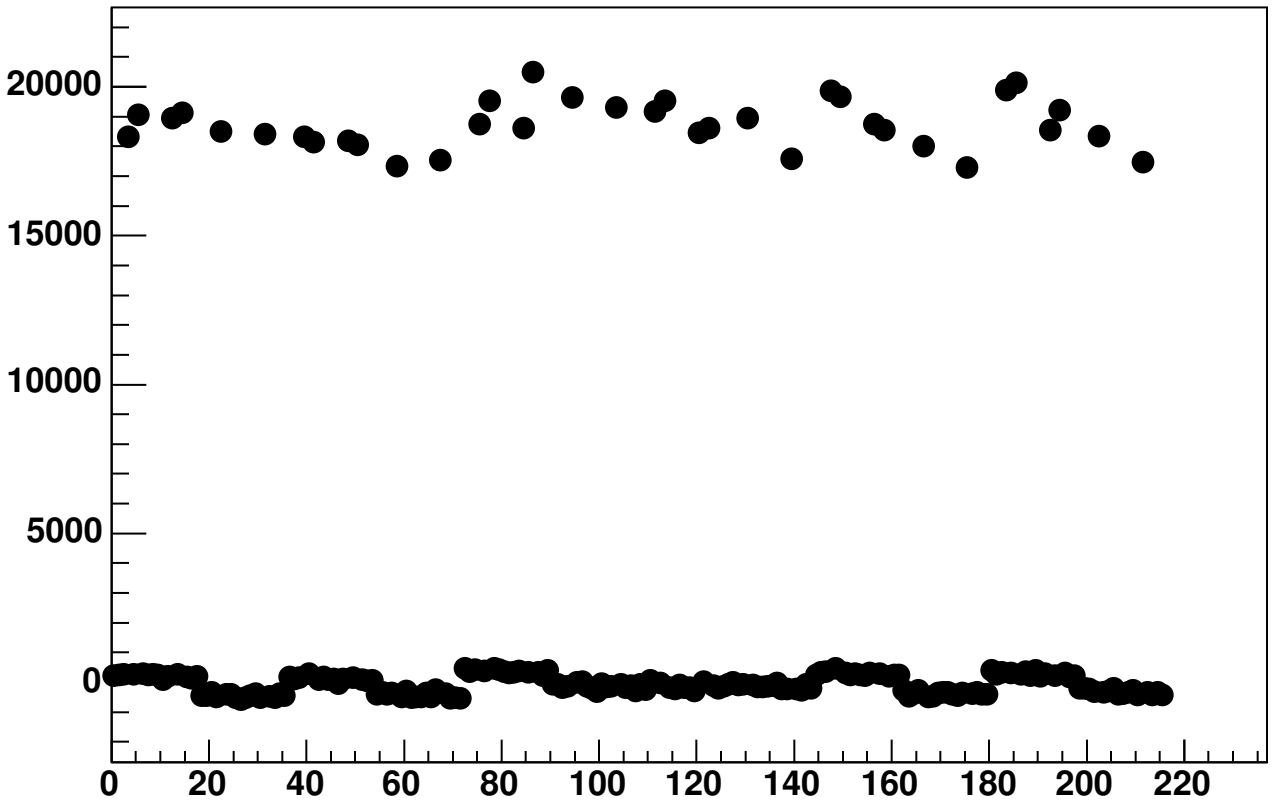
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



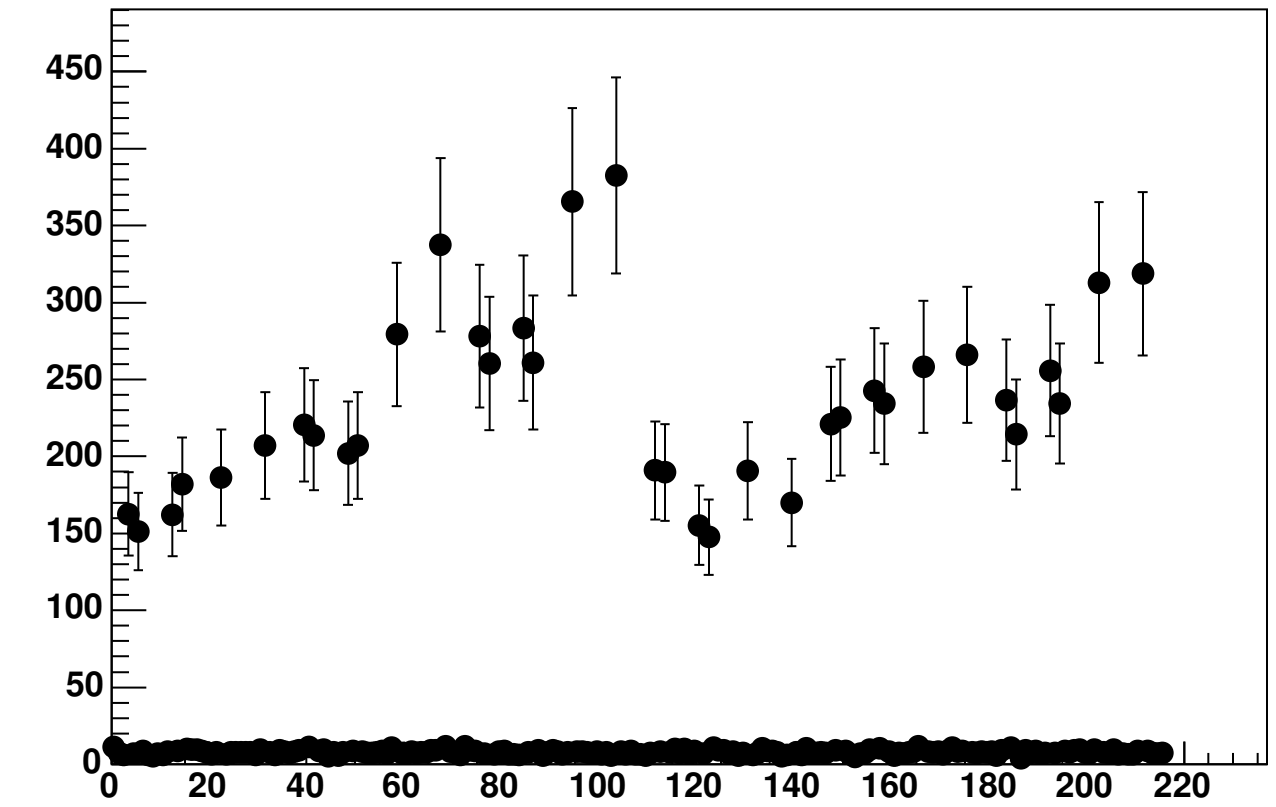
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



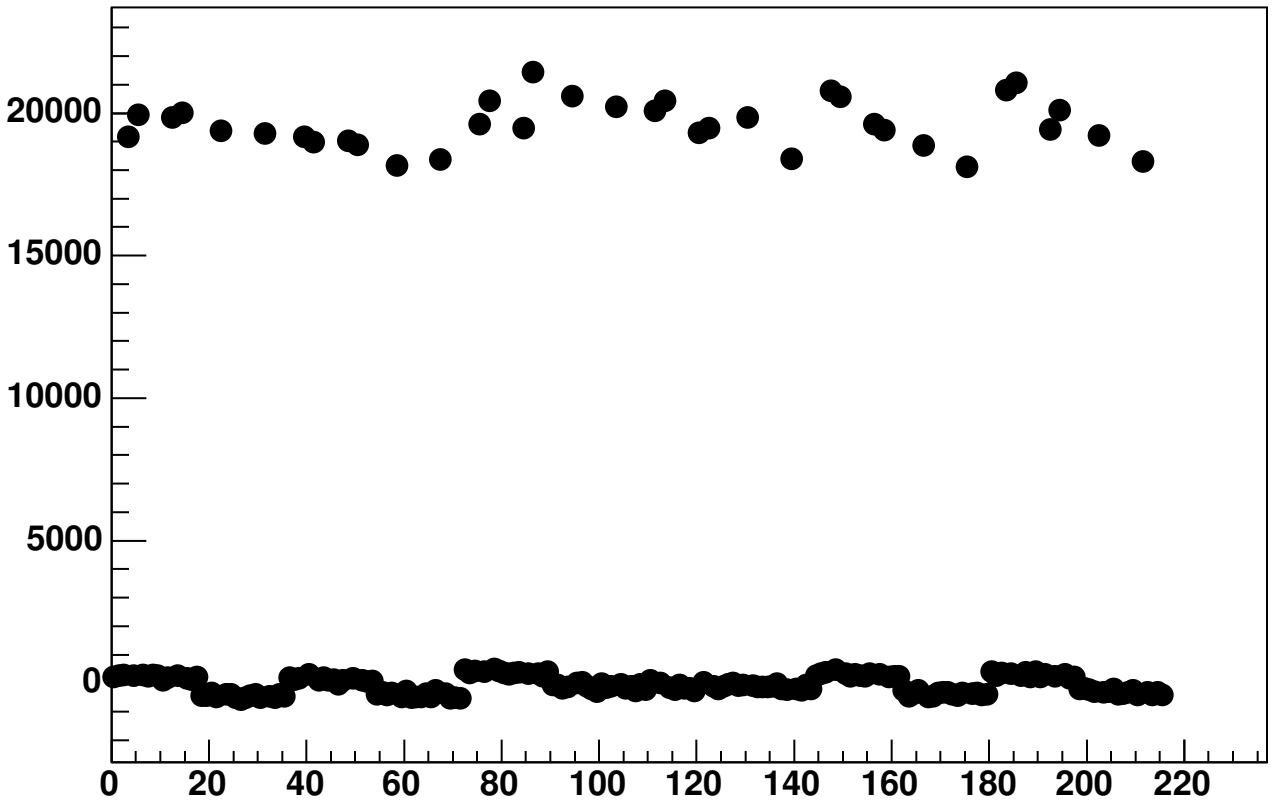
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



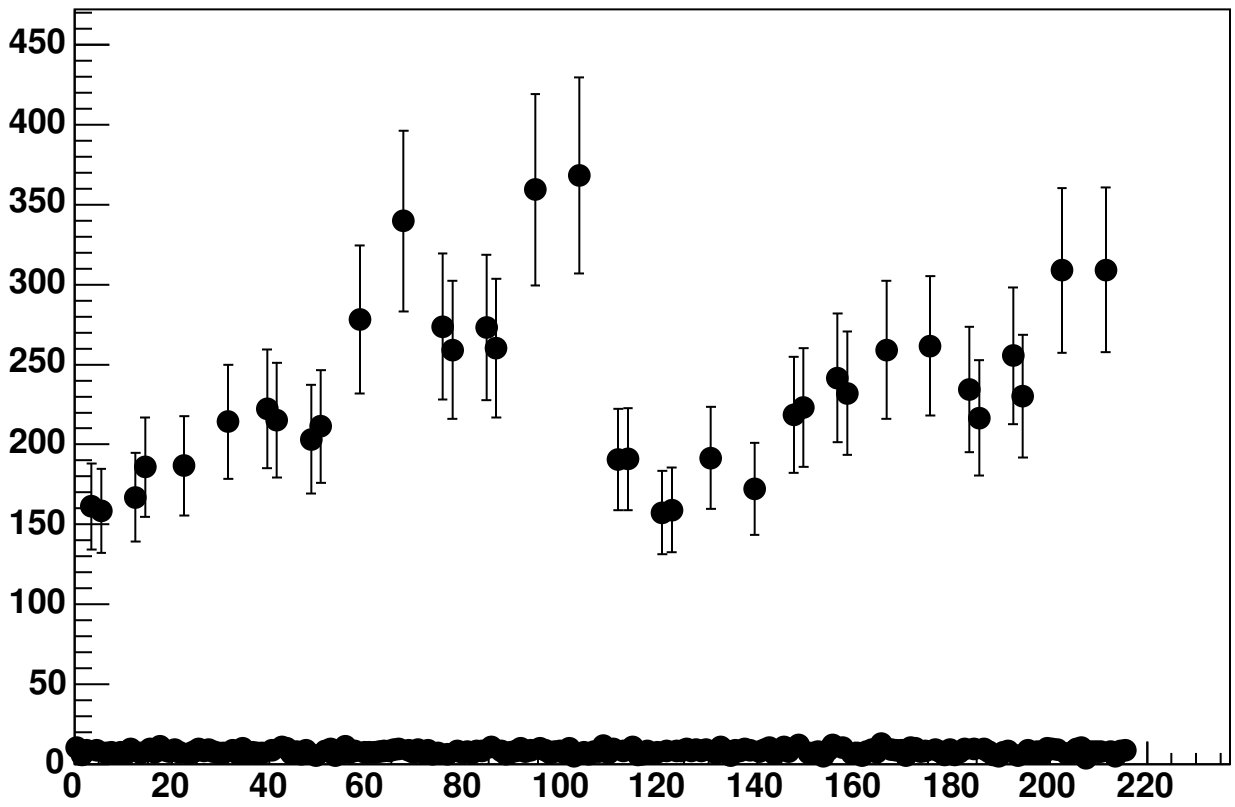
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



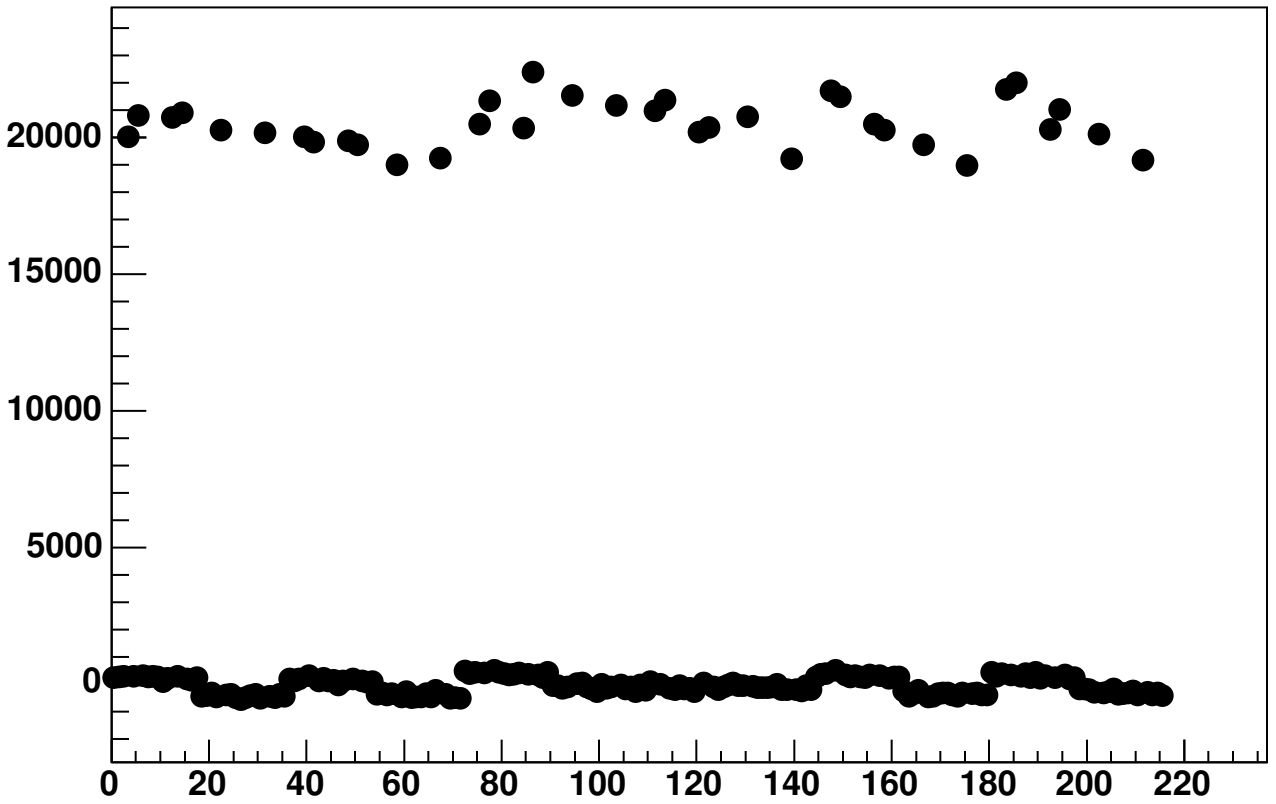
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



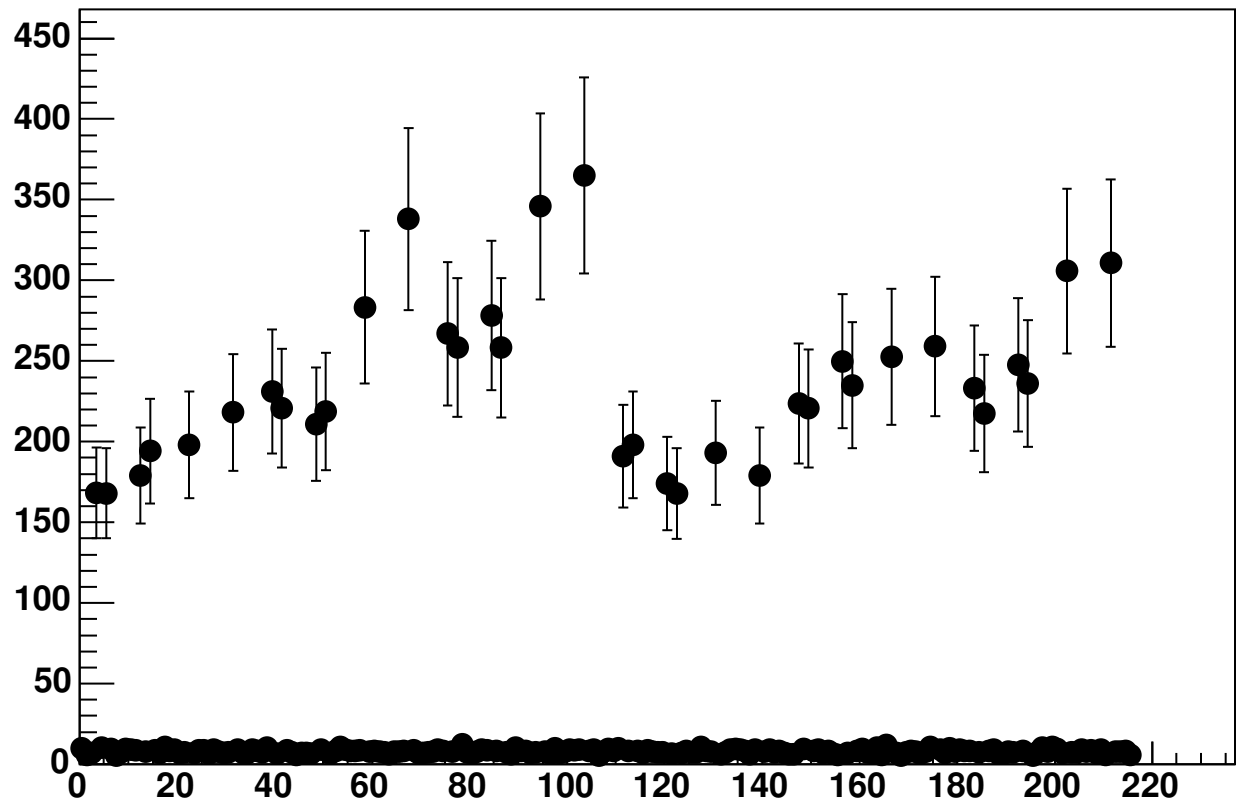
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



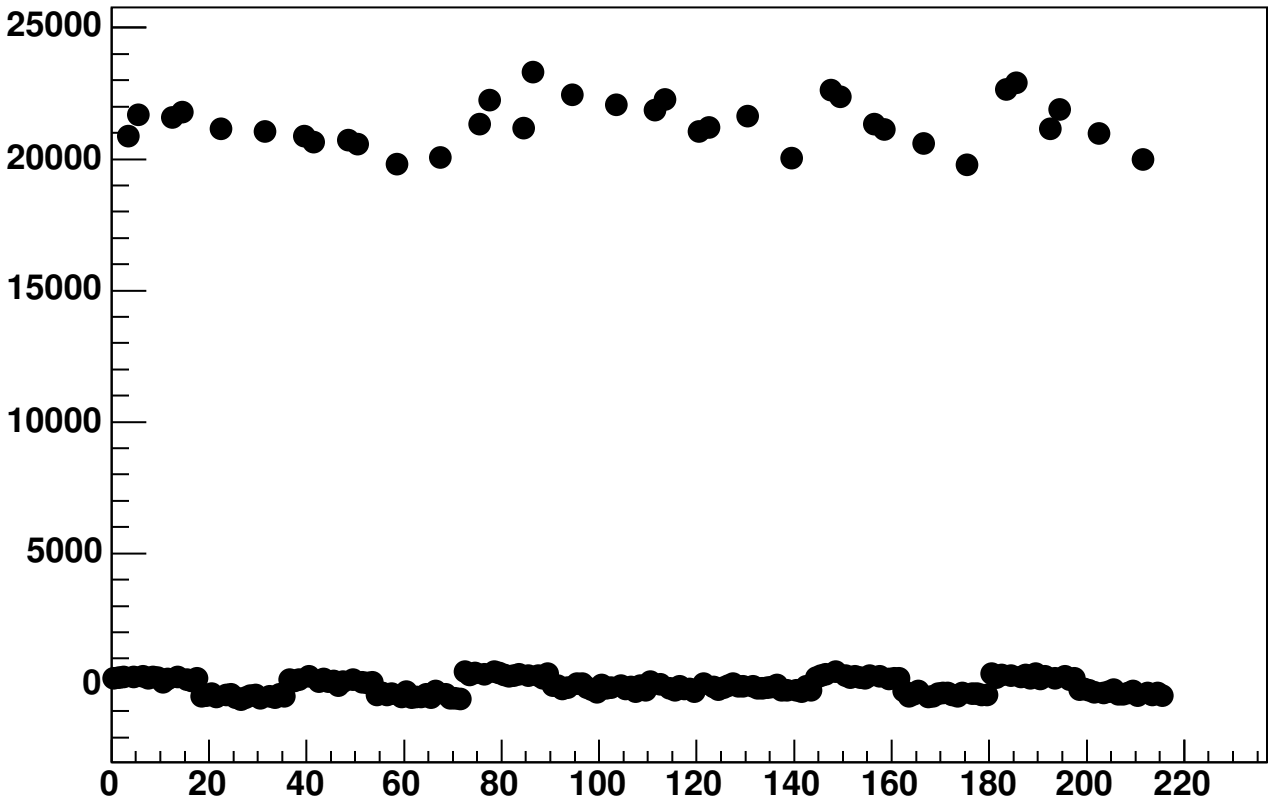
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



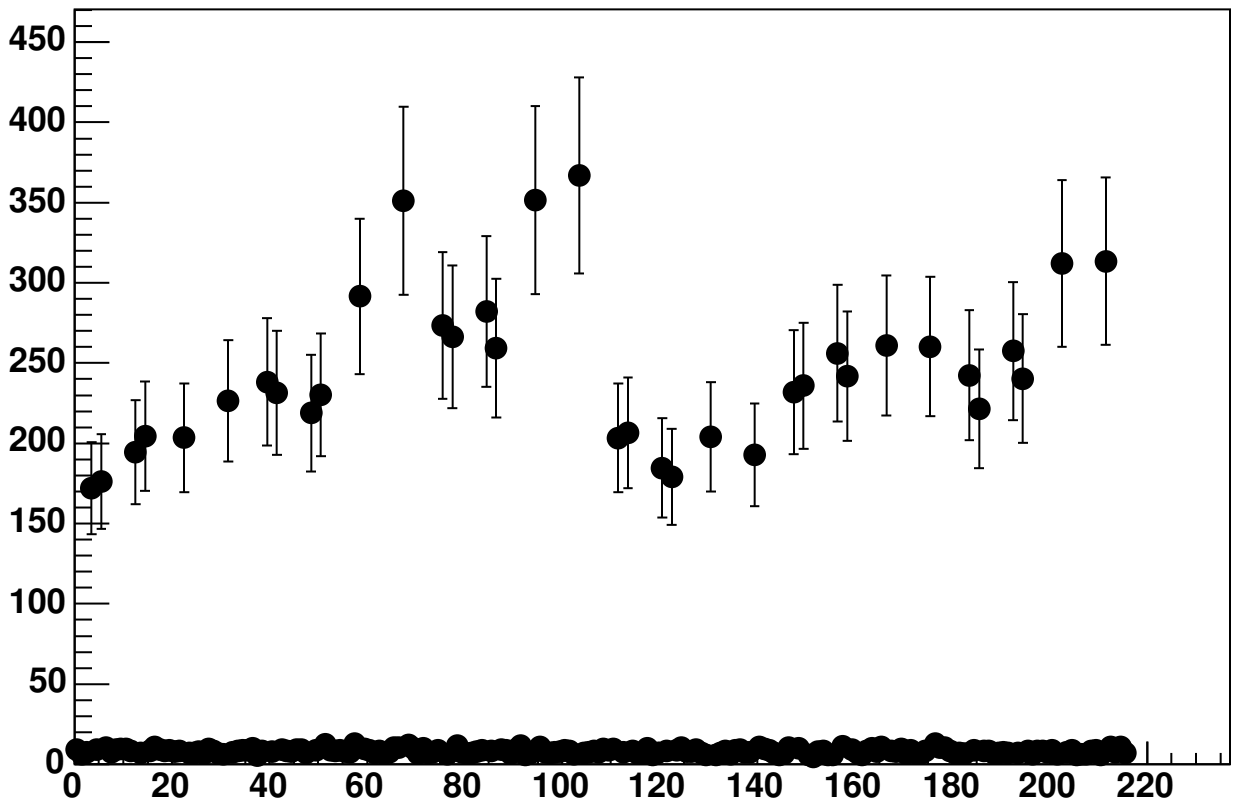
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



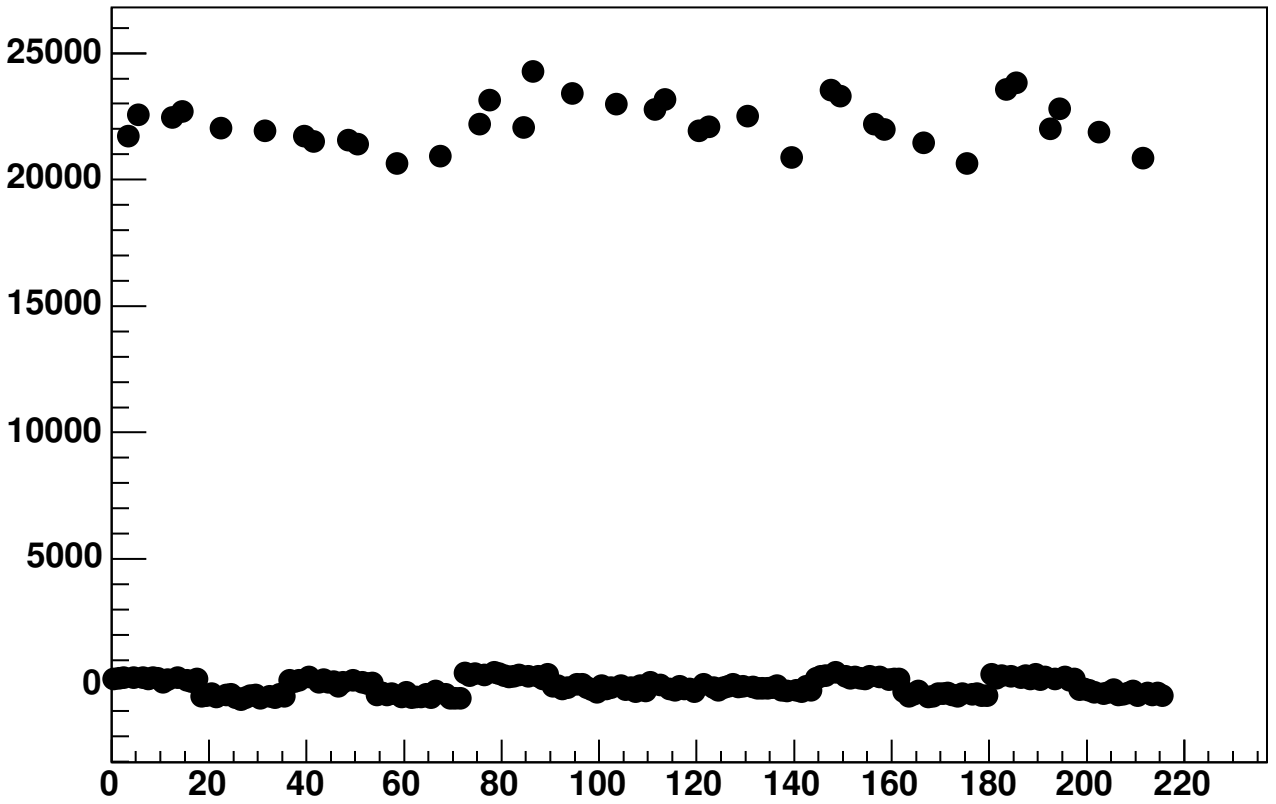
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



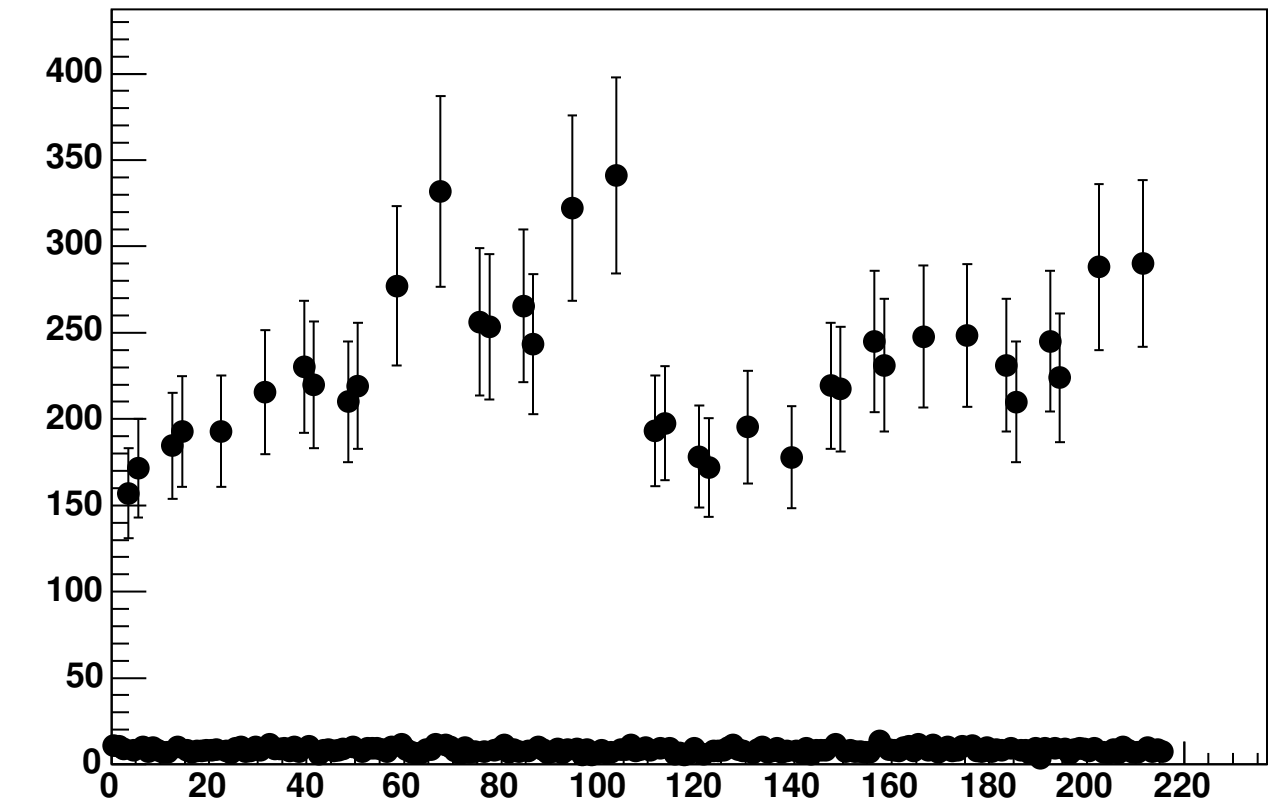
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



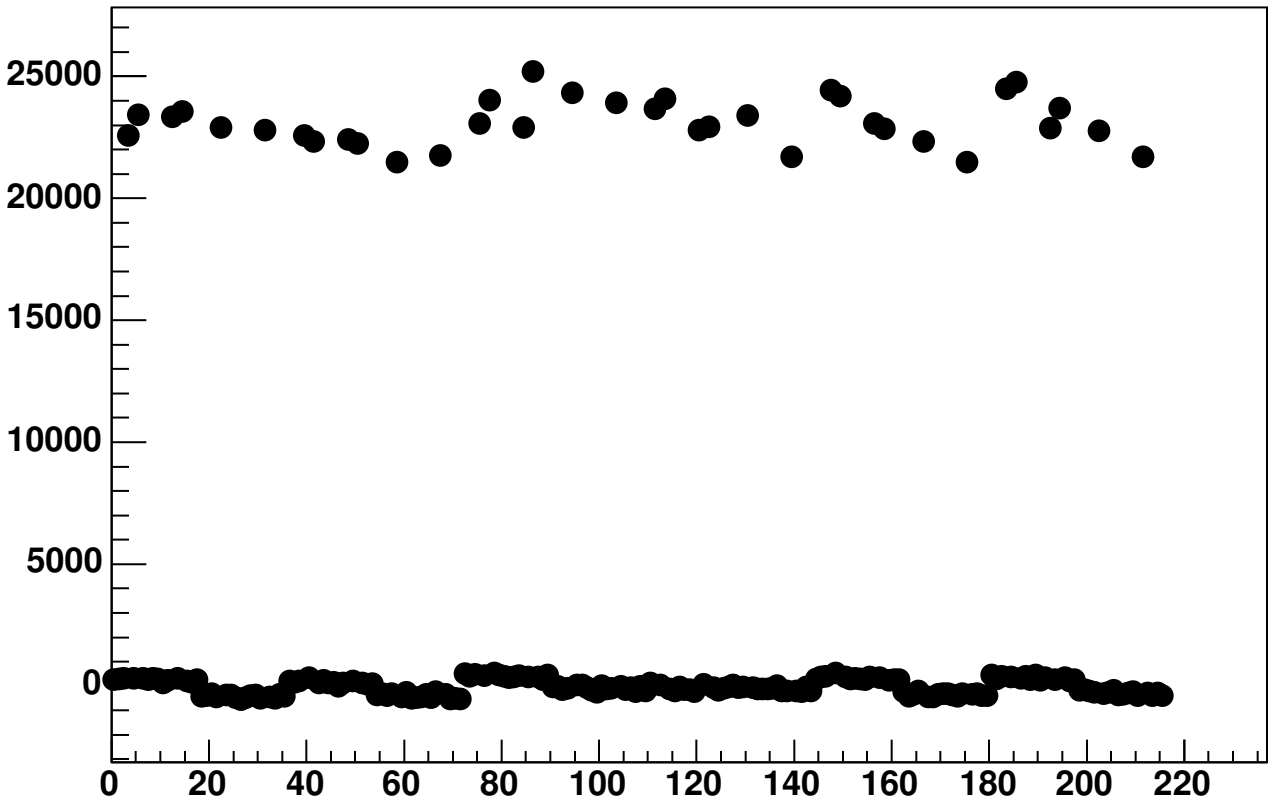
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



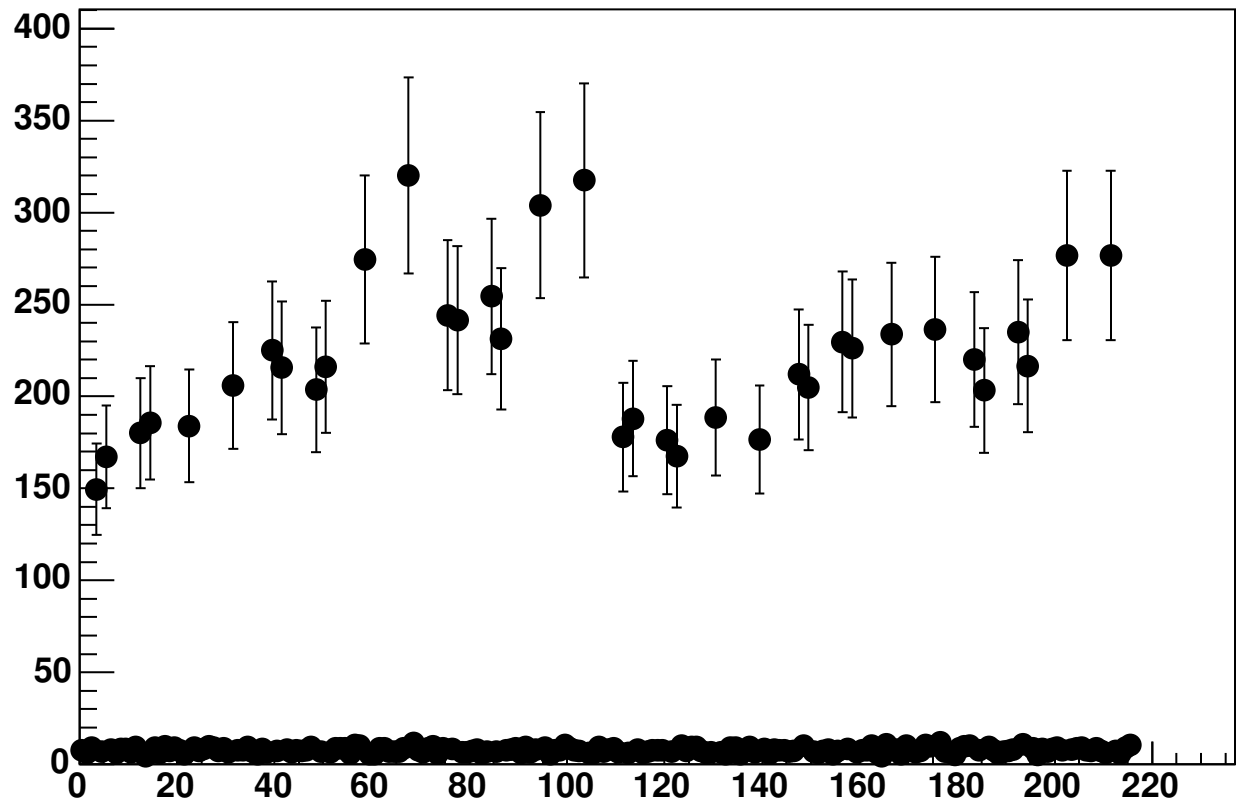
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



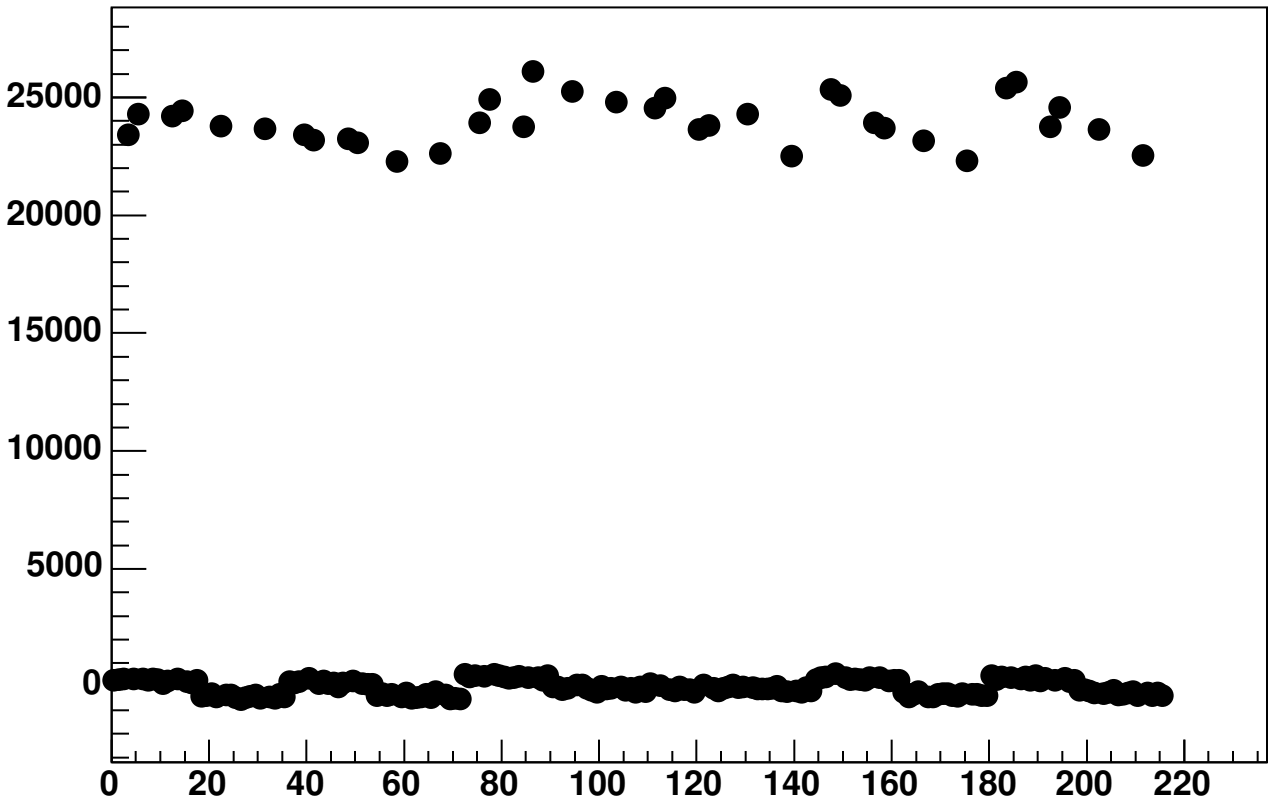
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



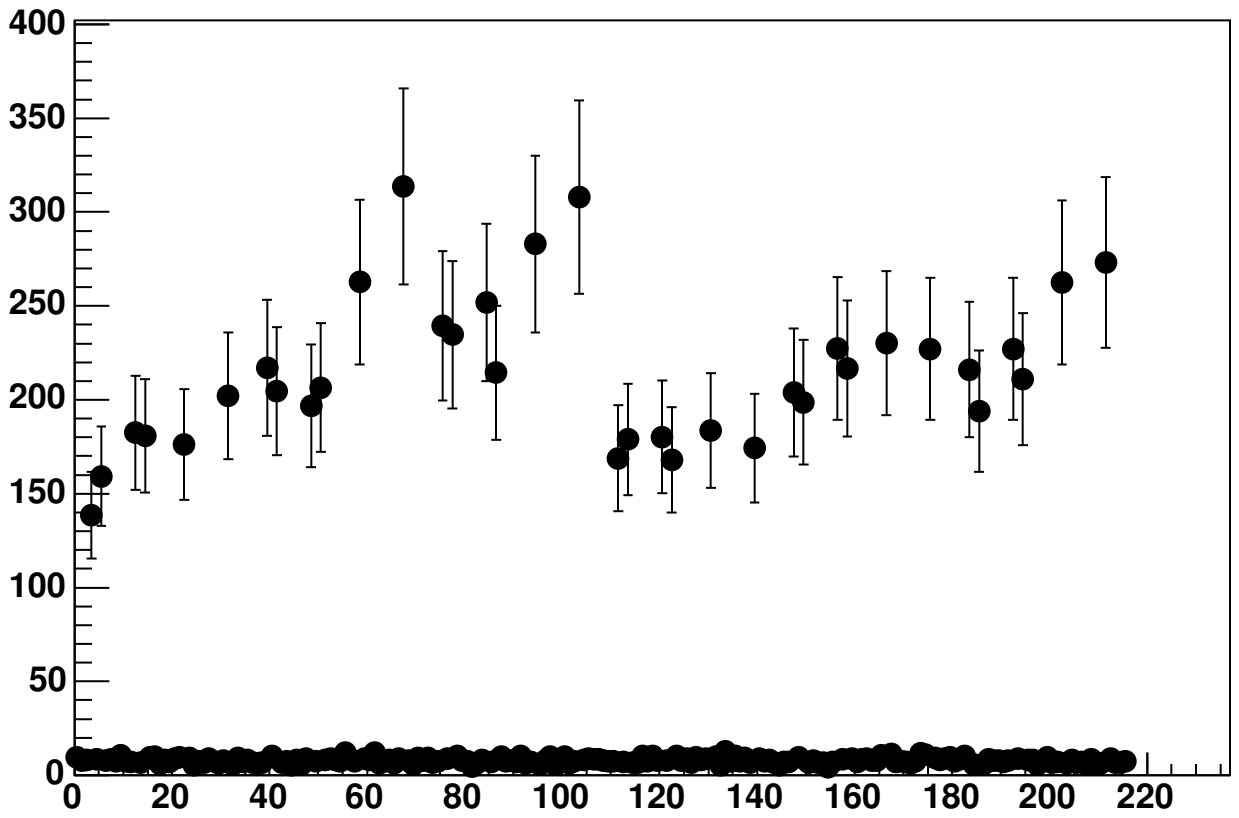
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



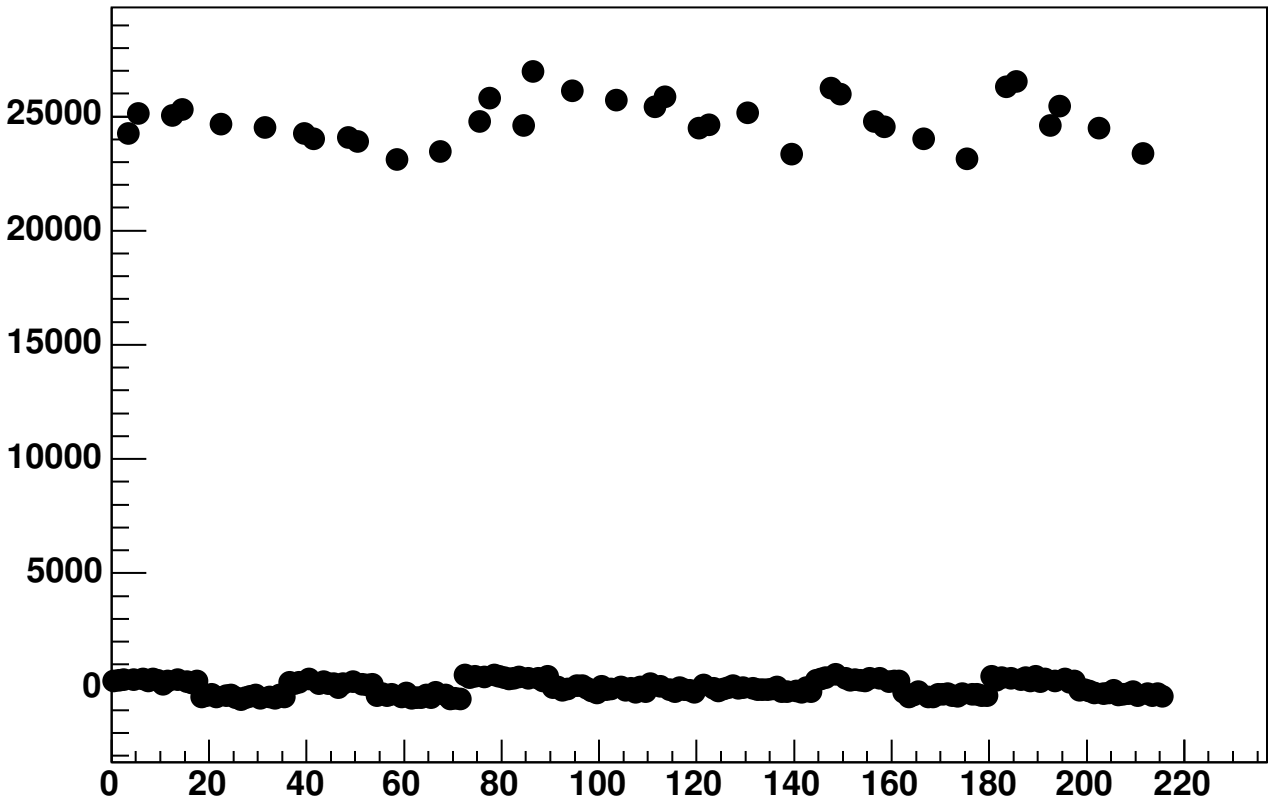
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



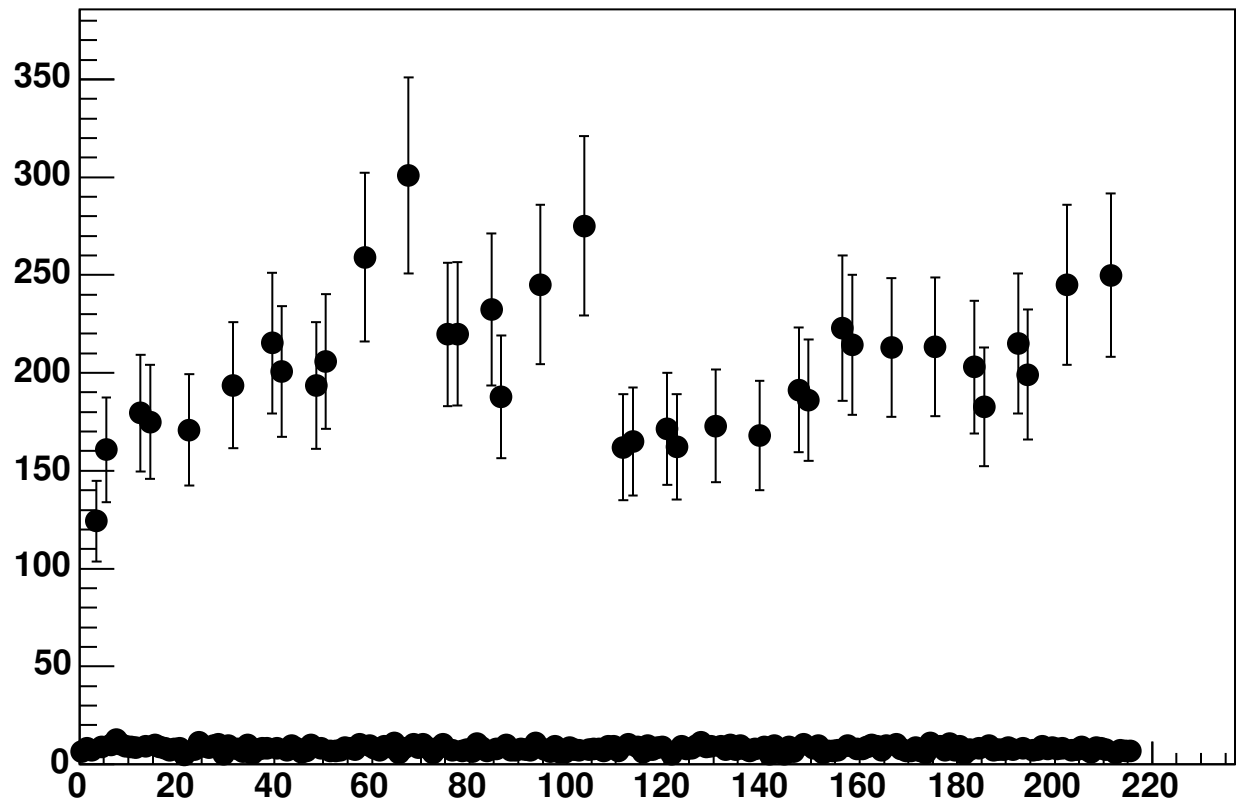
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



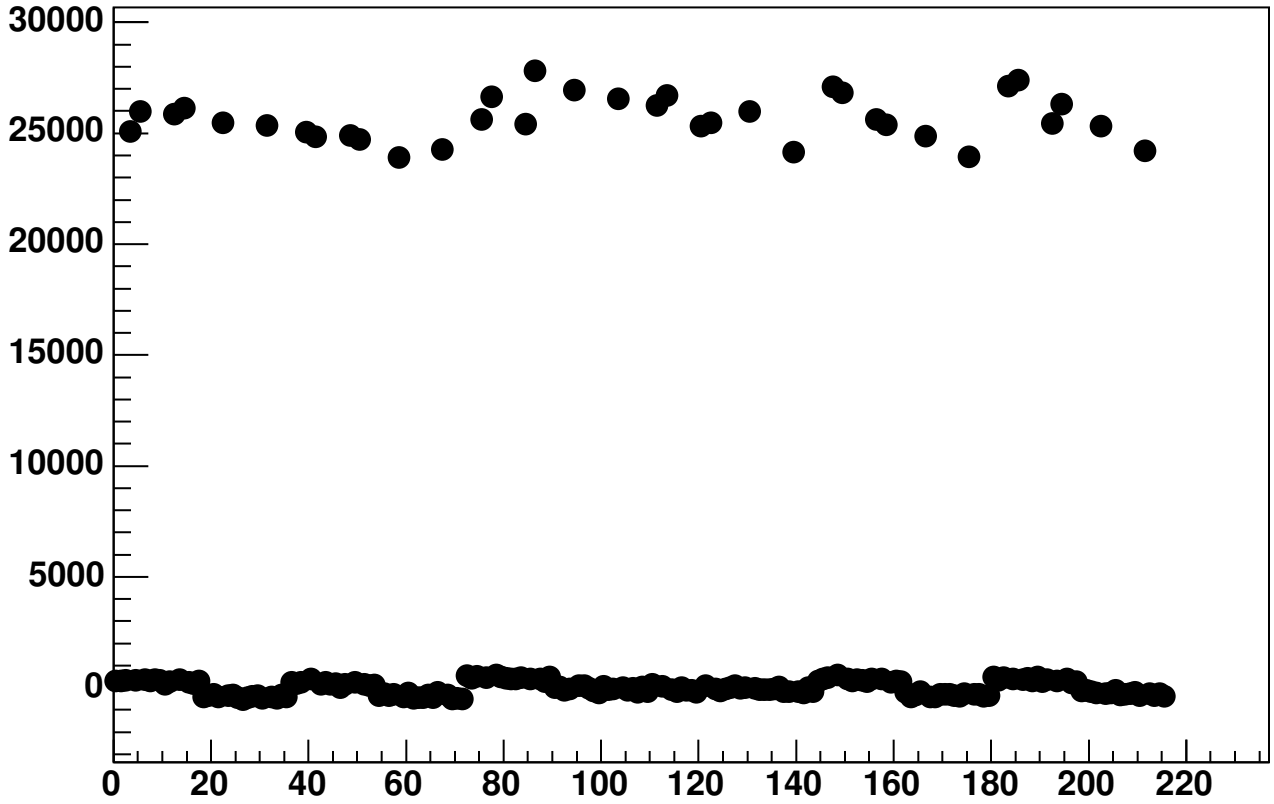
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



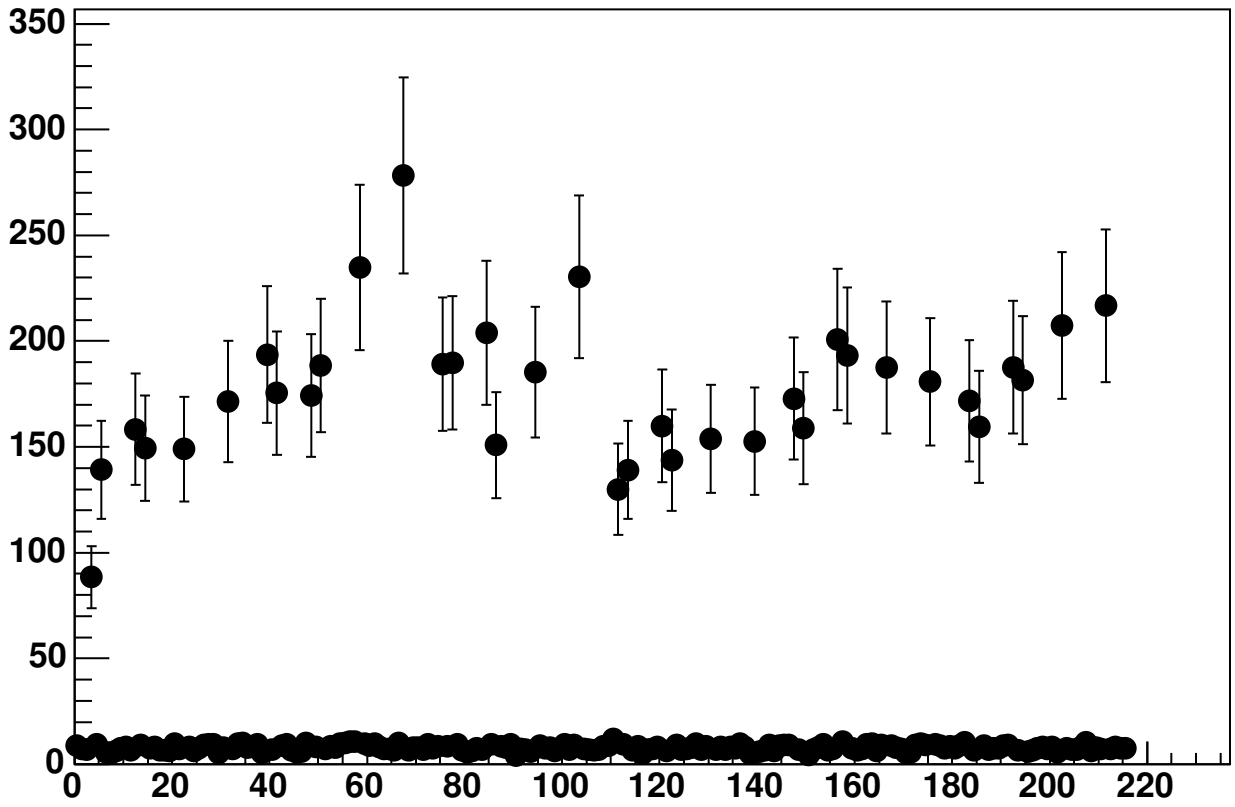
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



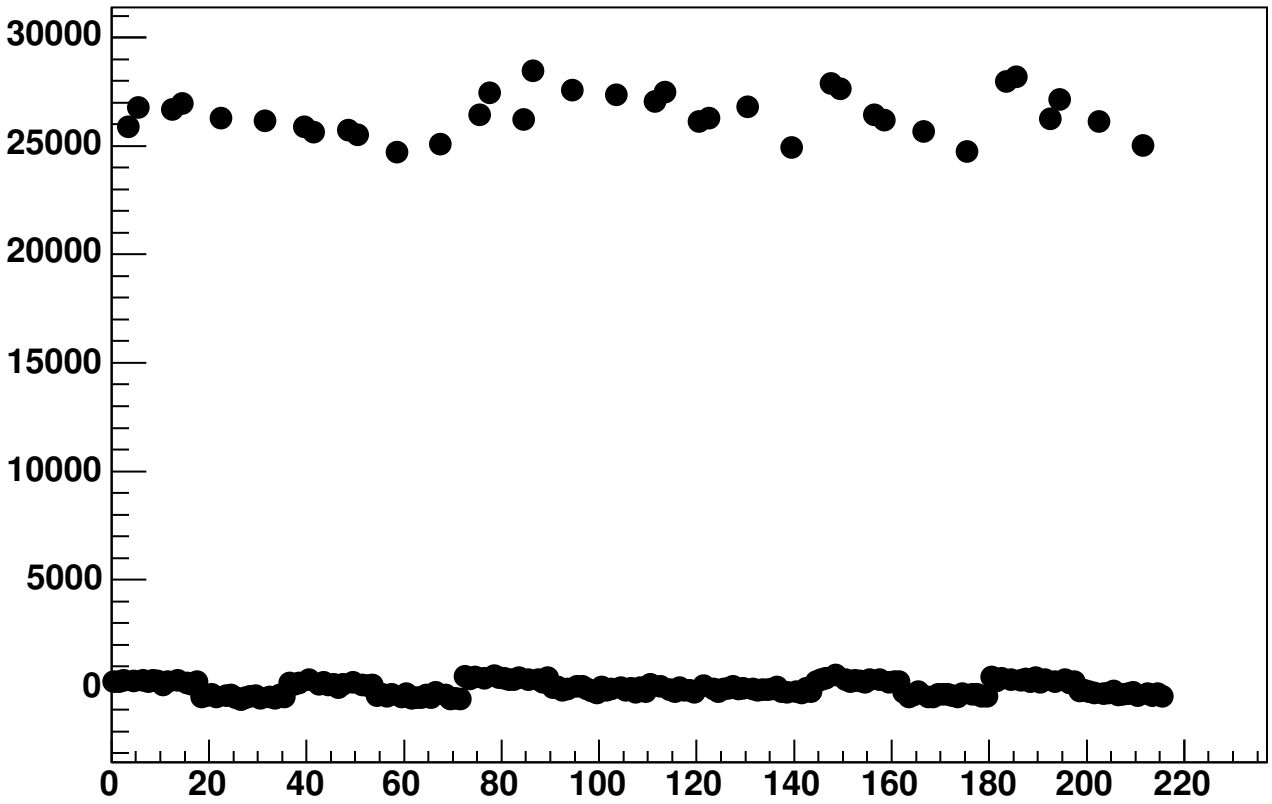
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



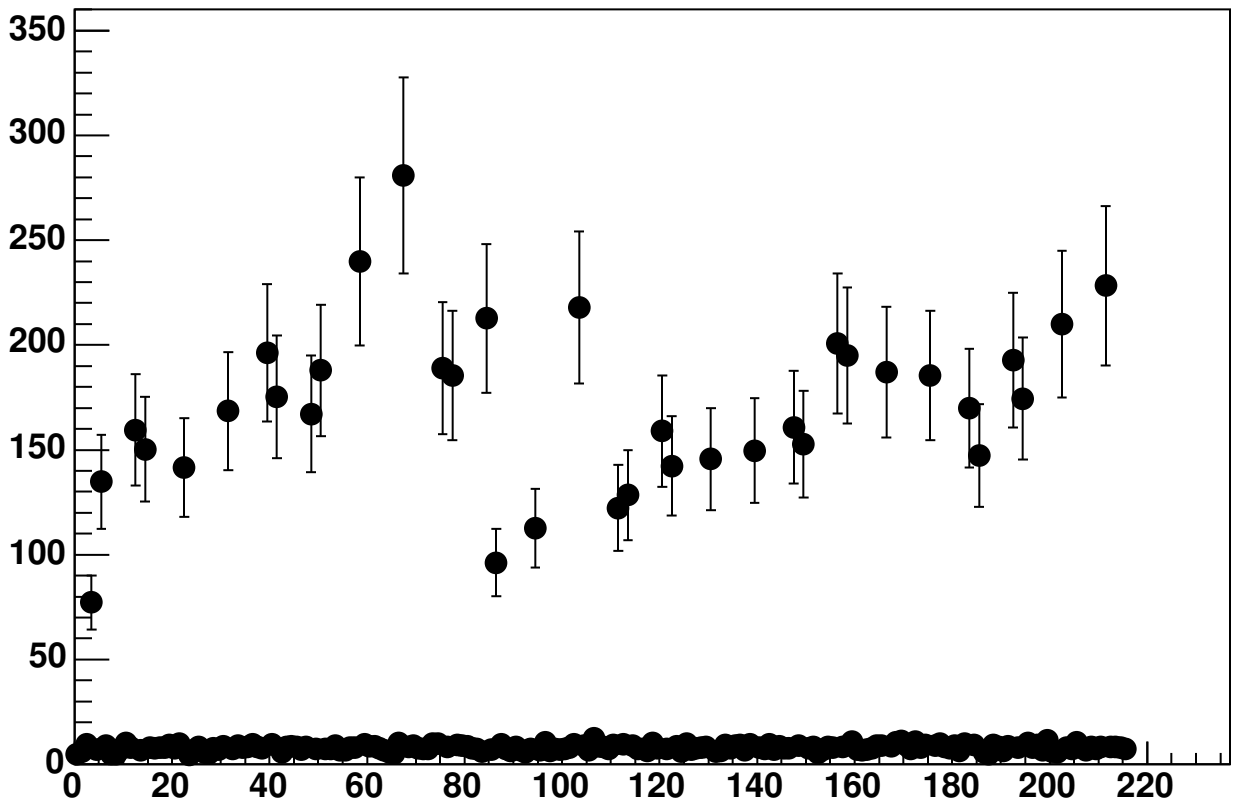
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



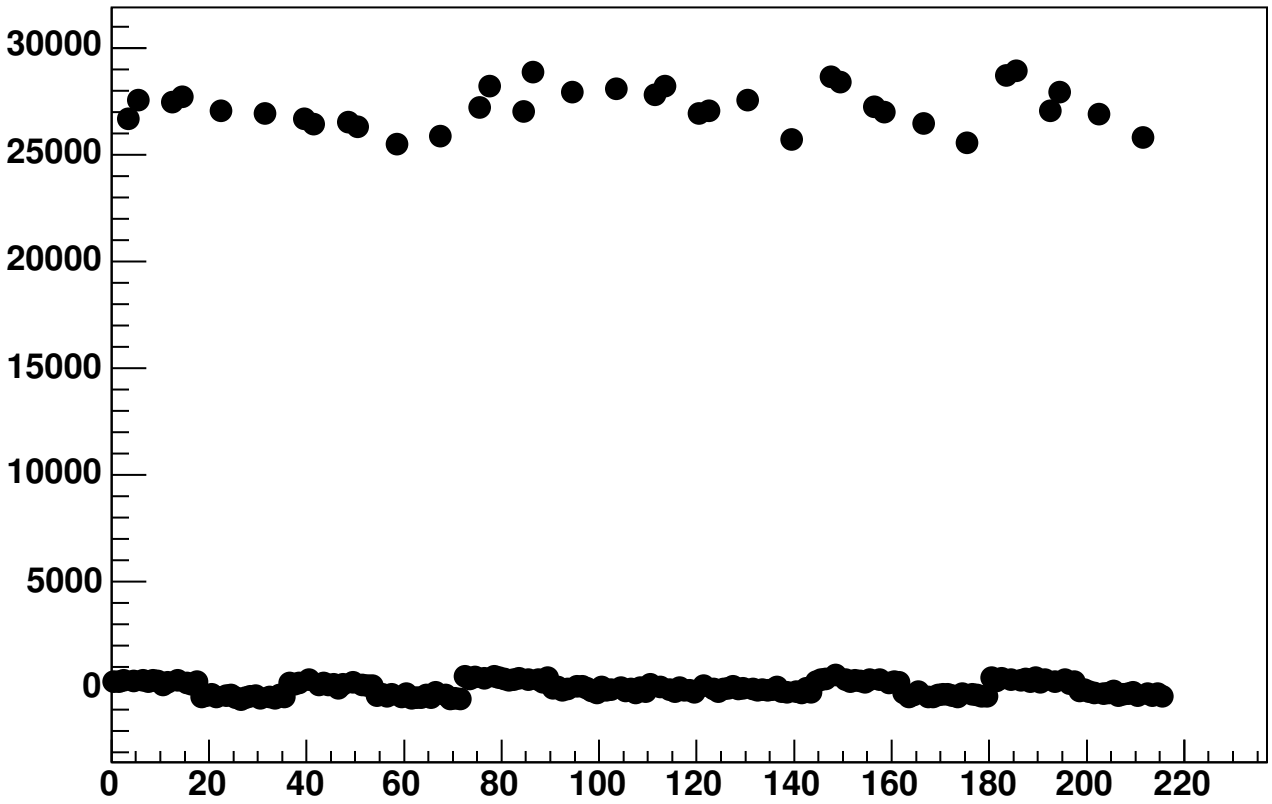
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



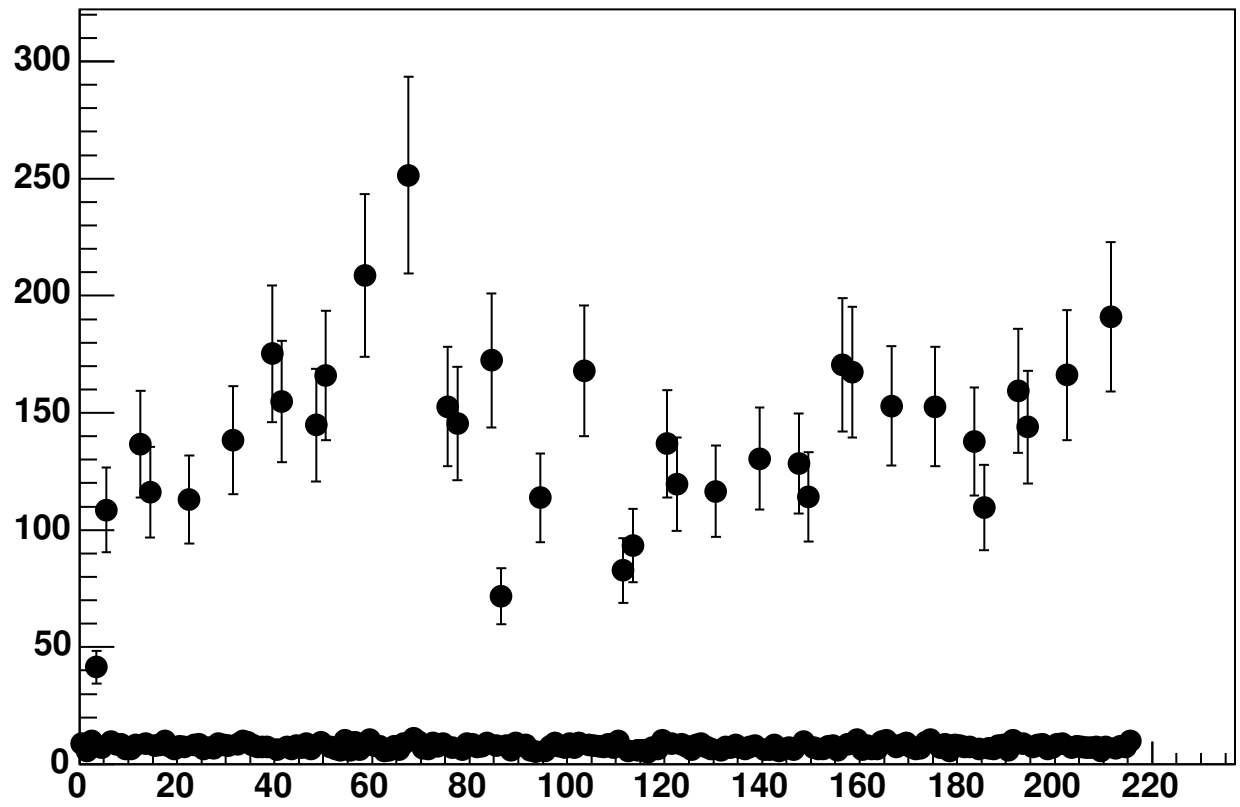
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



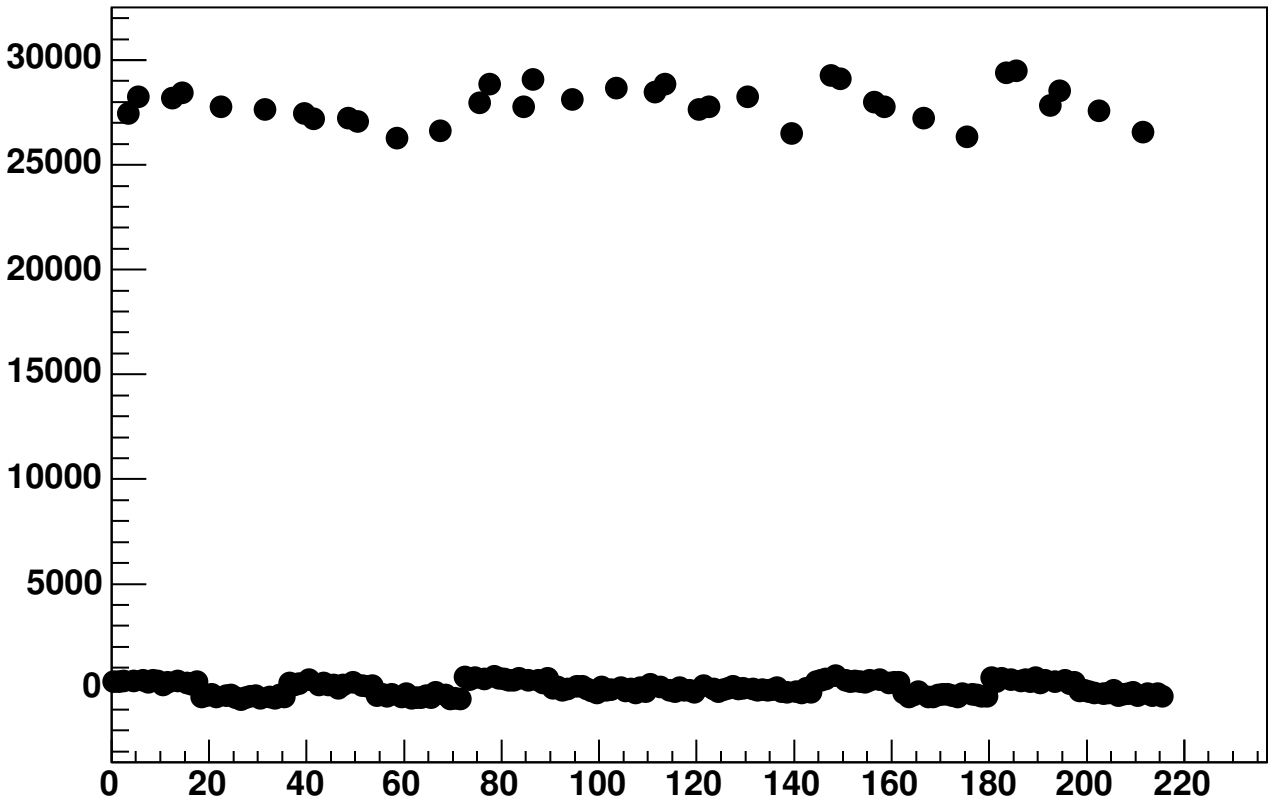
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



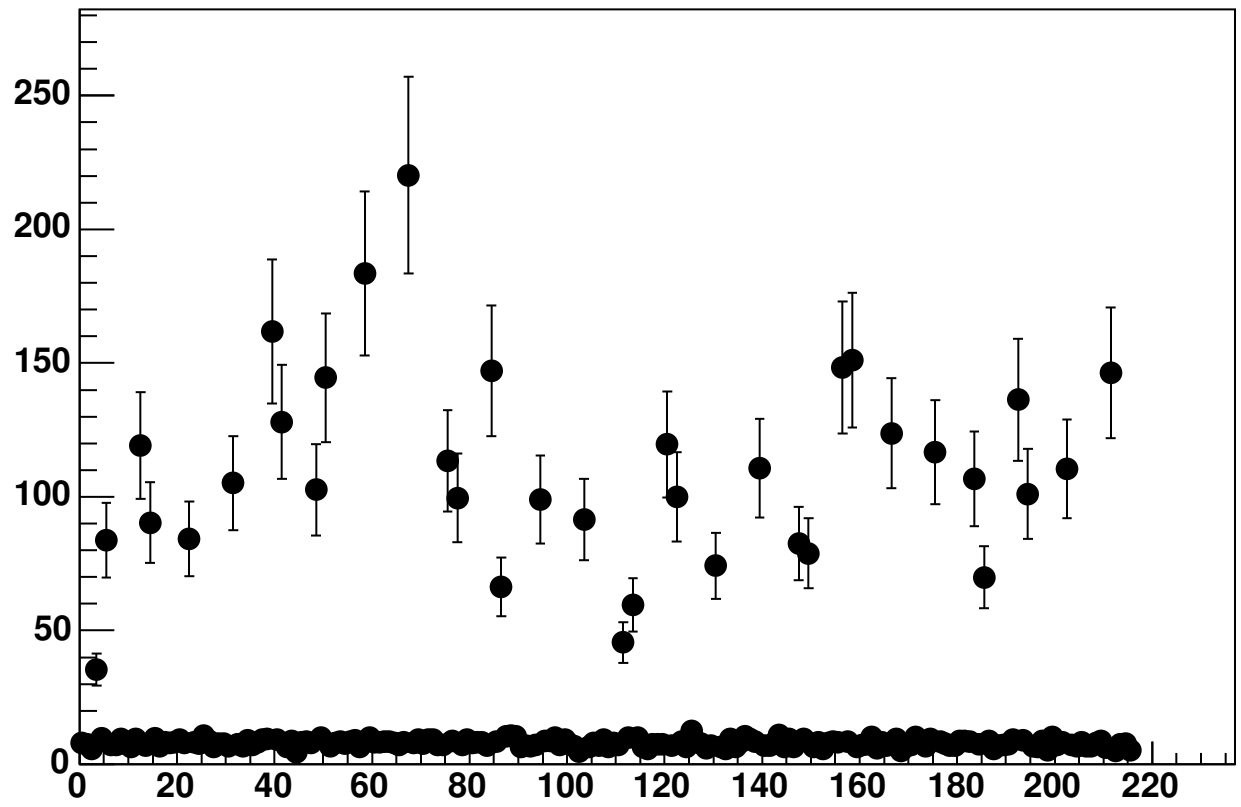
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



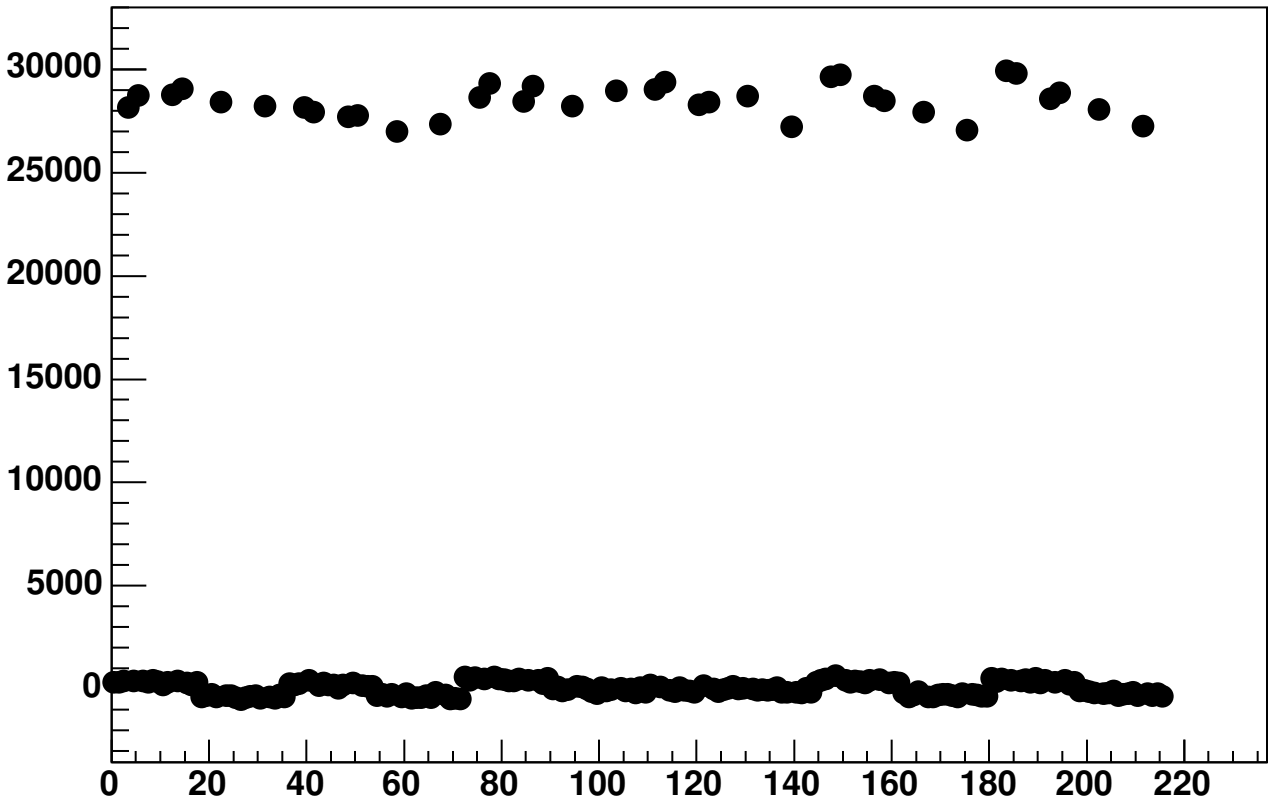
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



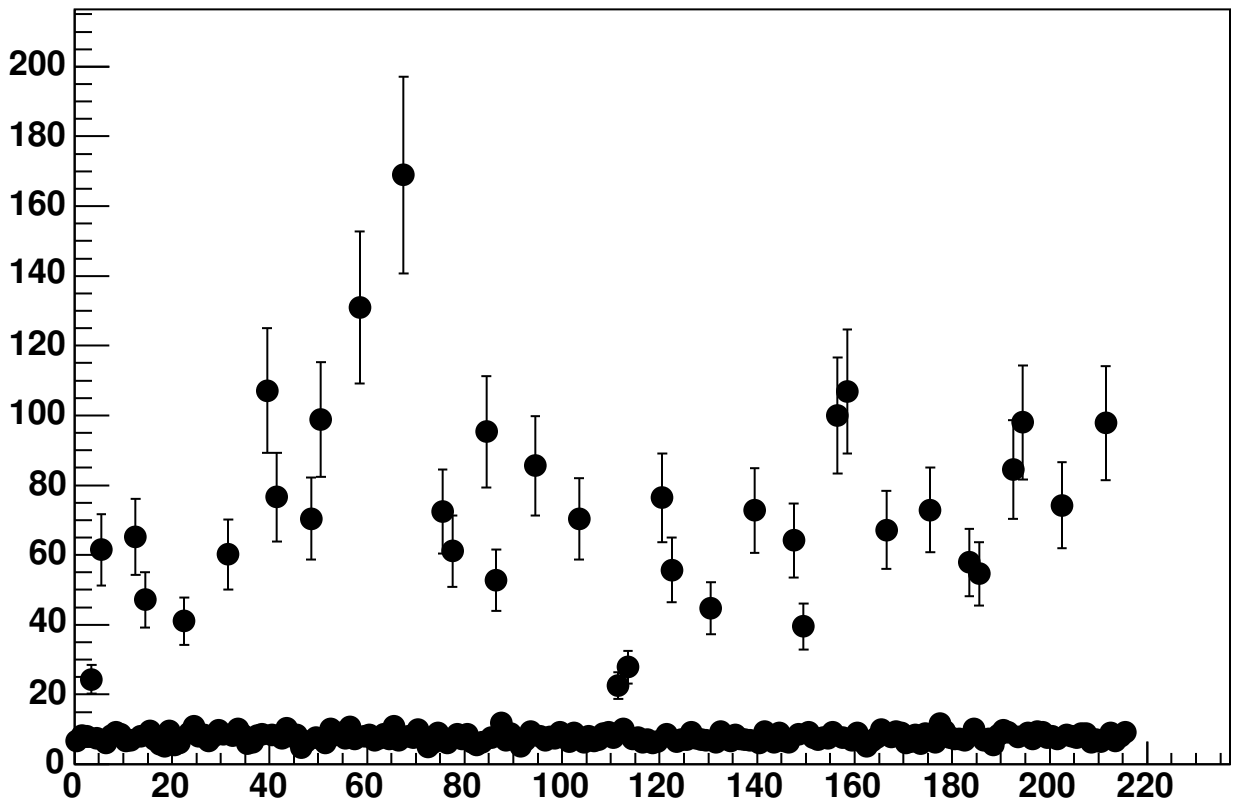
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



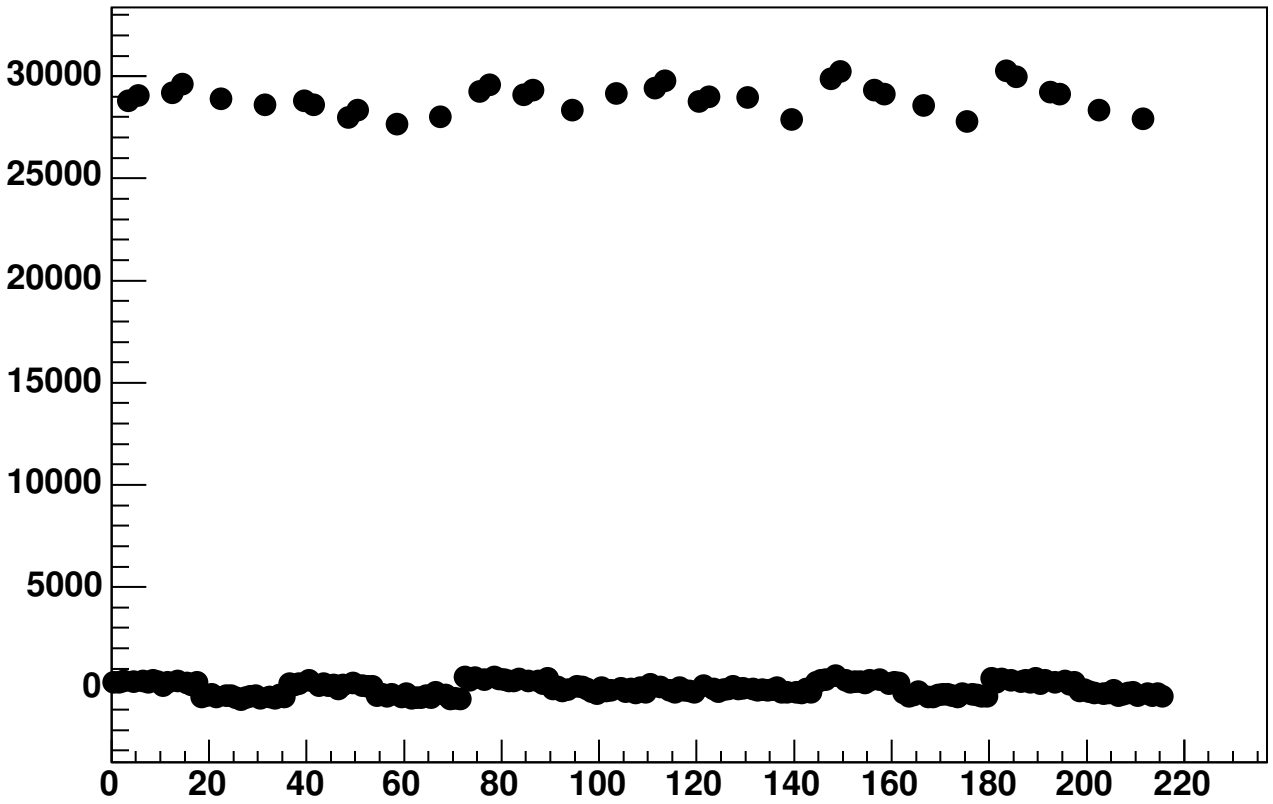
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



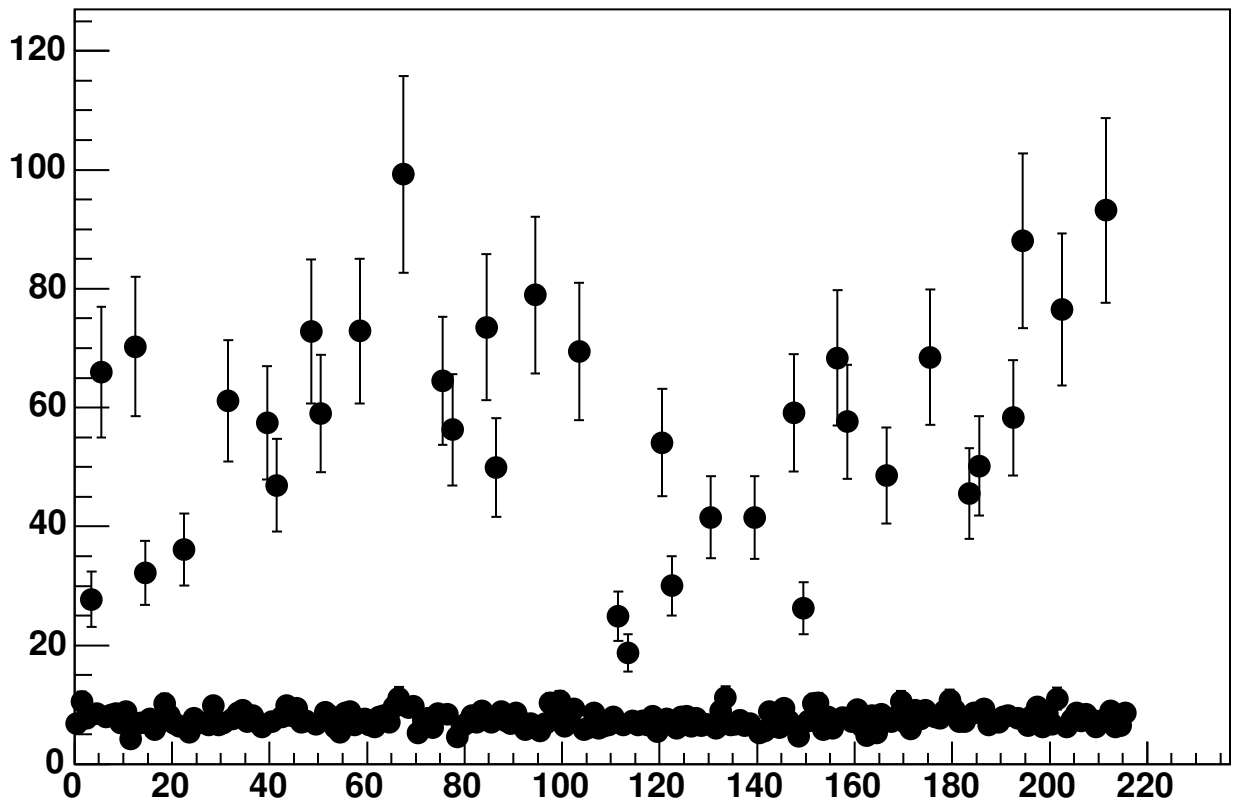
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



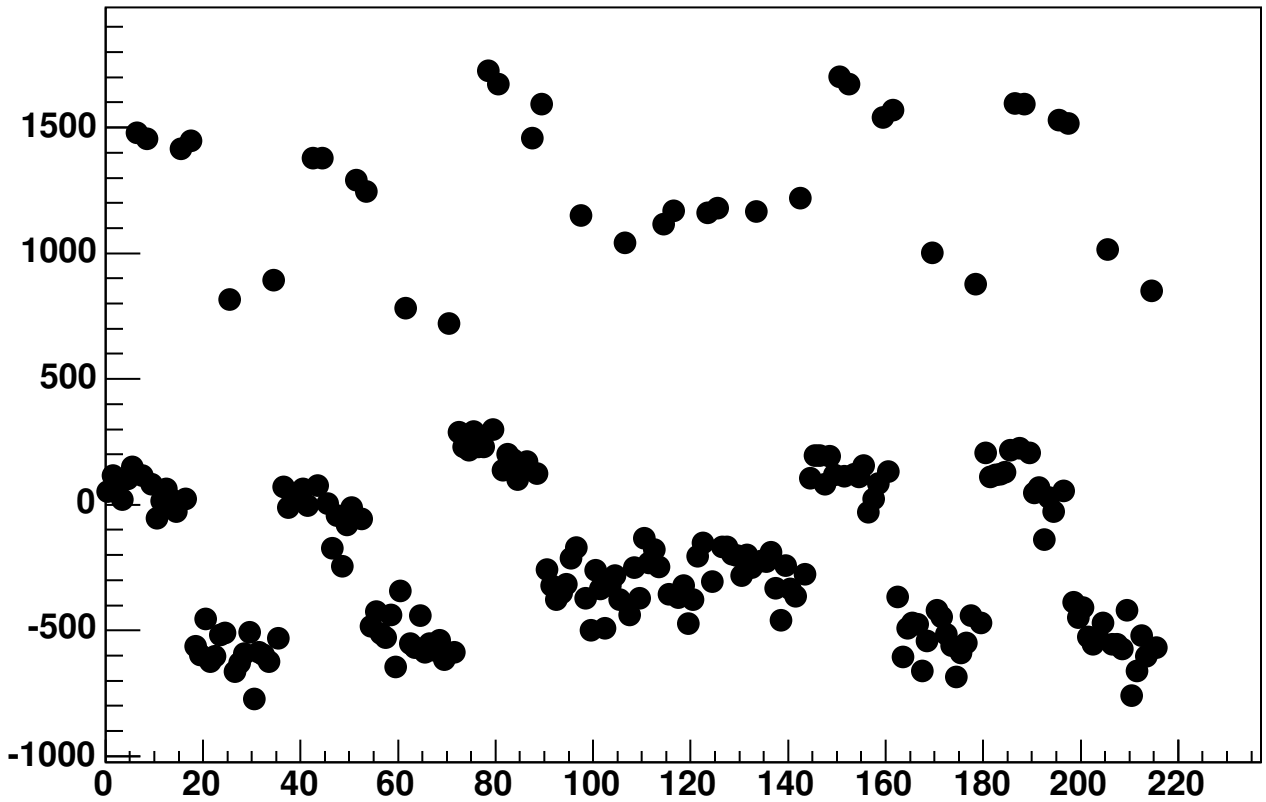
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



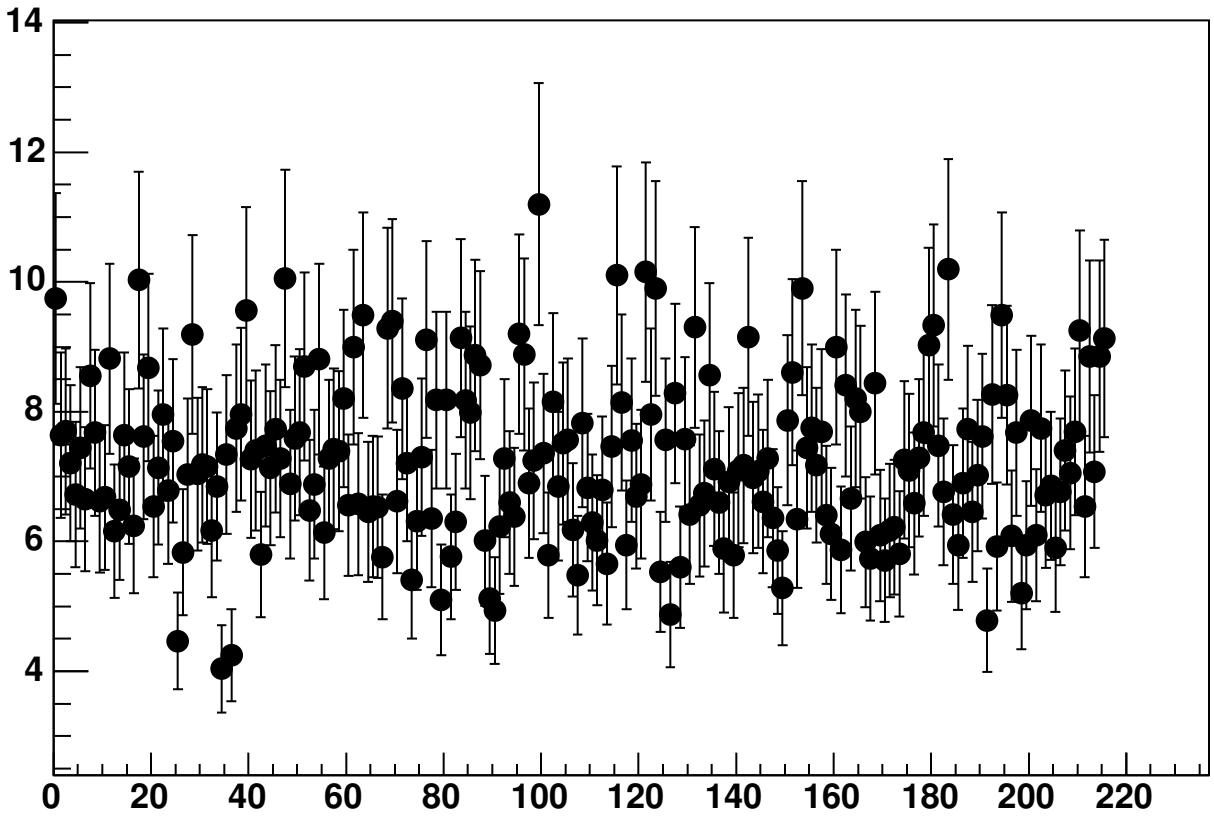
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



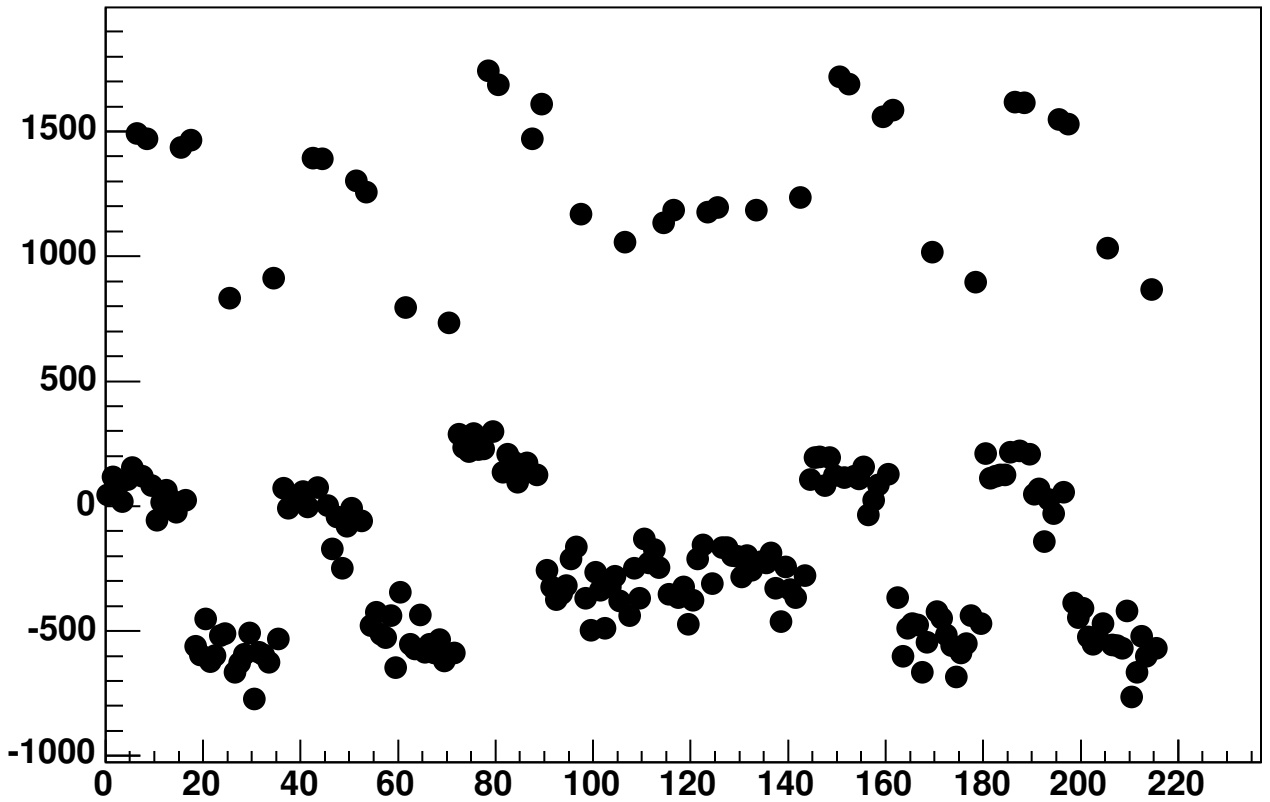
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



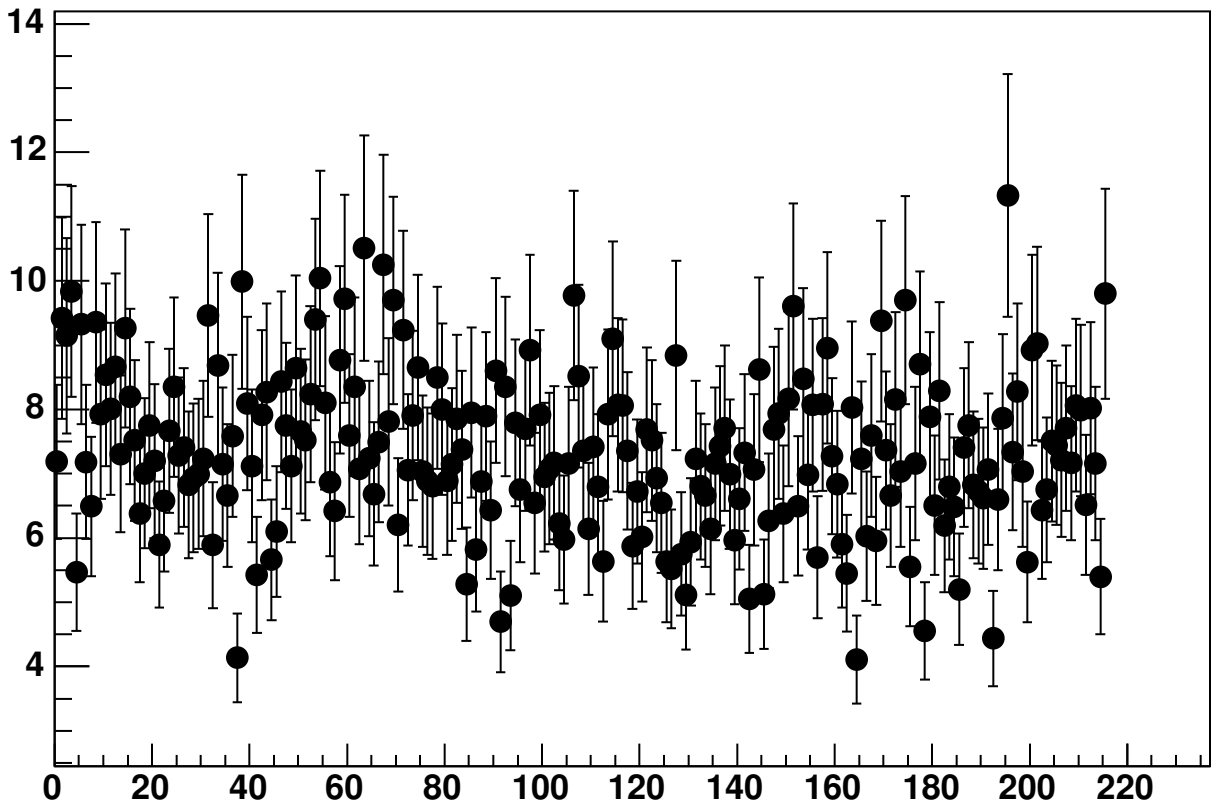
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



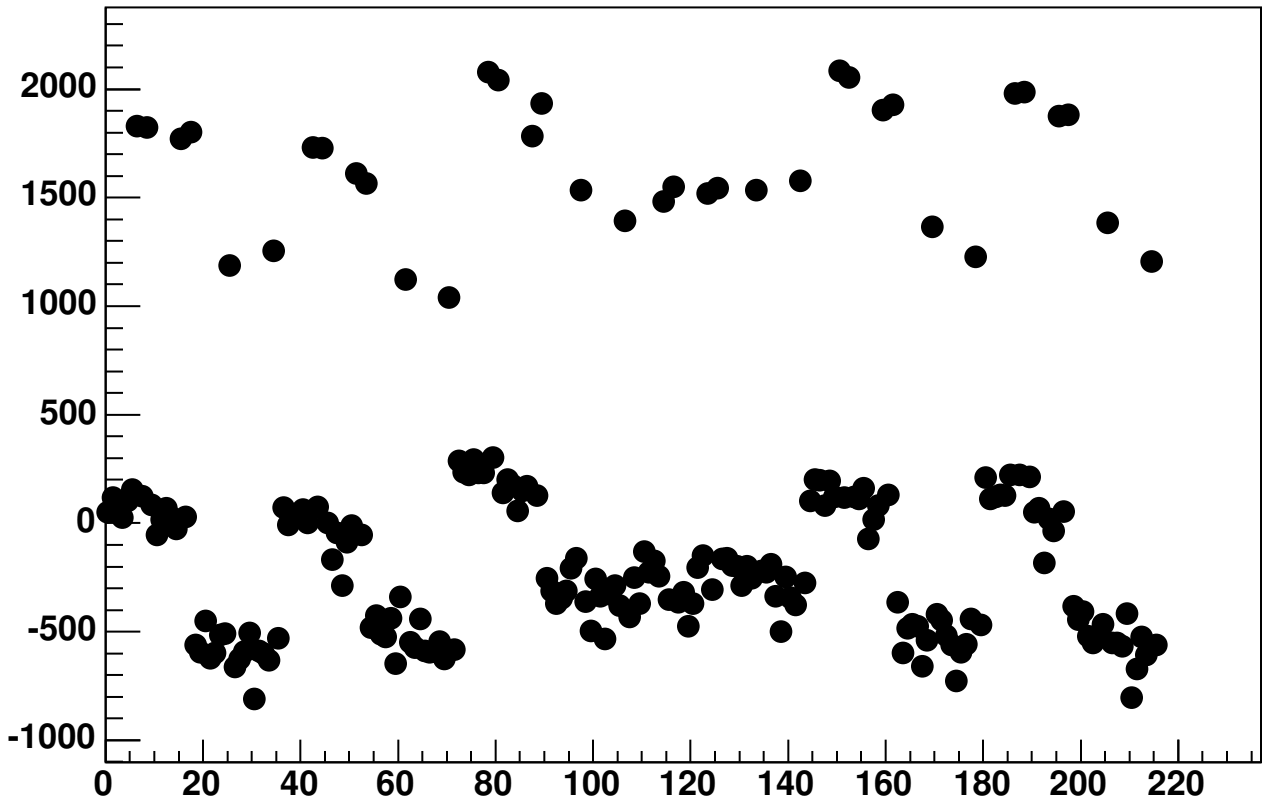
Enable 2, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



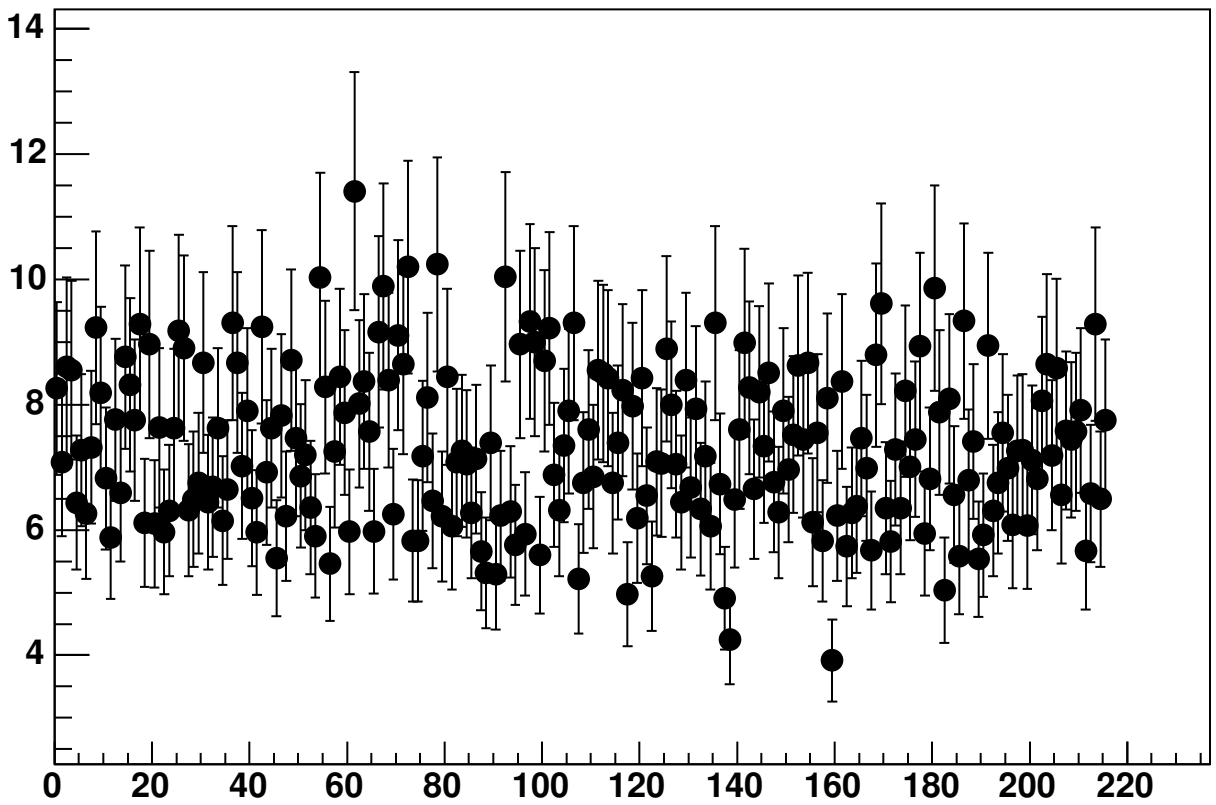
Enable 2, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



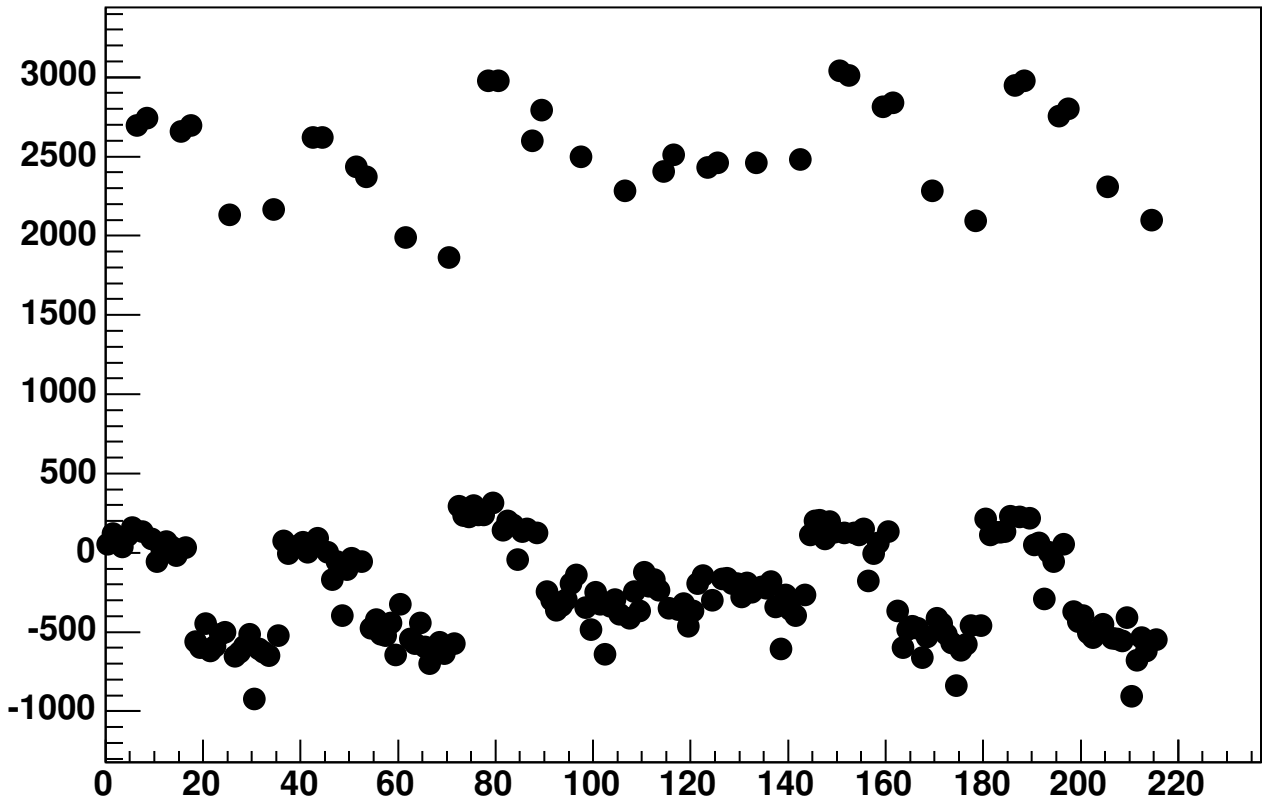
Enable 2, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



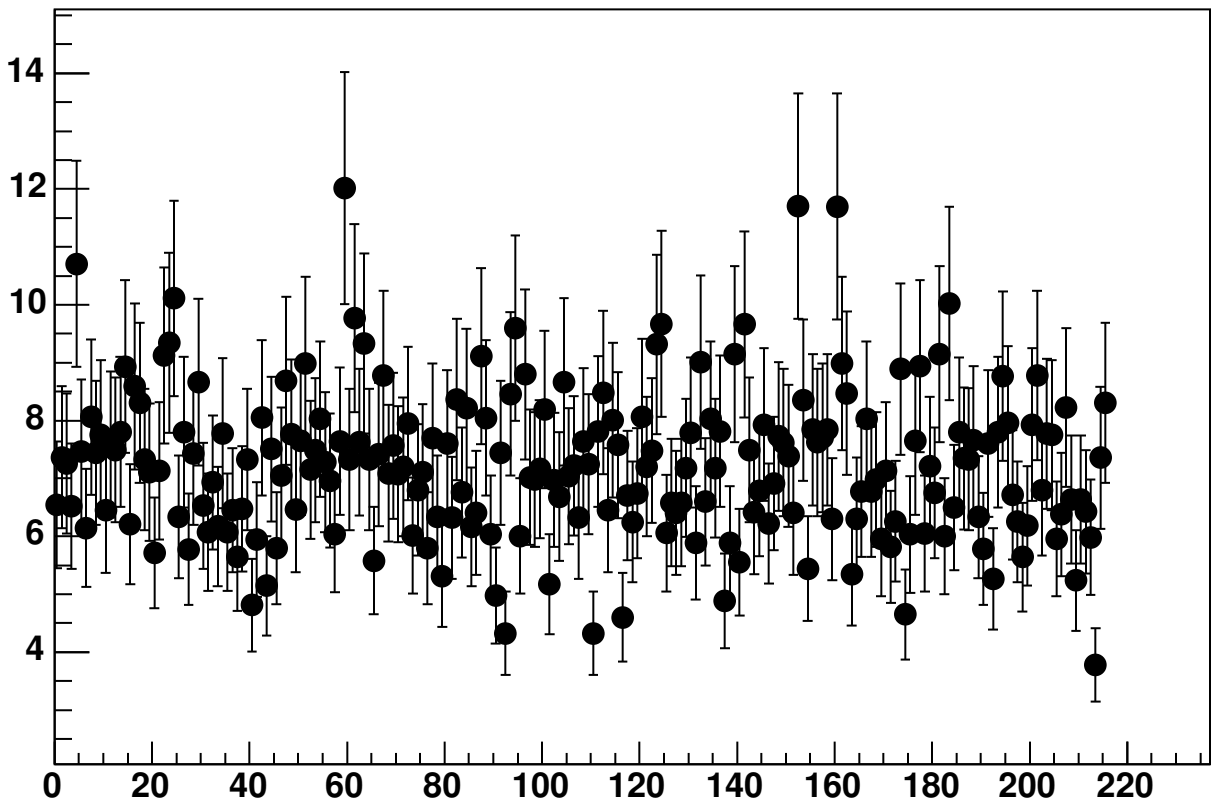
Enable 2, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



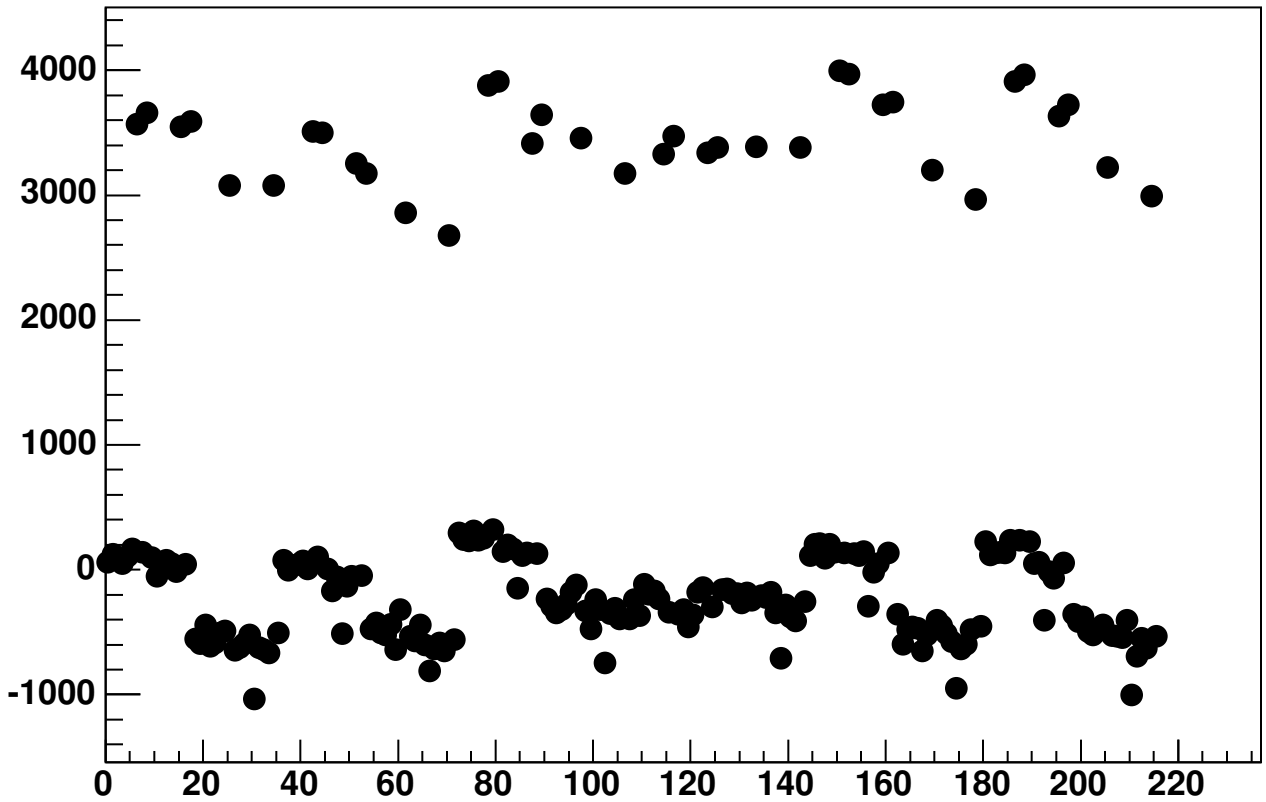
Enable 2, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



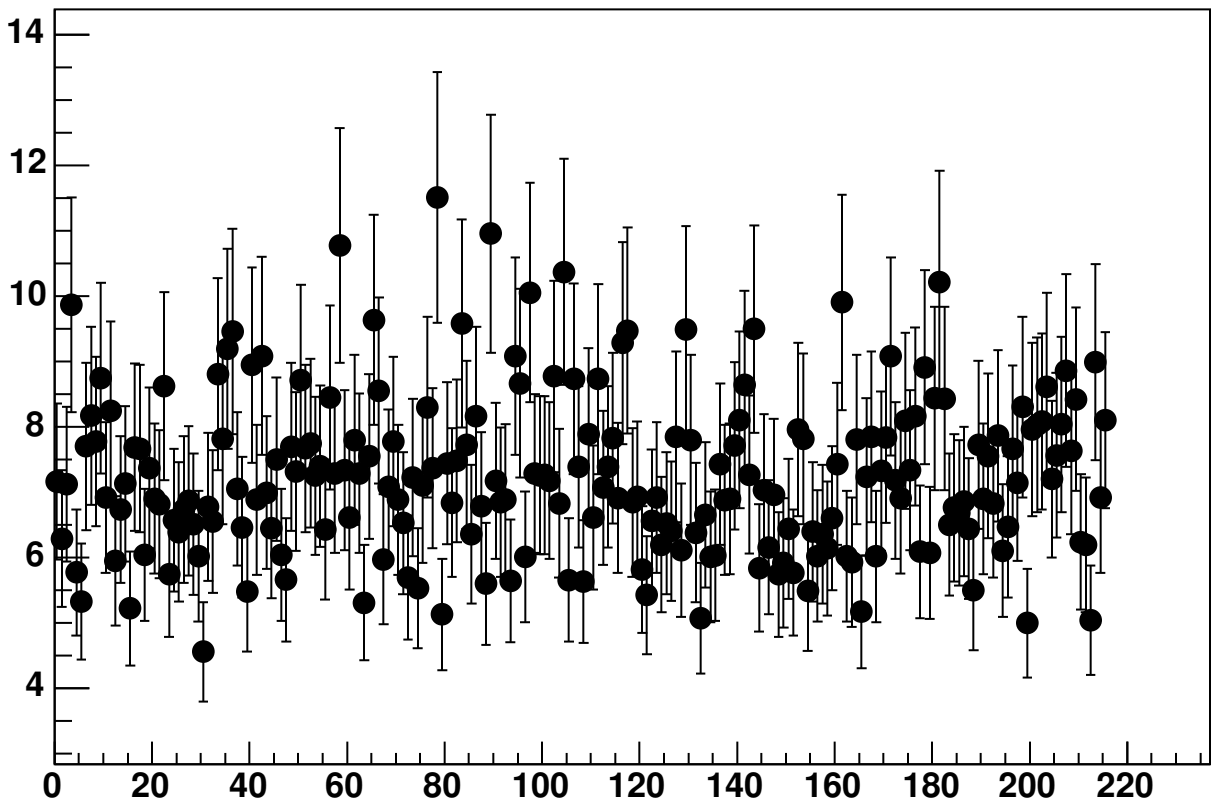
Enable 2, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



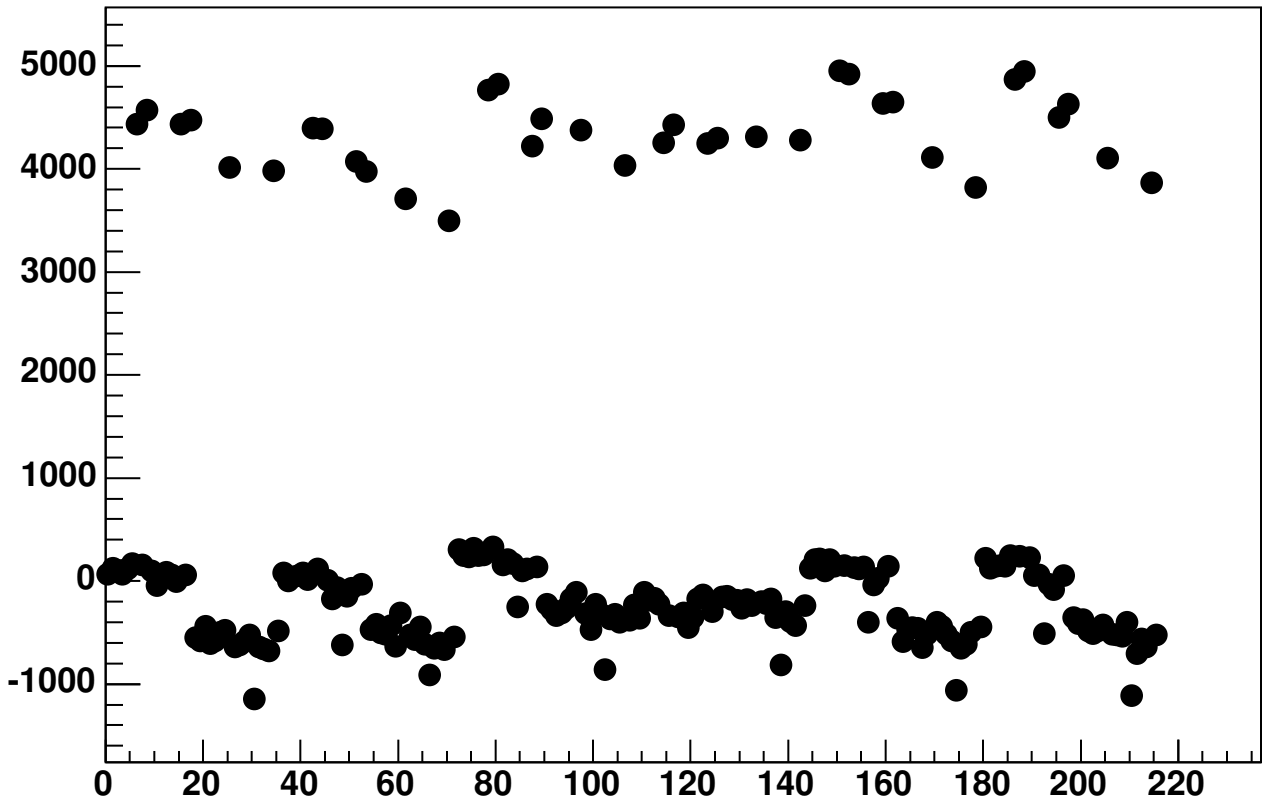
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



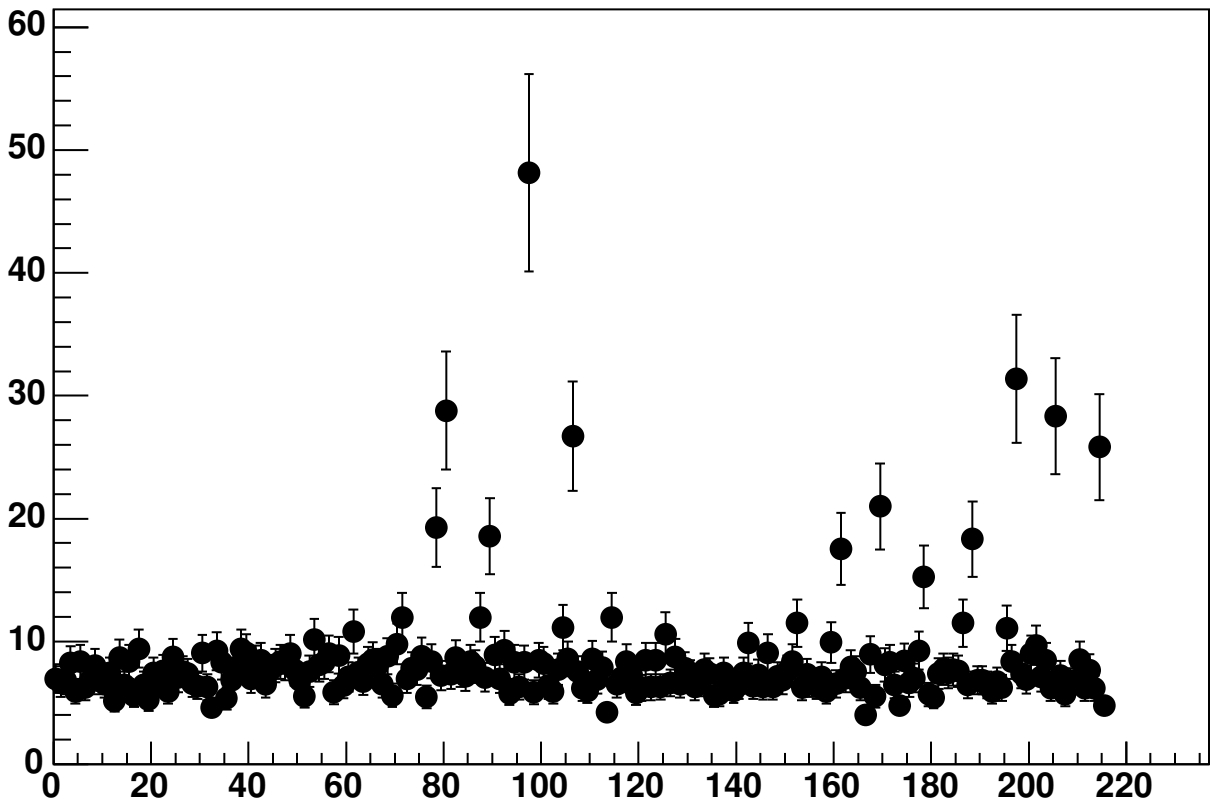
Enable 2, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



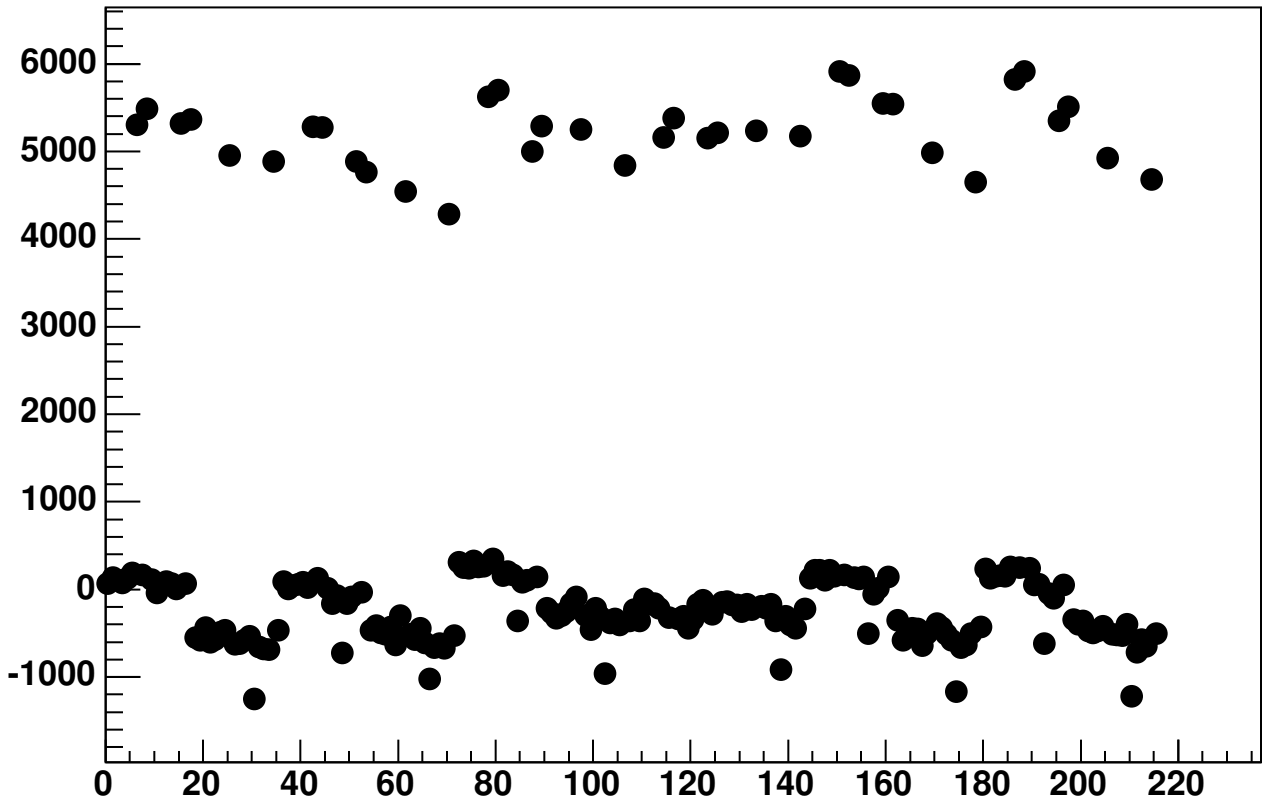
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



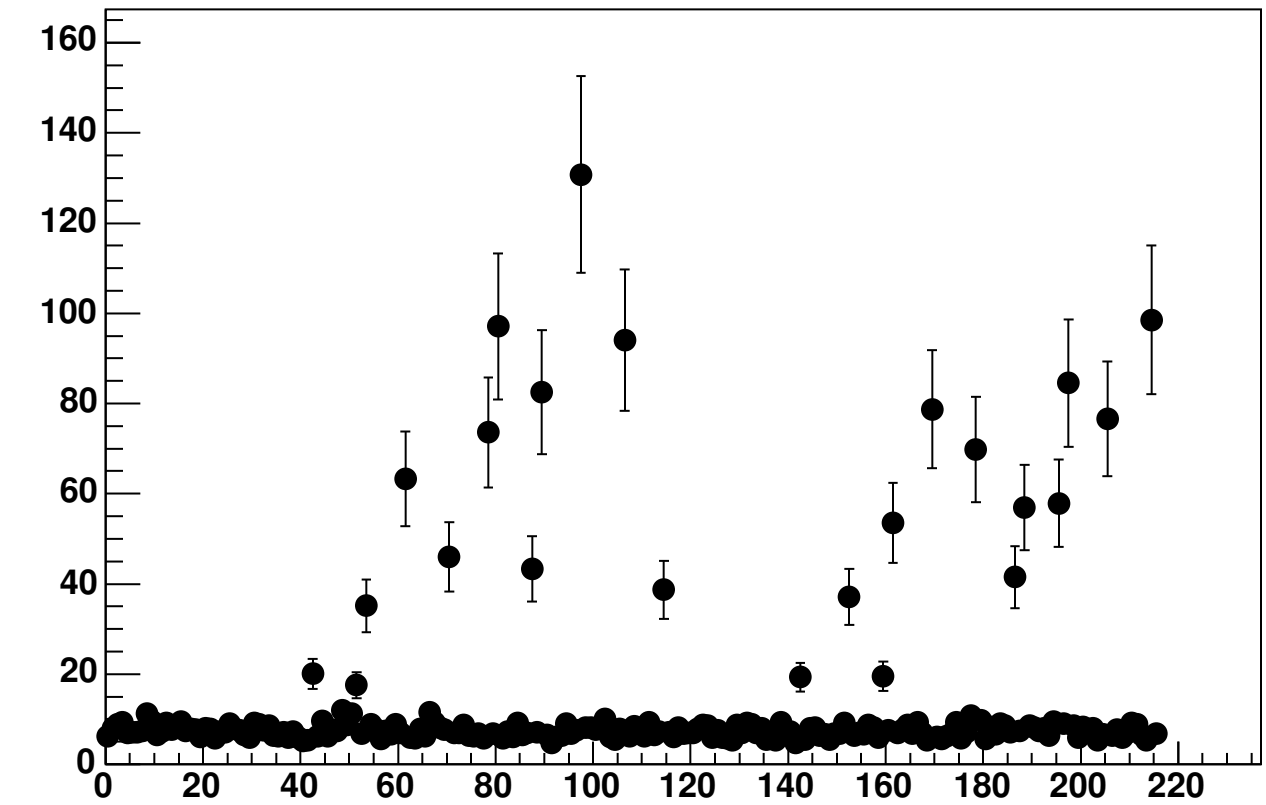
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



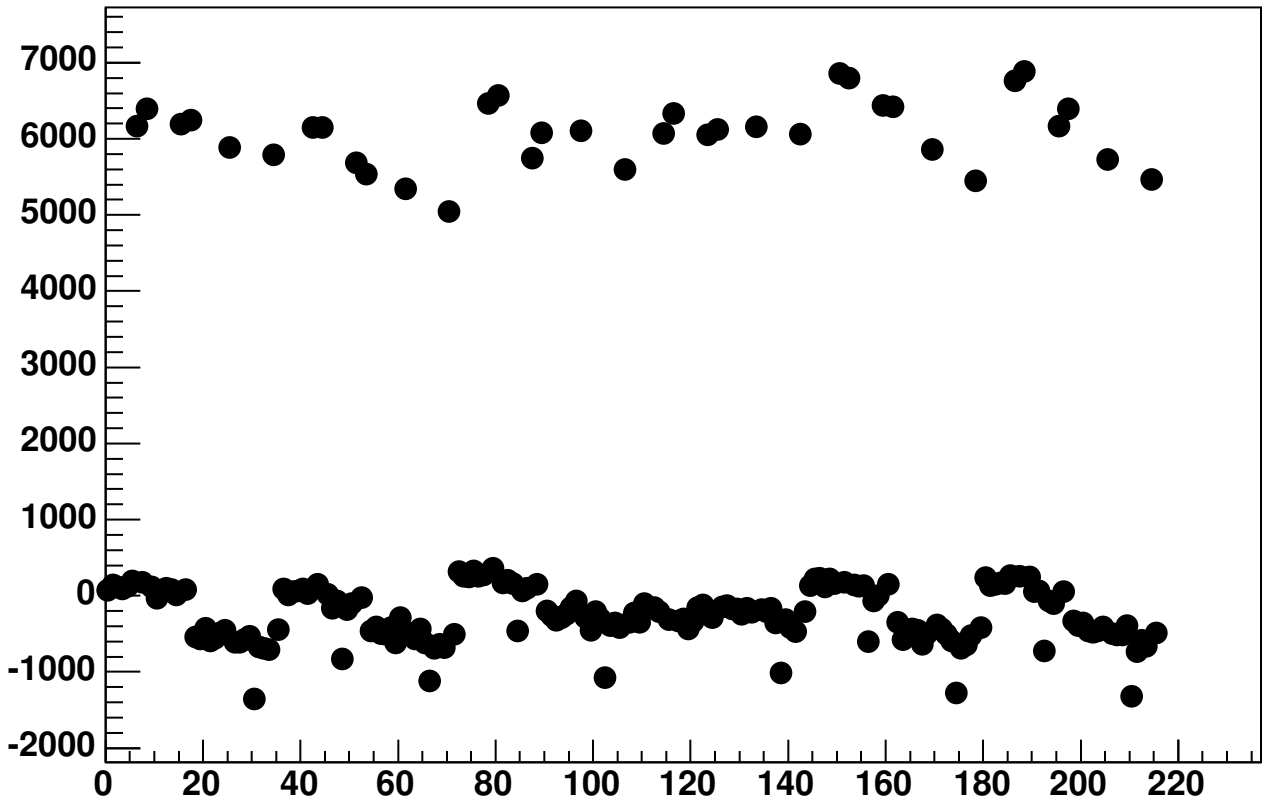
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



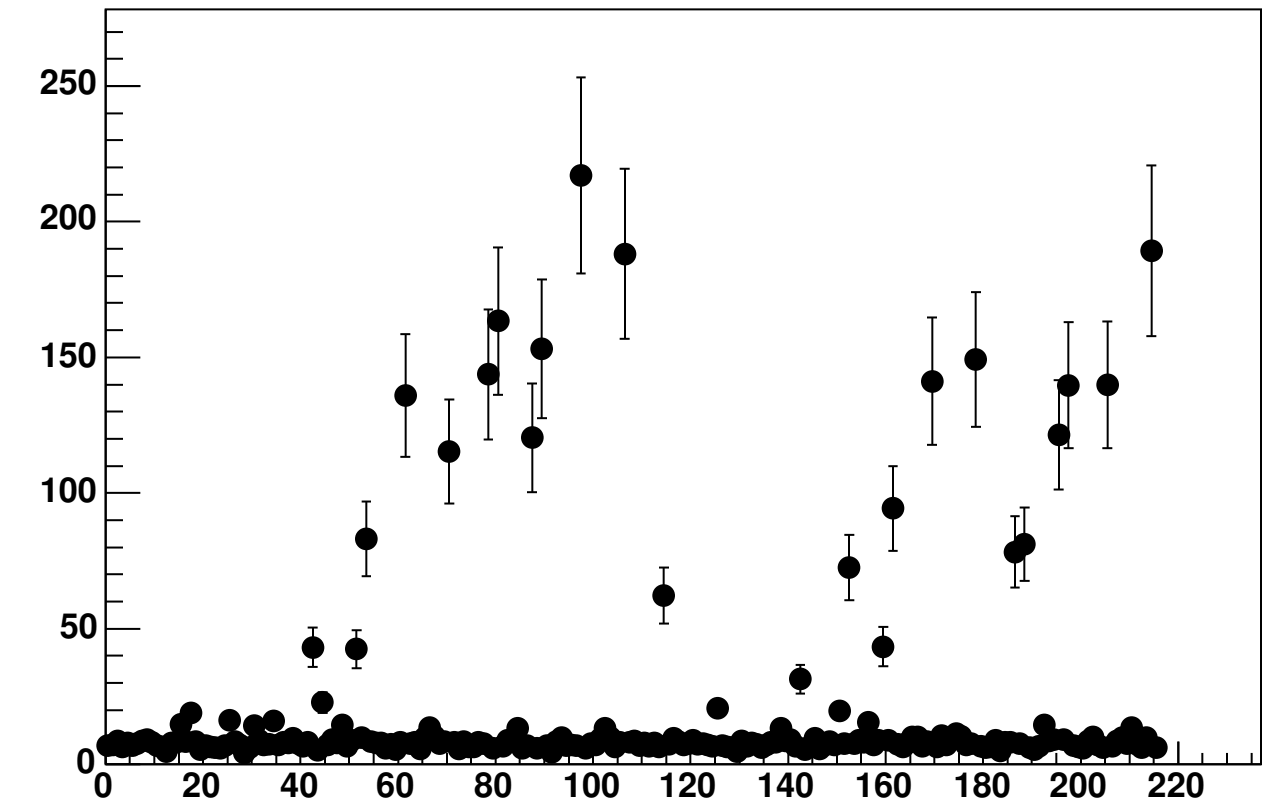
Enable 2, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



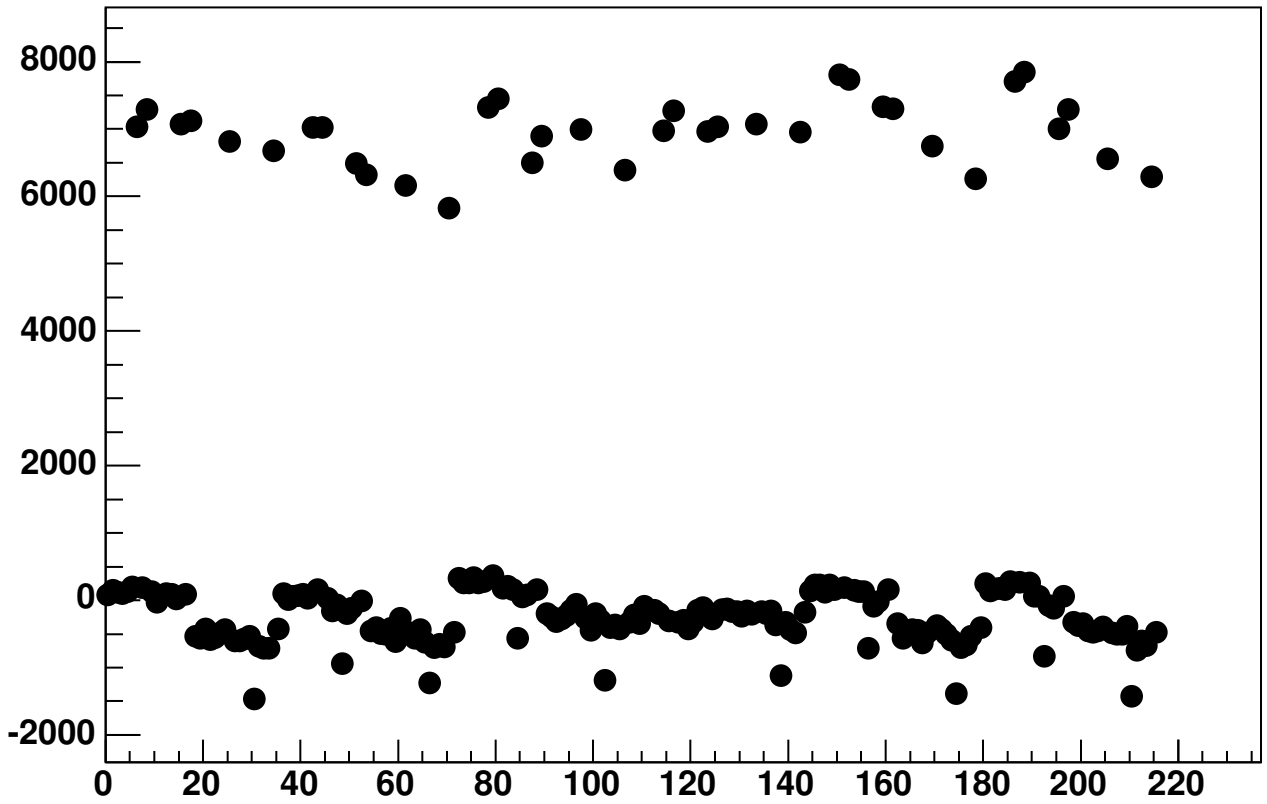
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



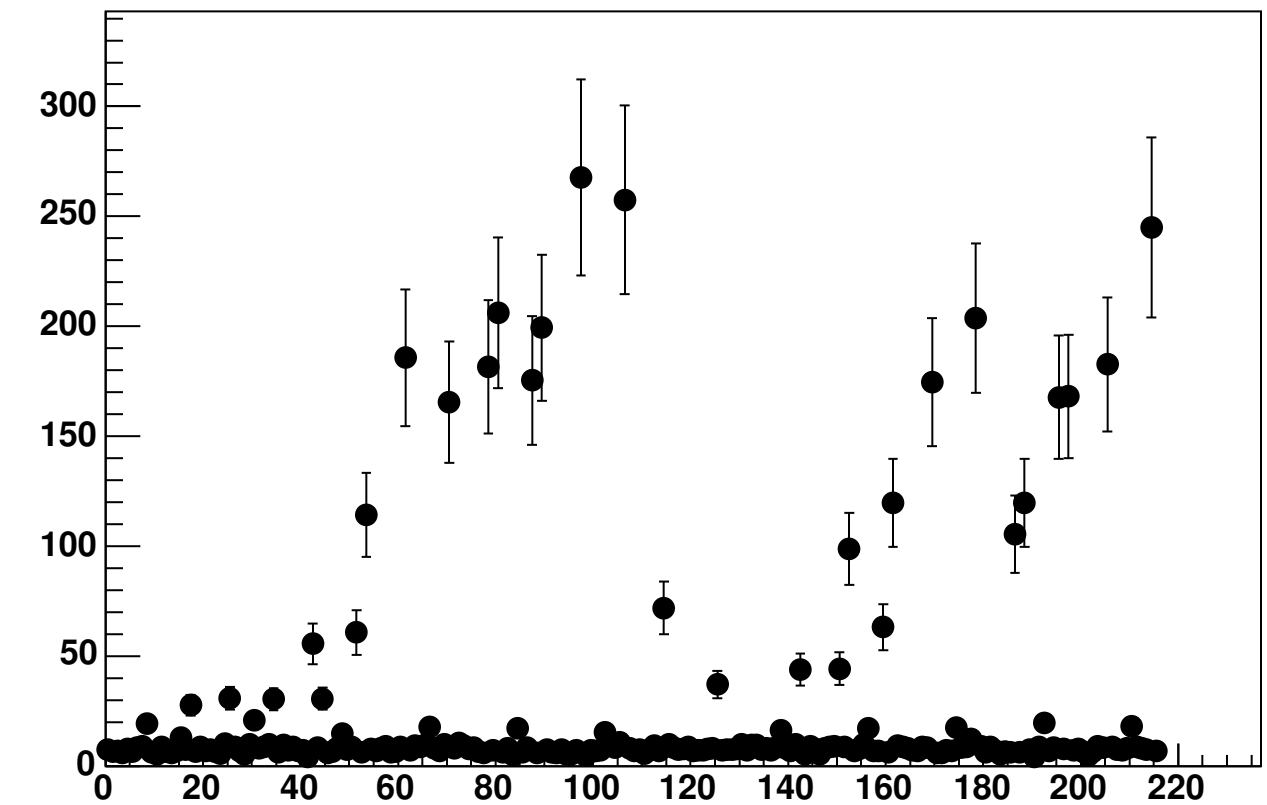
Enable 2, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



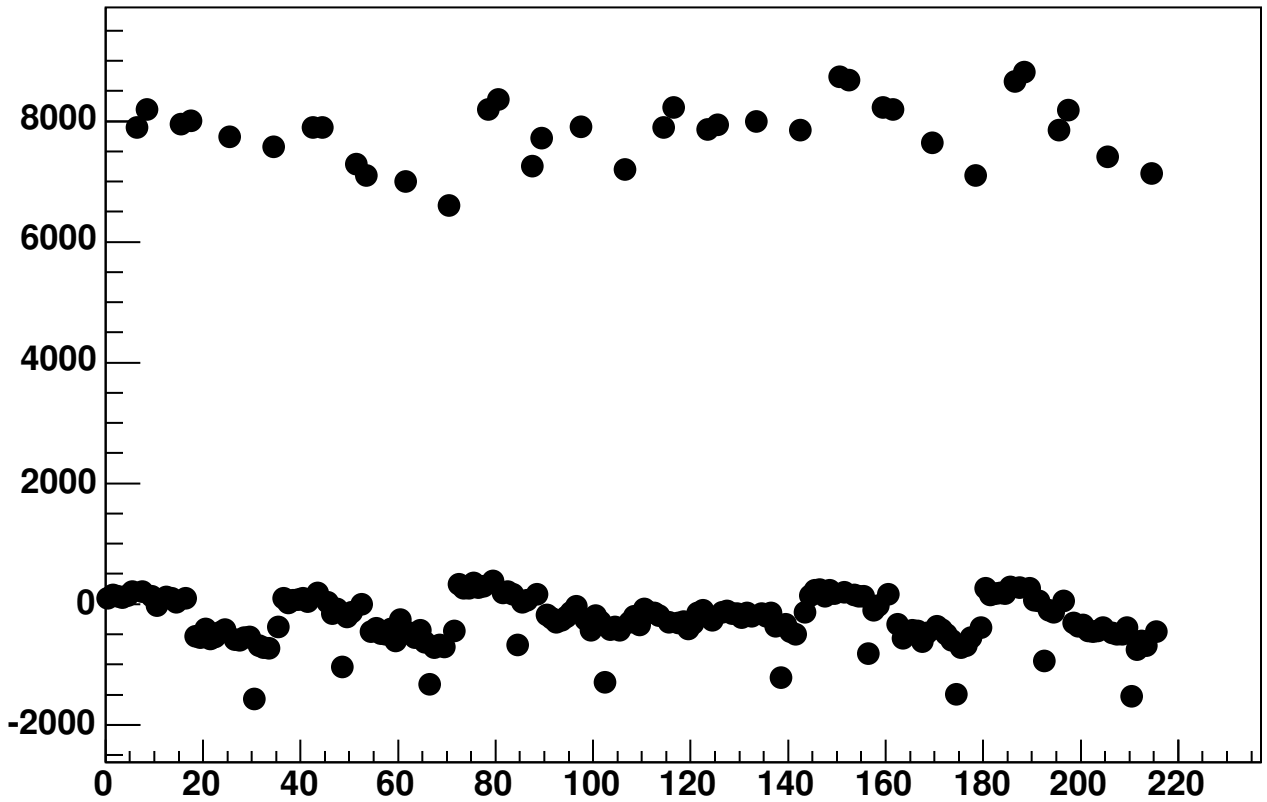
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



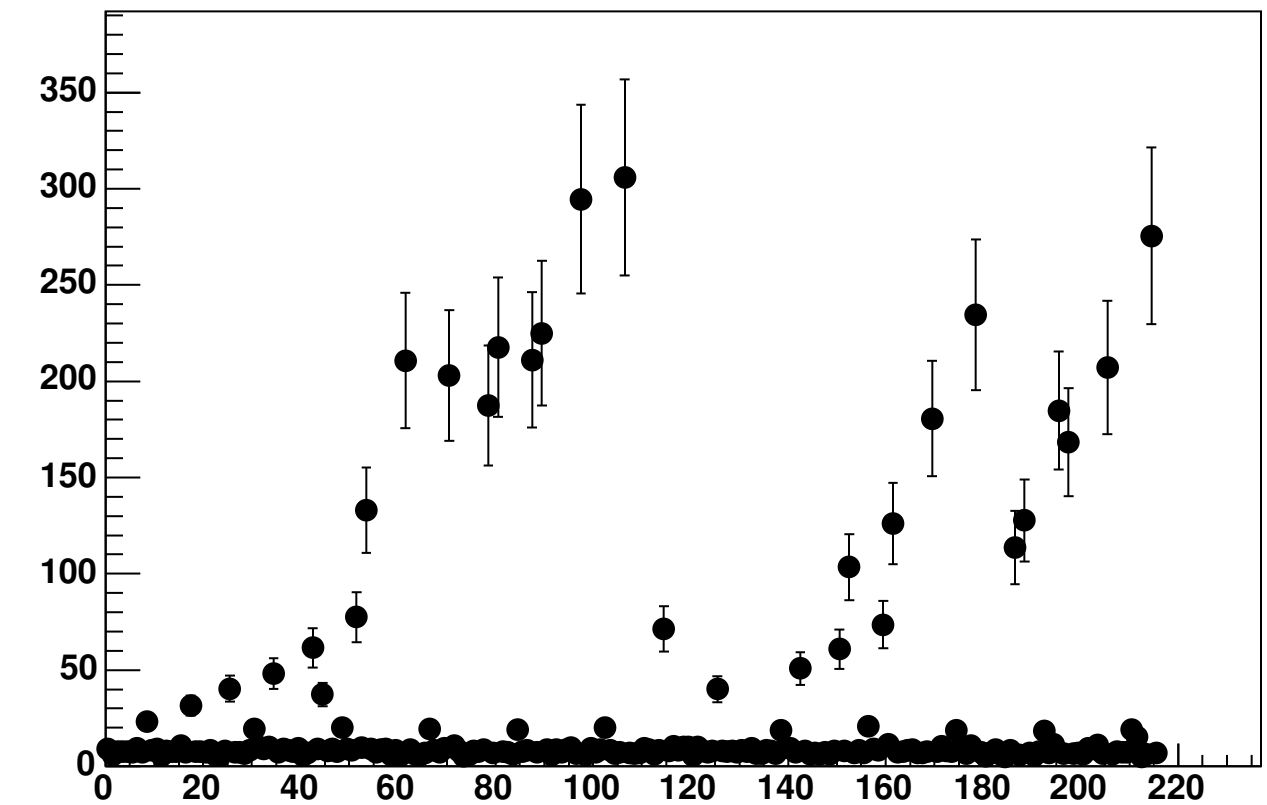
Enable 2, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



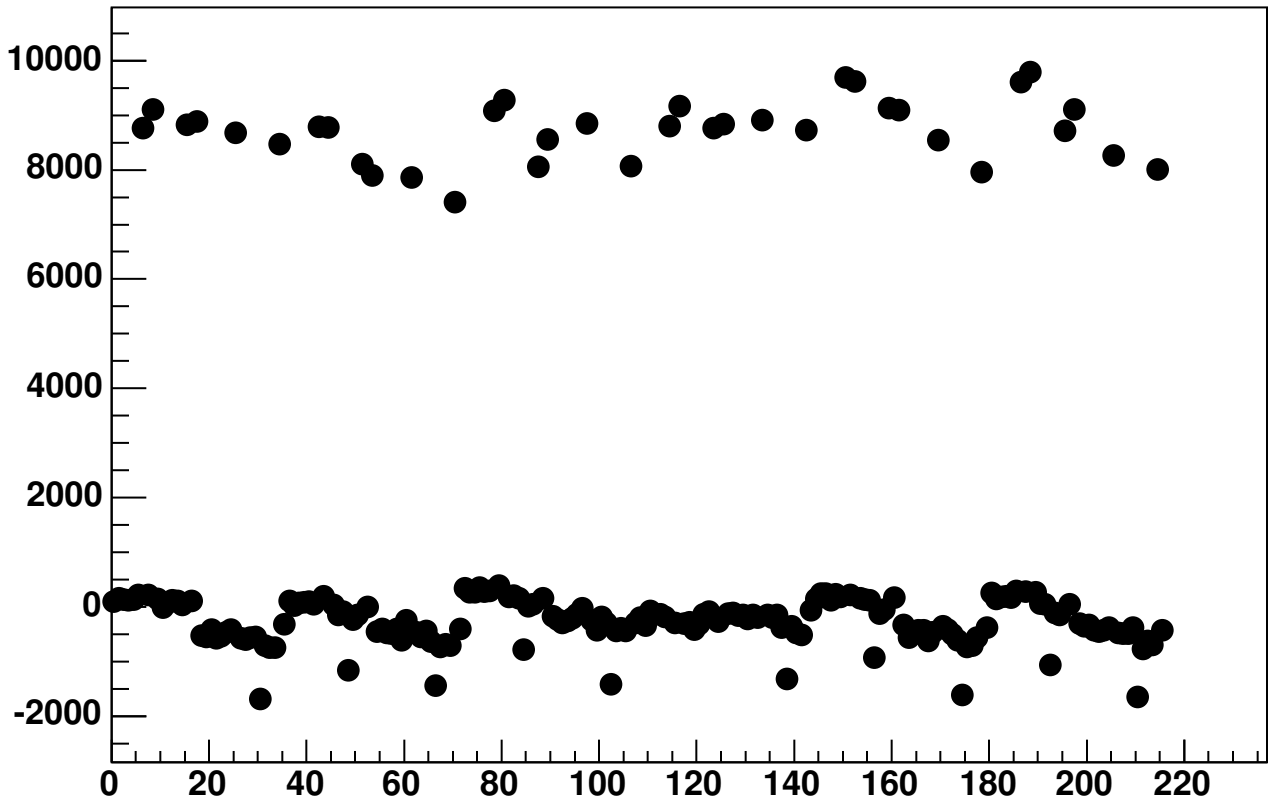
Enable 2, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



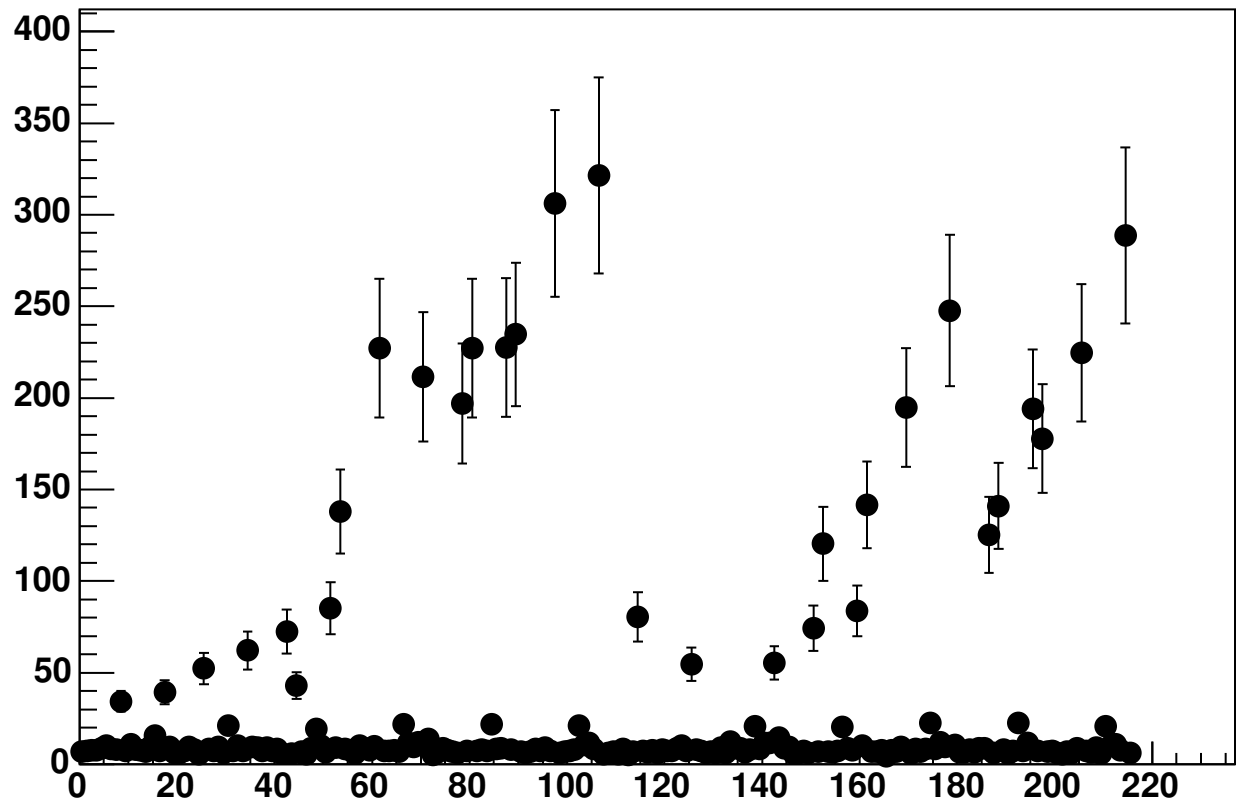
Enable 2, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



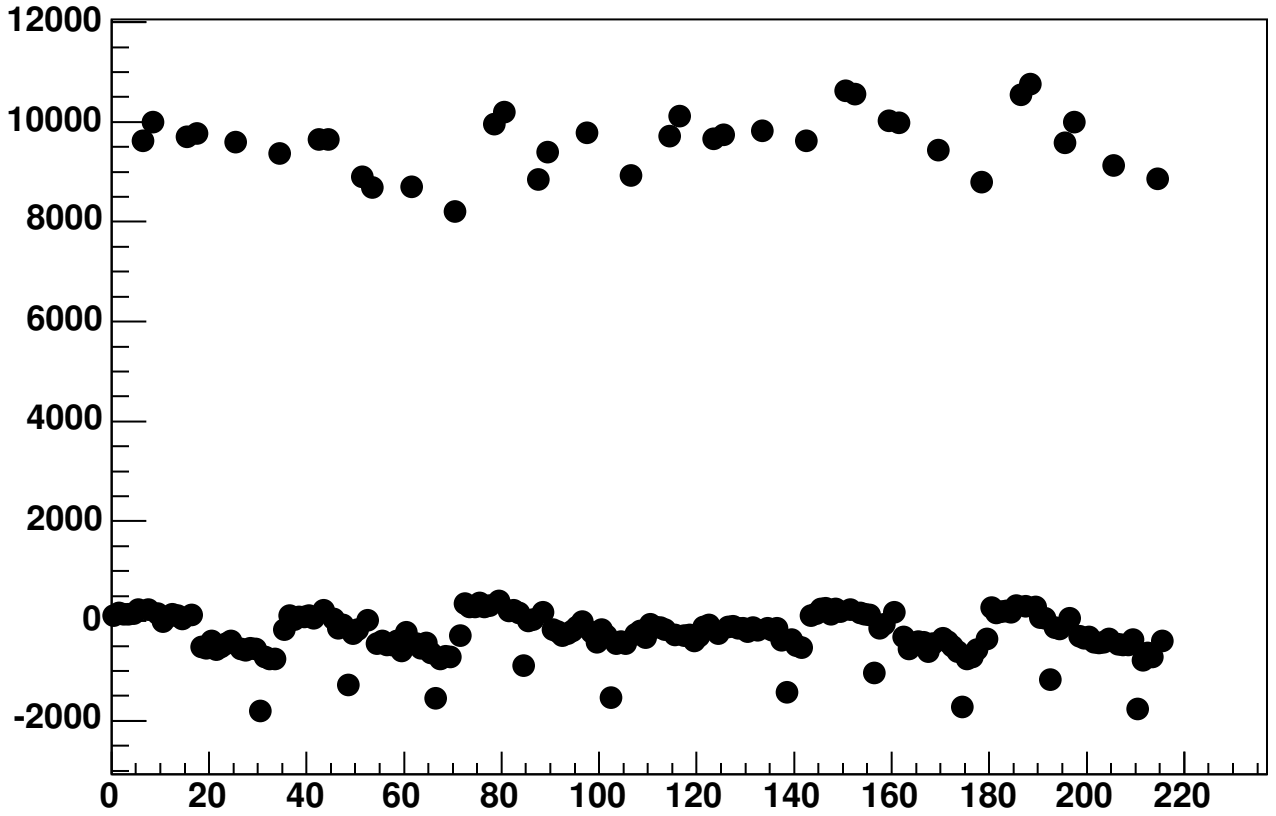
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



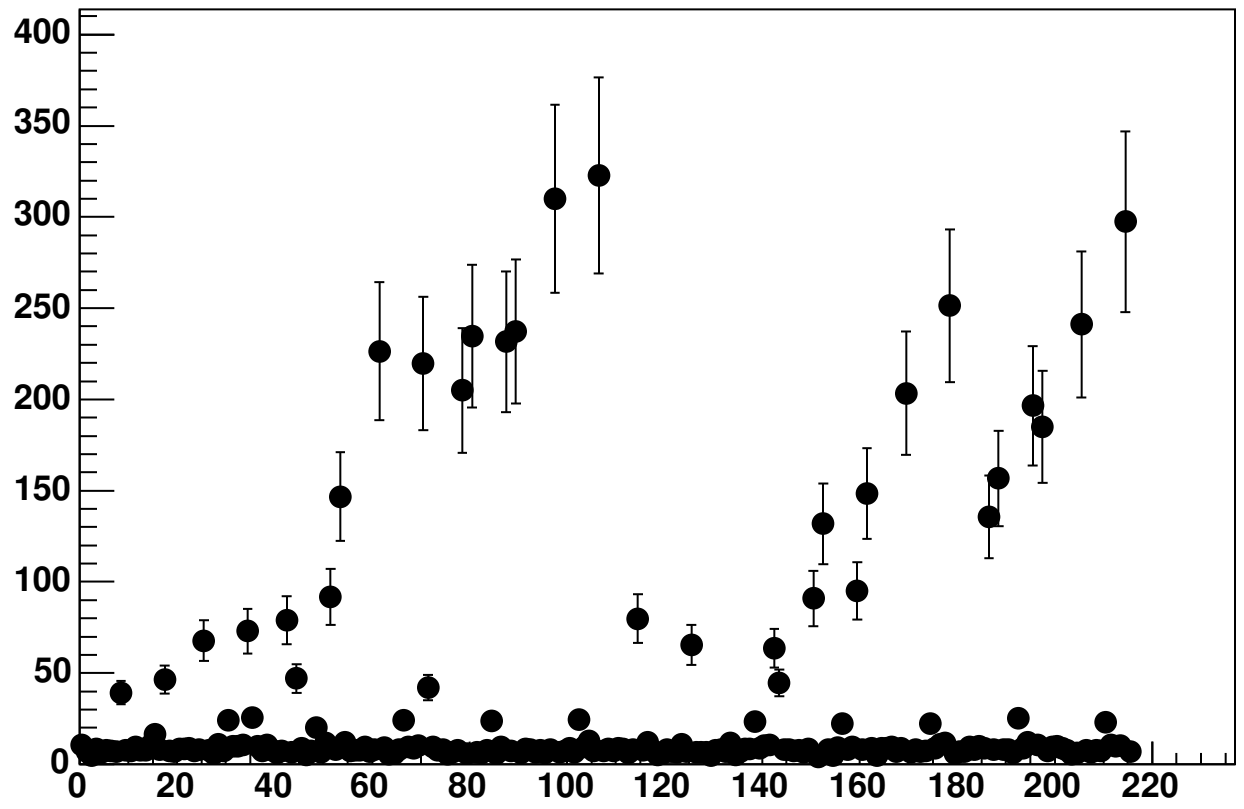
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



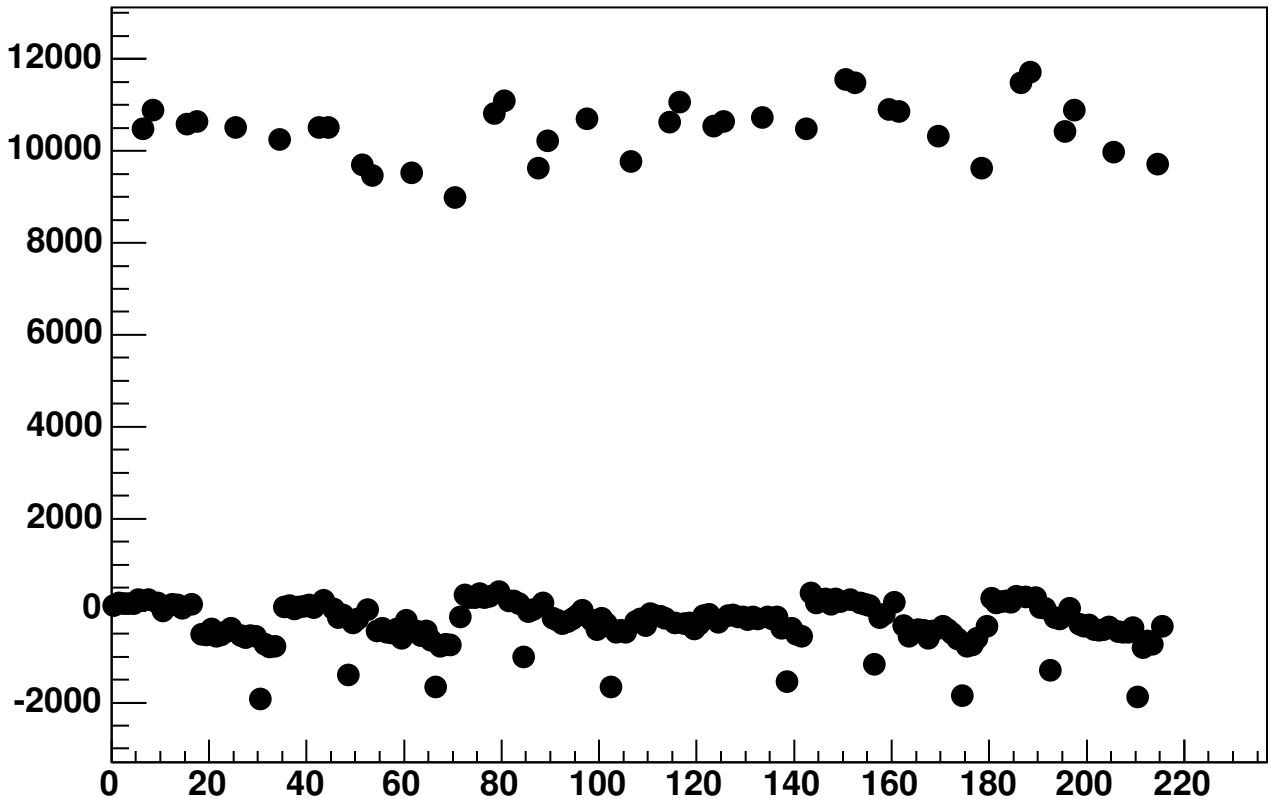
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



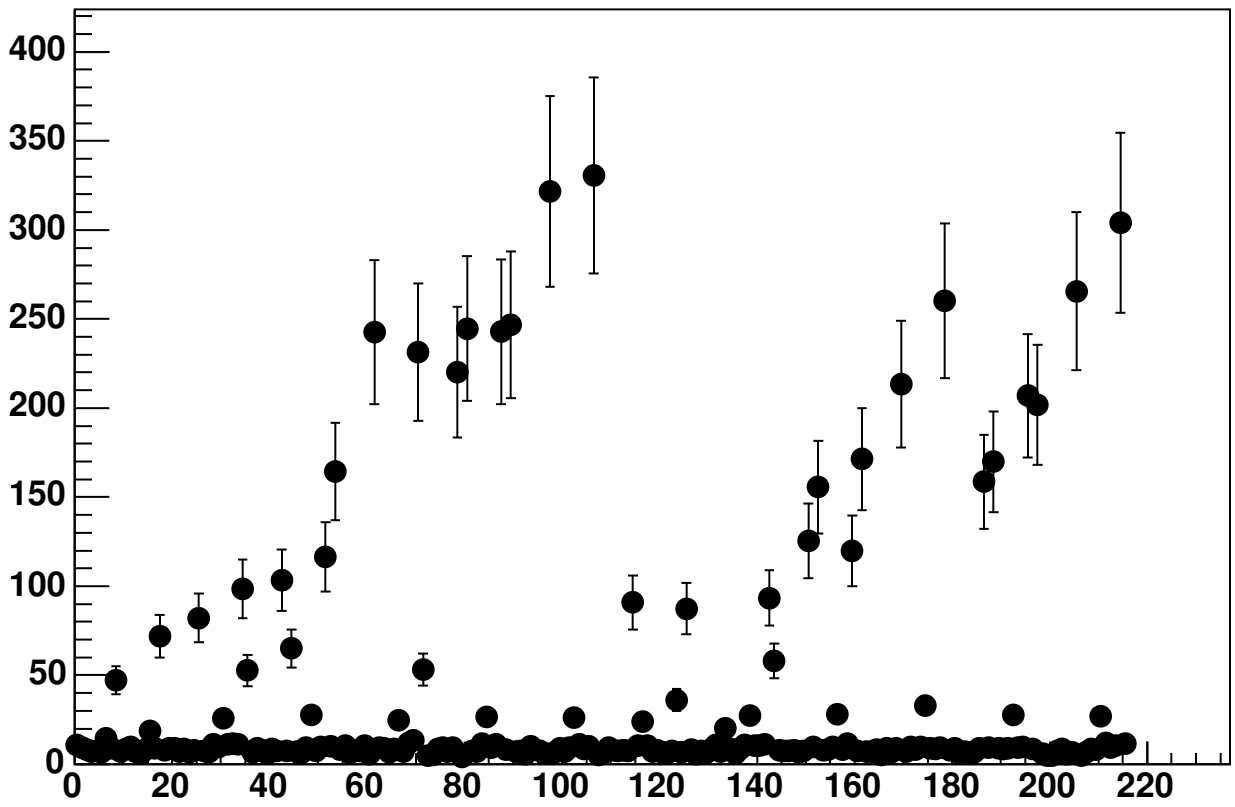
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



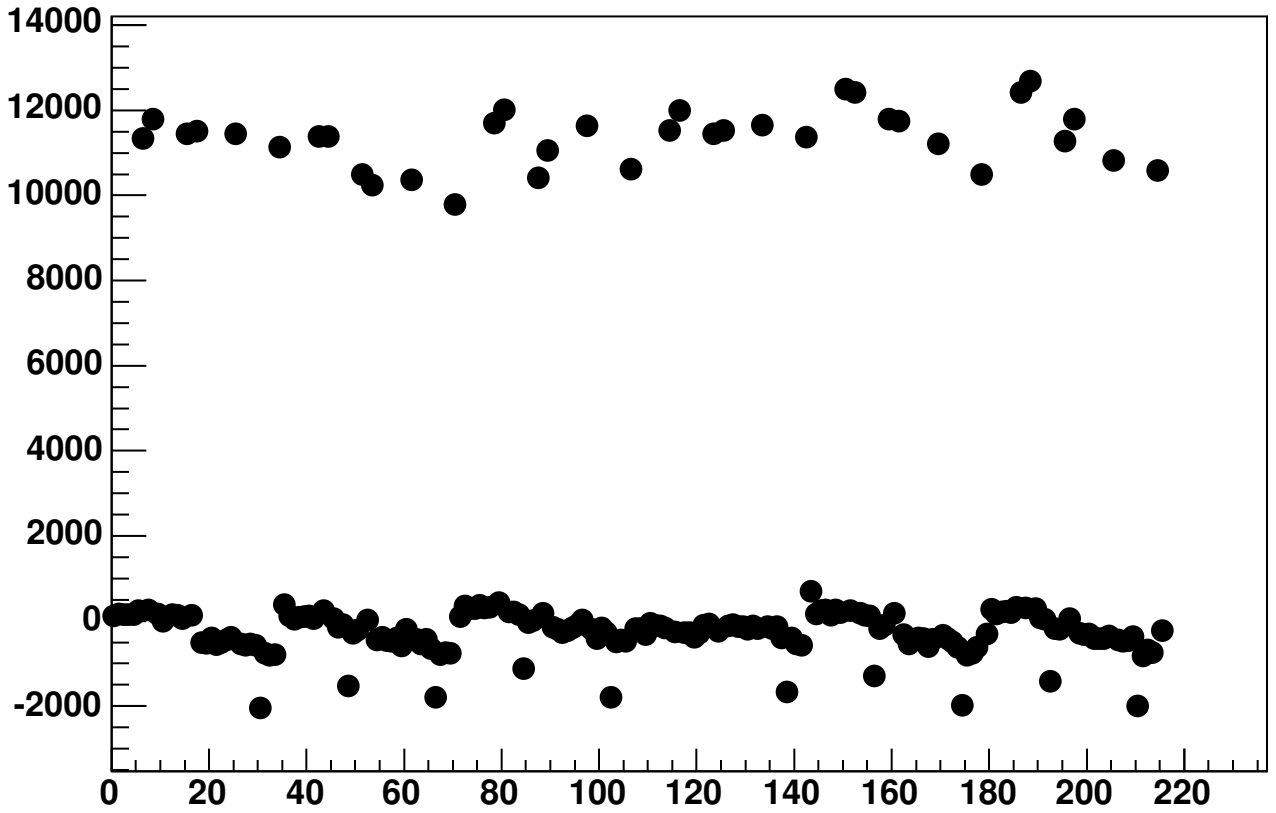
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



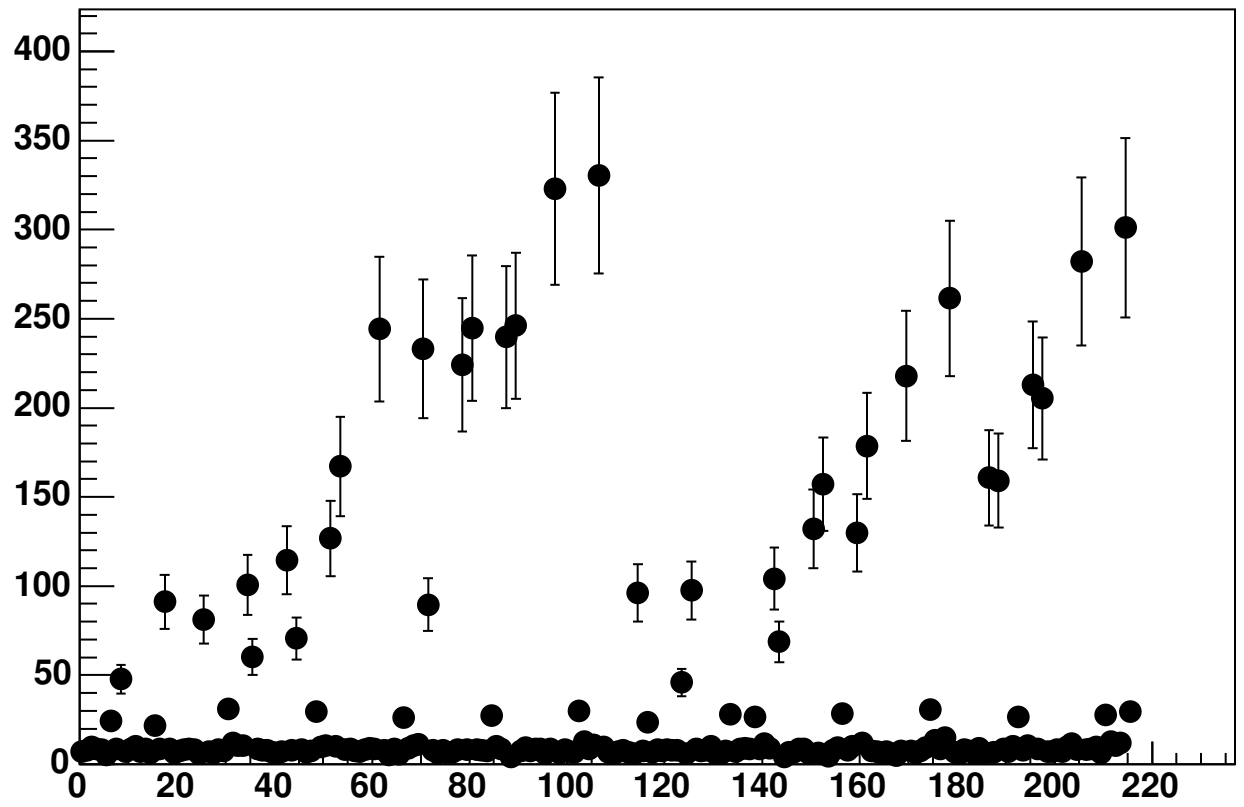
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



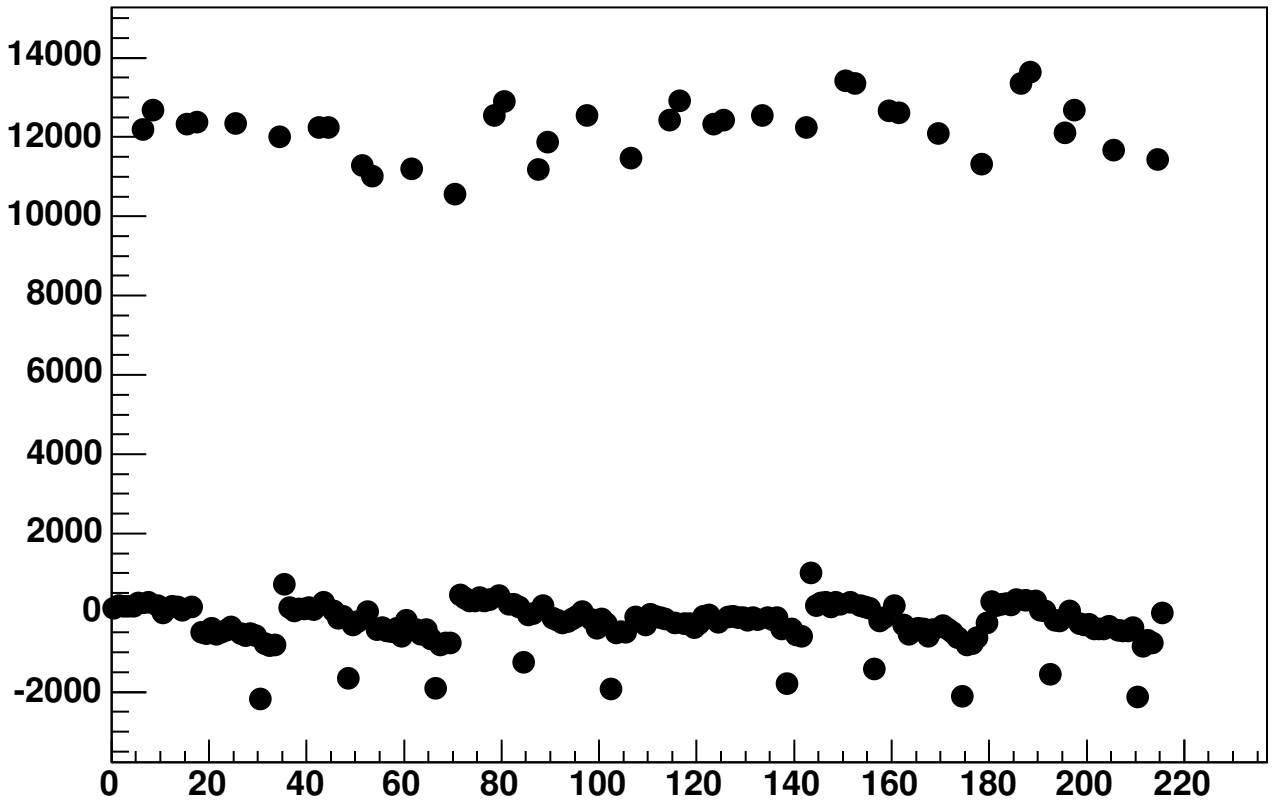
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



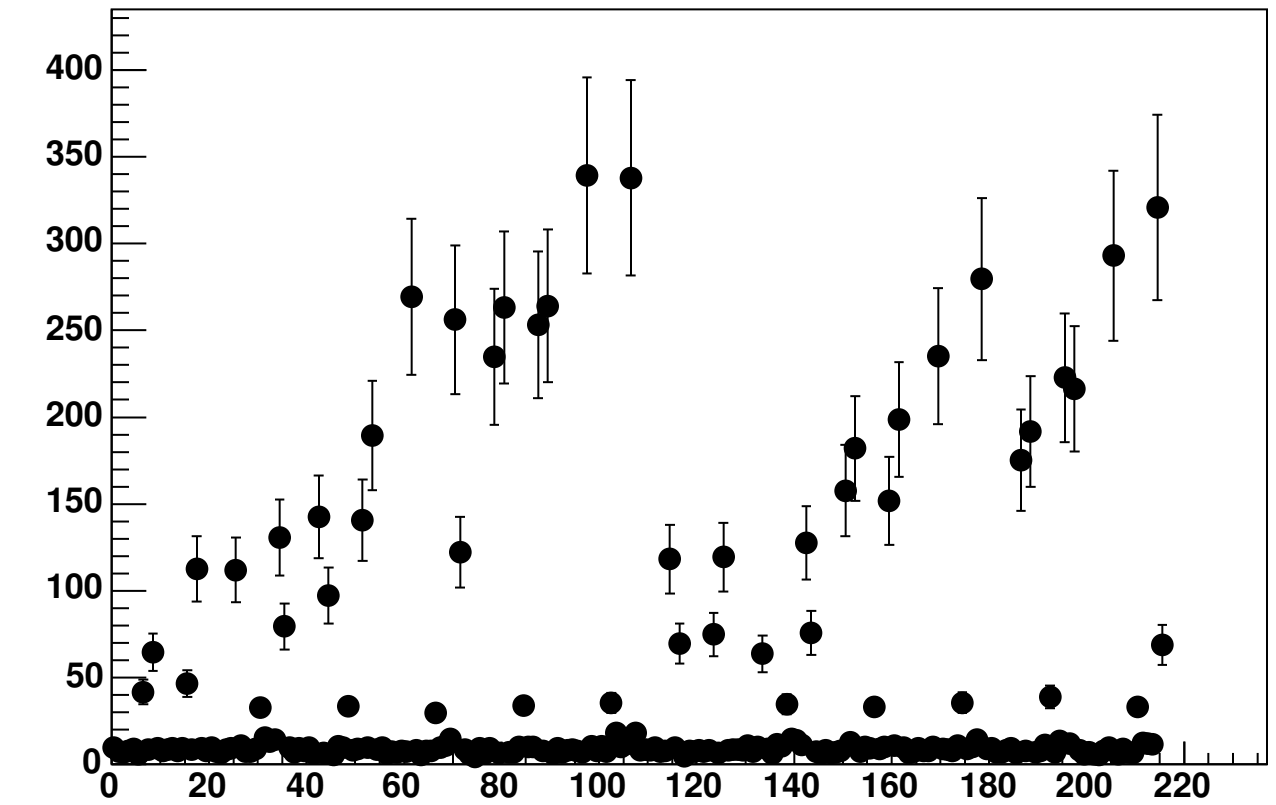
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



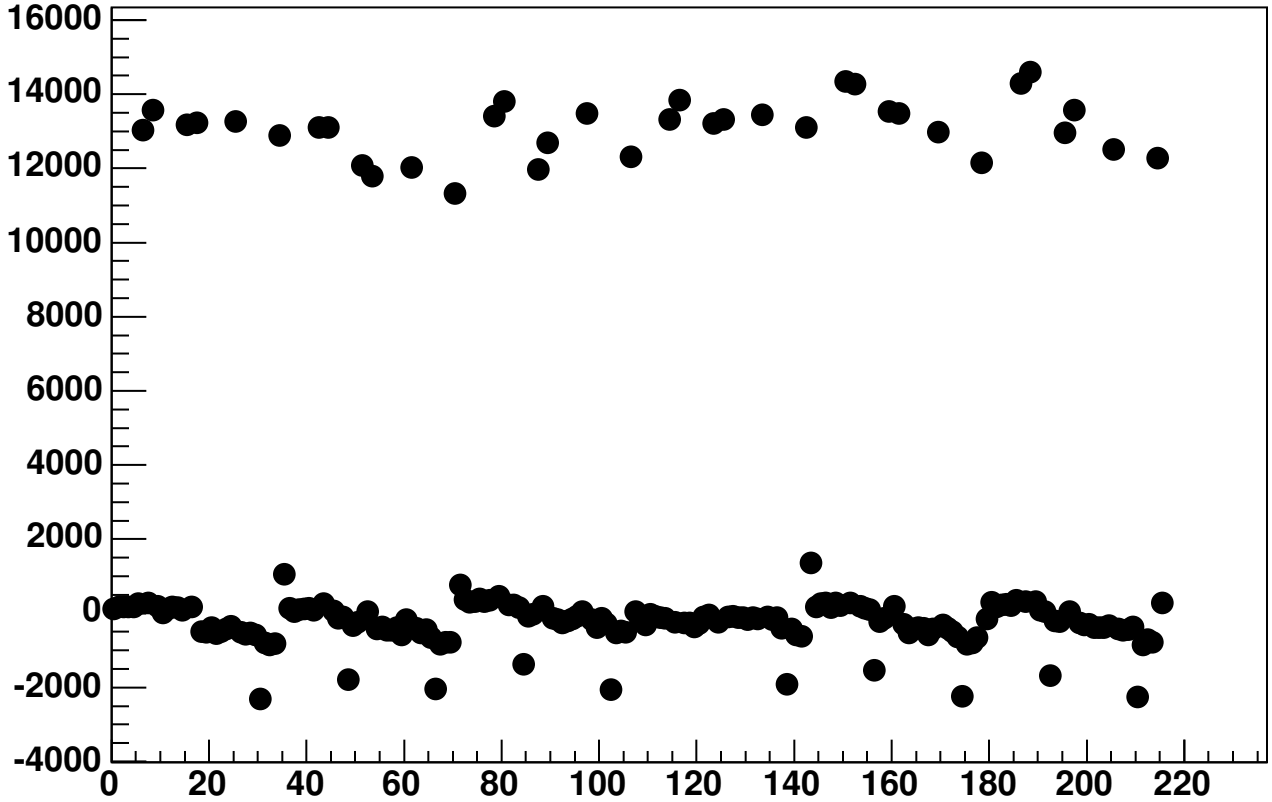
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



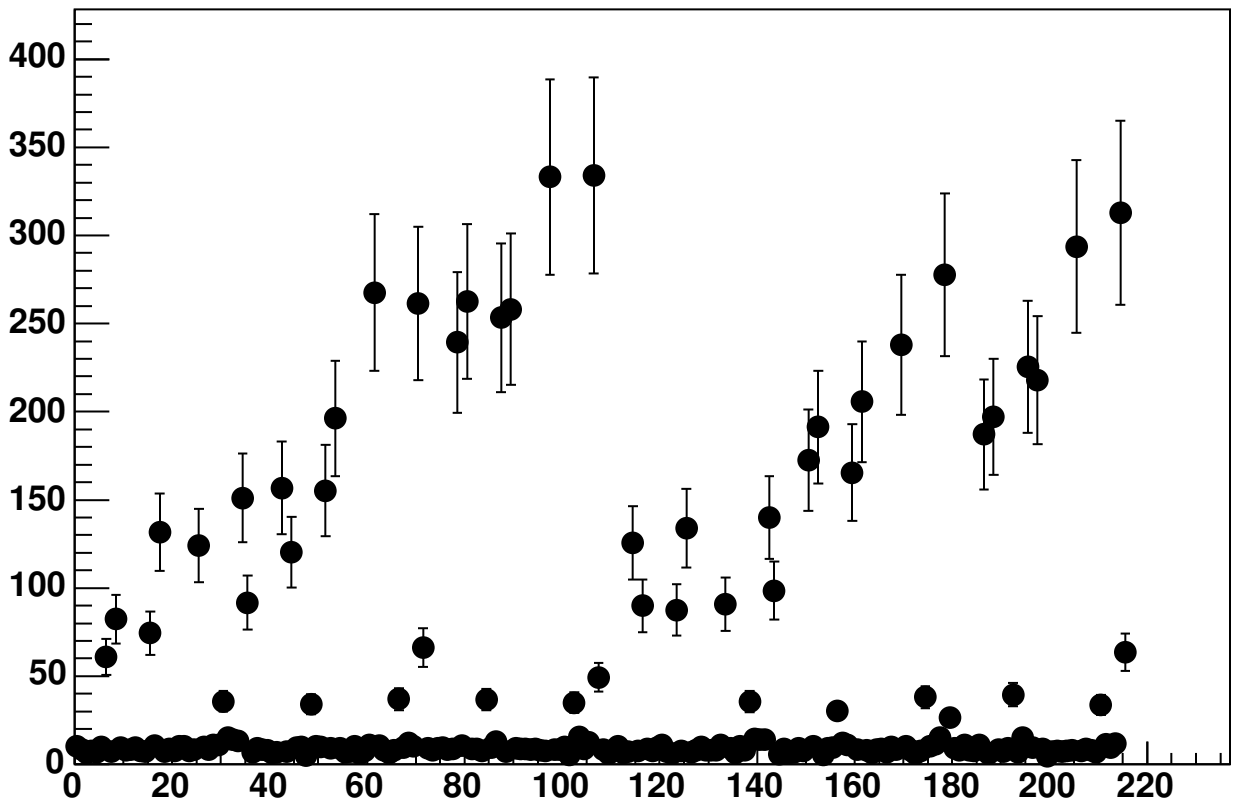
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



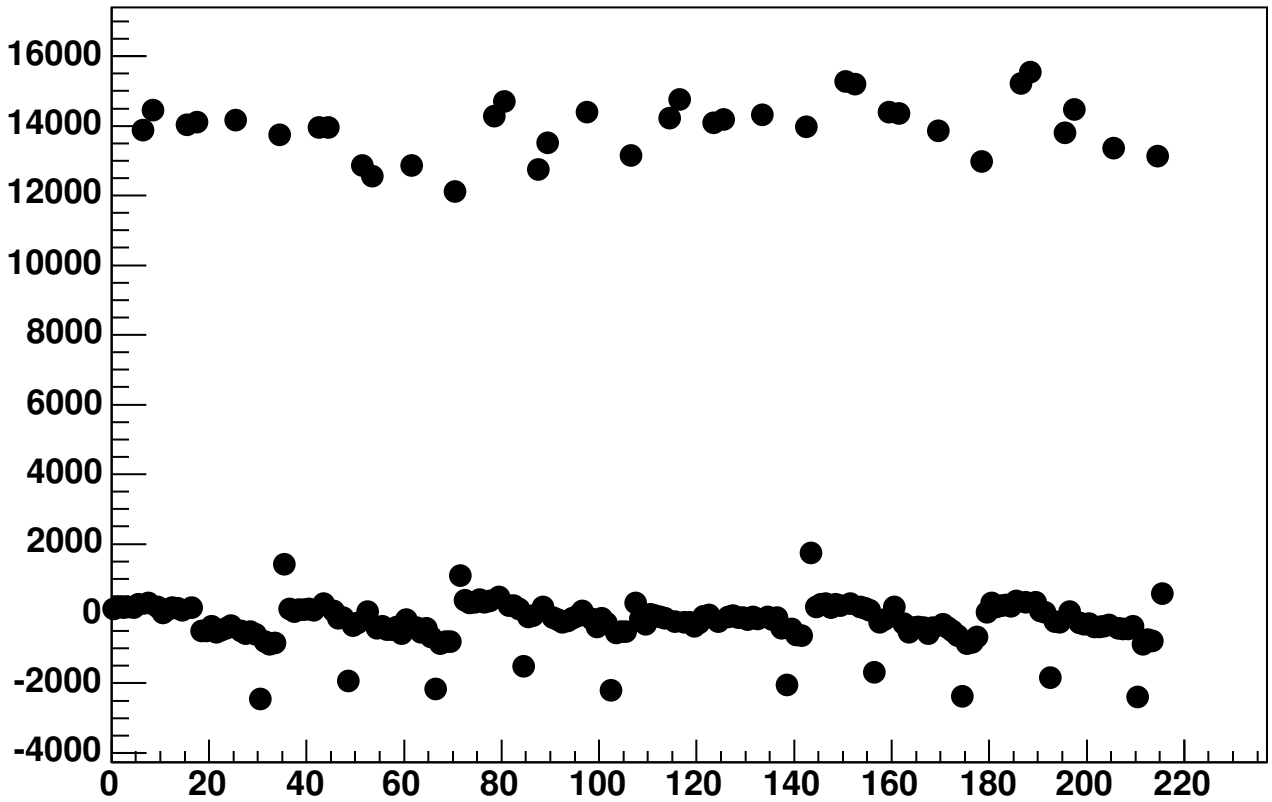
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



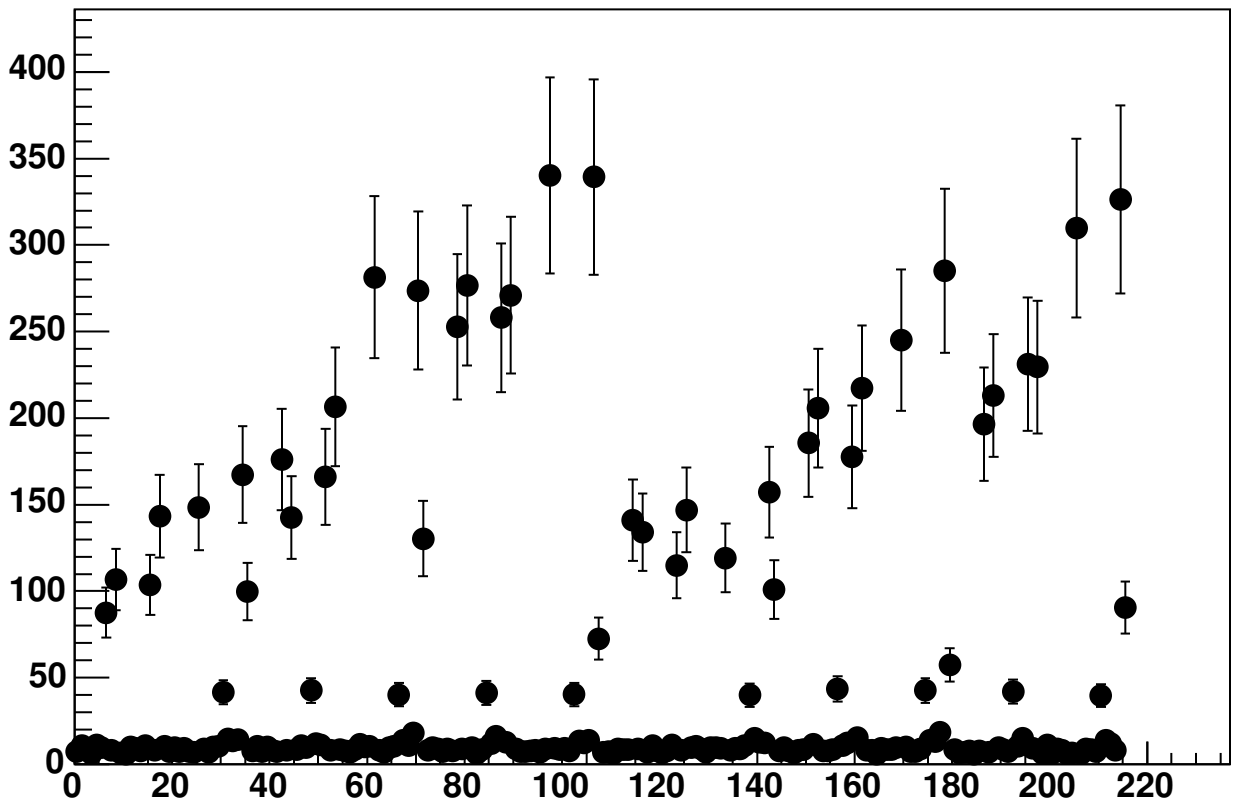
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



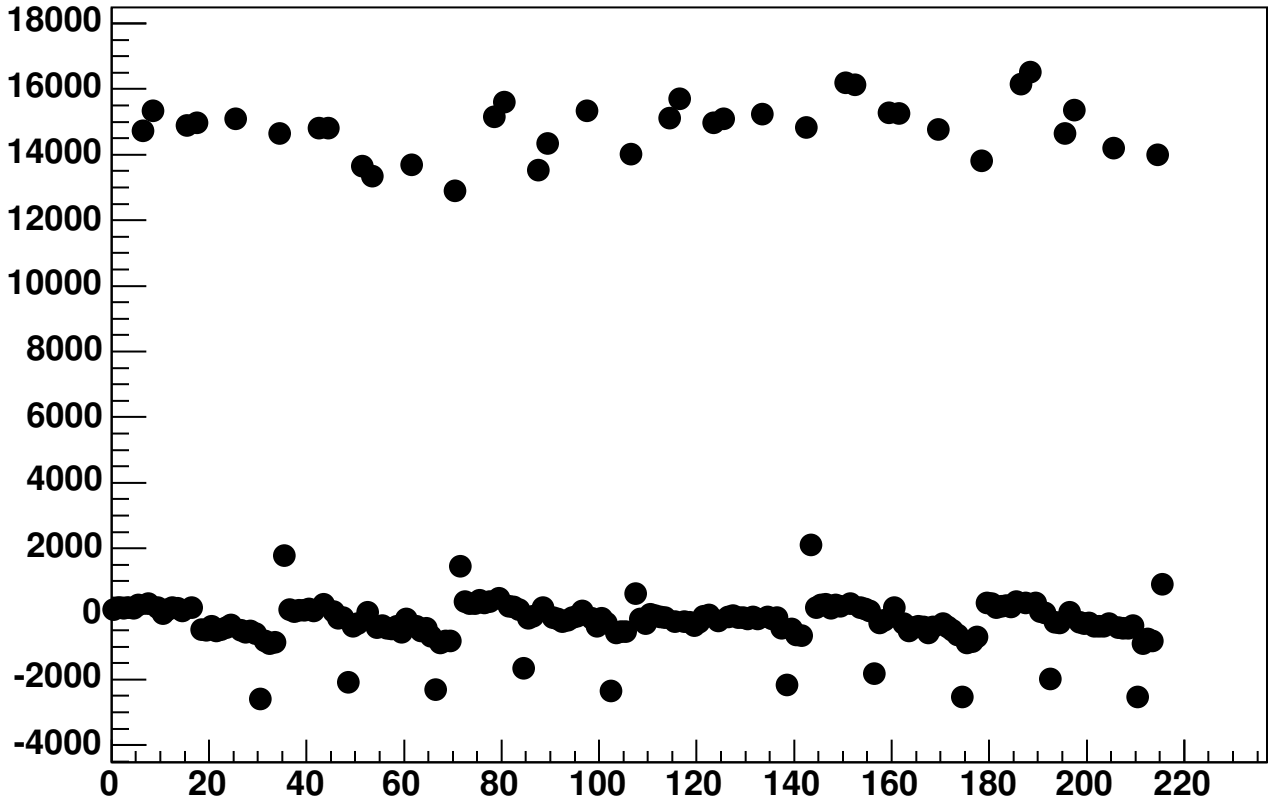
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



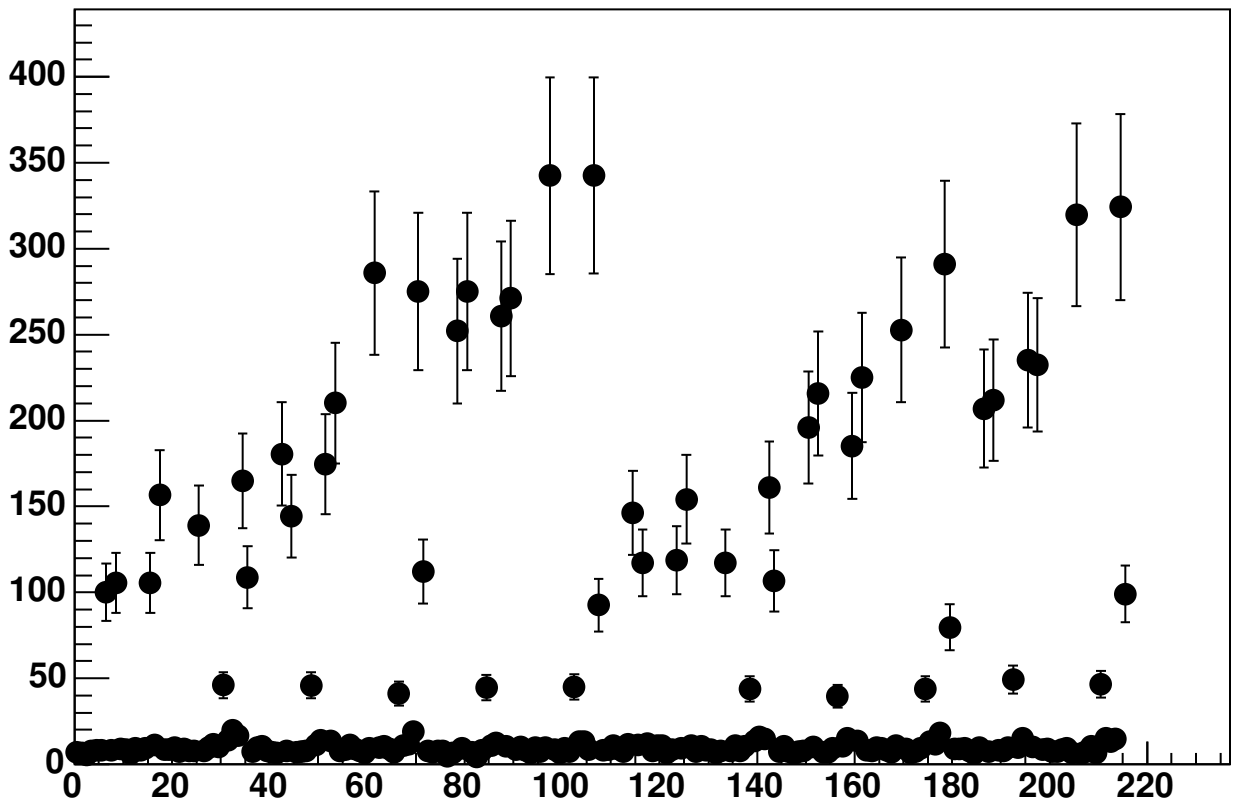
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



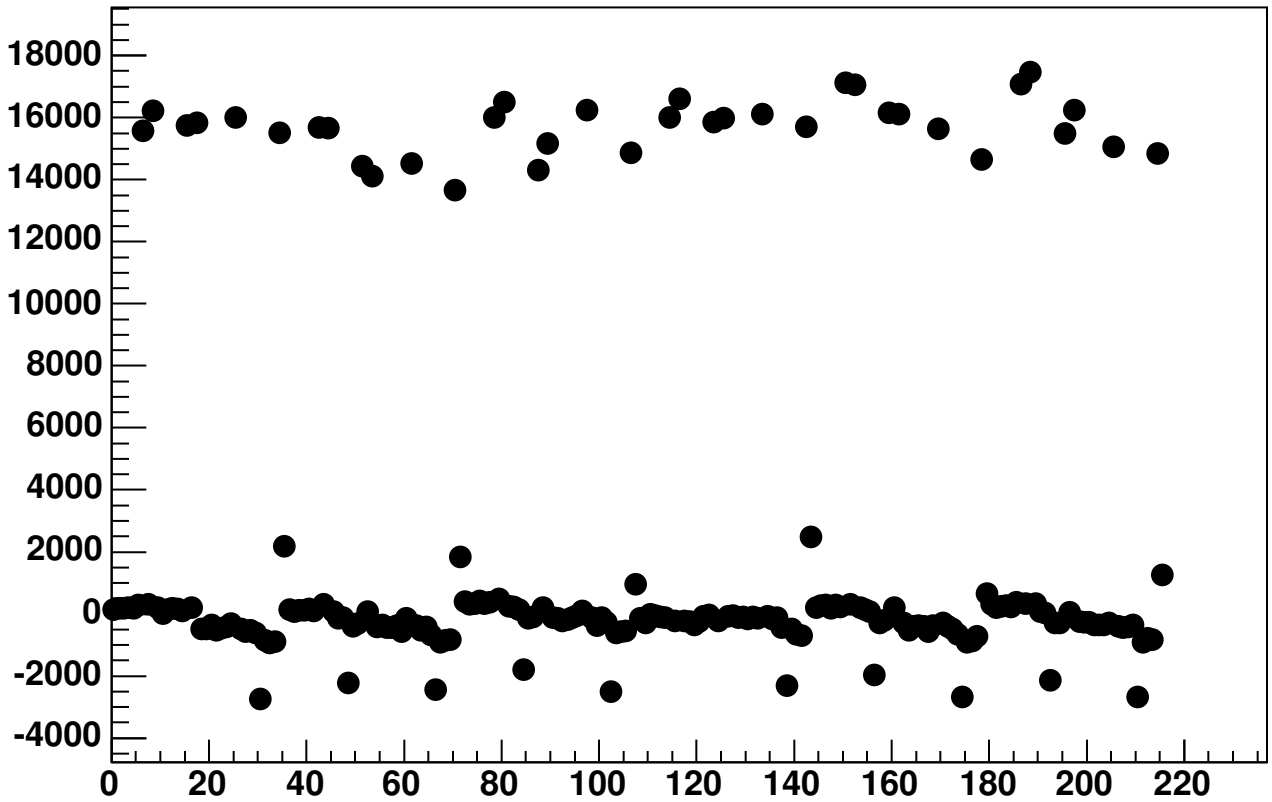
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



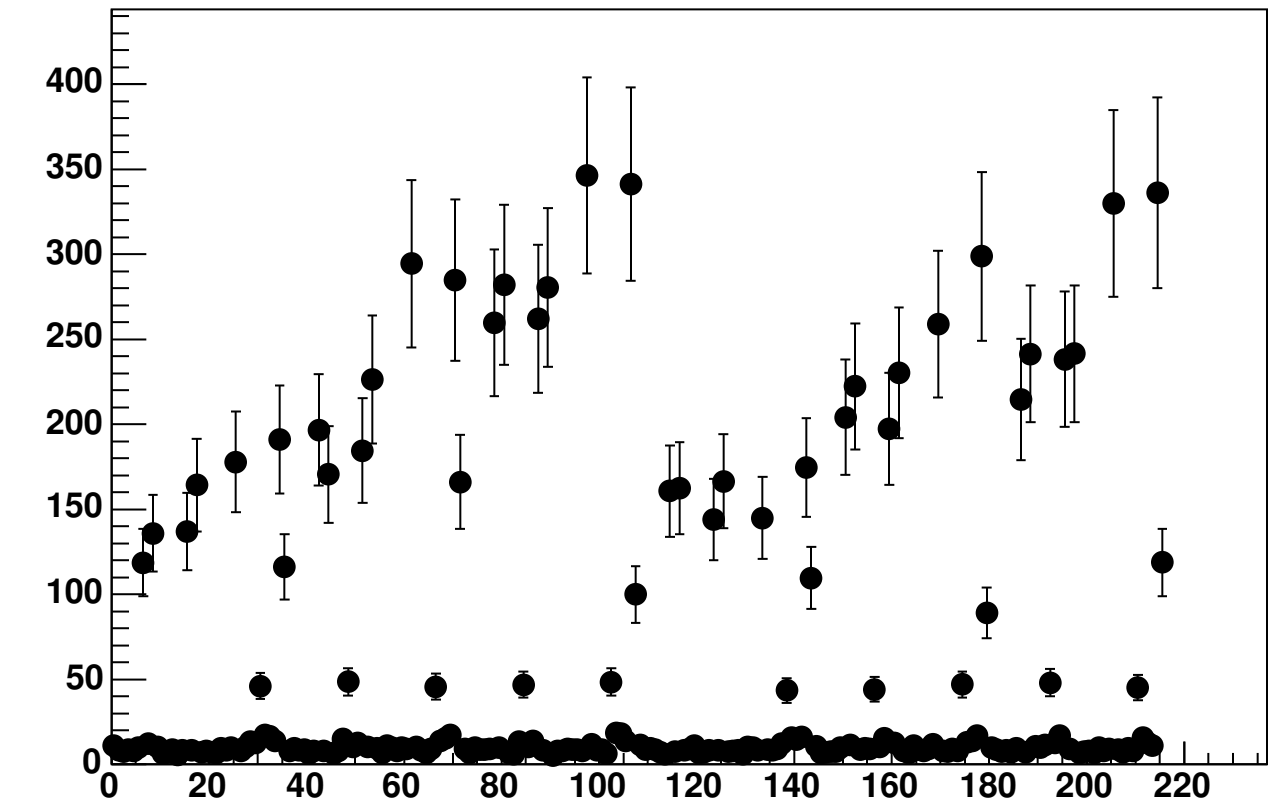
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



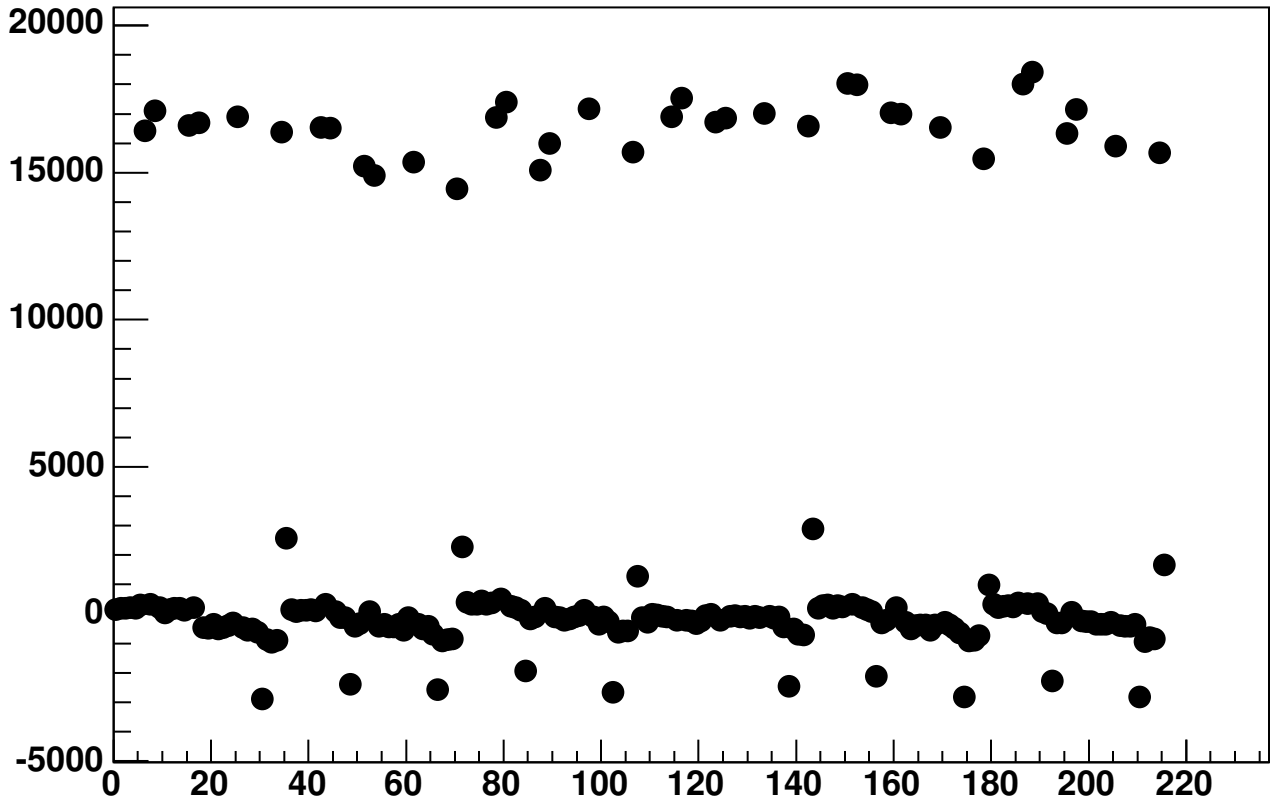
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



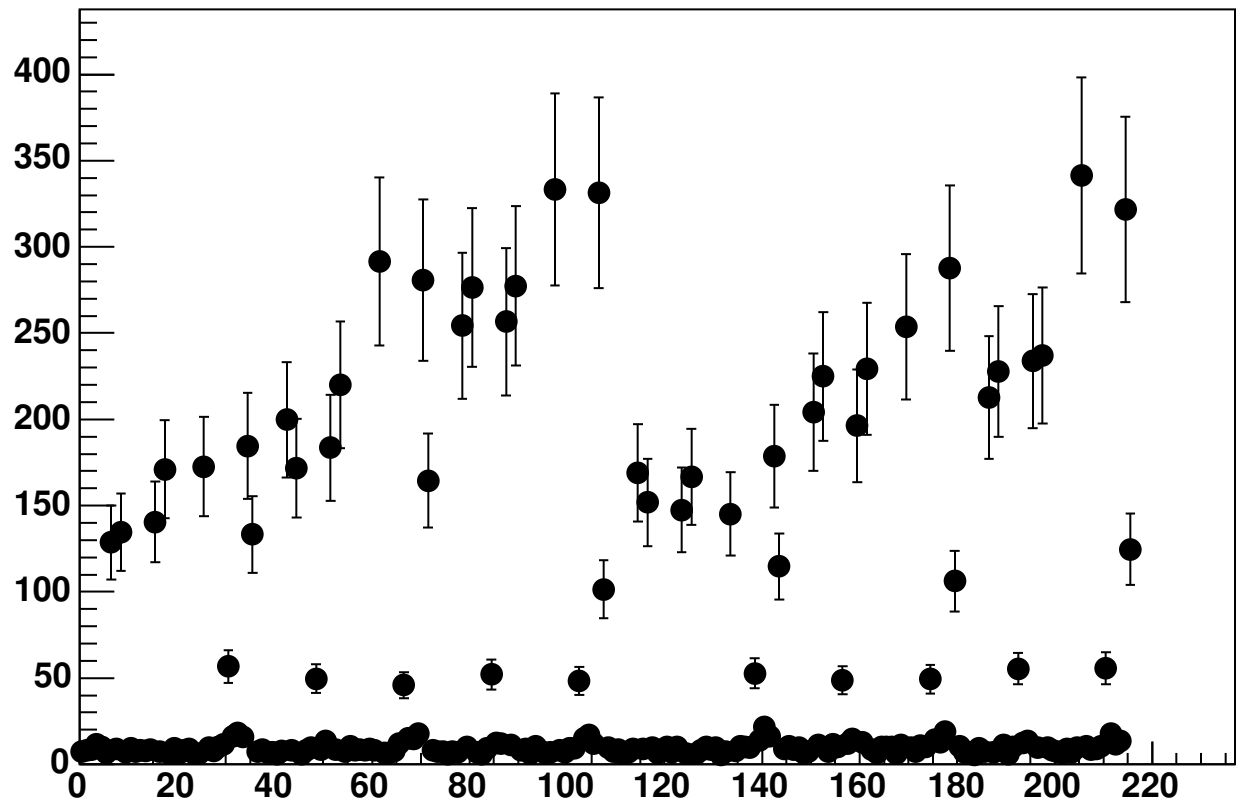
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



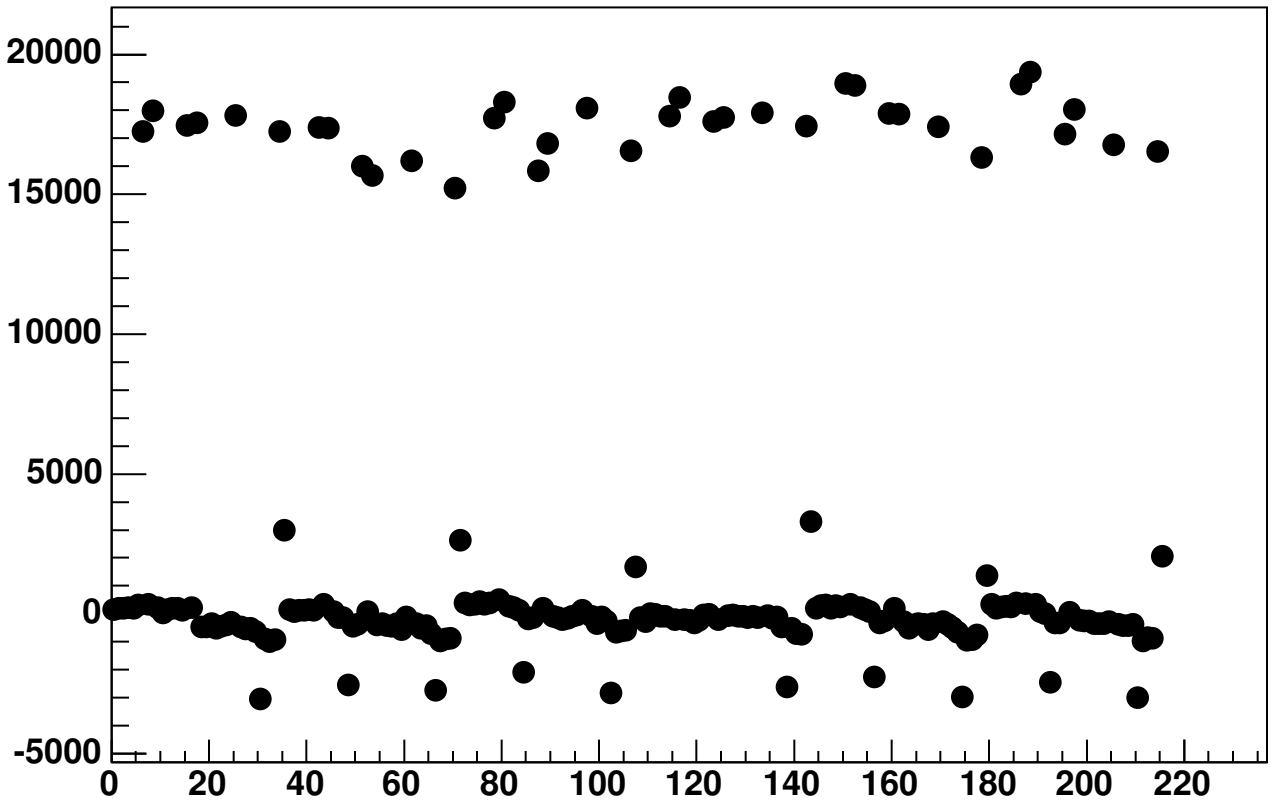
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



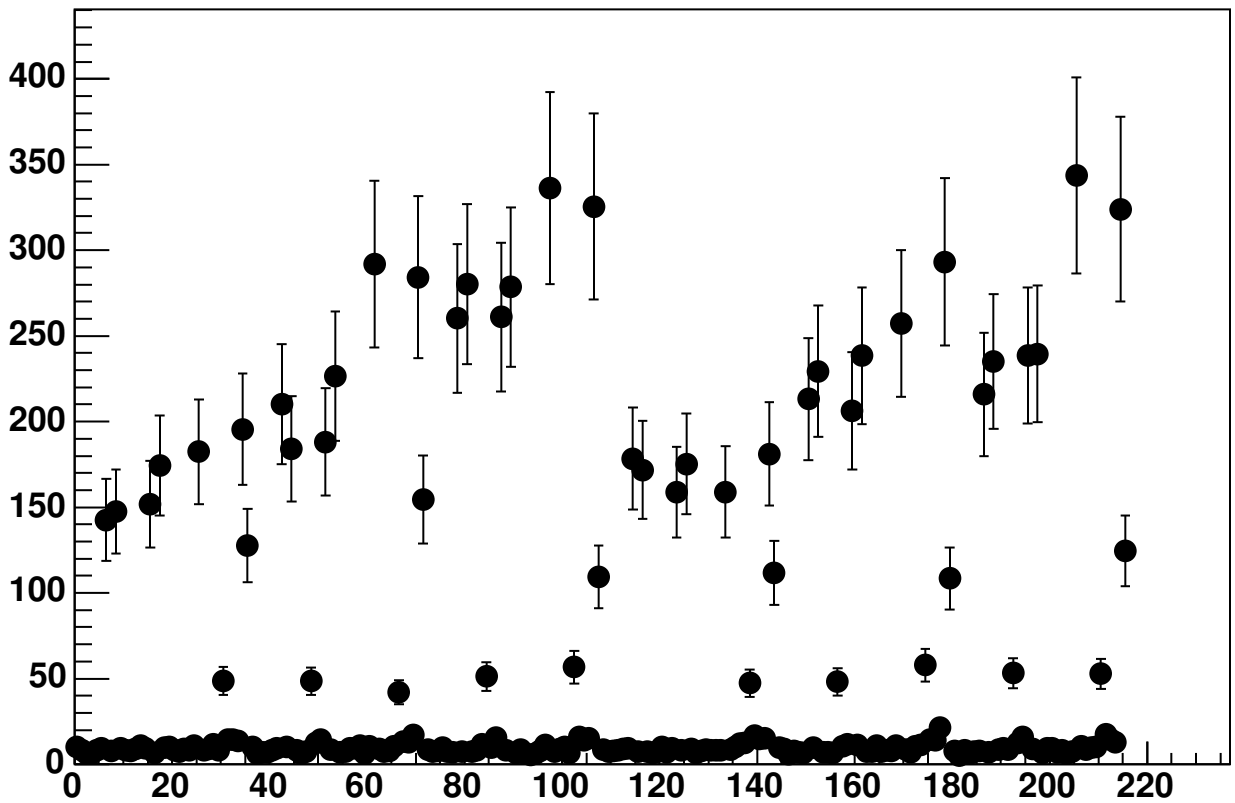
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



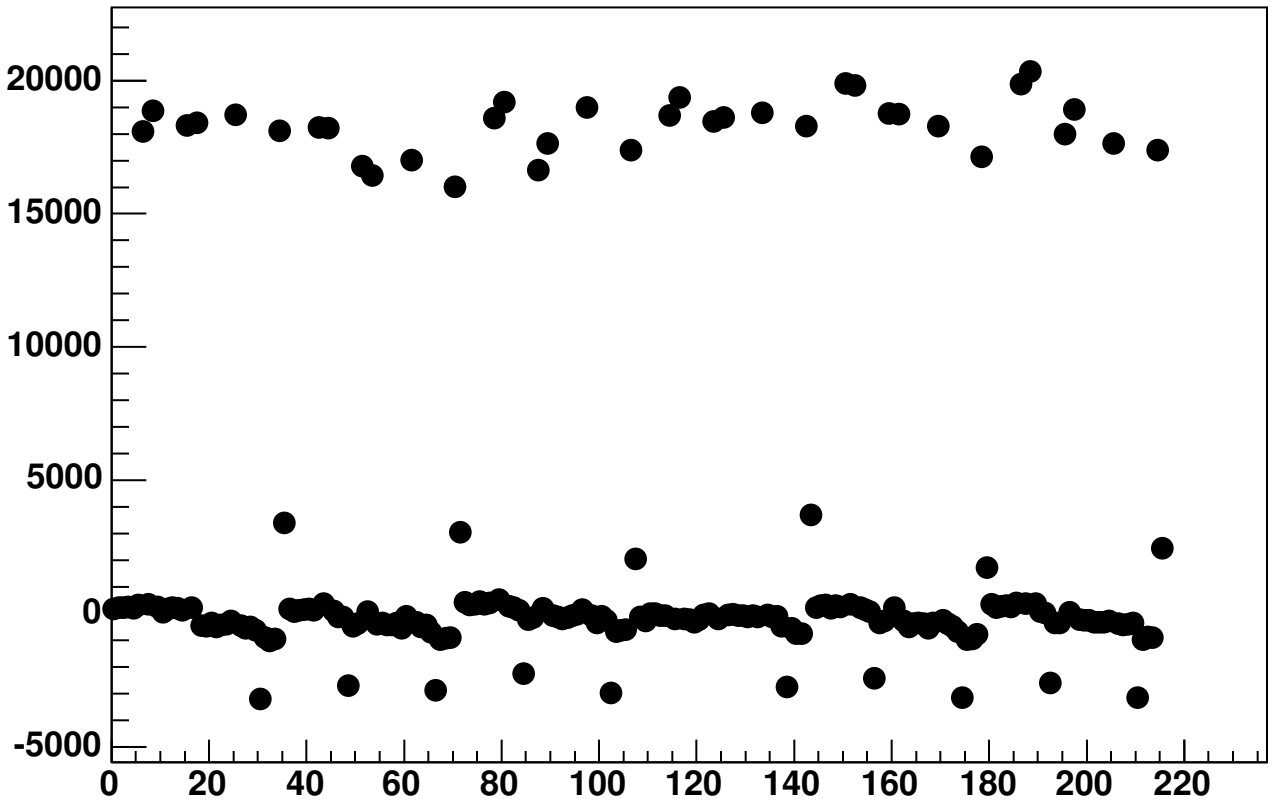
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



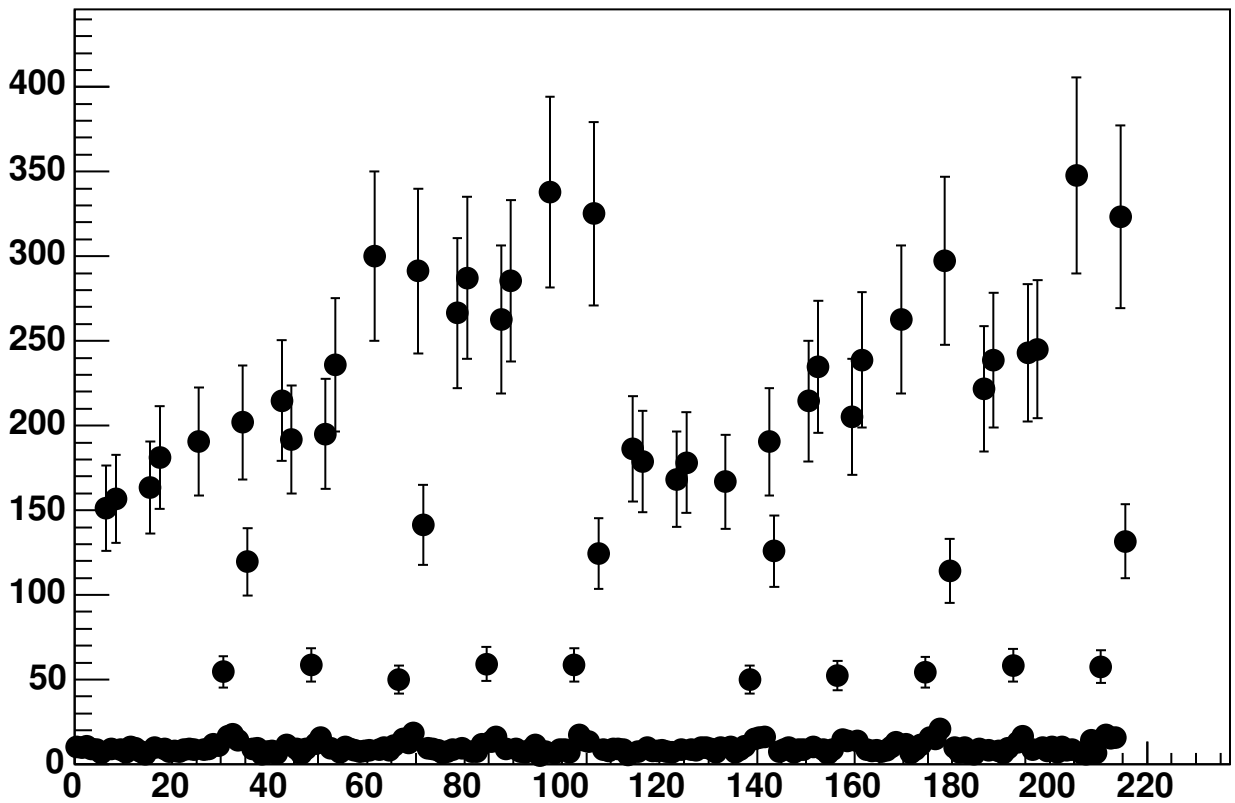
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



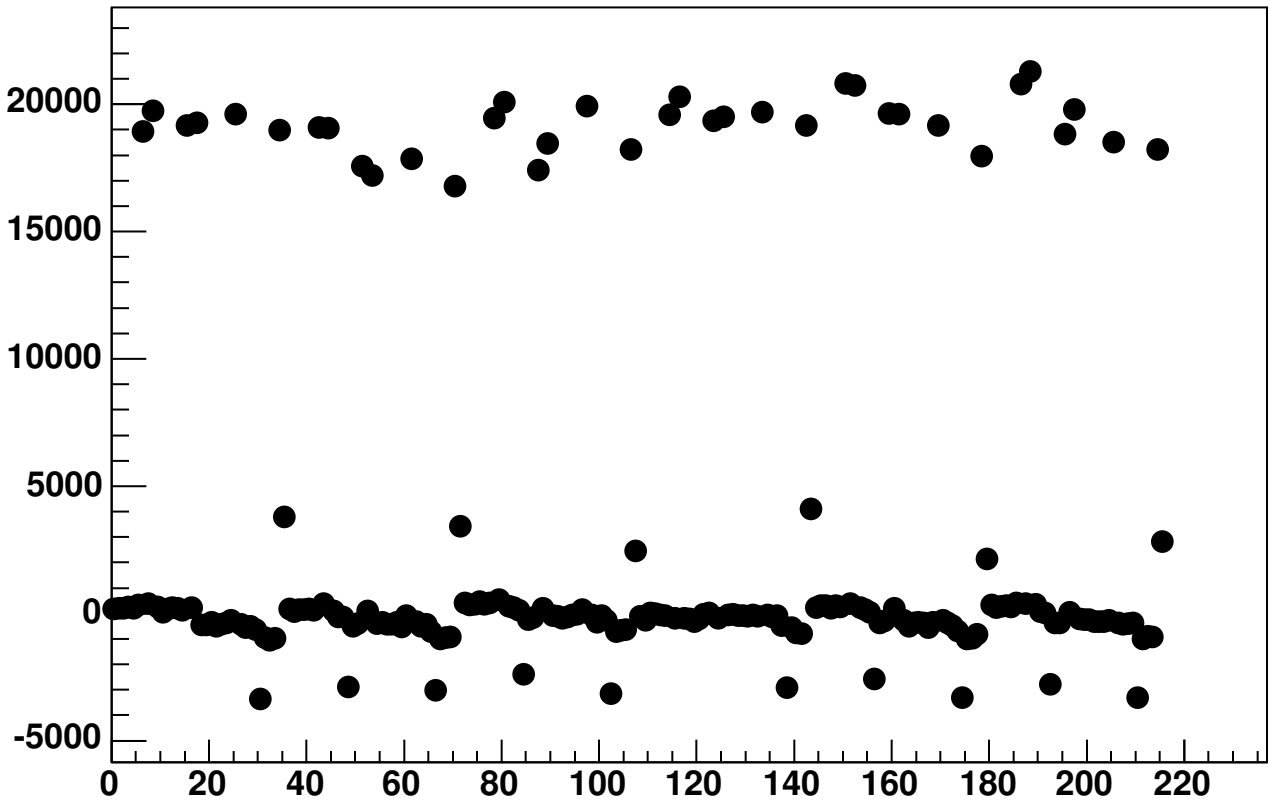
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



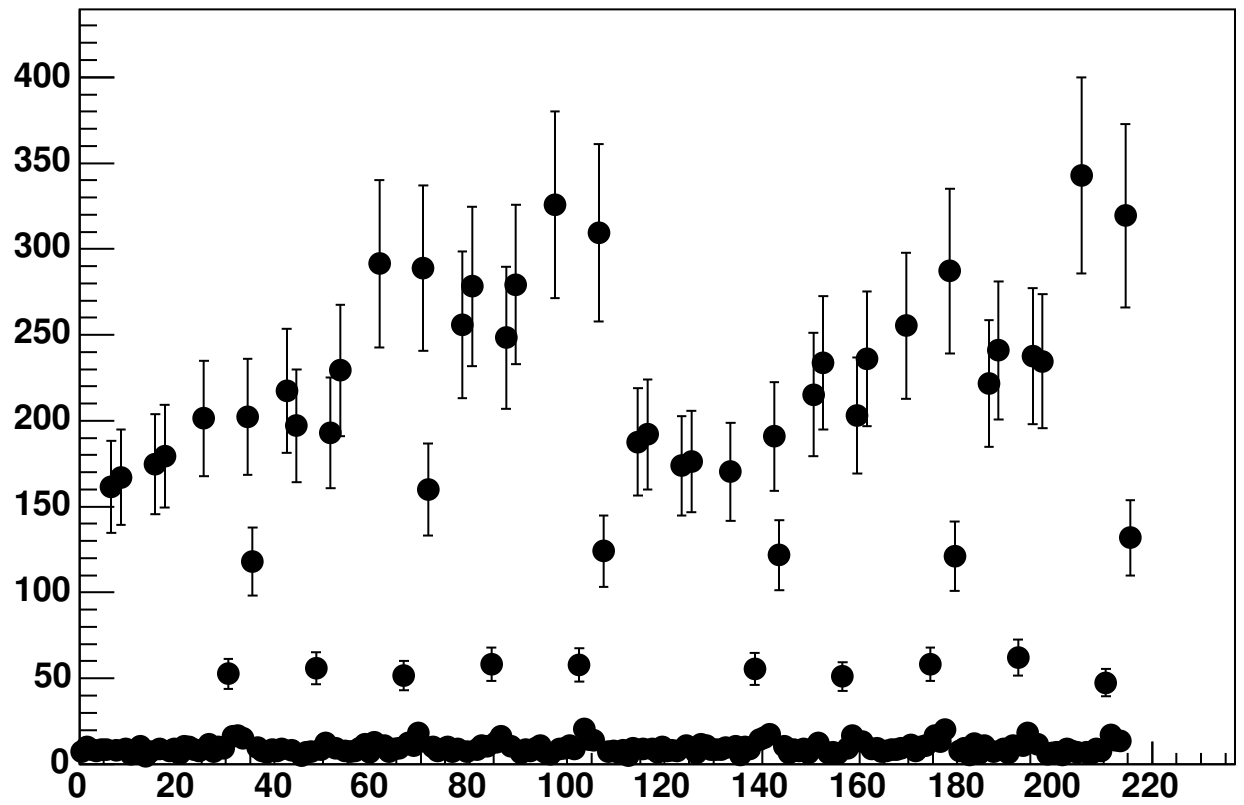
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



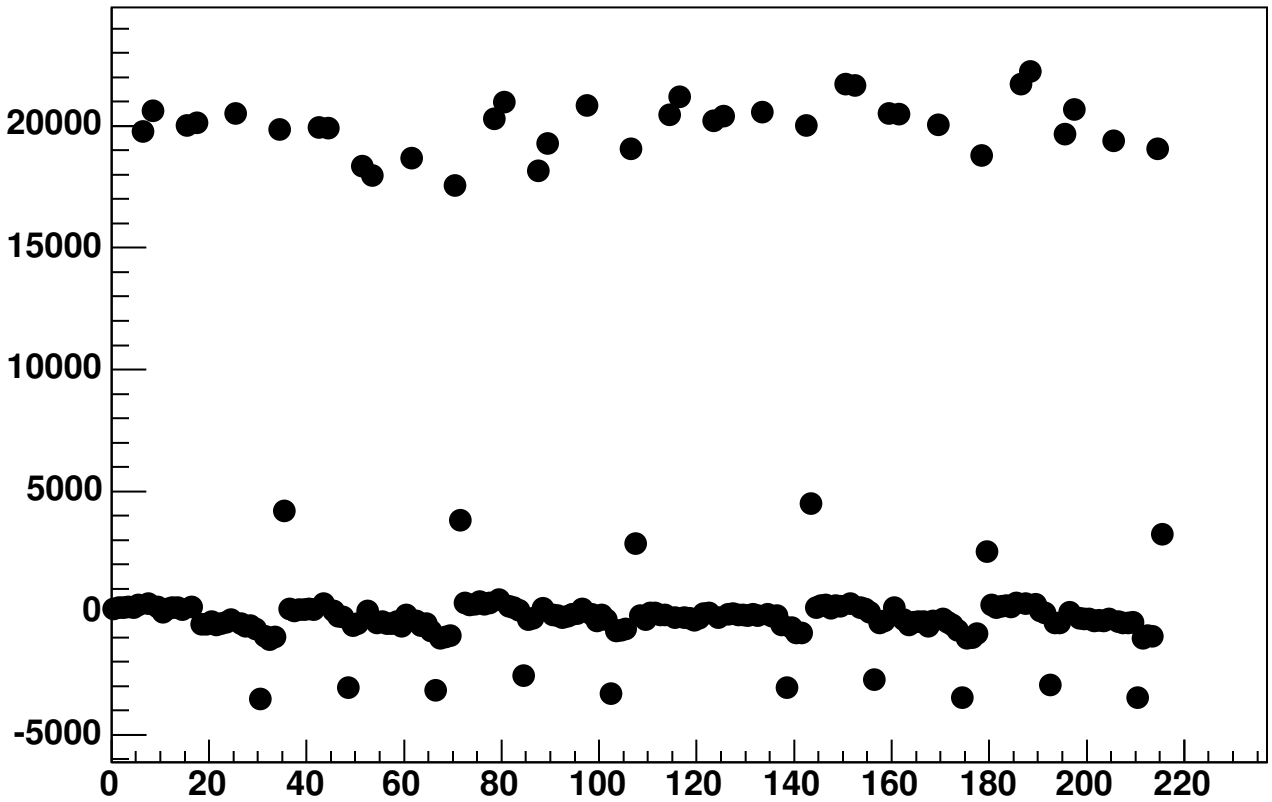
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



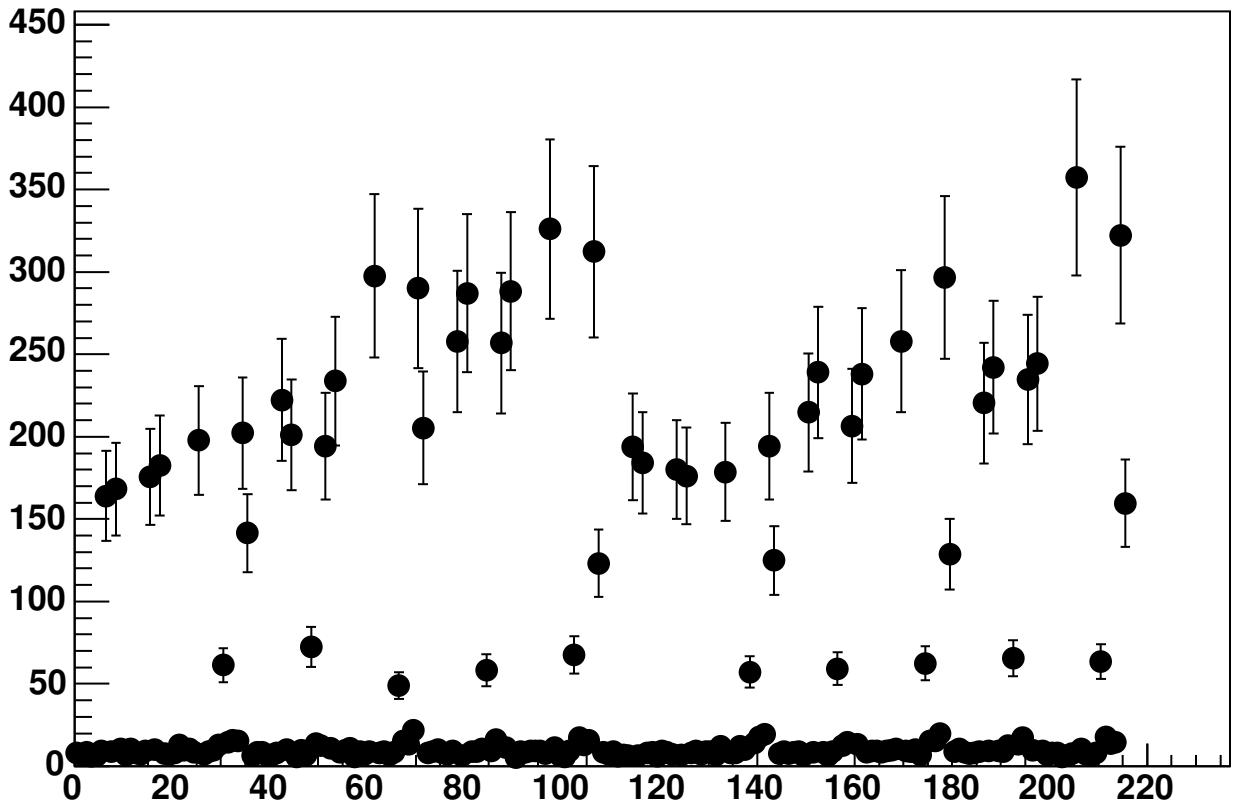
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



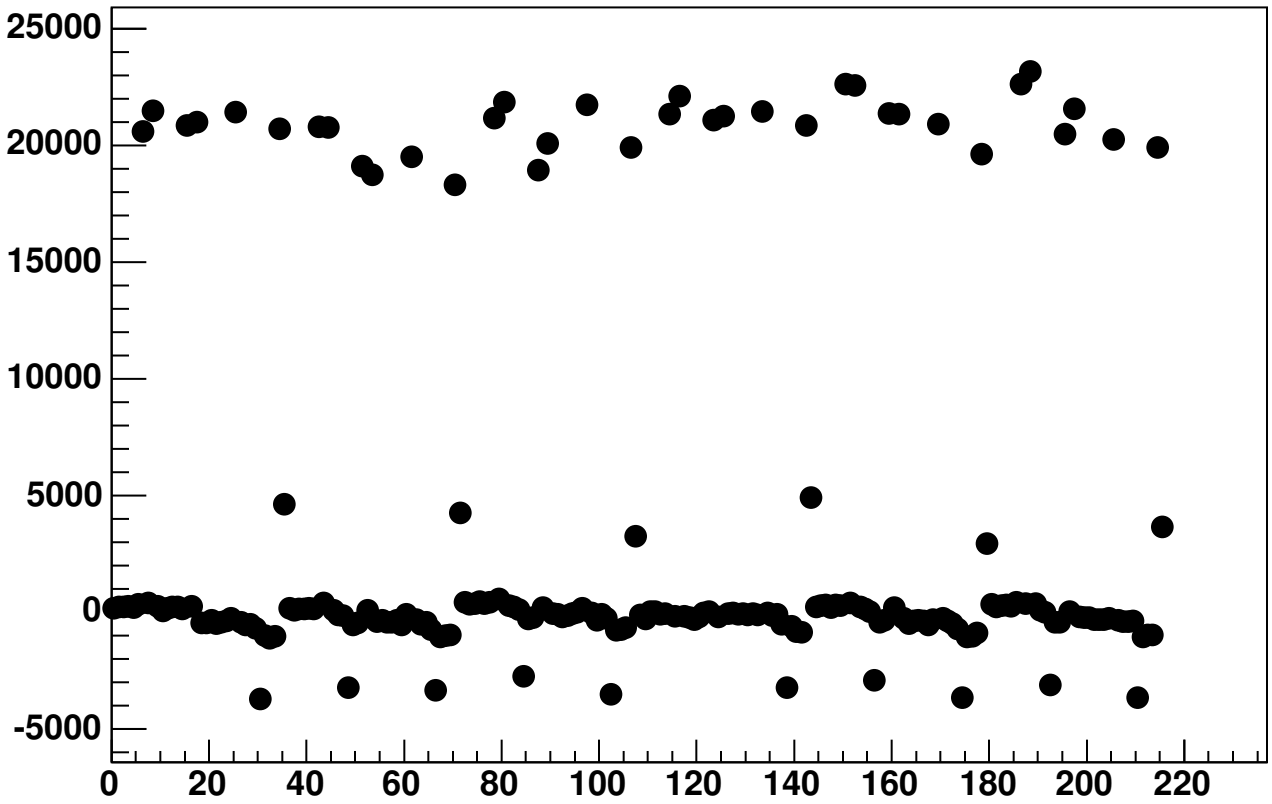
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



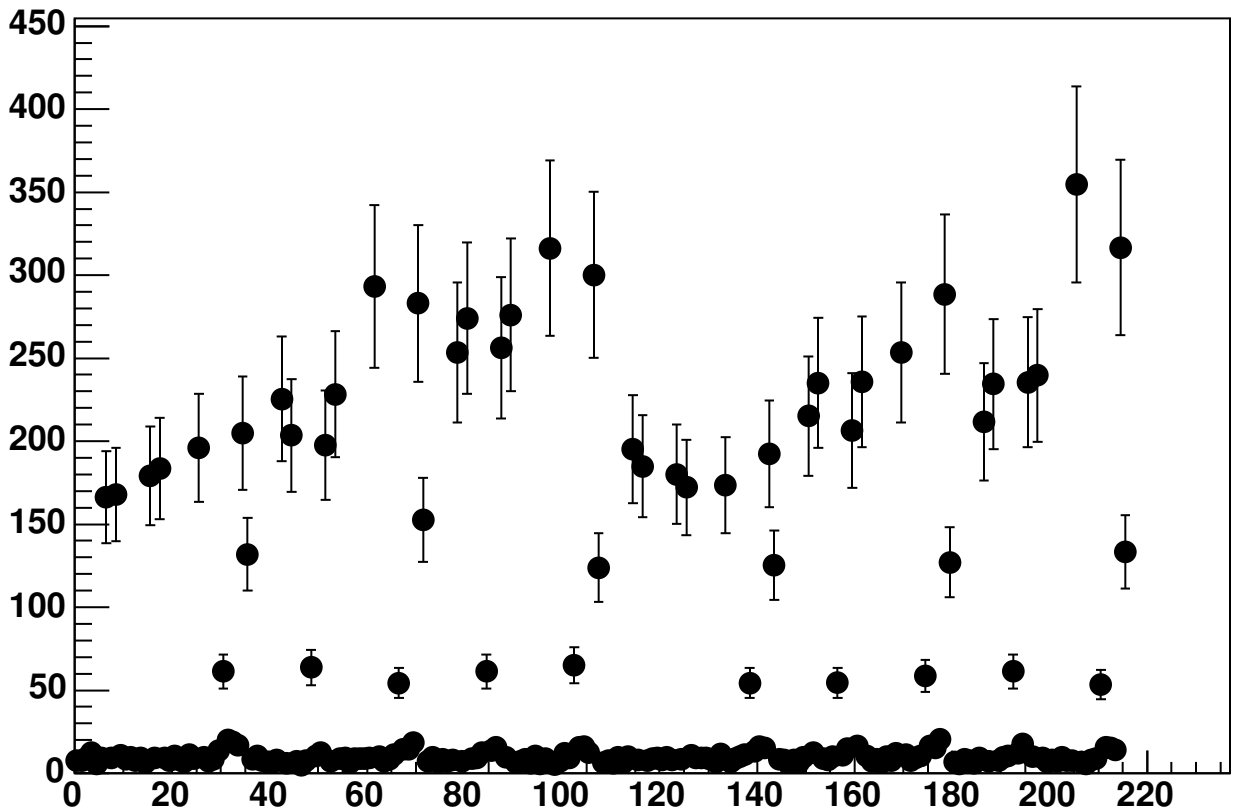
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



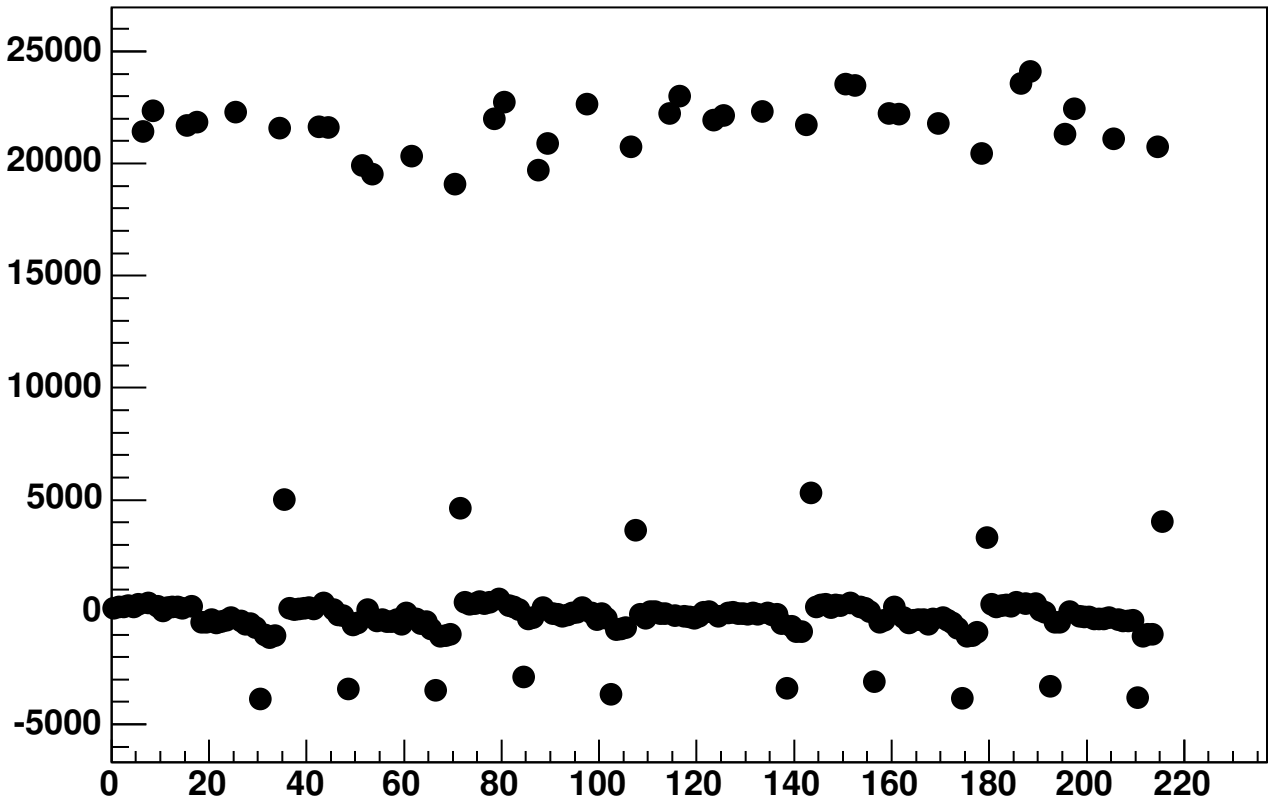
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



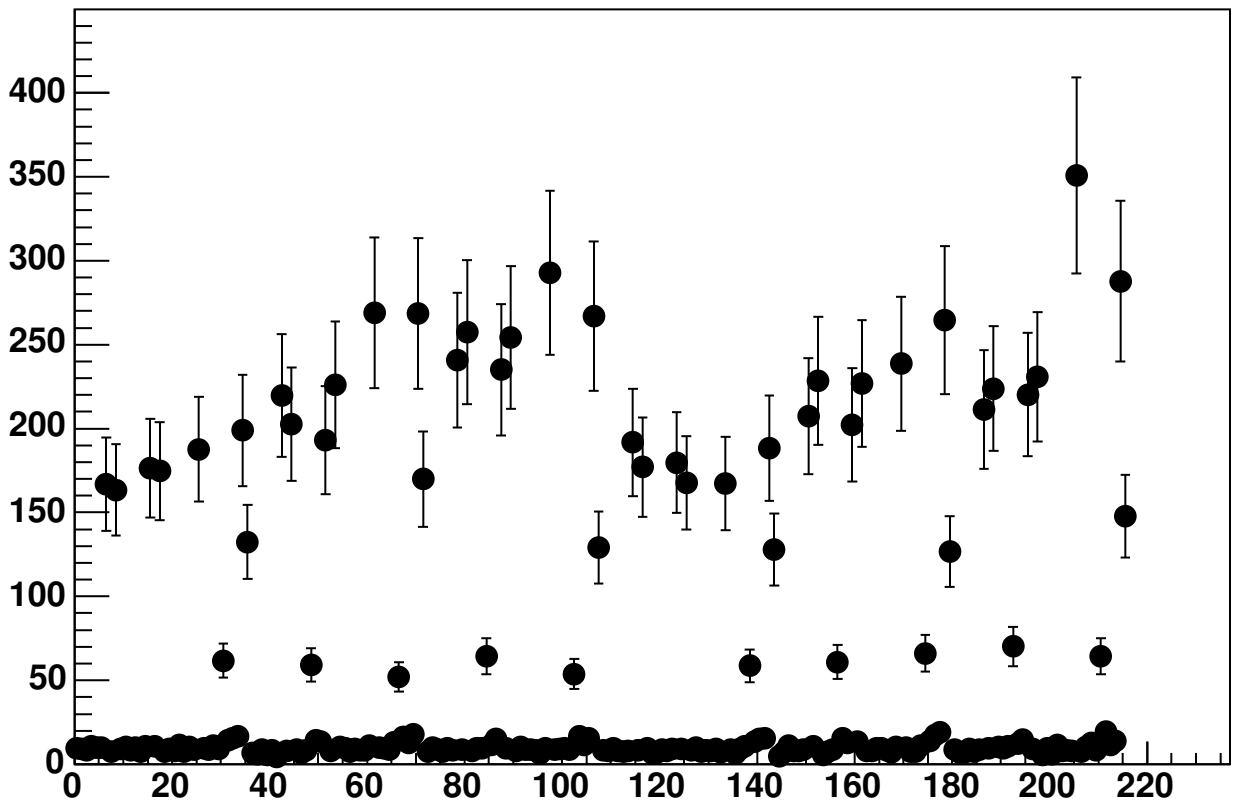
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



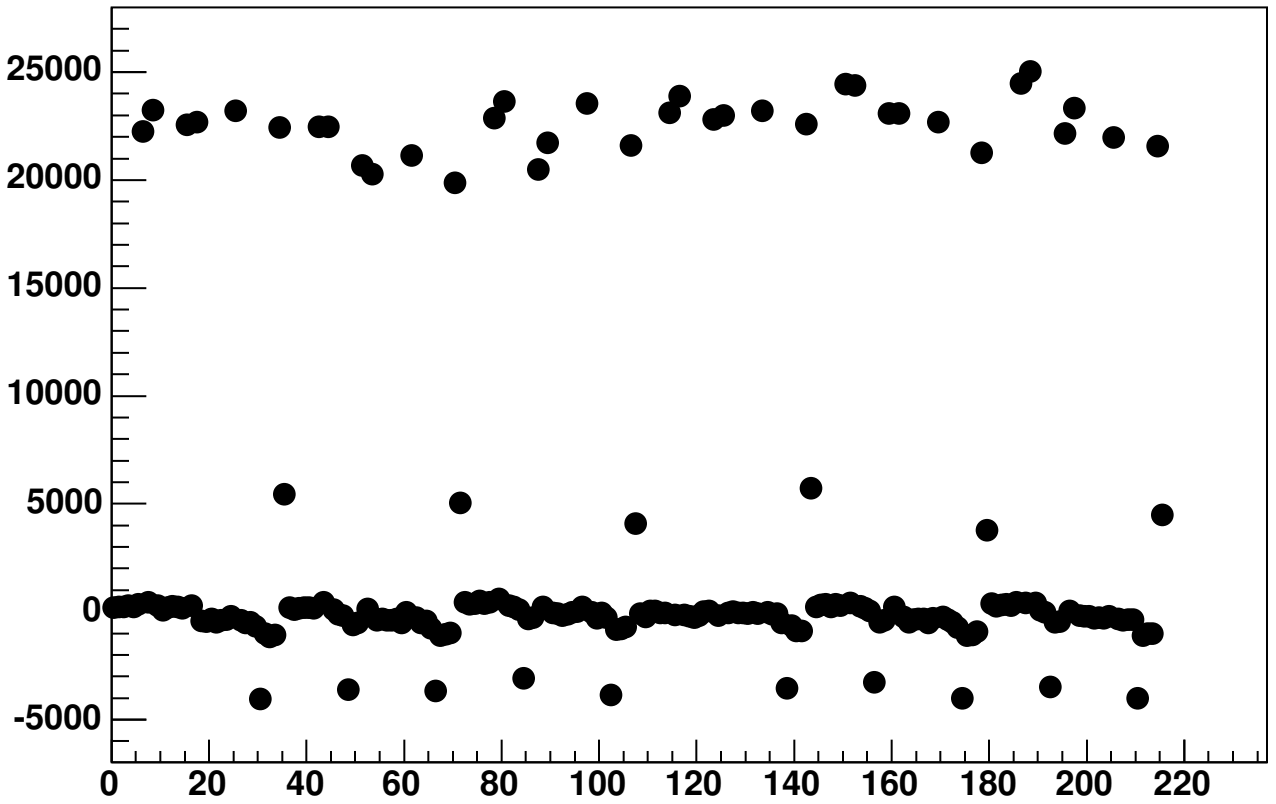
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



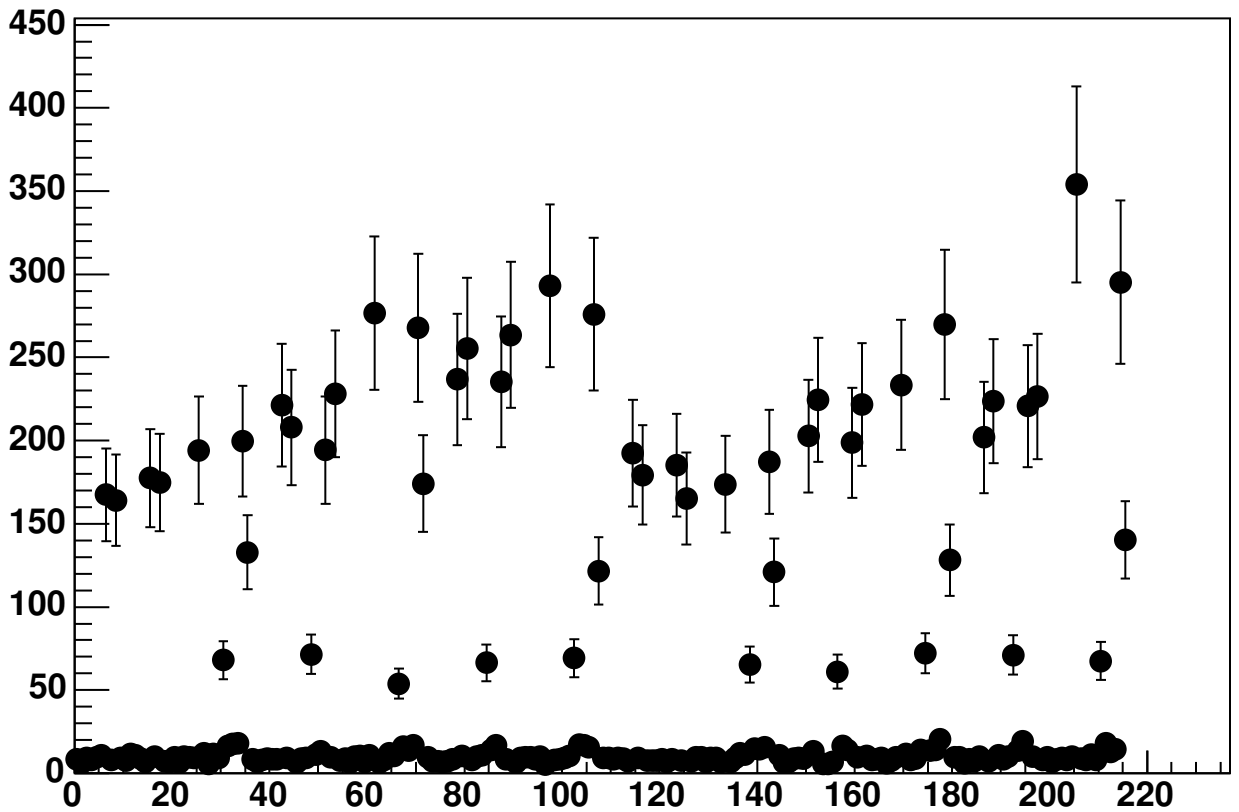
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



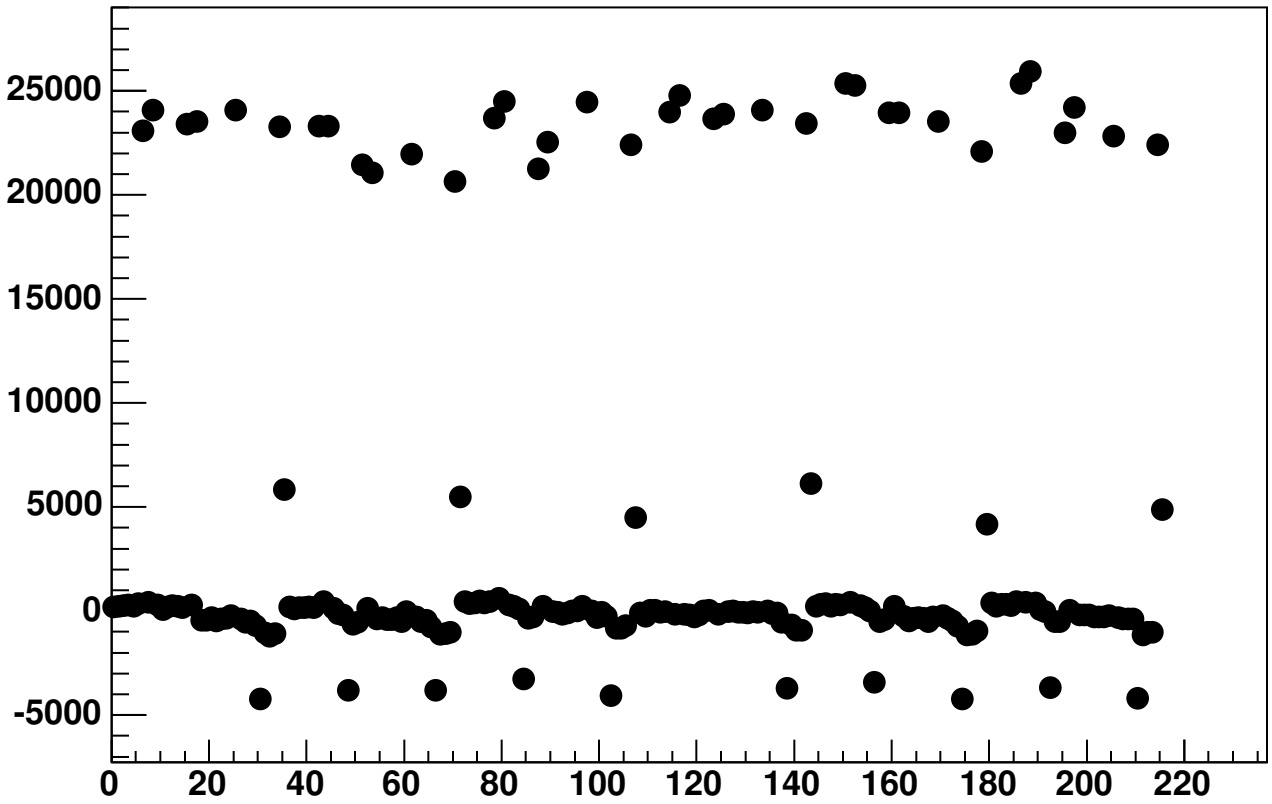
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



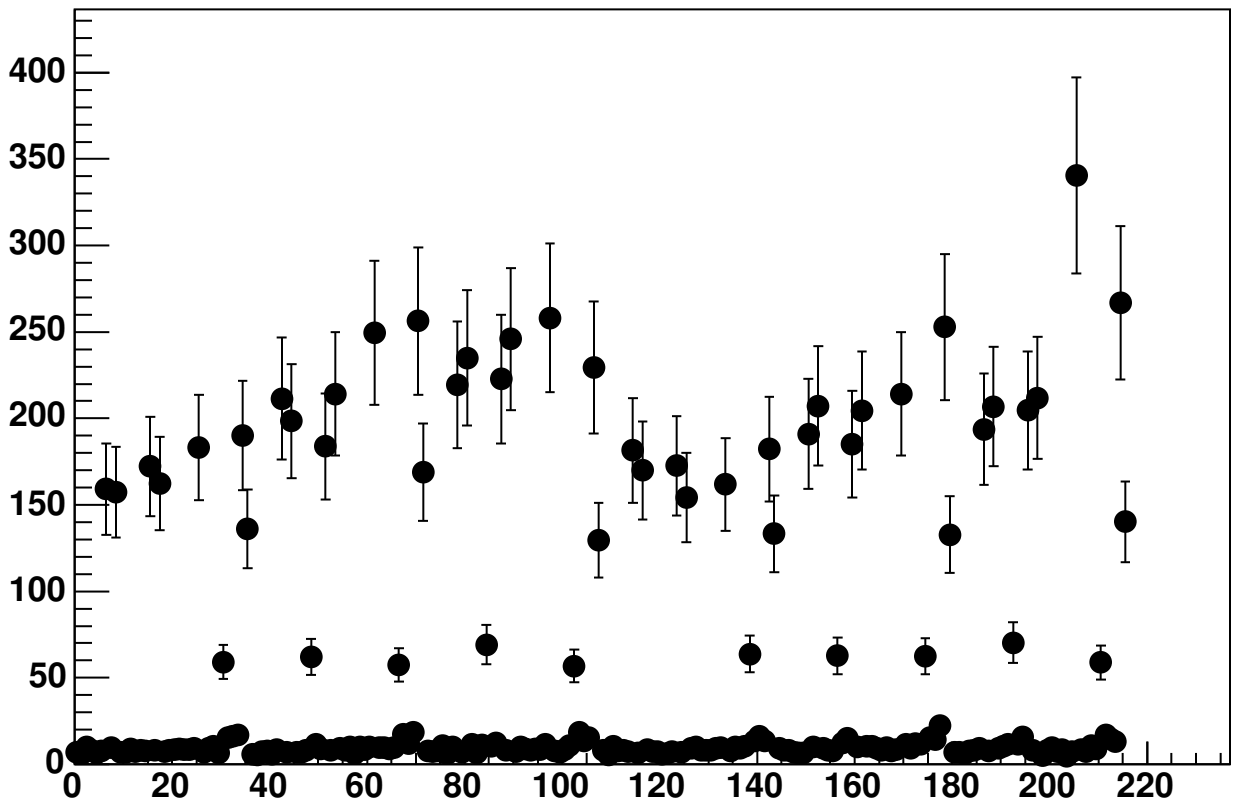
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



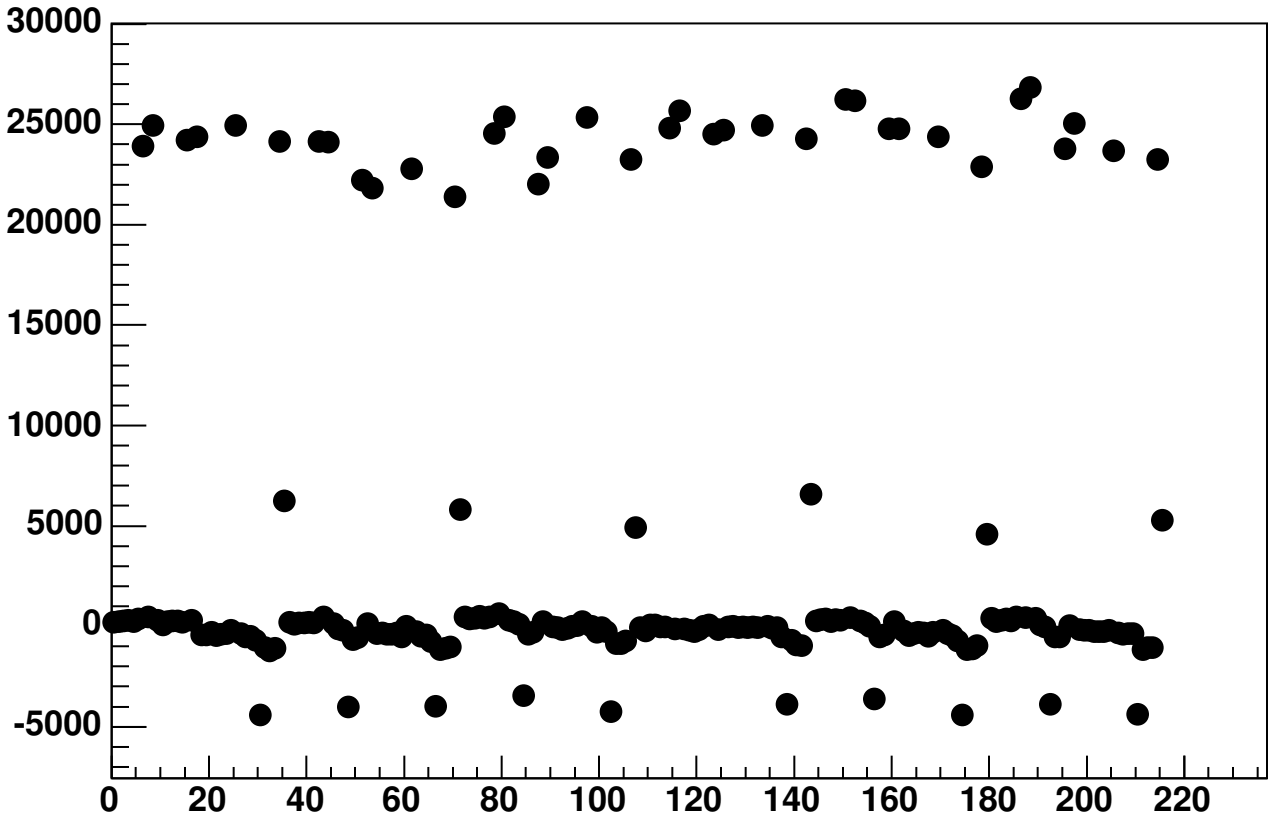
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



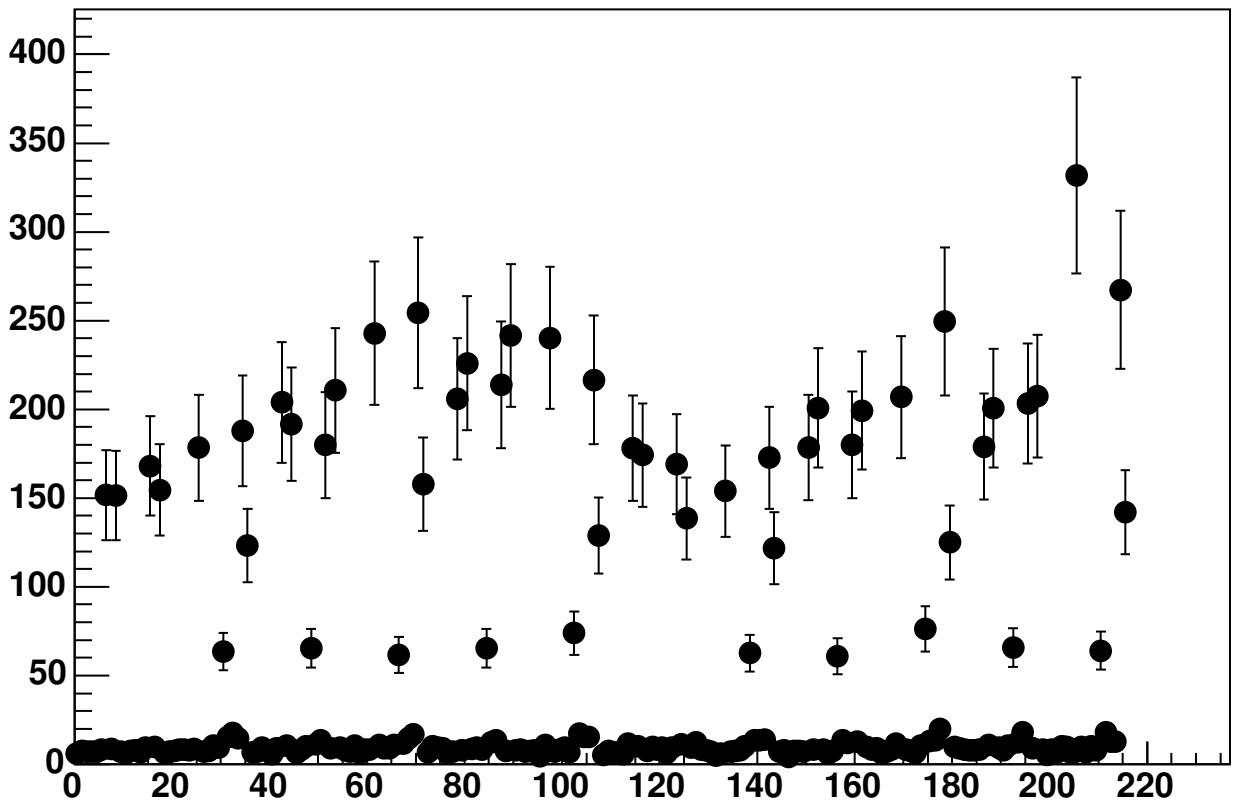
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



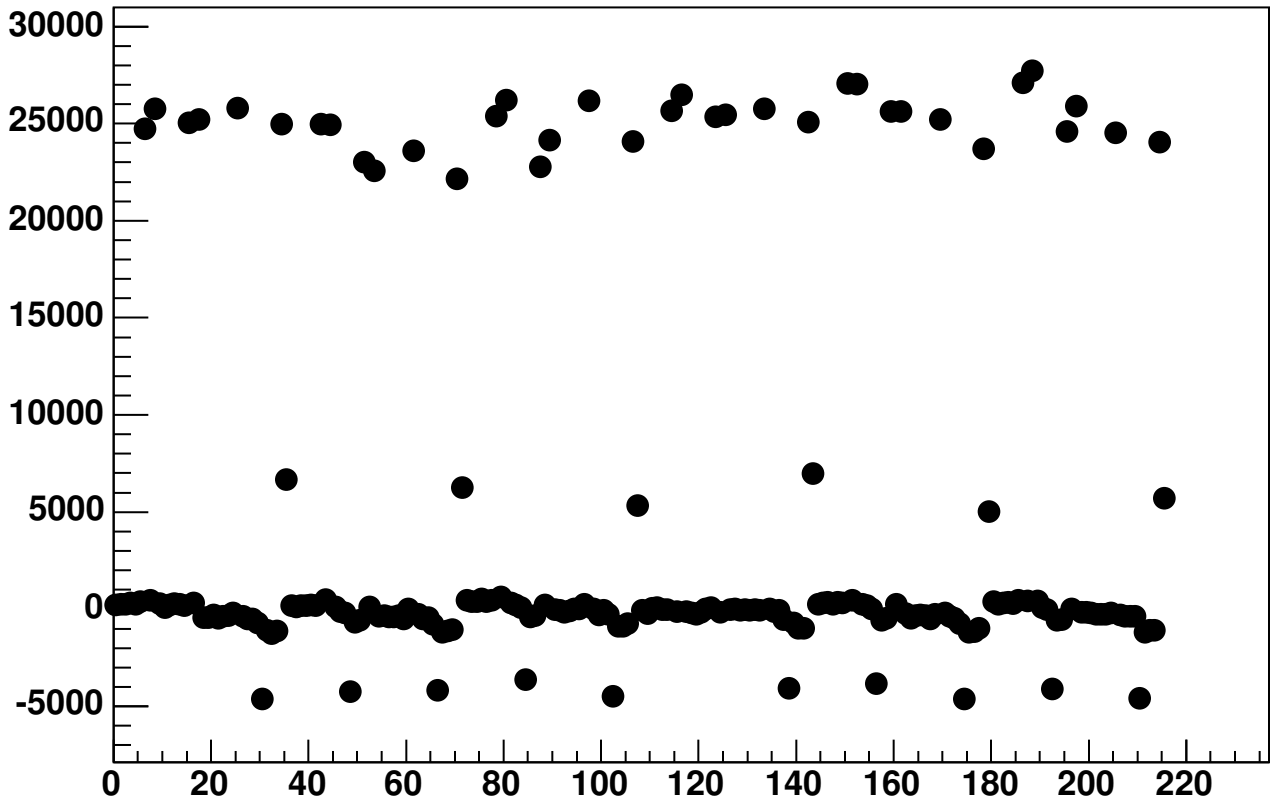
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



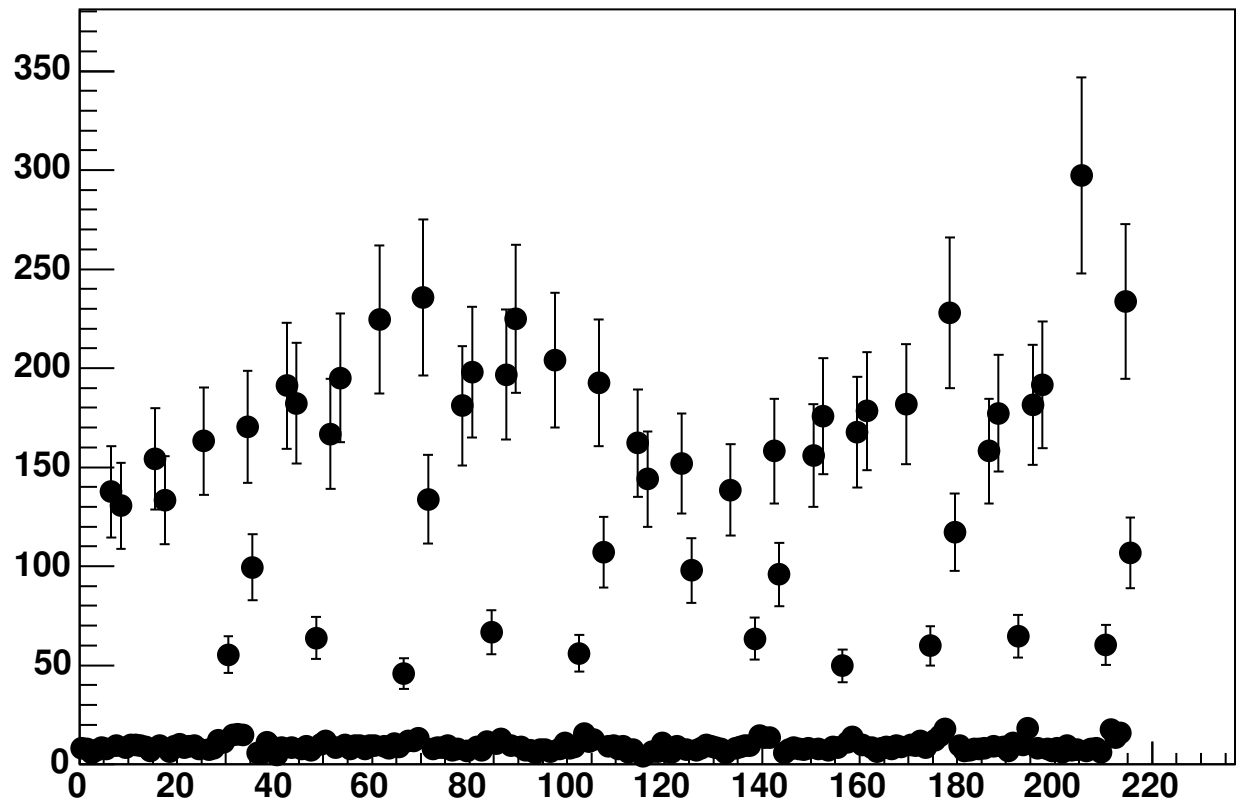
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



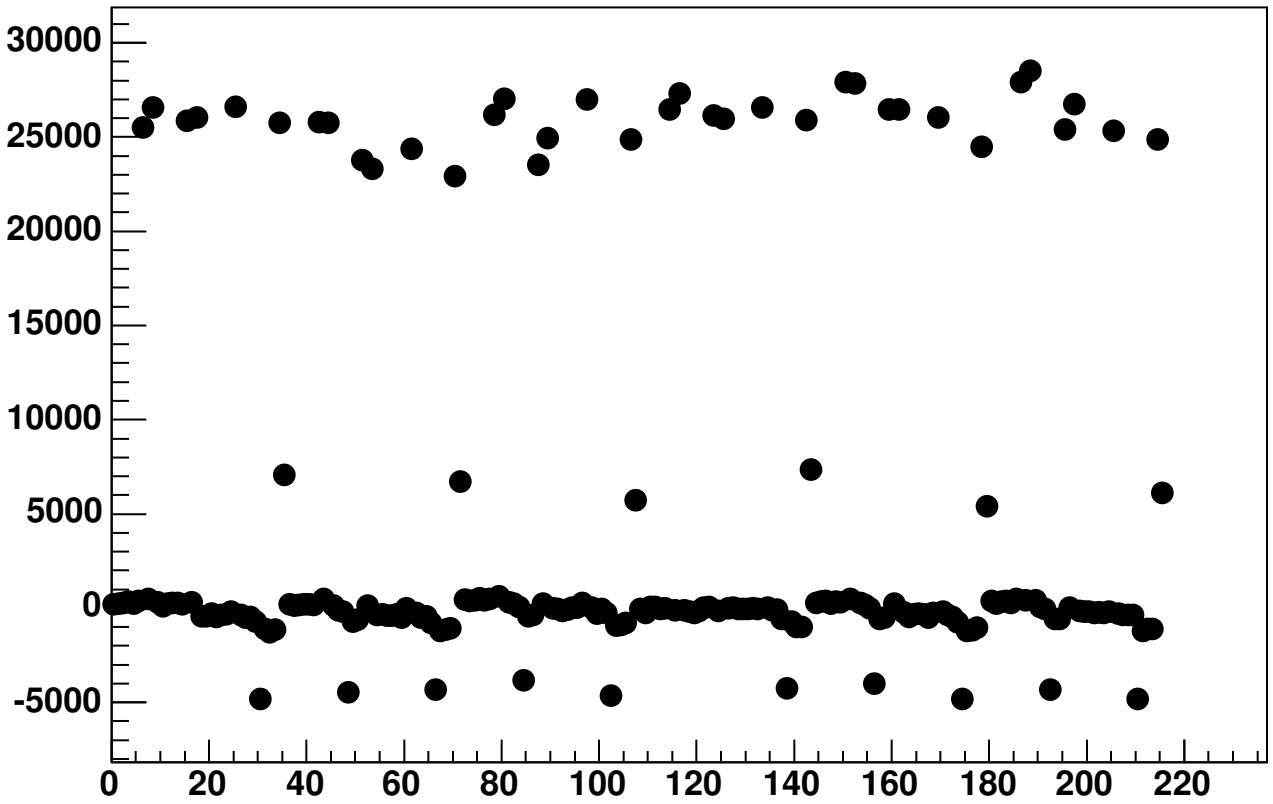
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



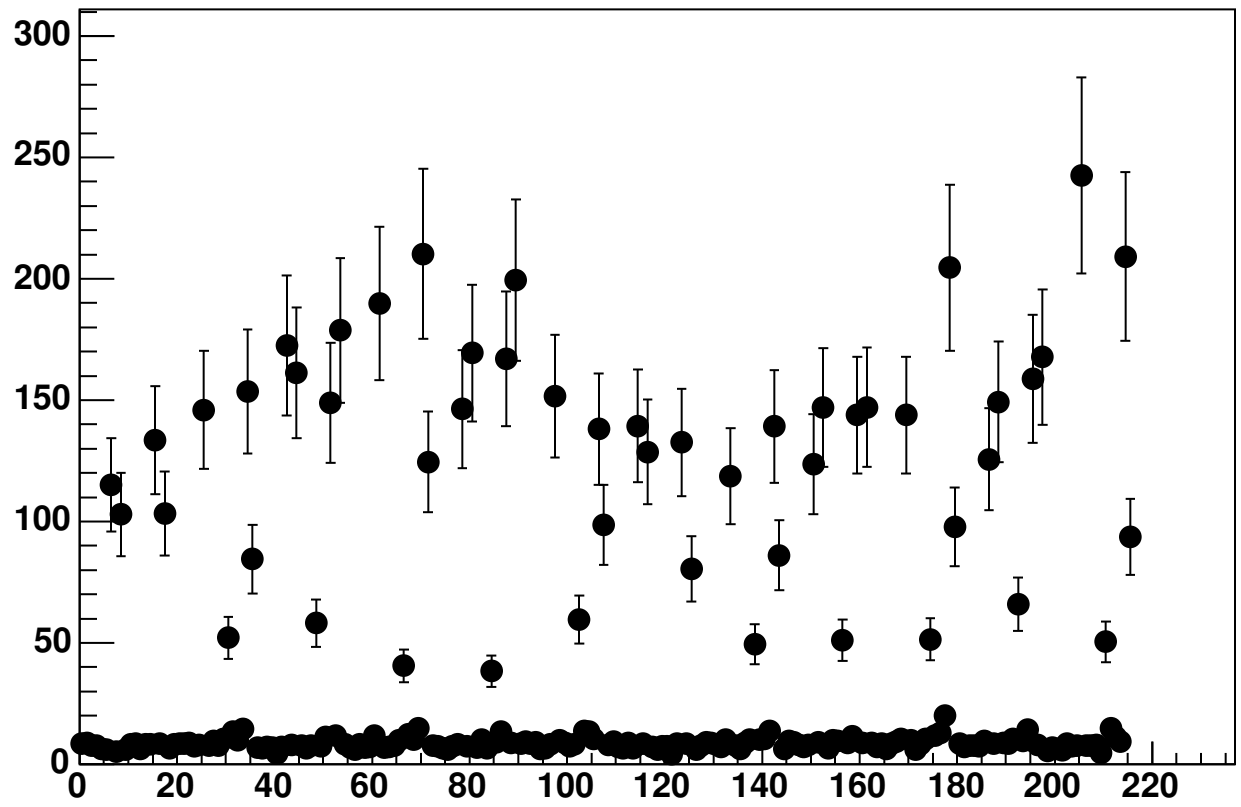
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



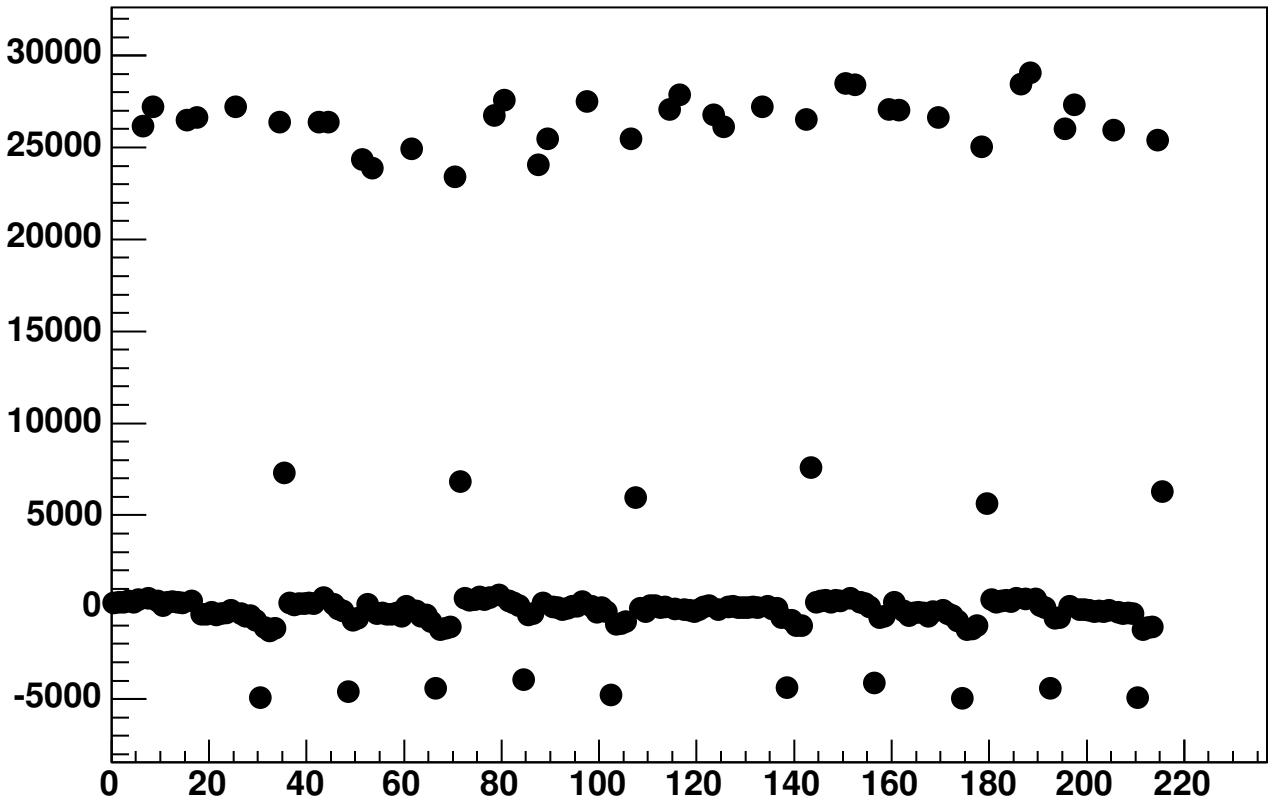
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



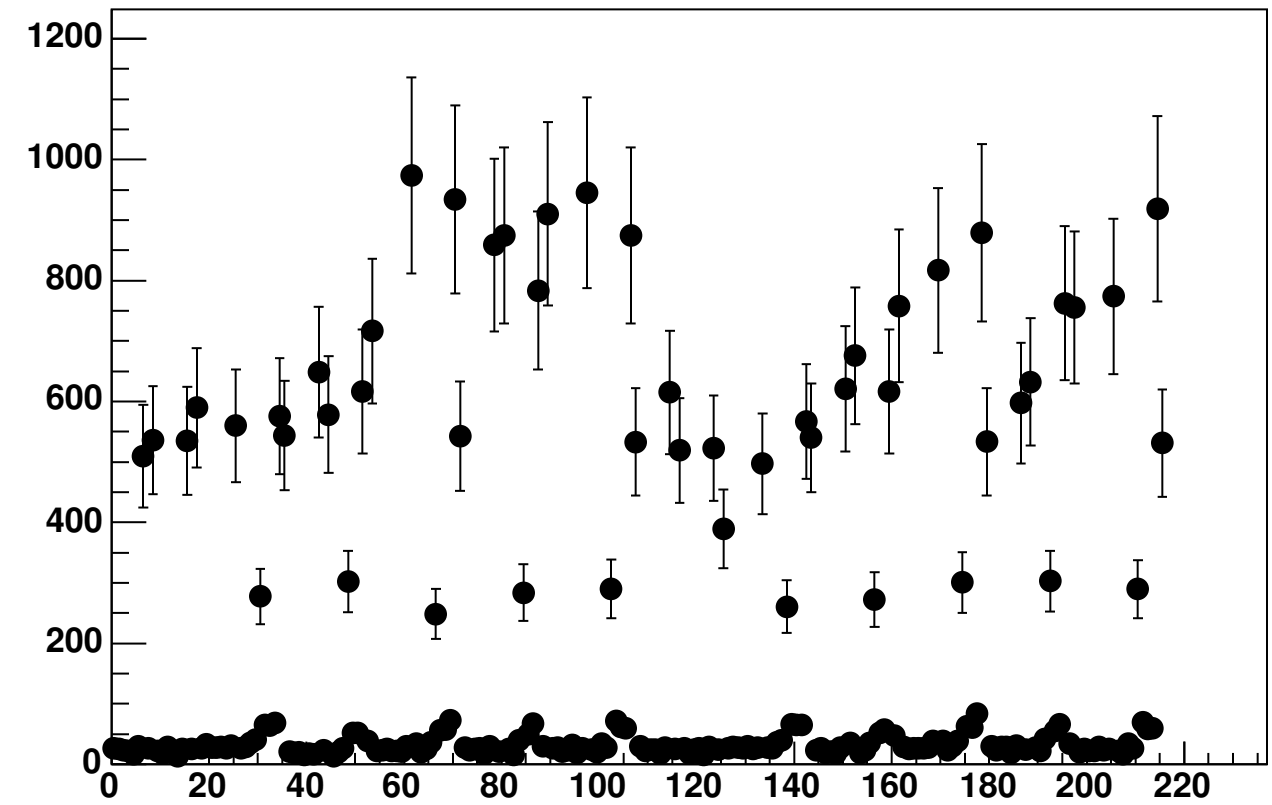
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



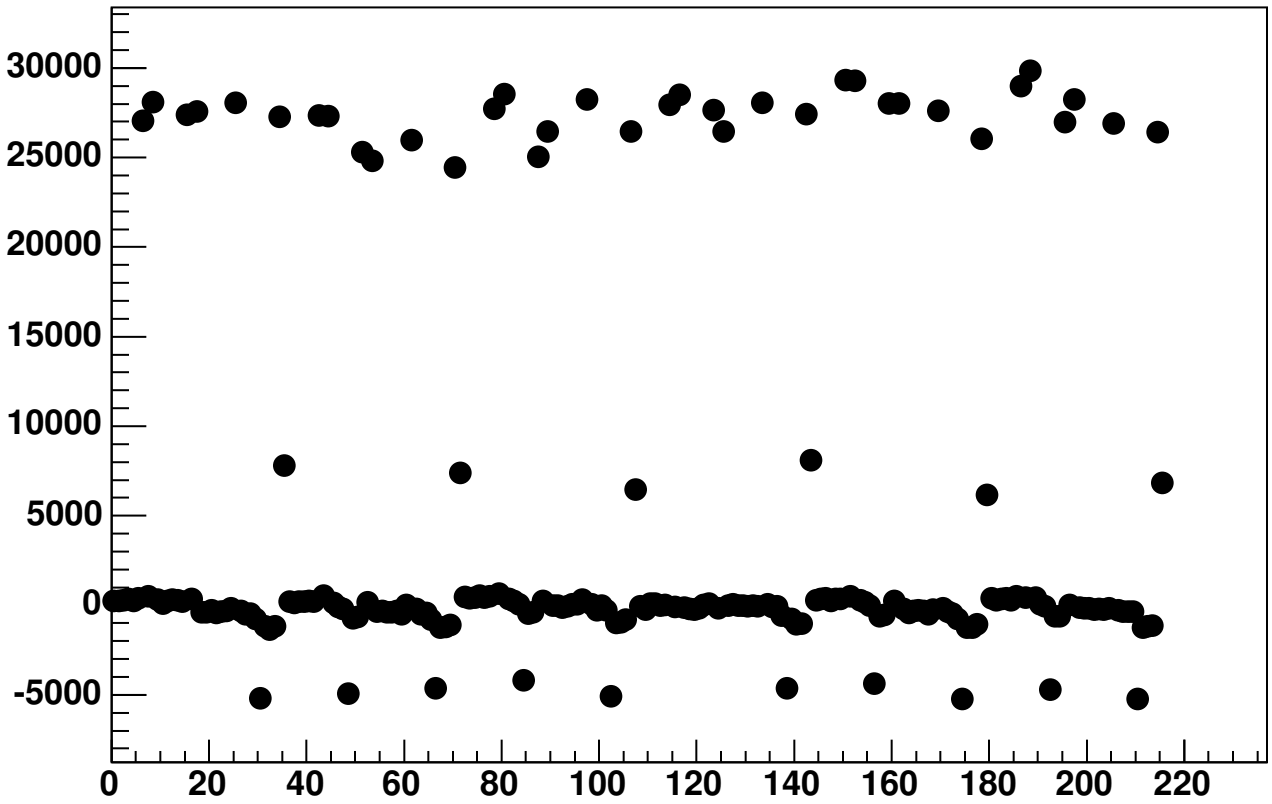
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



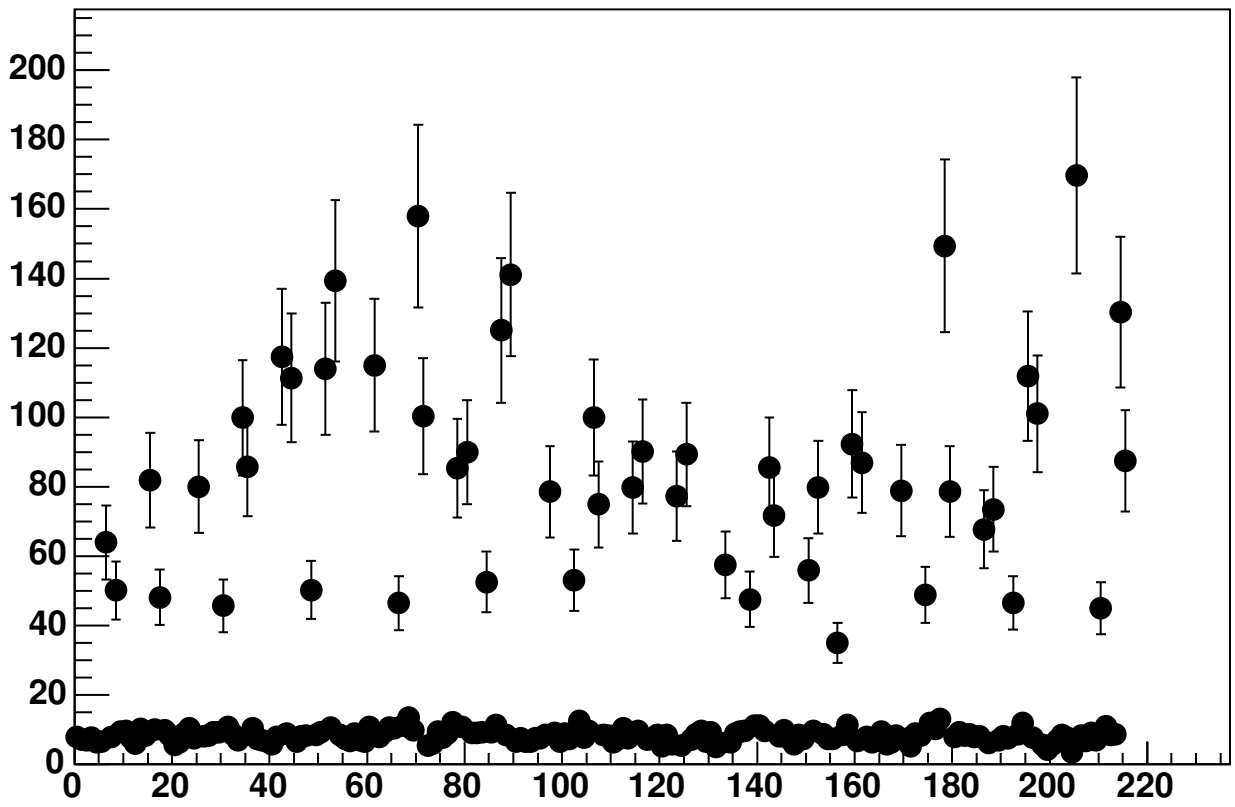
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



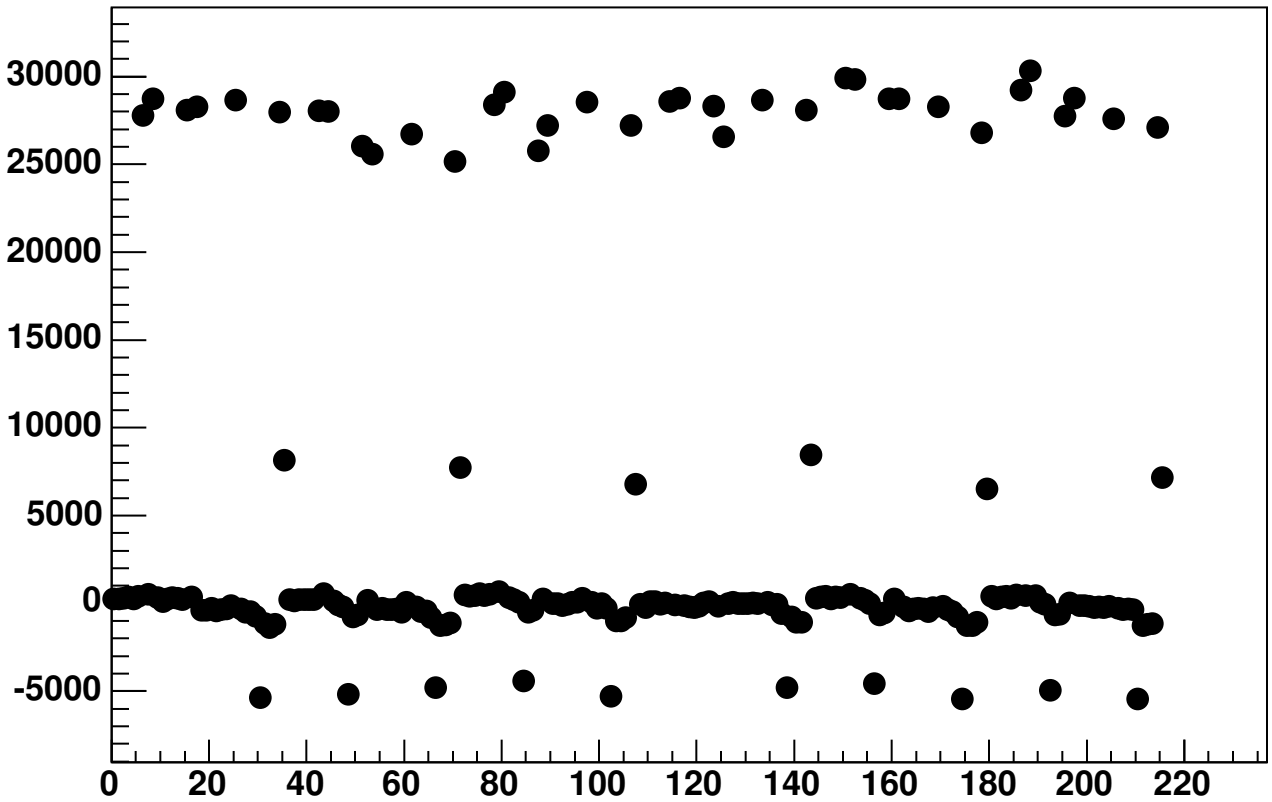
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



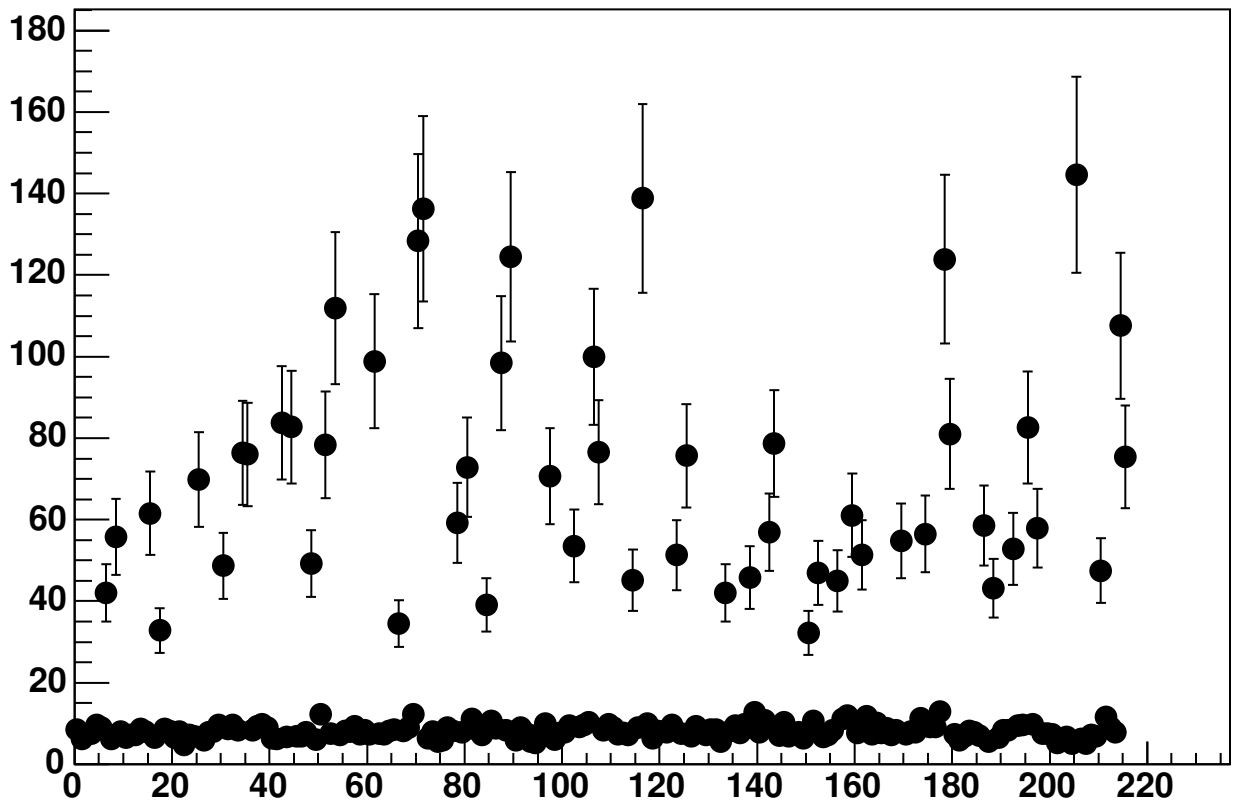
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



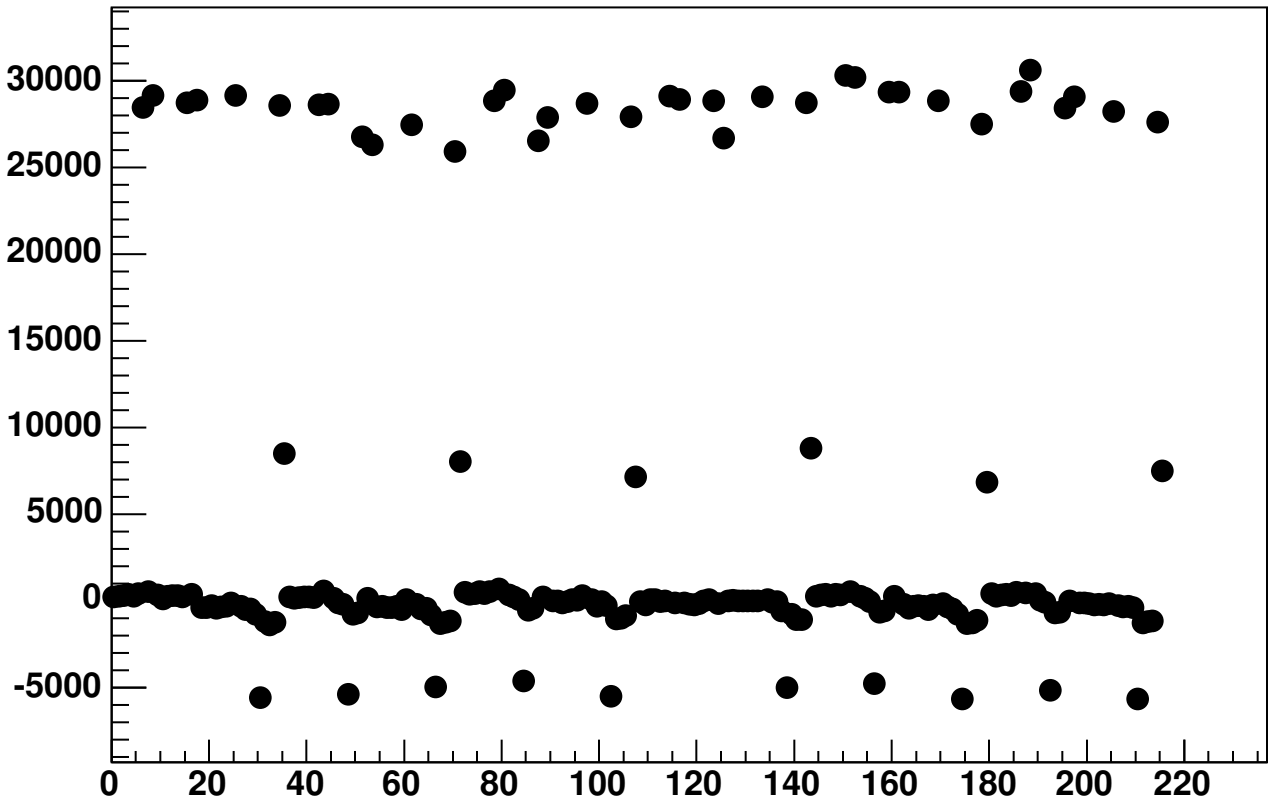
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



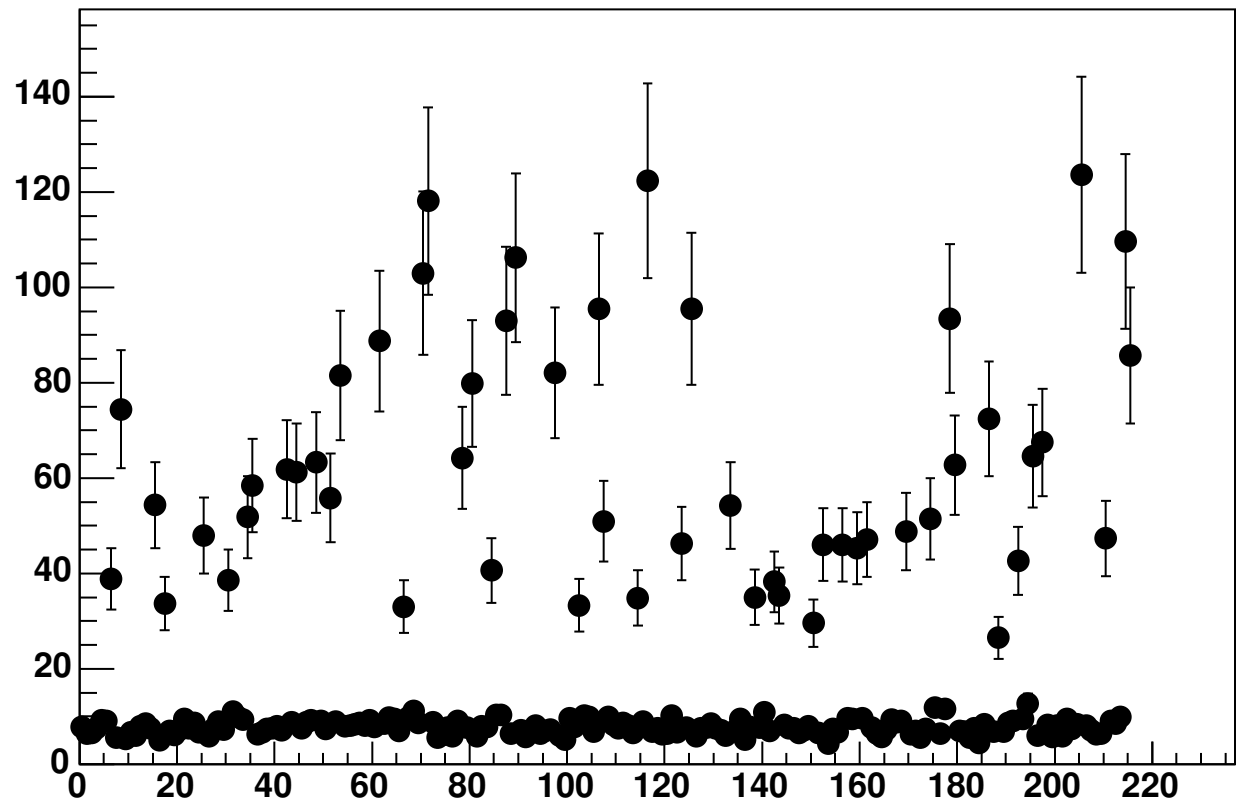
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



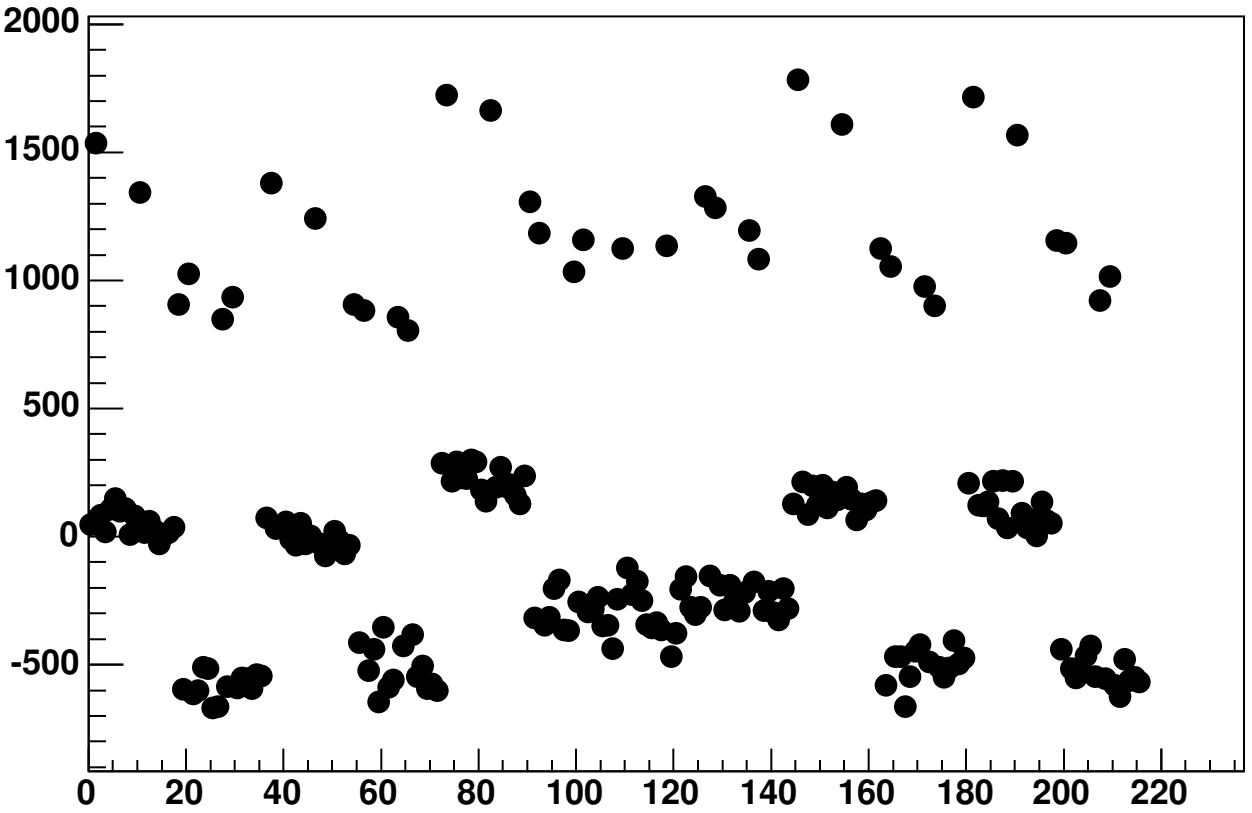
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



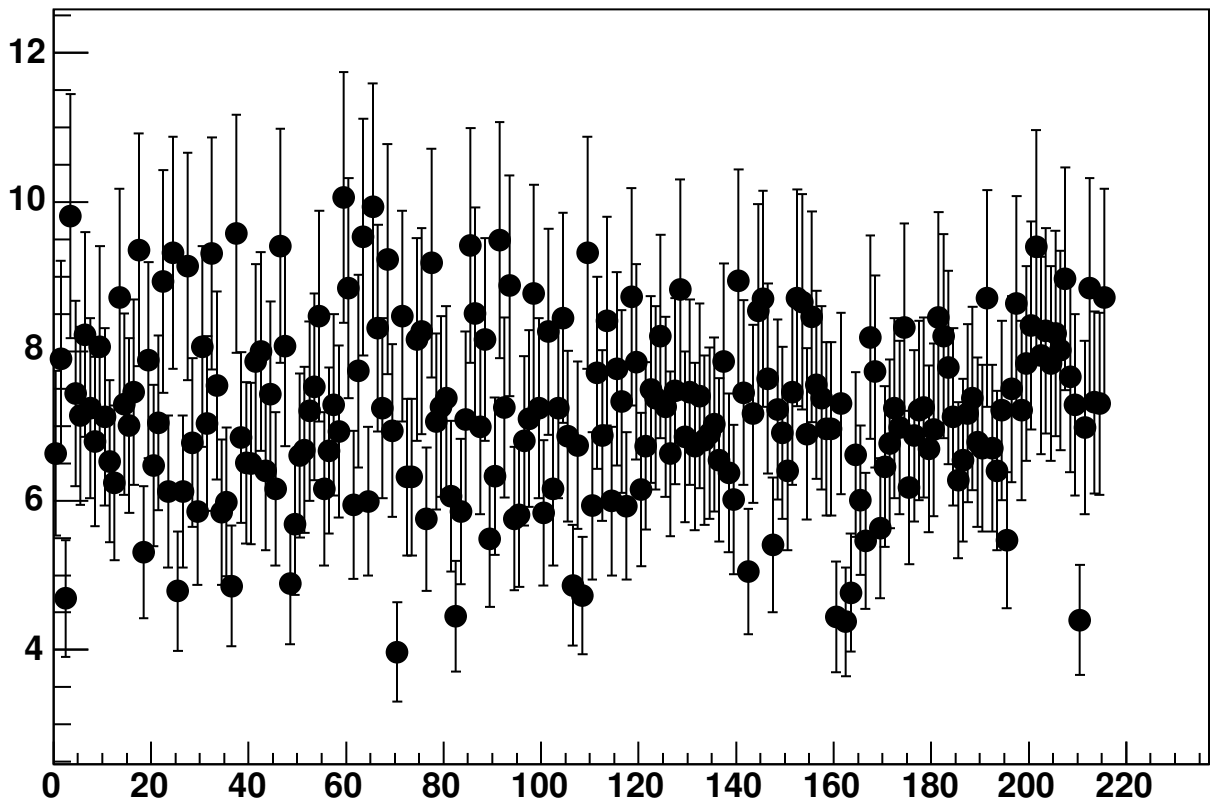
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



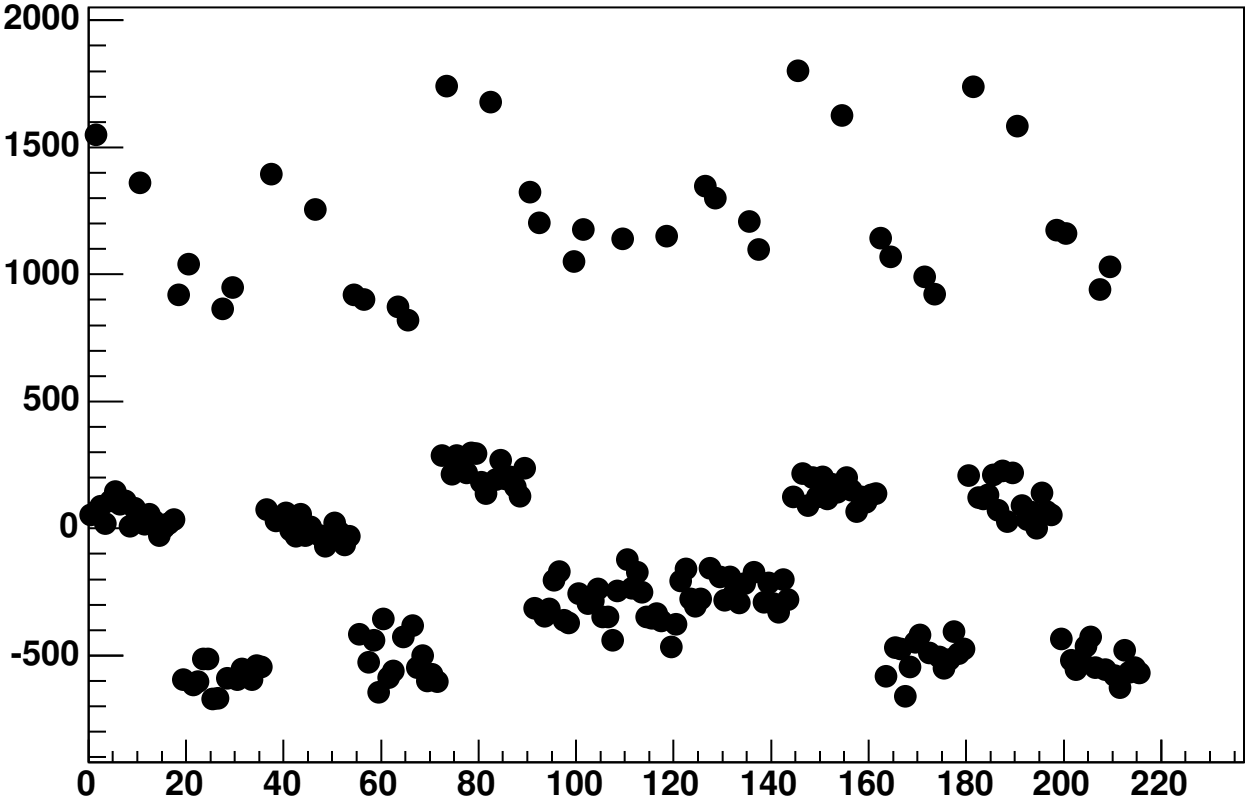
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



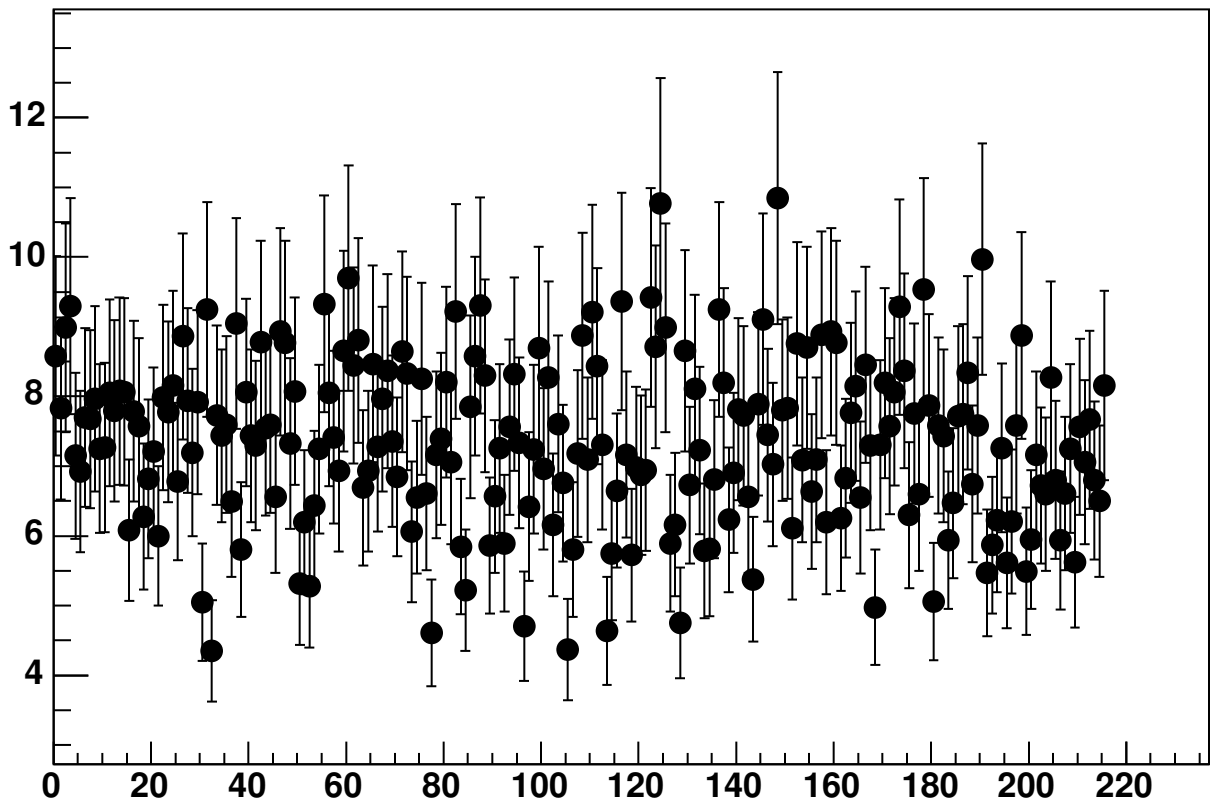
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



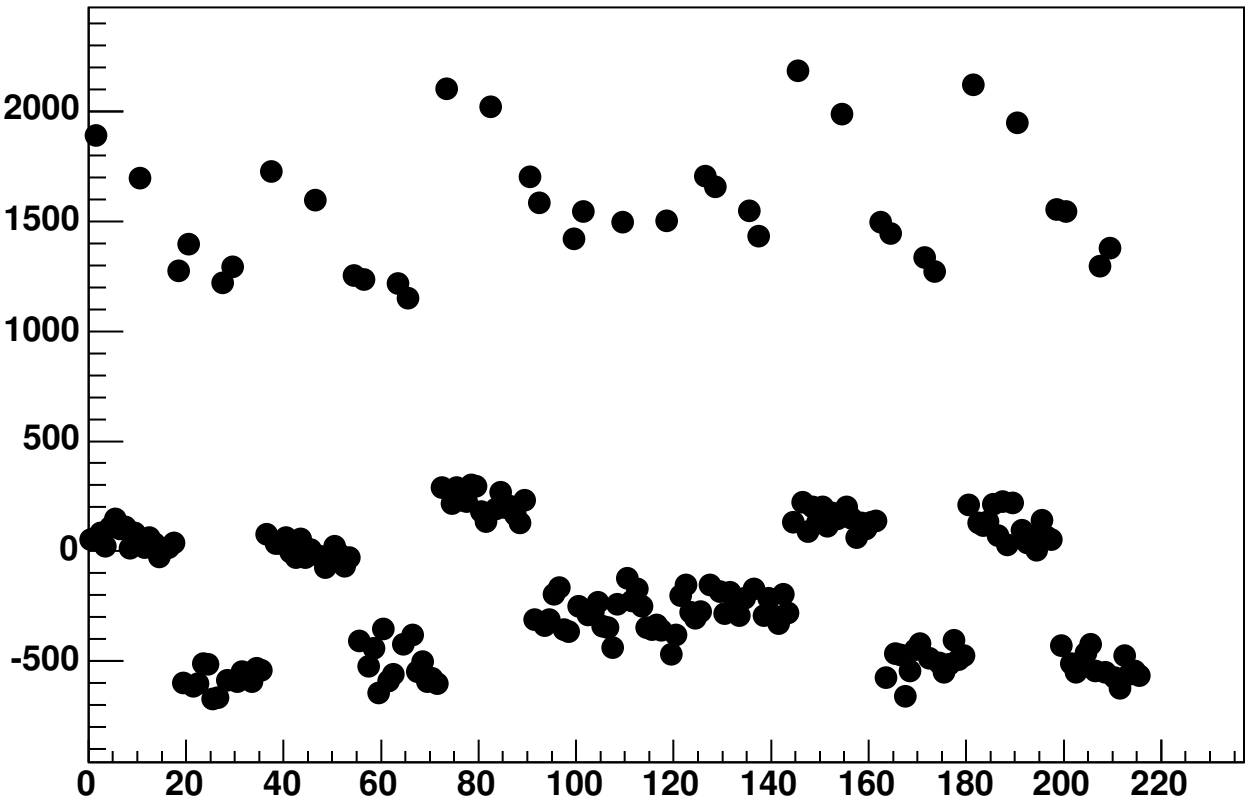
Enable 3, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



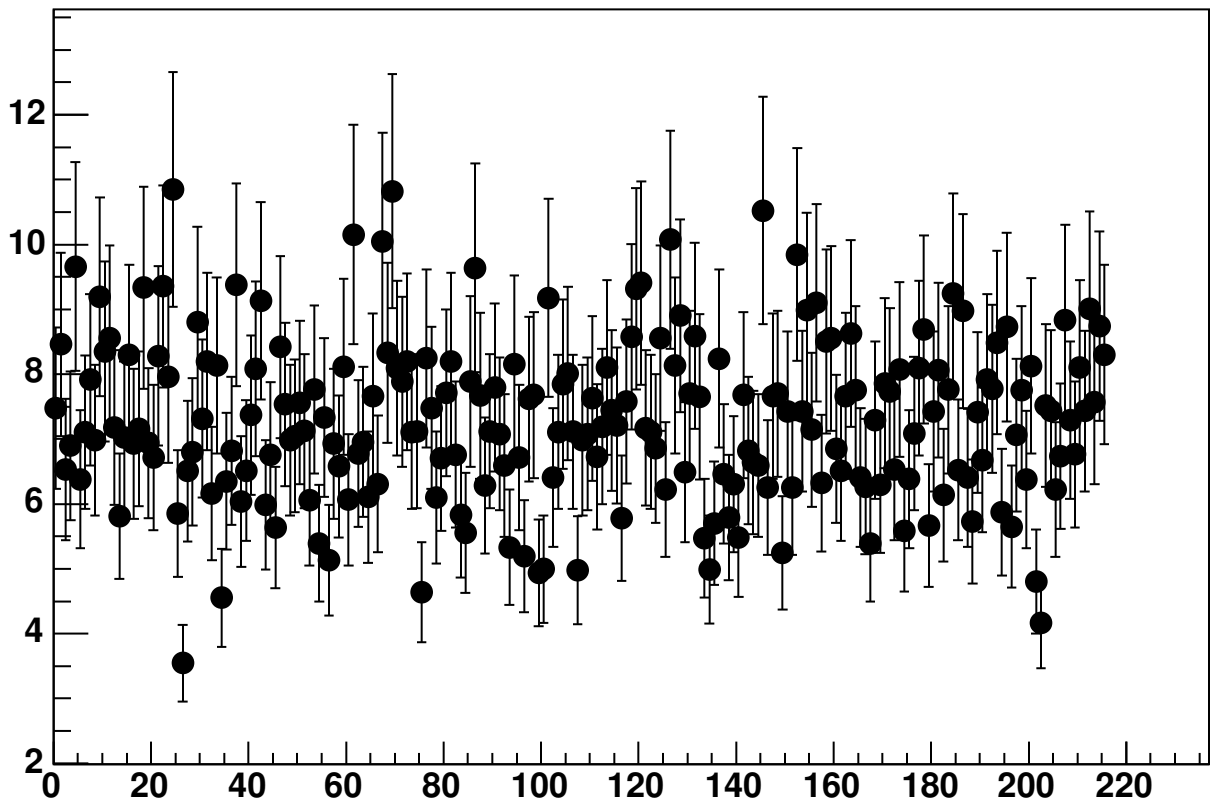
Enable 3, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



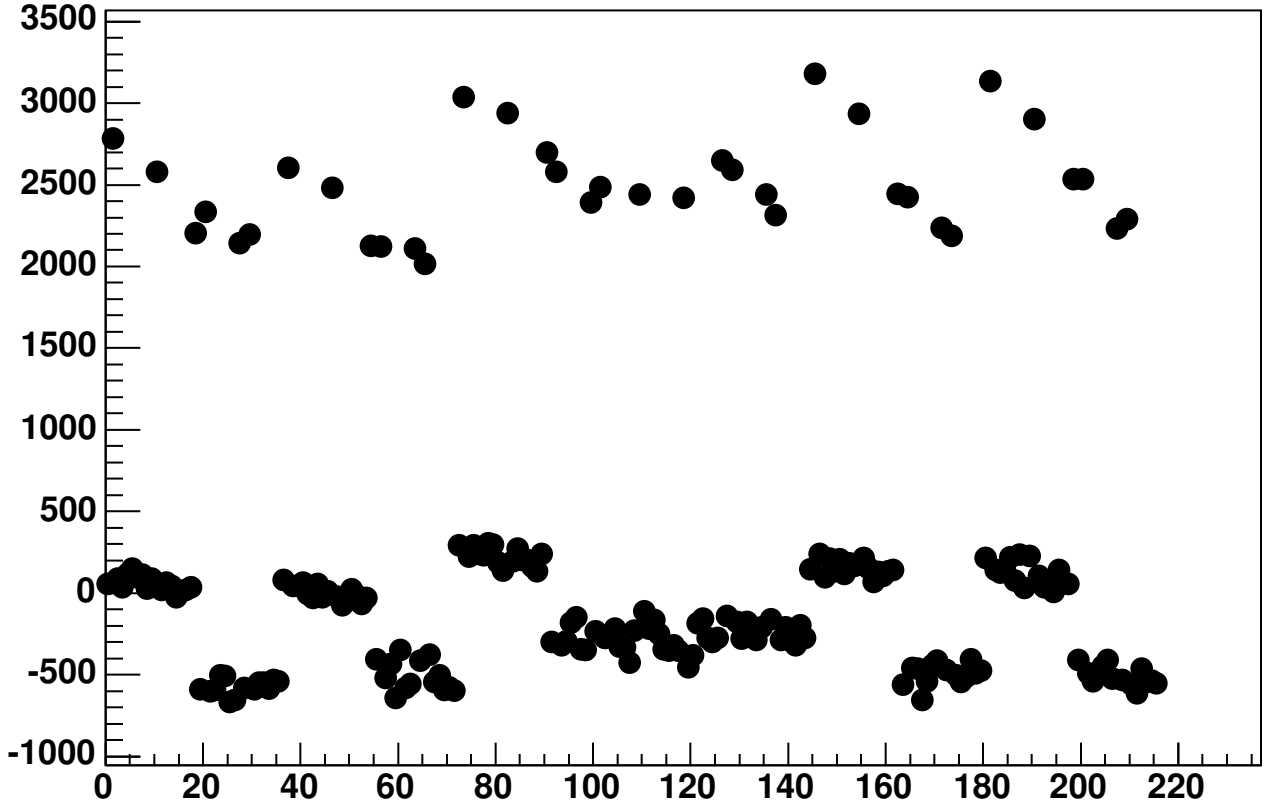
Enable 3, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



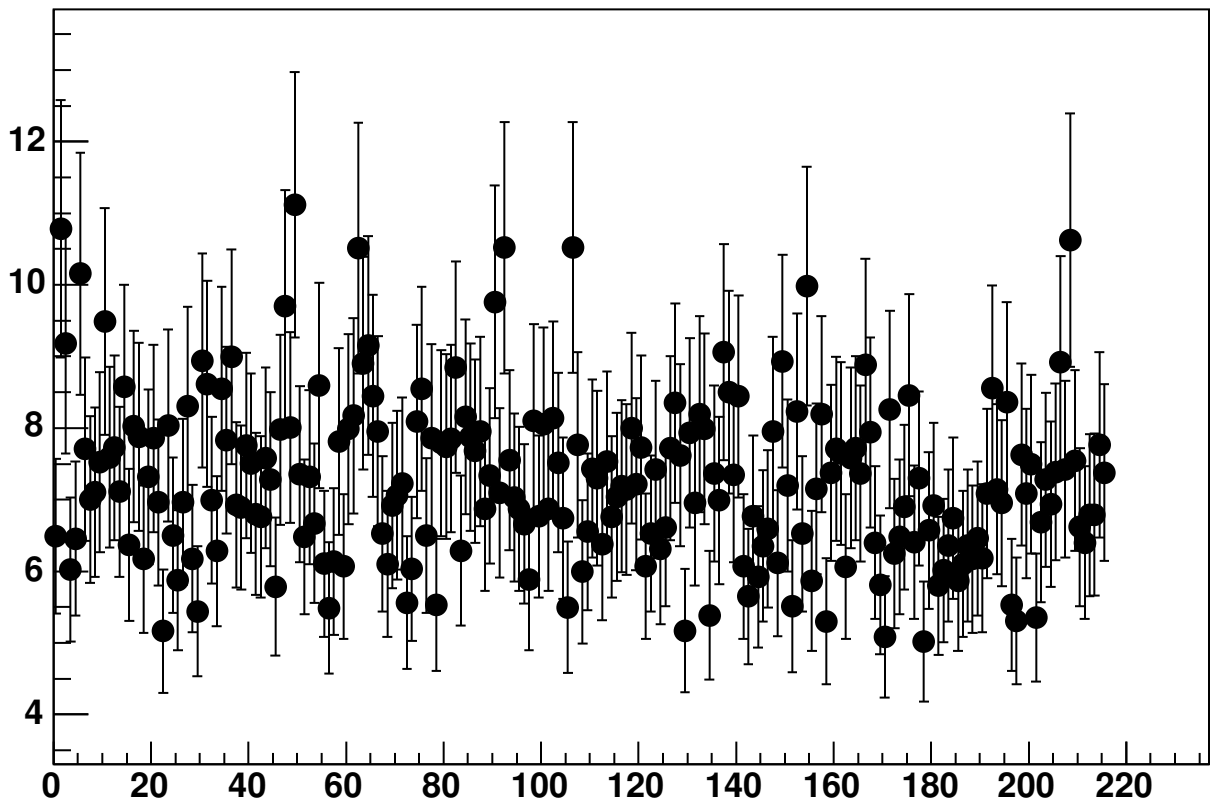
Enable 3, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



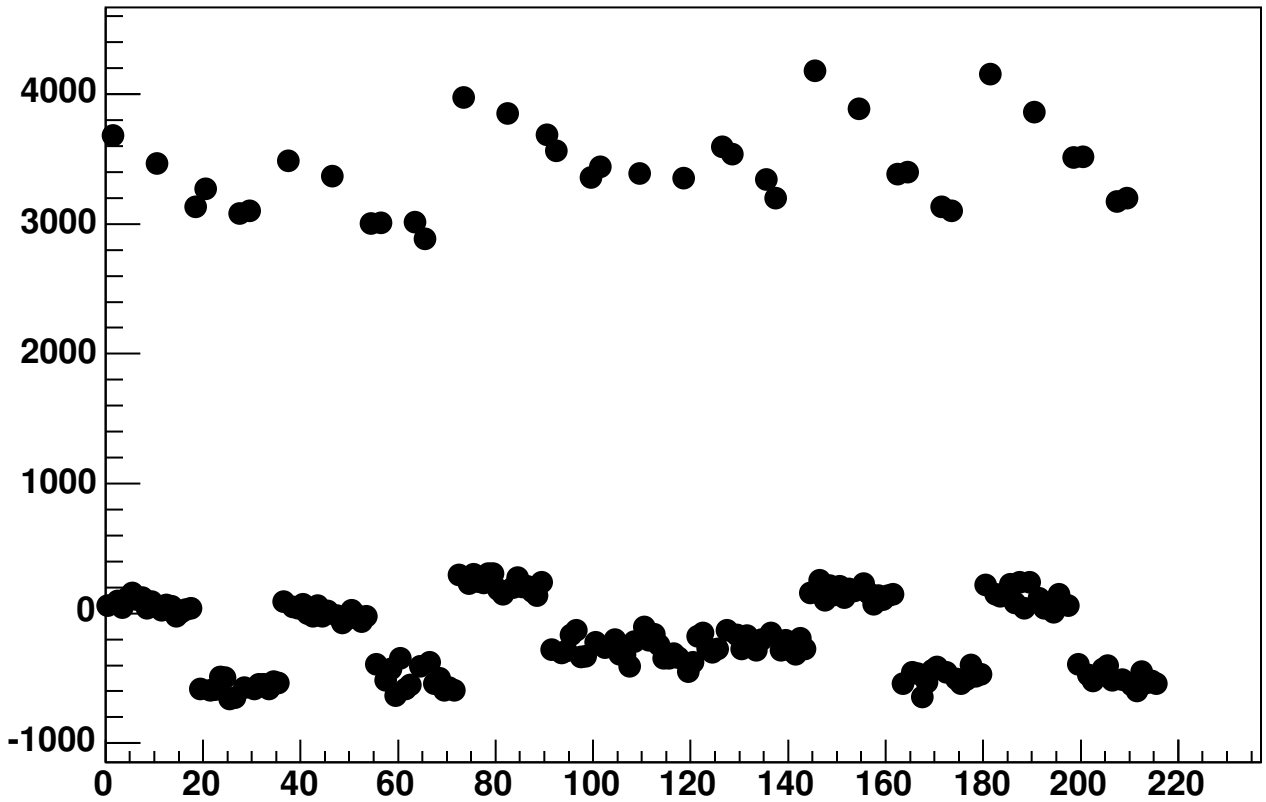
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



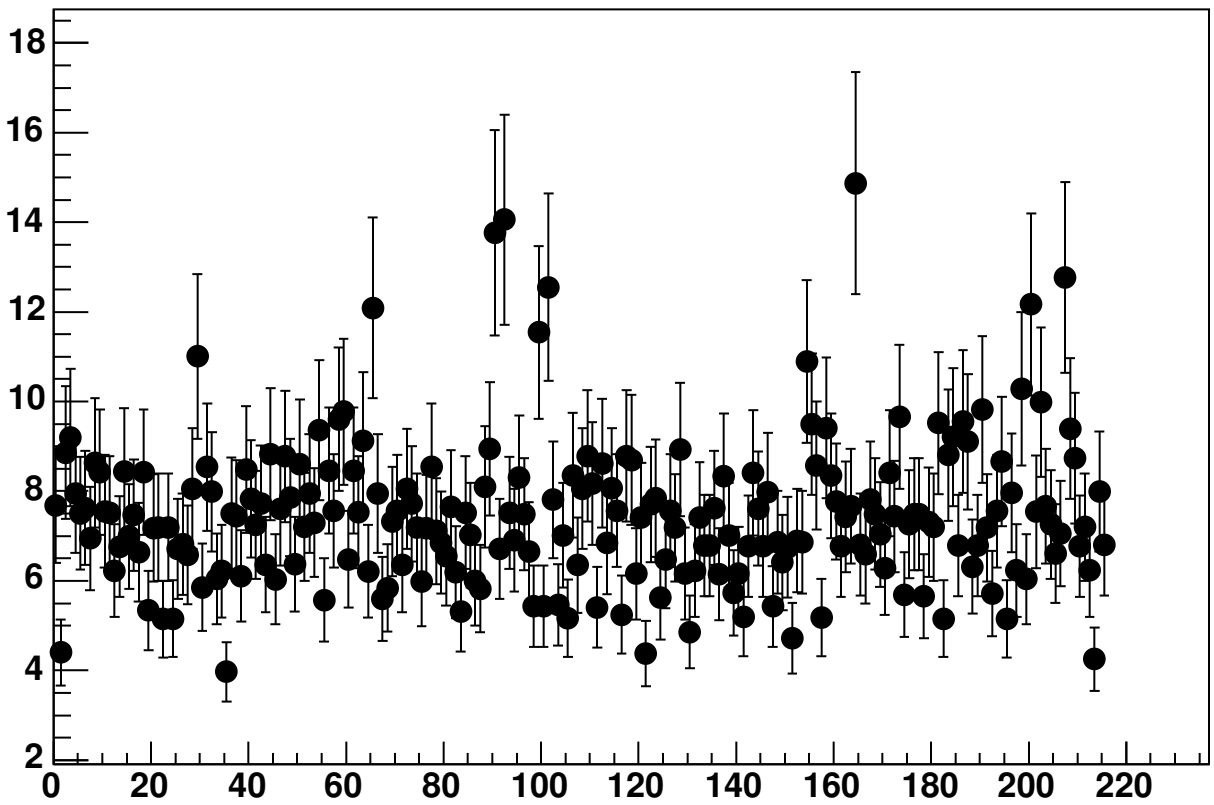
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



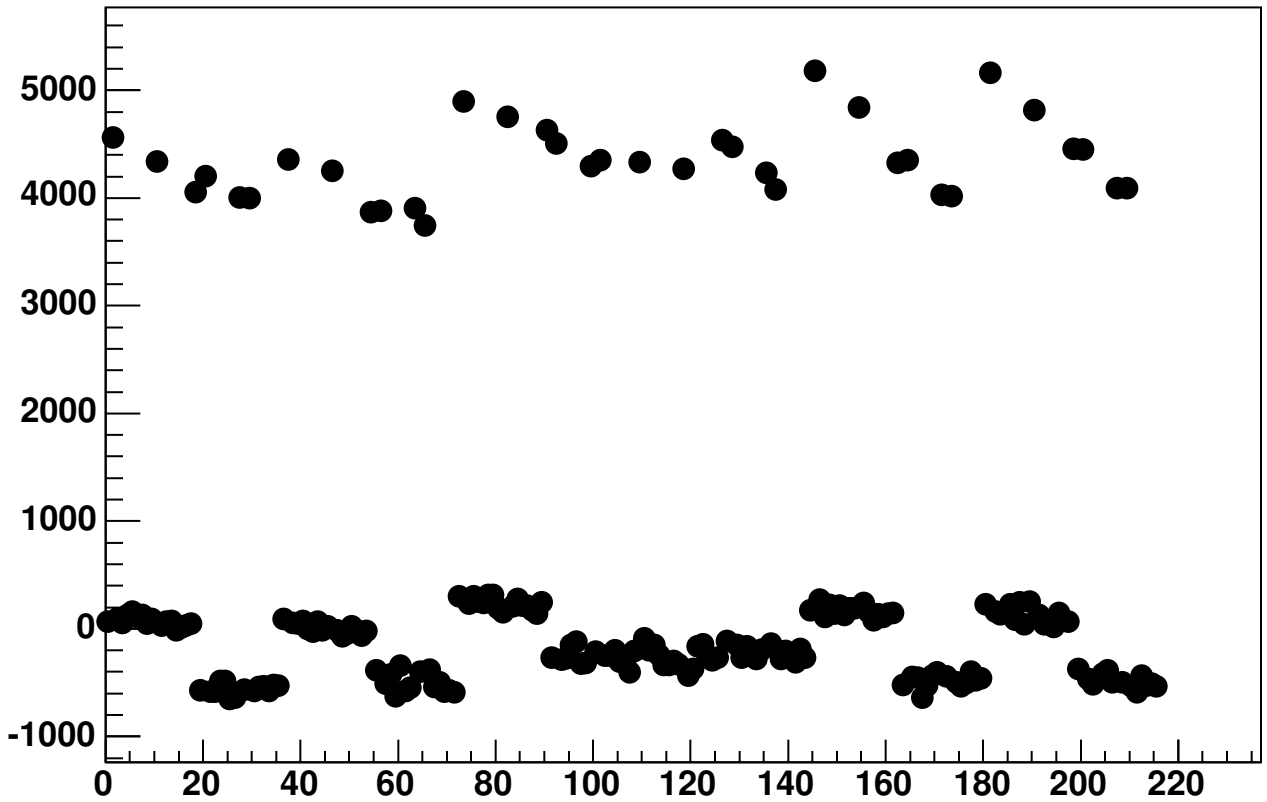
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



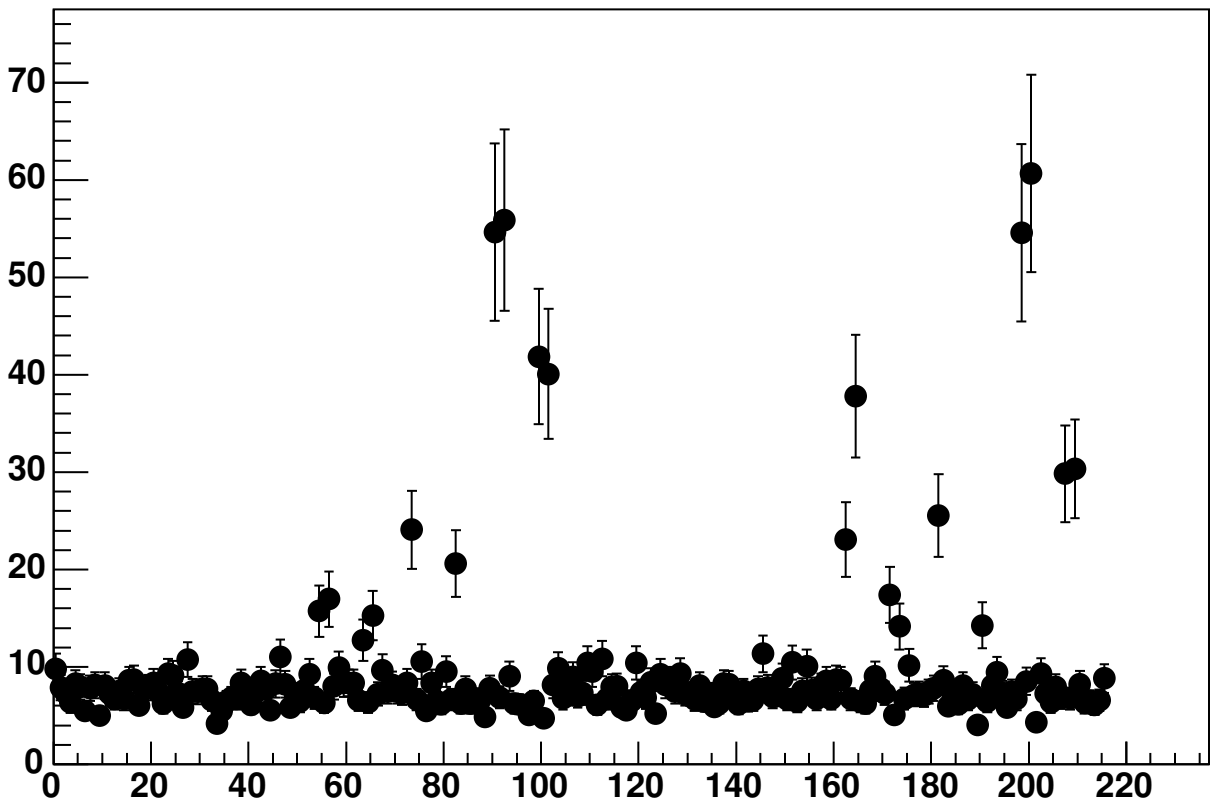
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



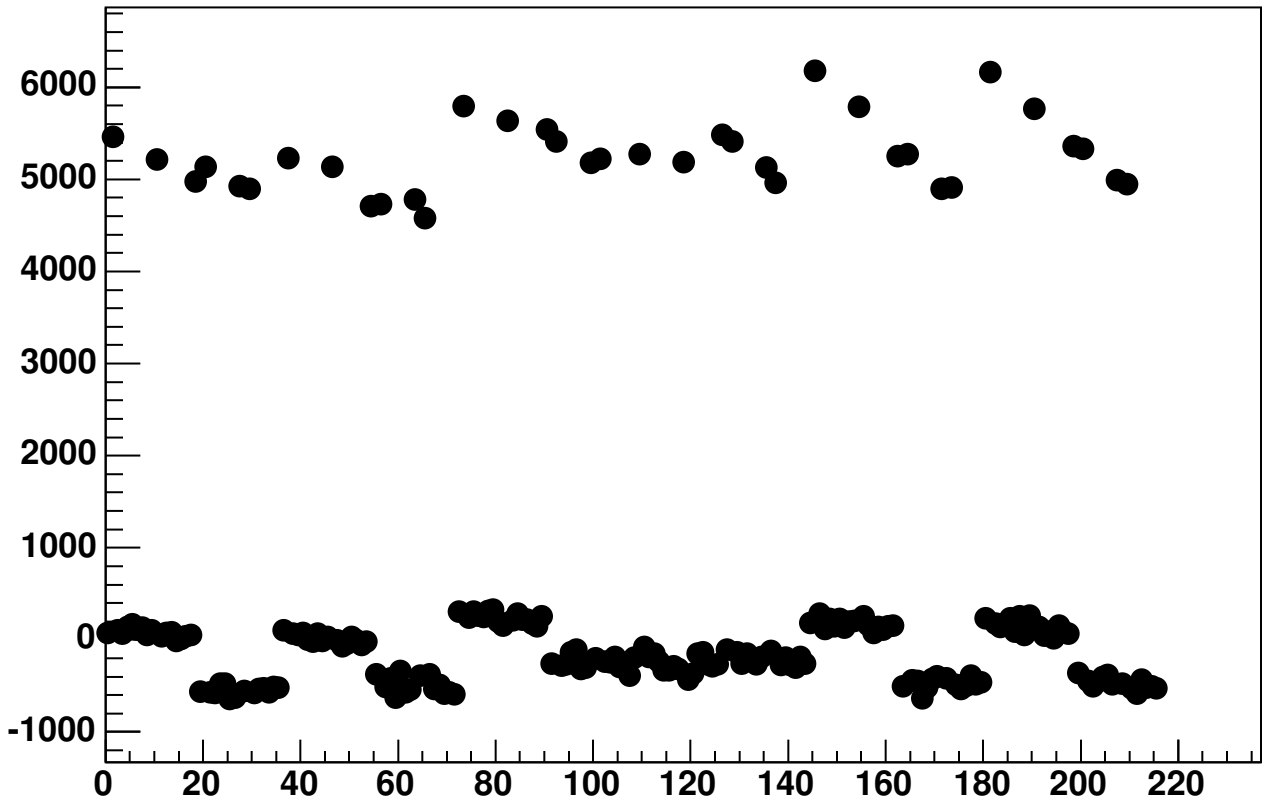
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



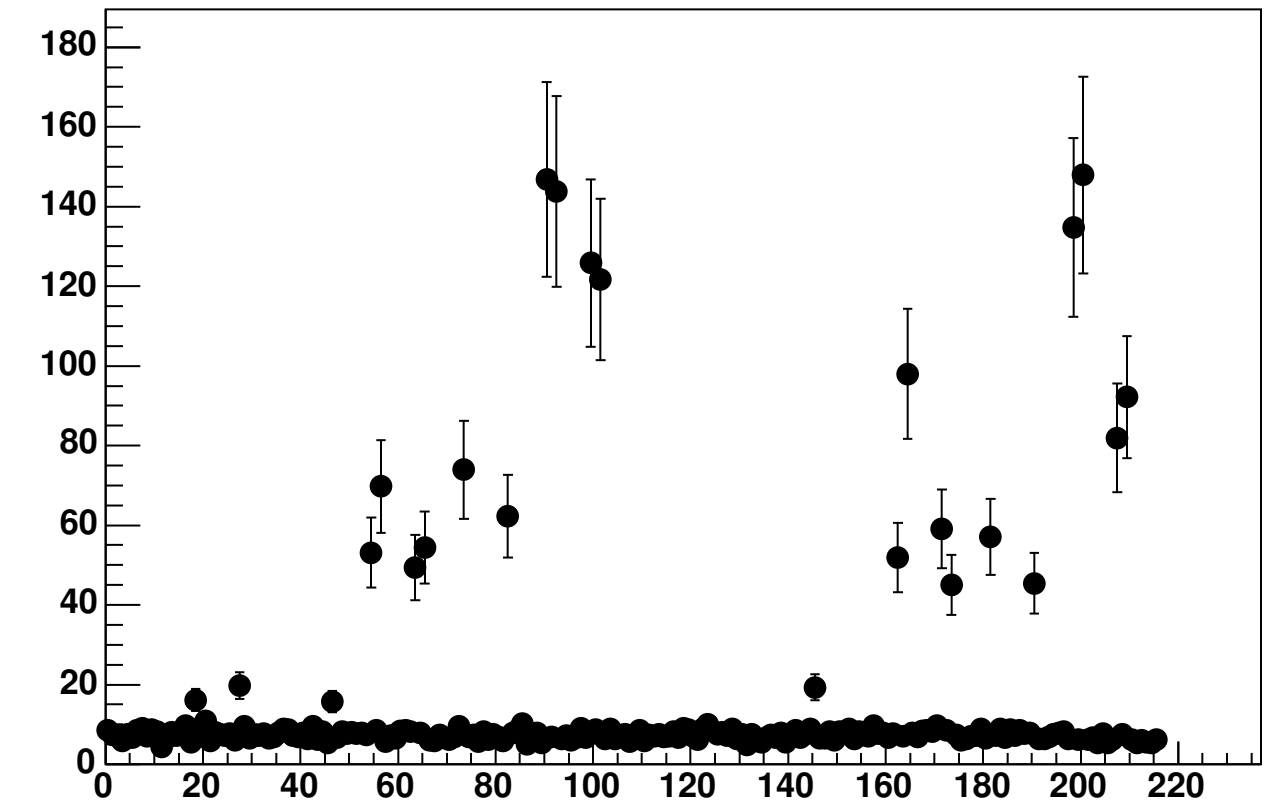
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



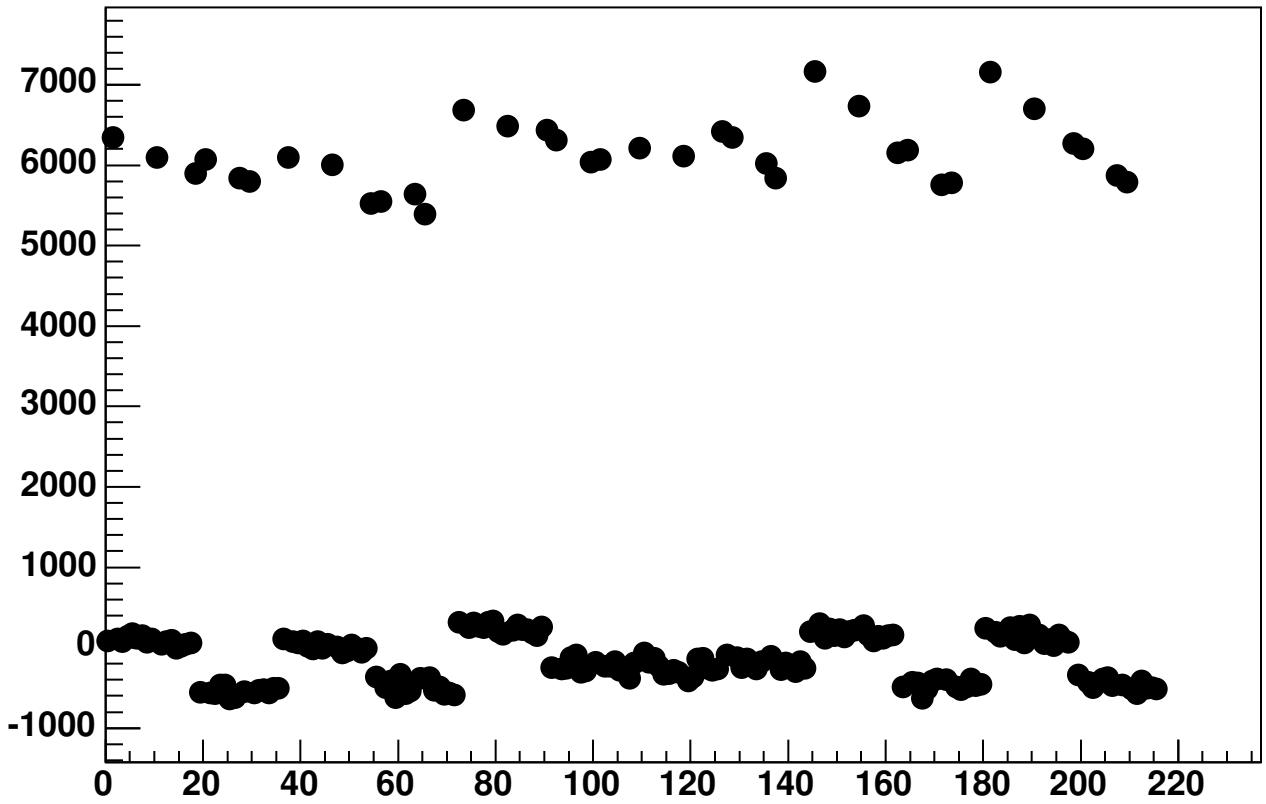
Enable 3, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



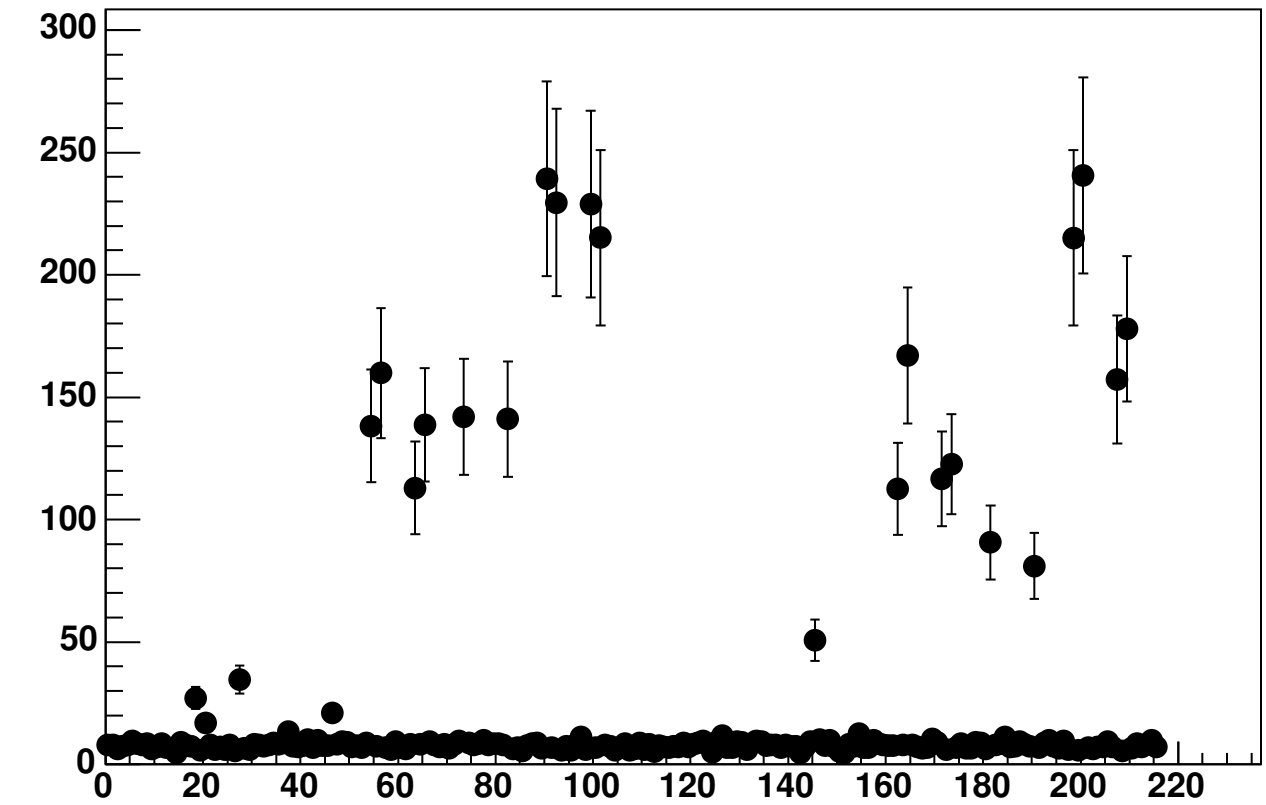
Enable 3, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



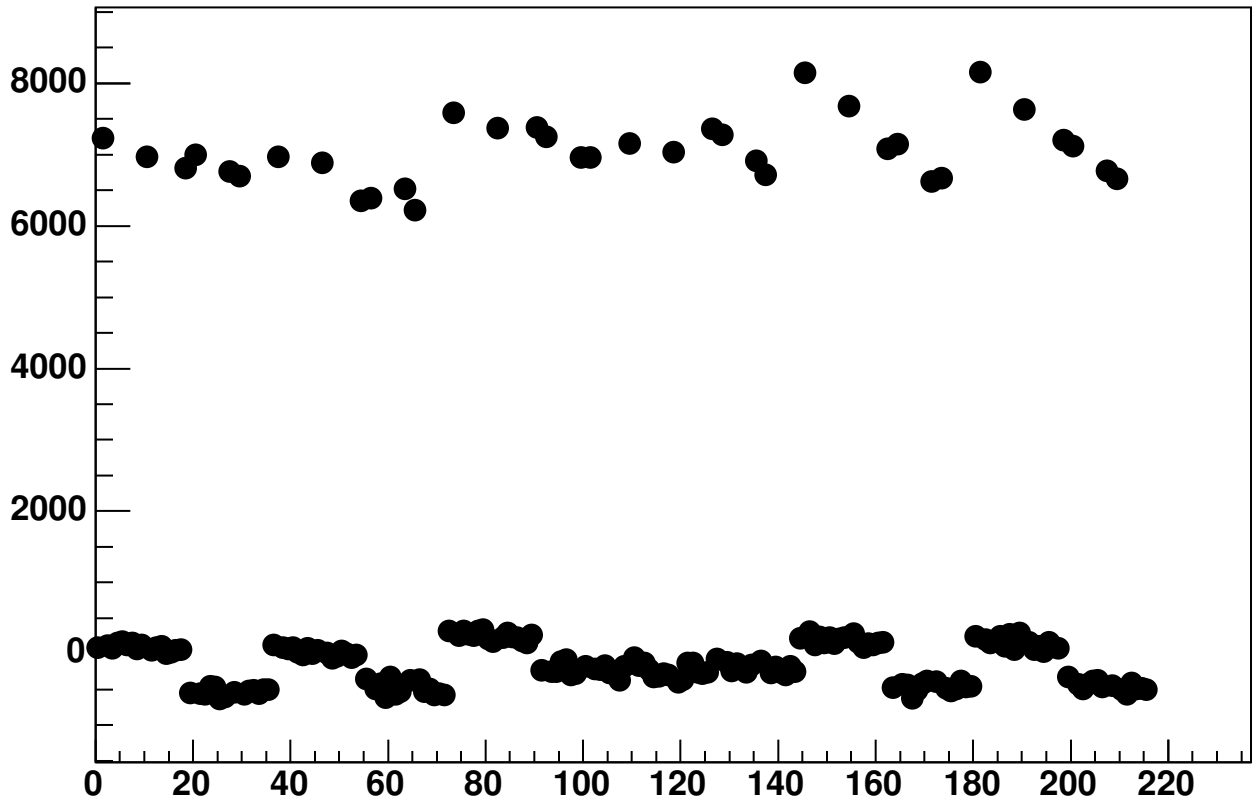
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



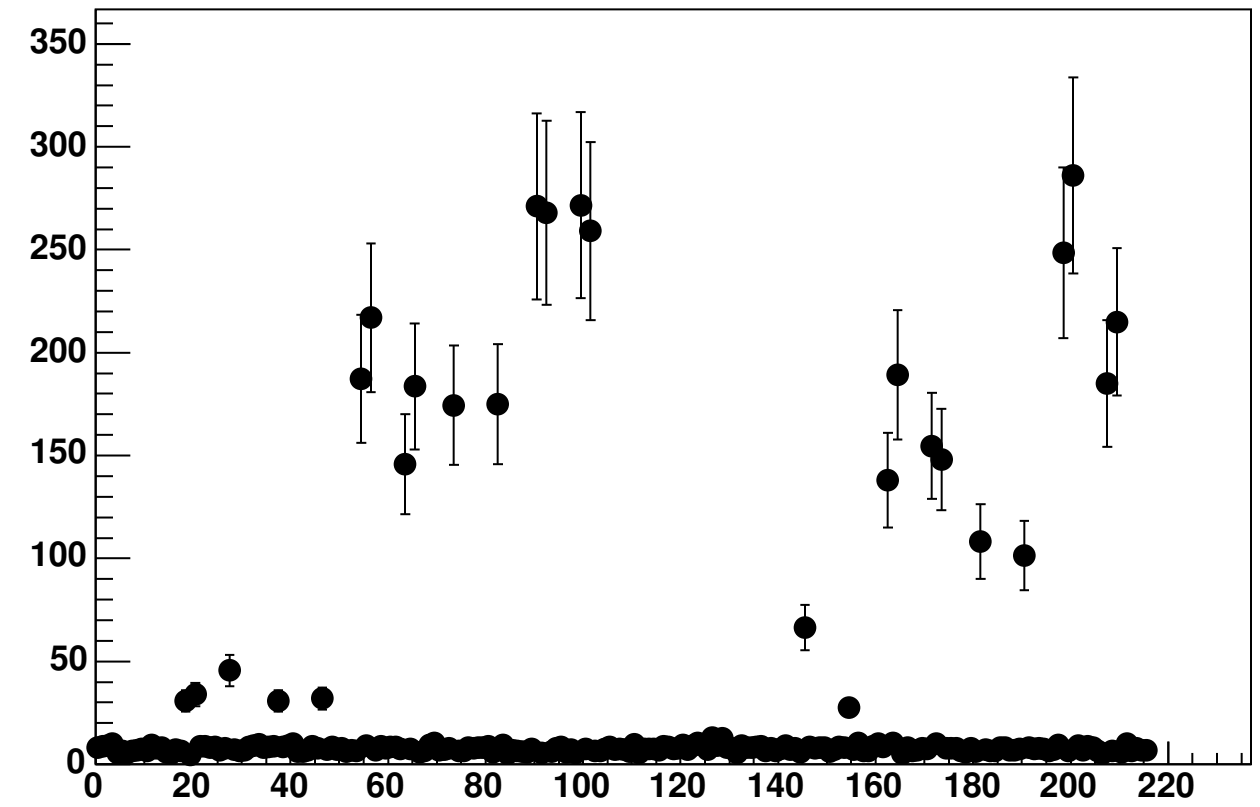
Enable 3, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



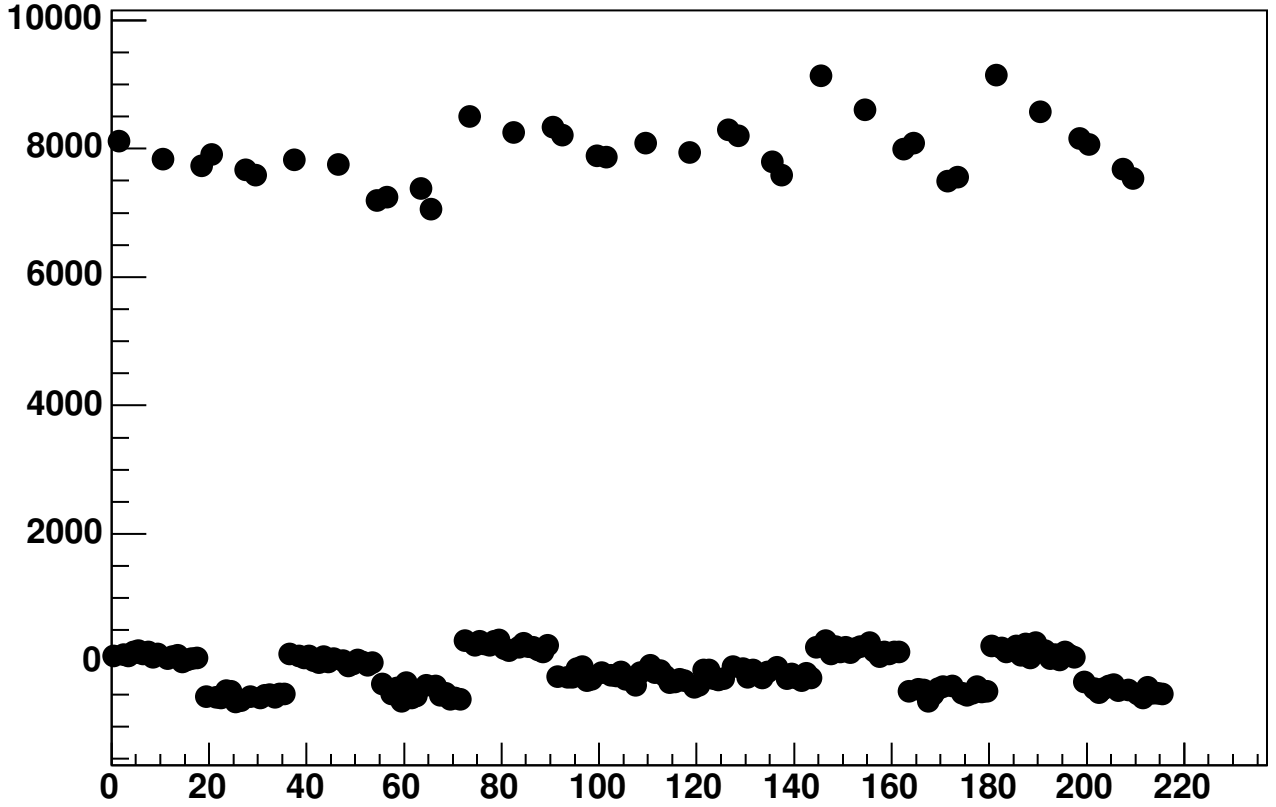
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



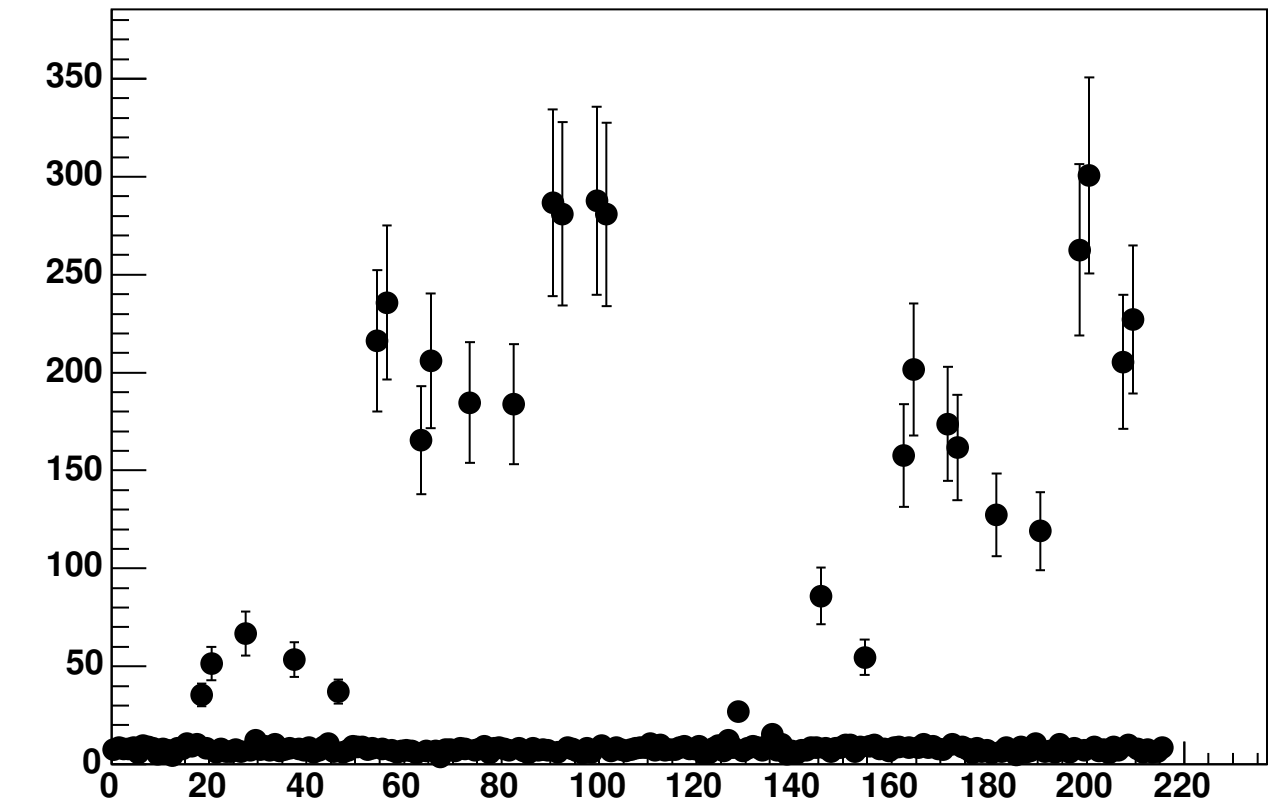
Enable 3, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



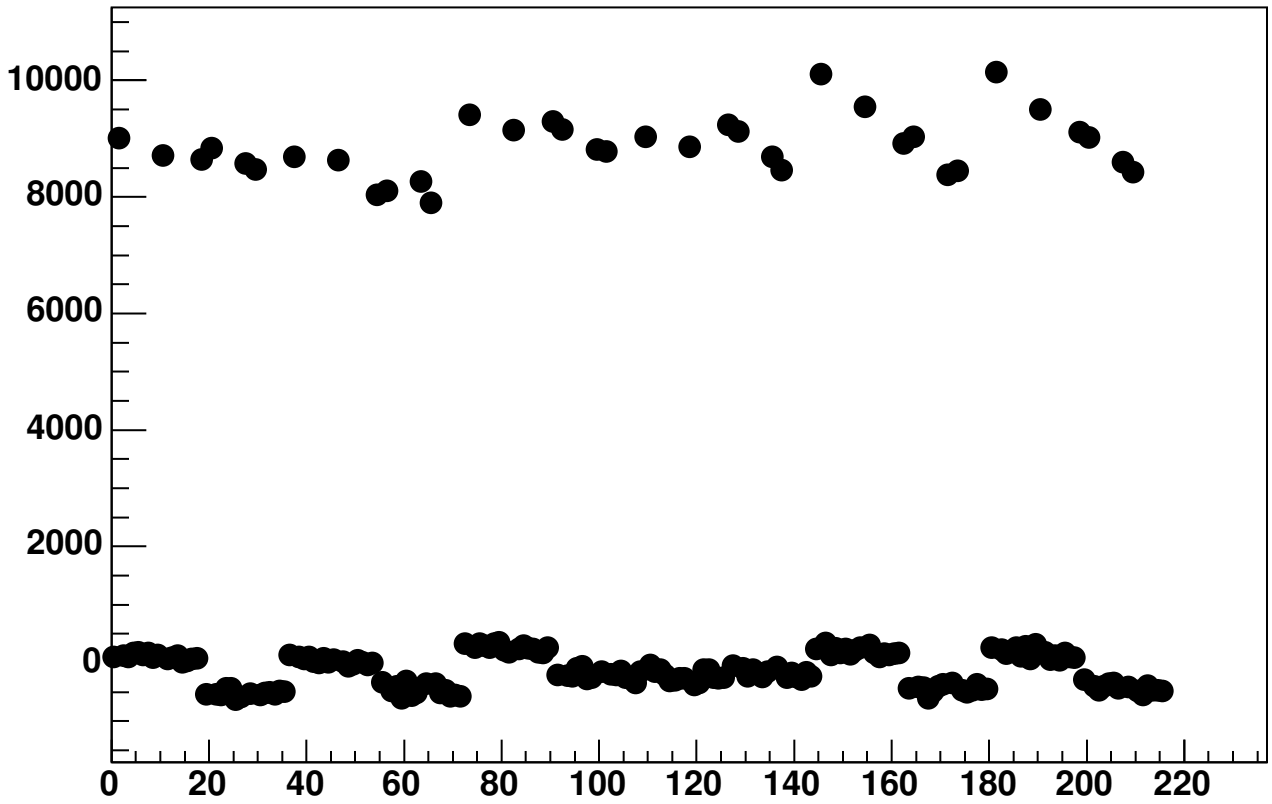
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



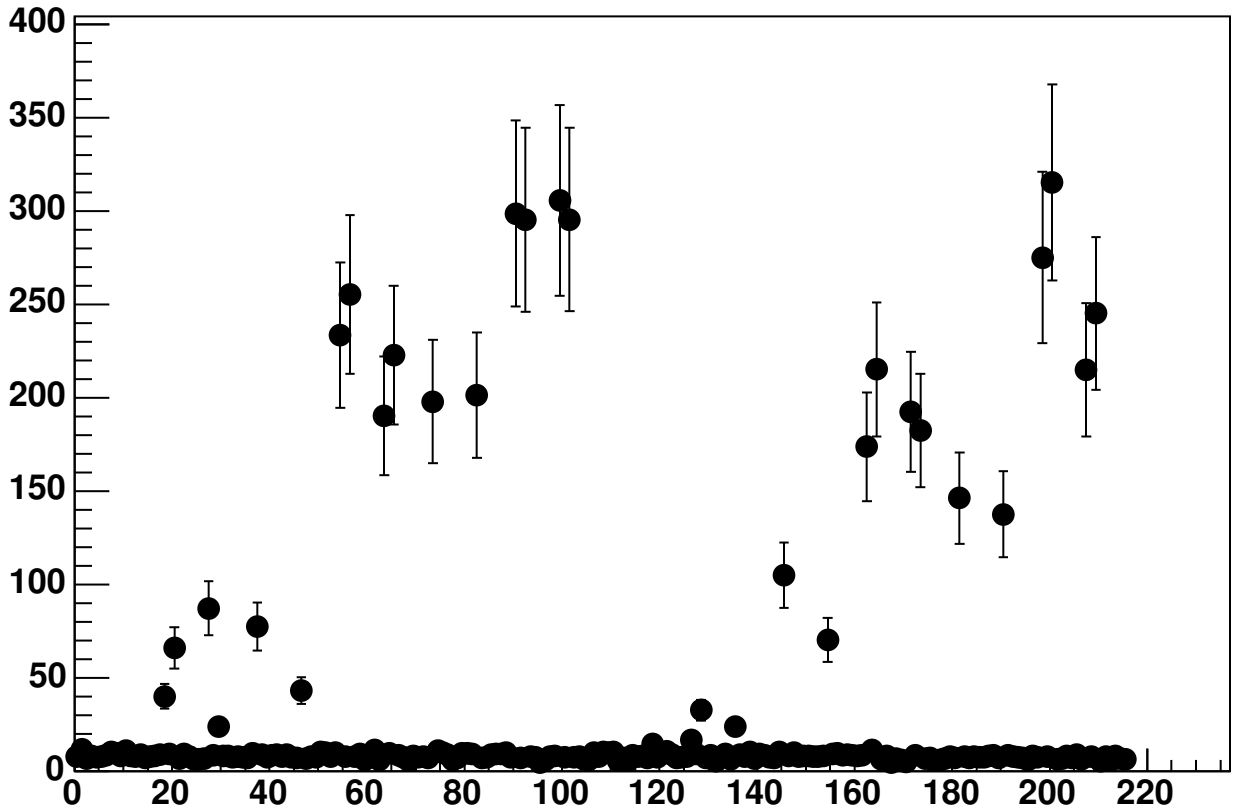
Enable 3, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



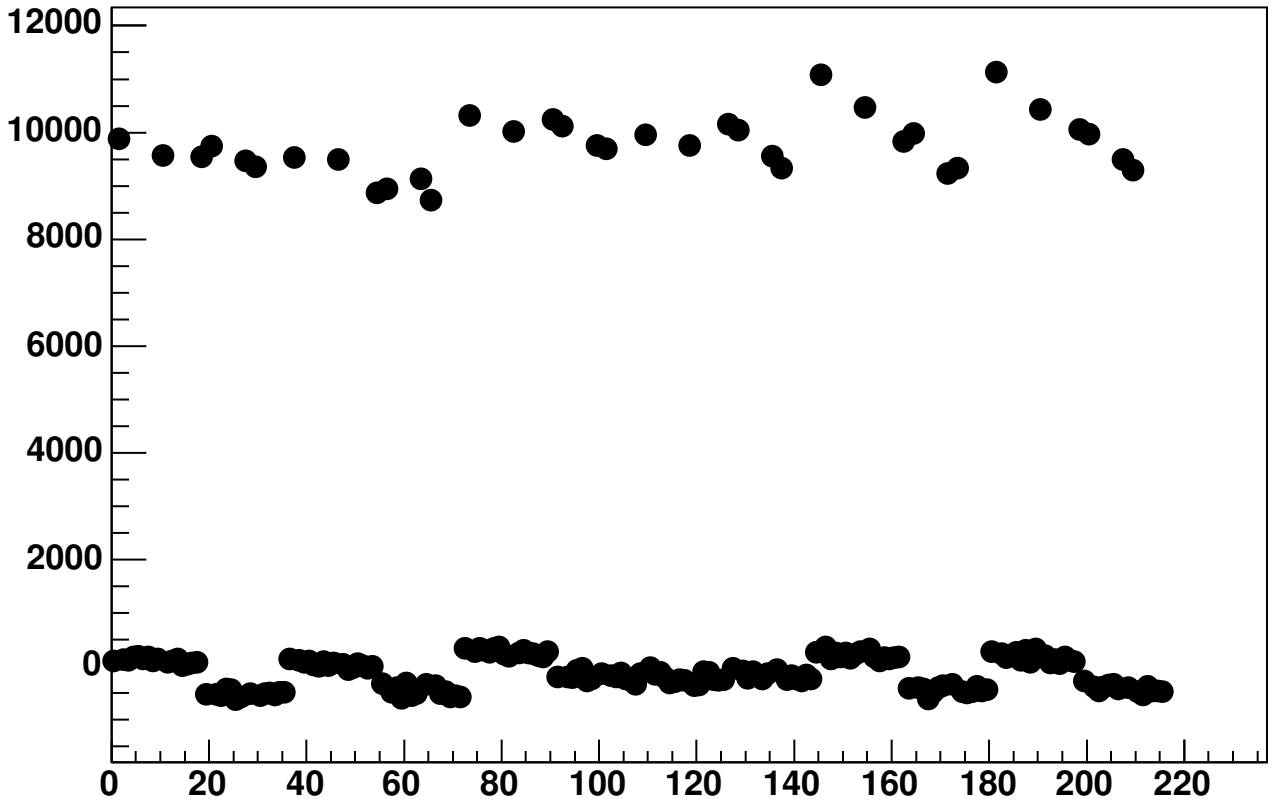
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



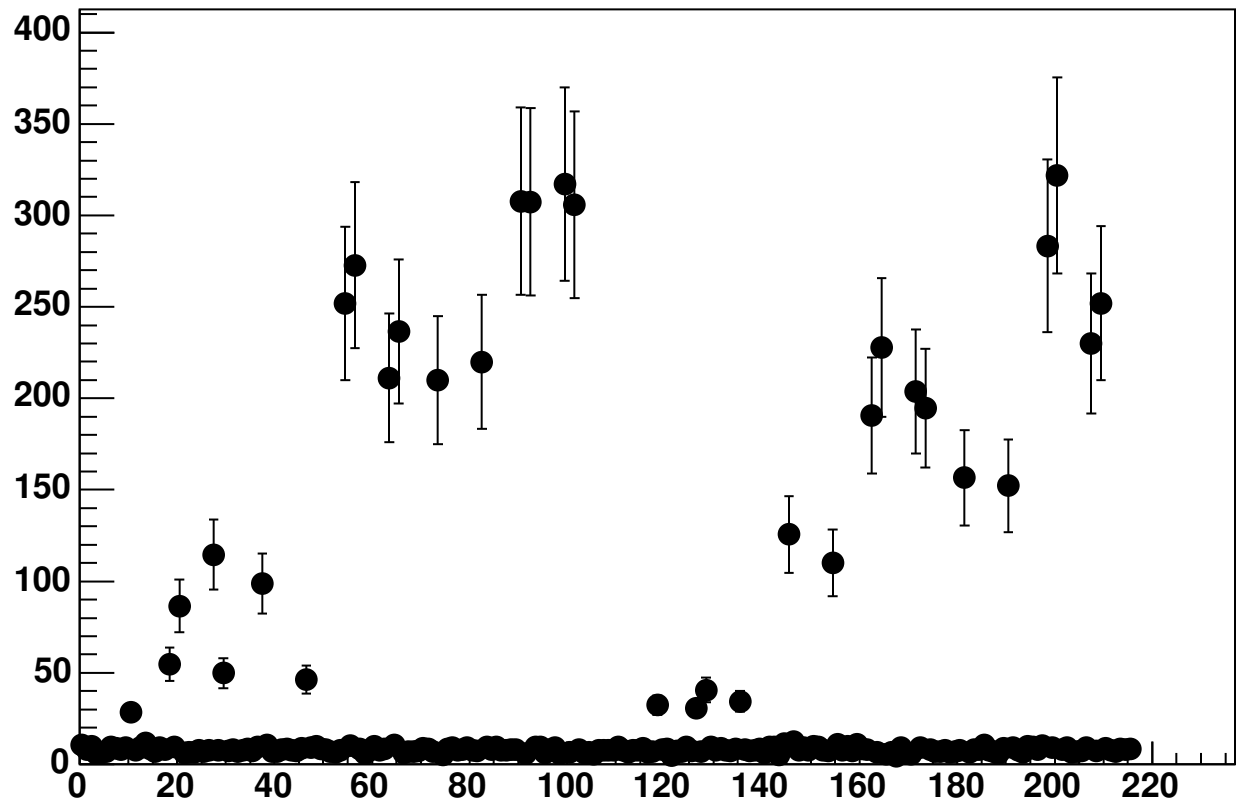
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



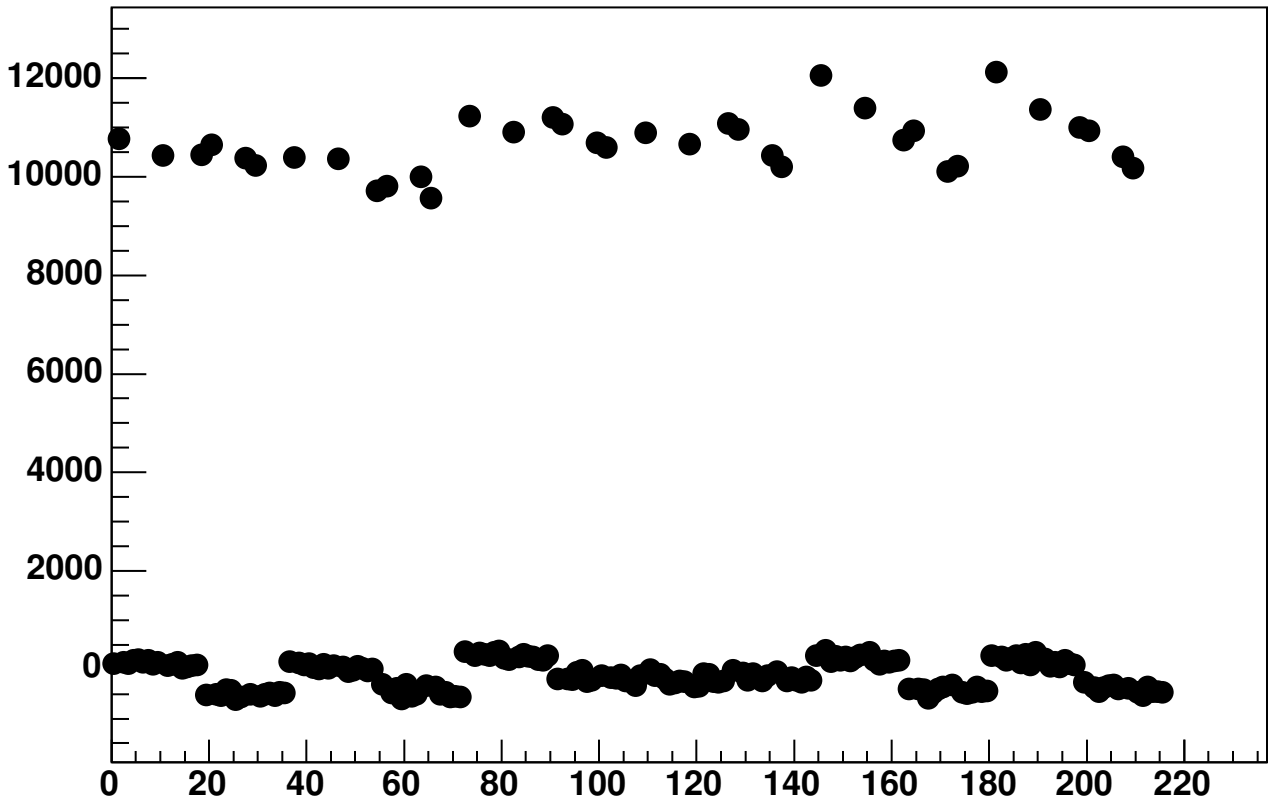
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



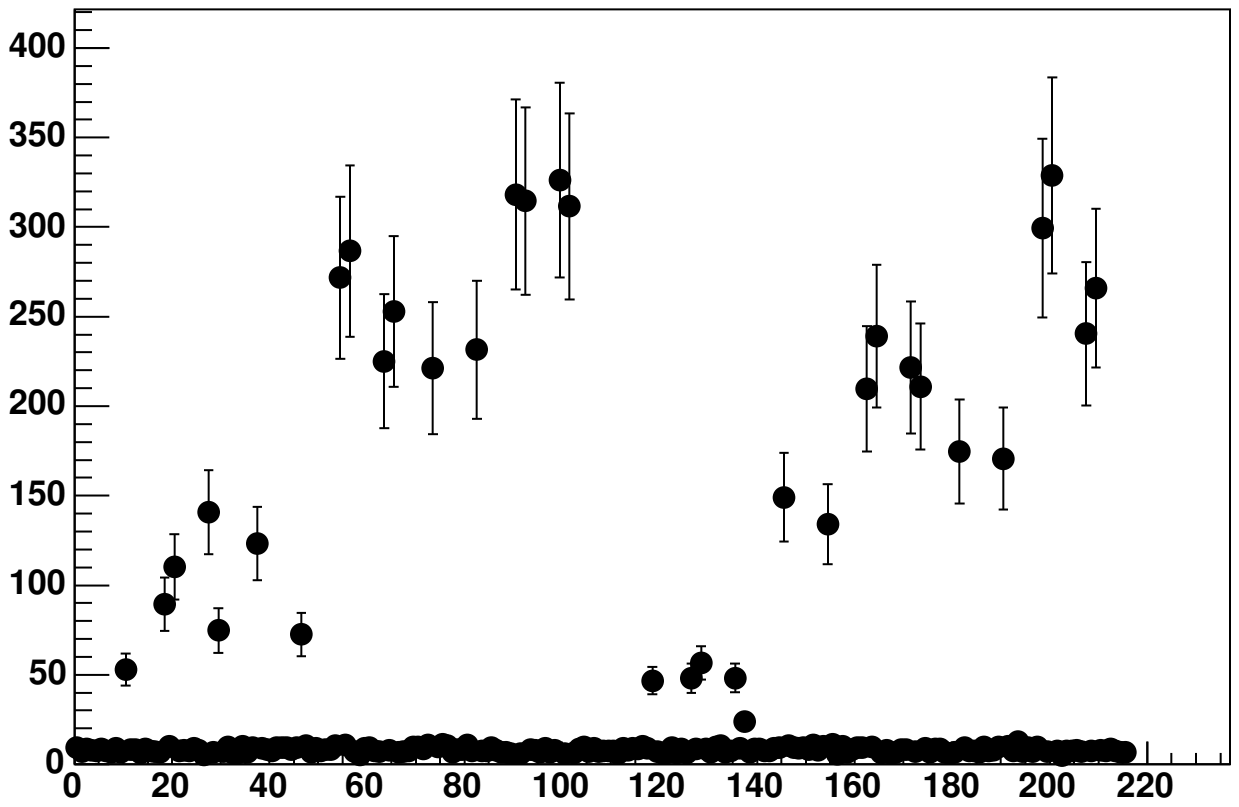
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



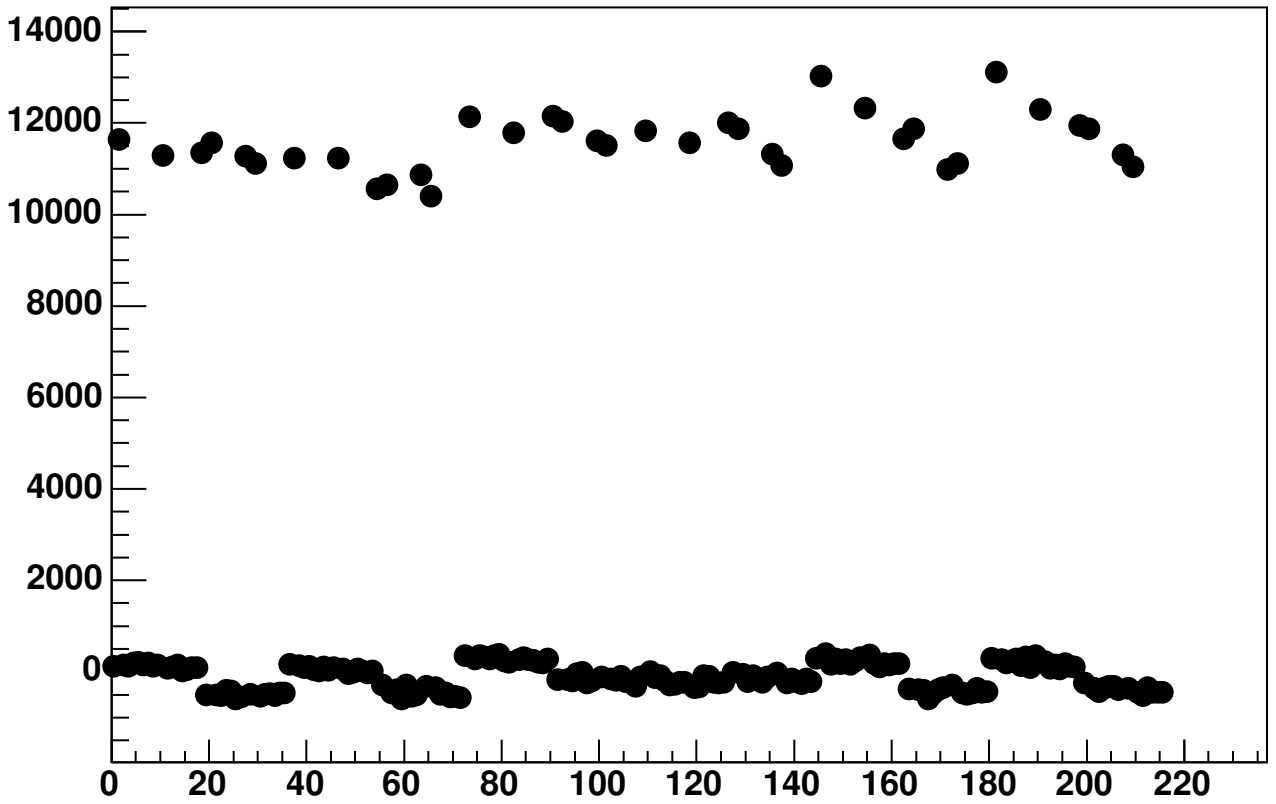
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



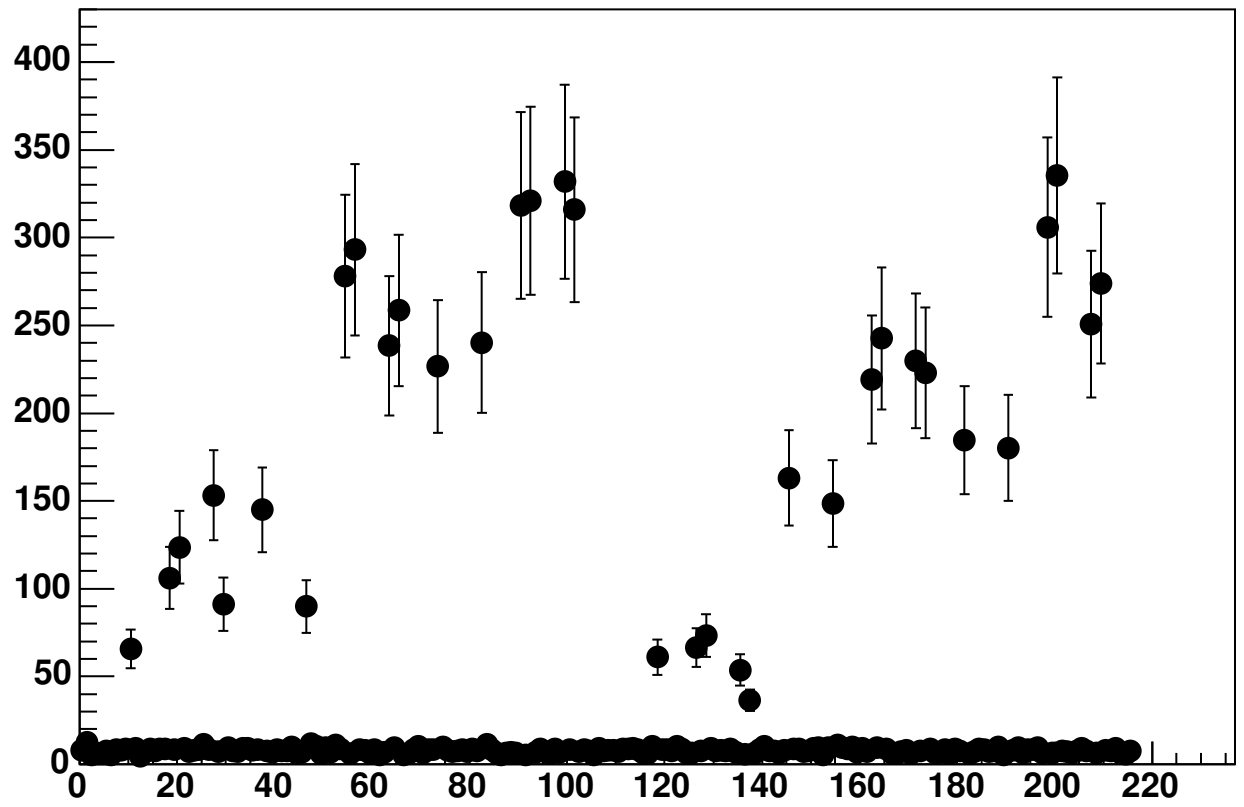
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



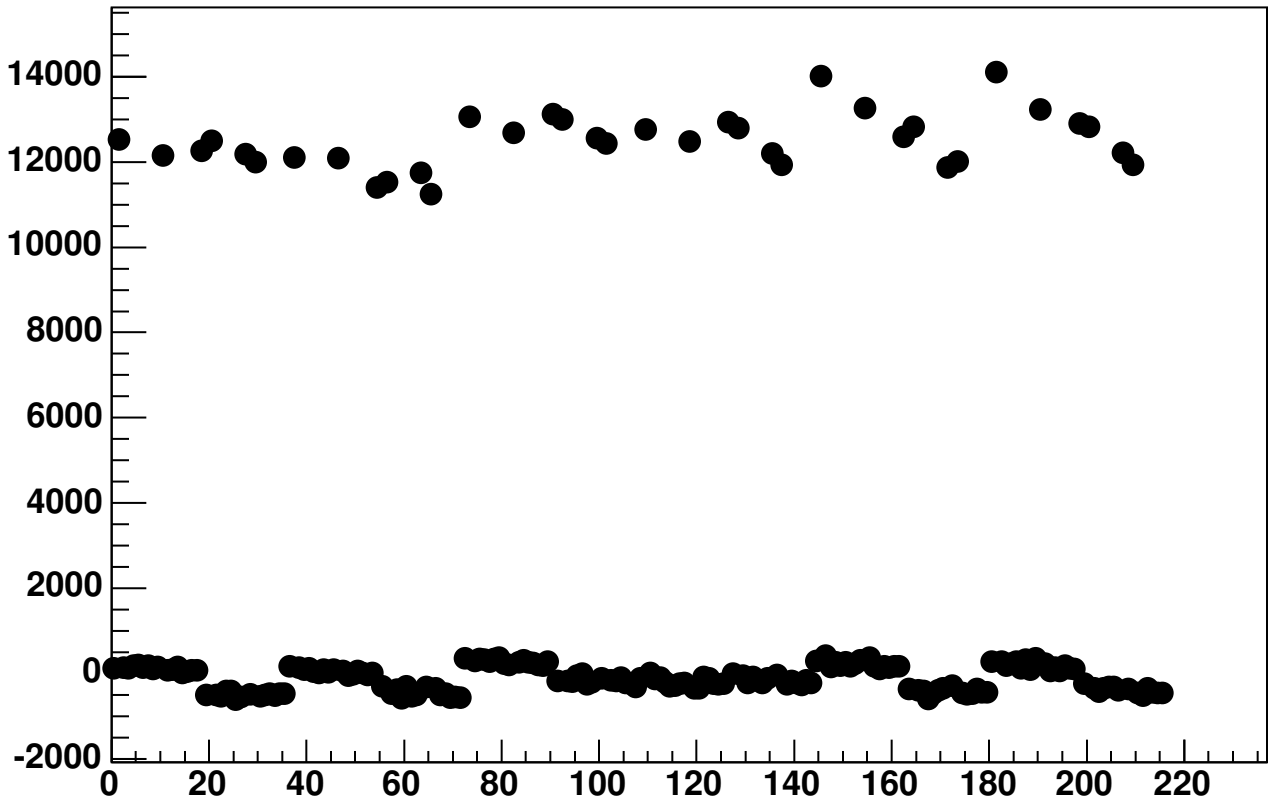
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



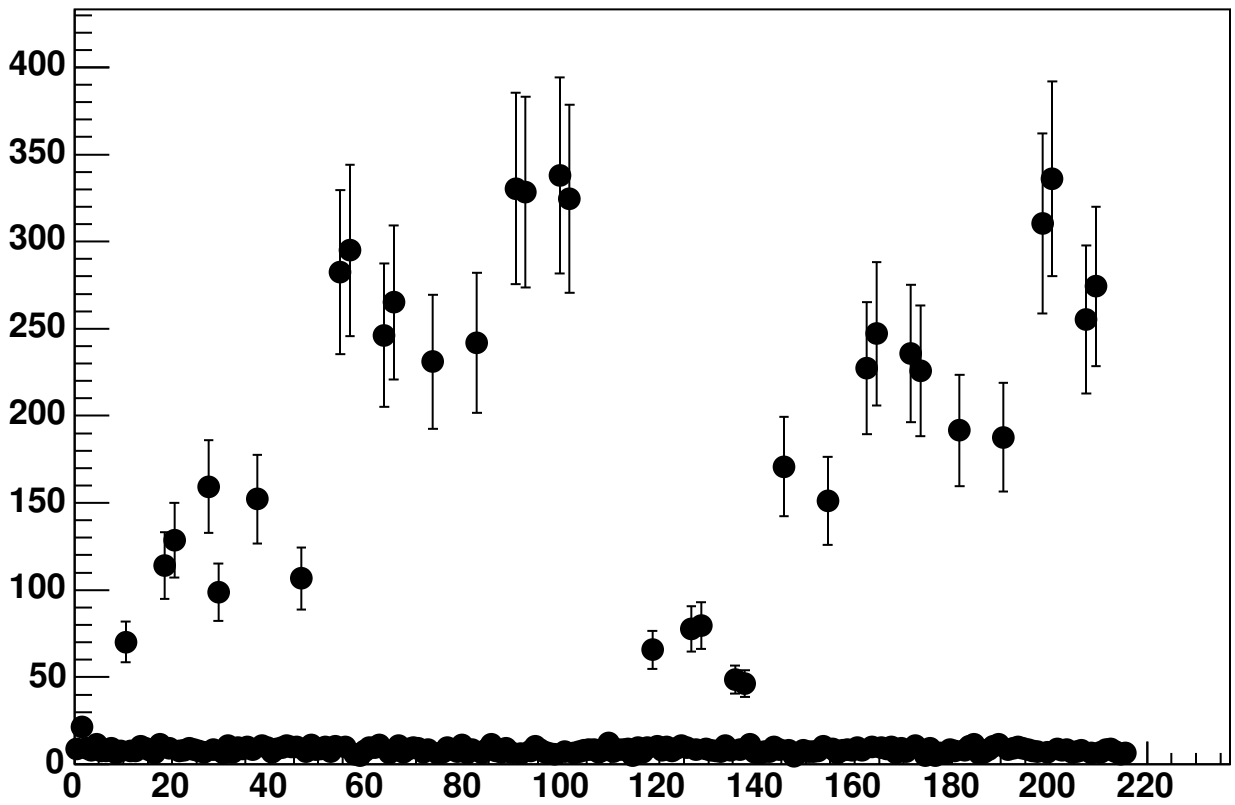
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



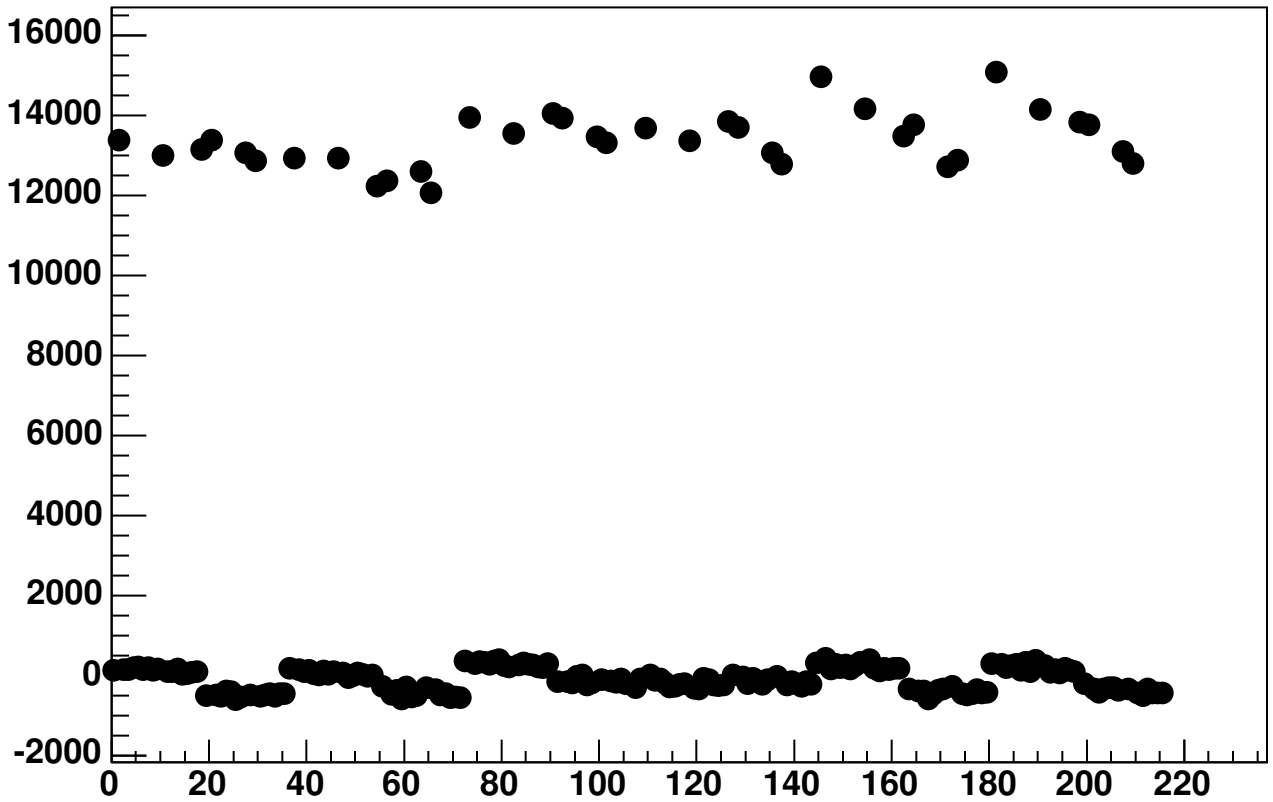
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



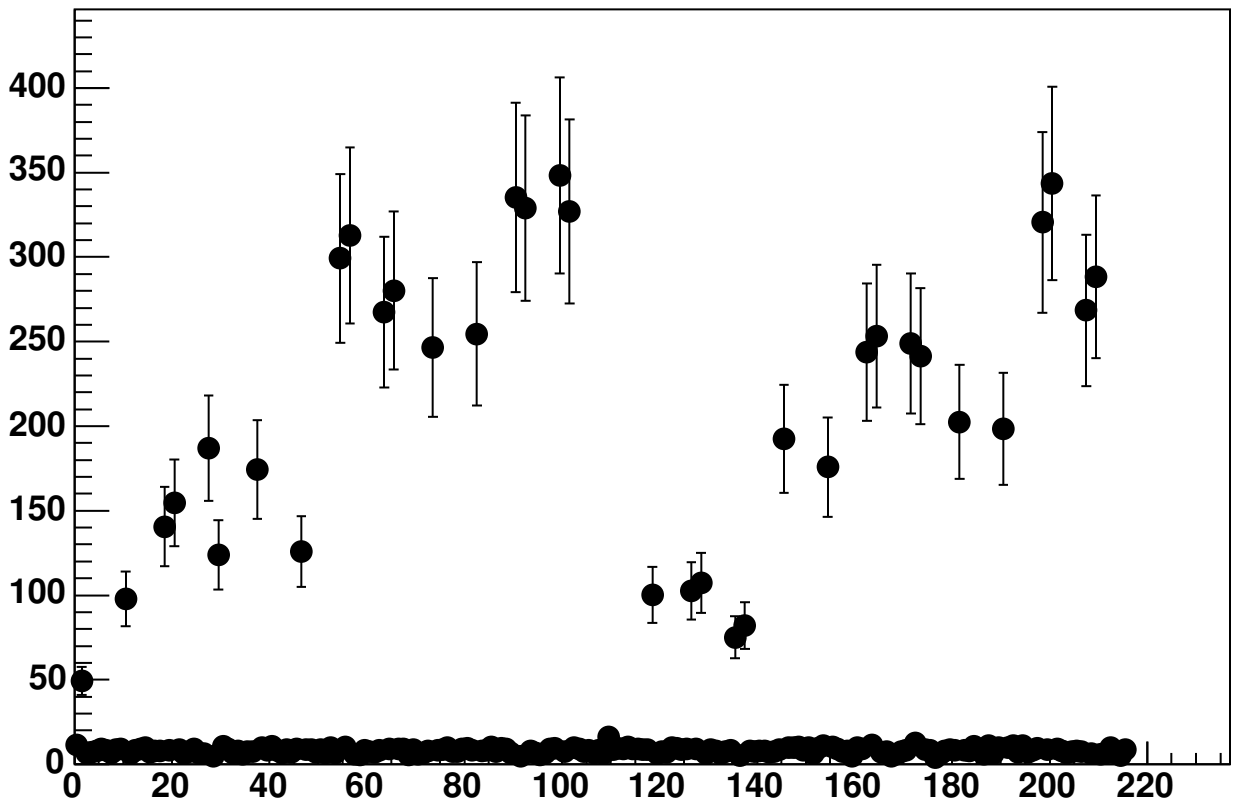
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



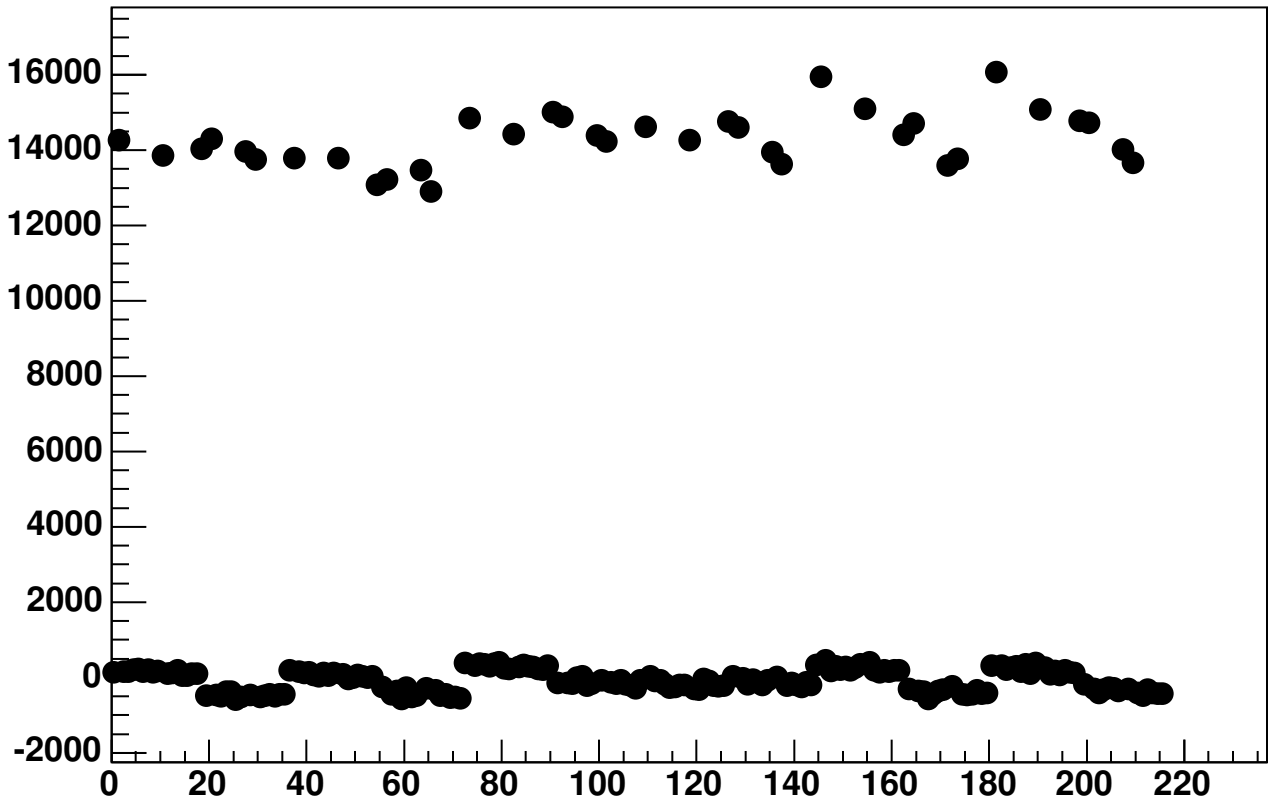
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



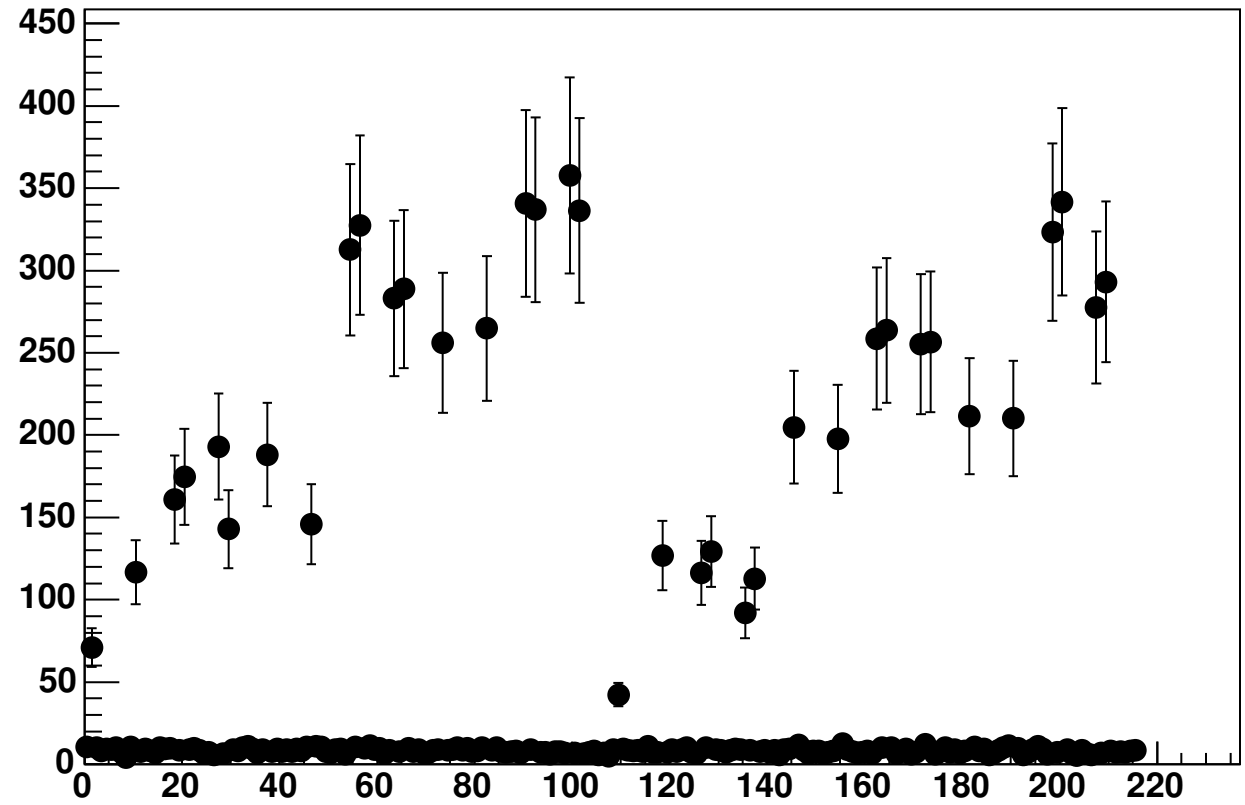
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



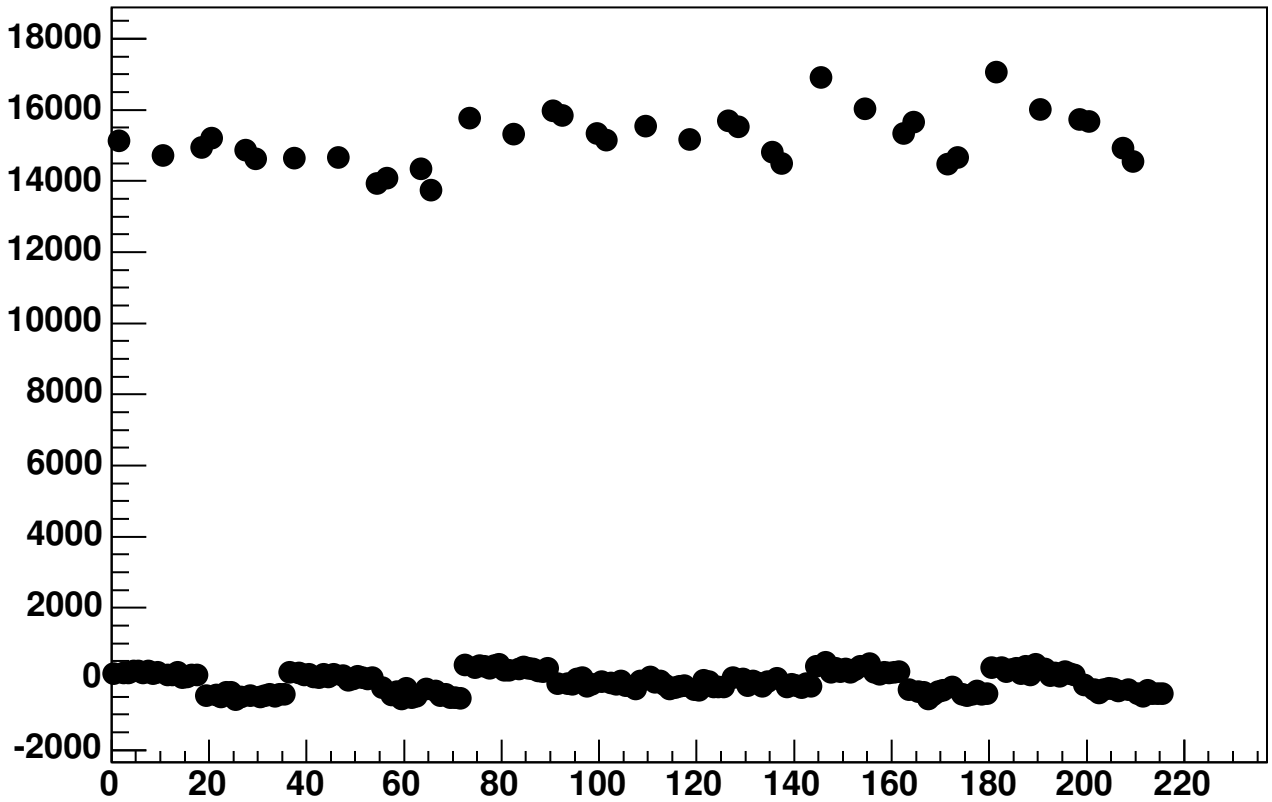
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



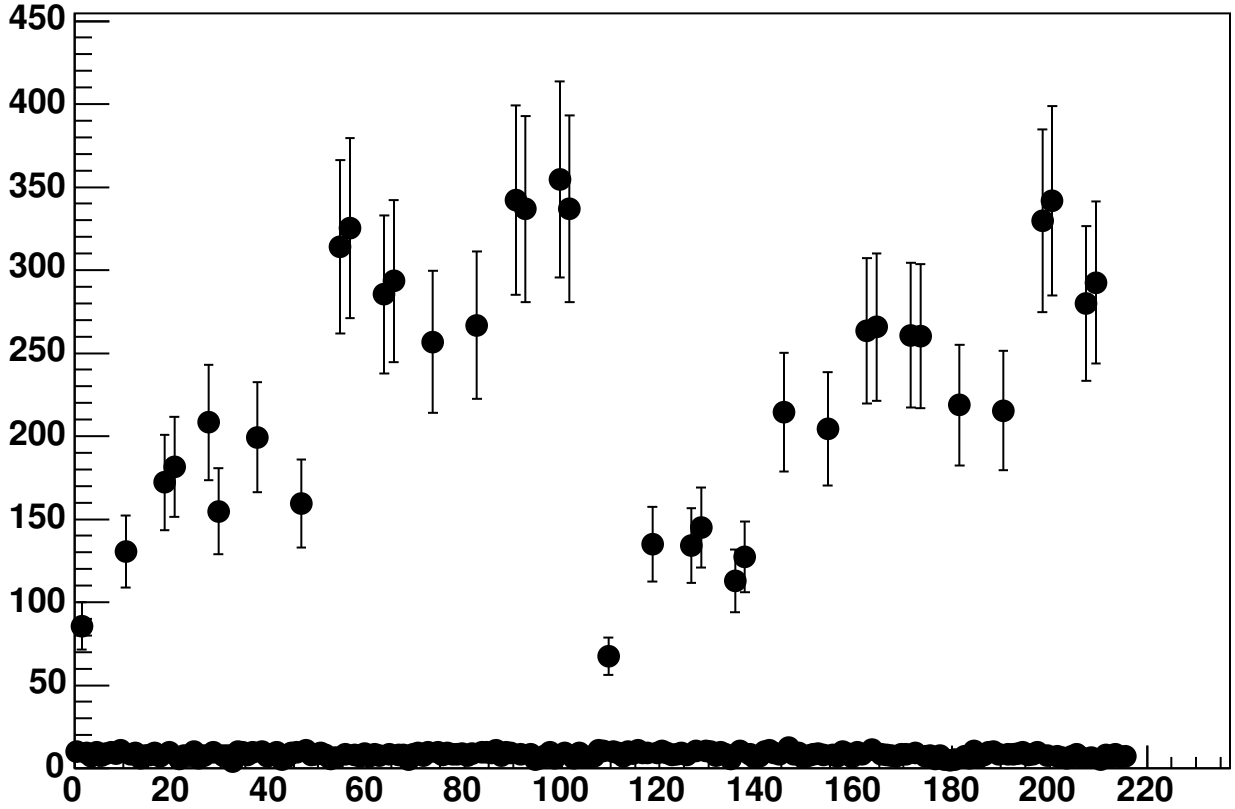
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



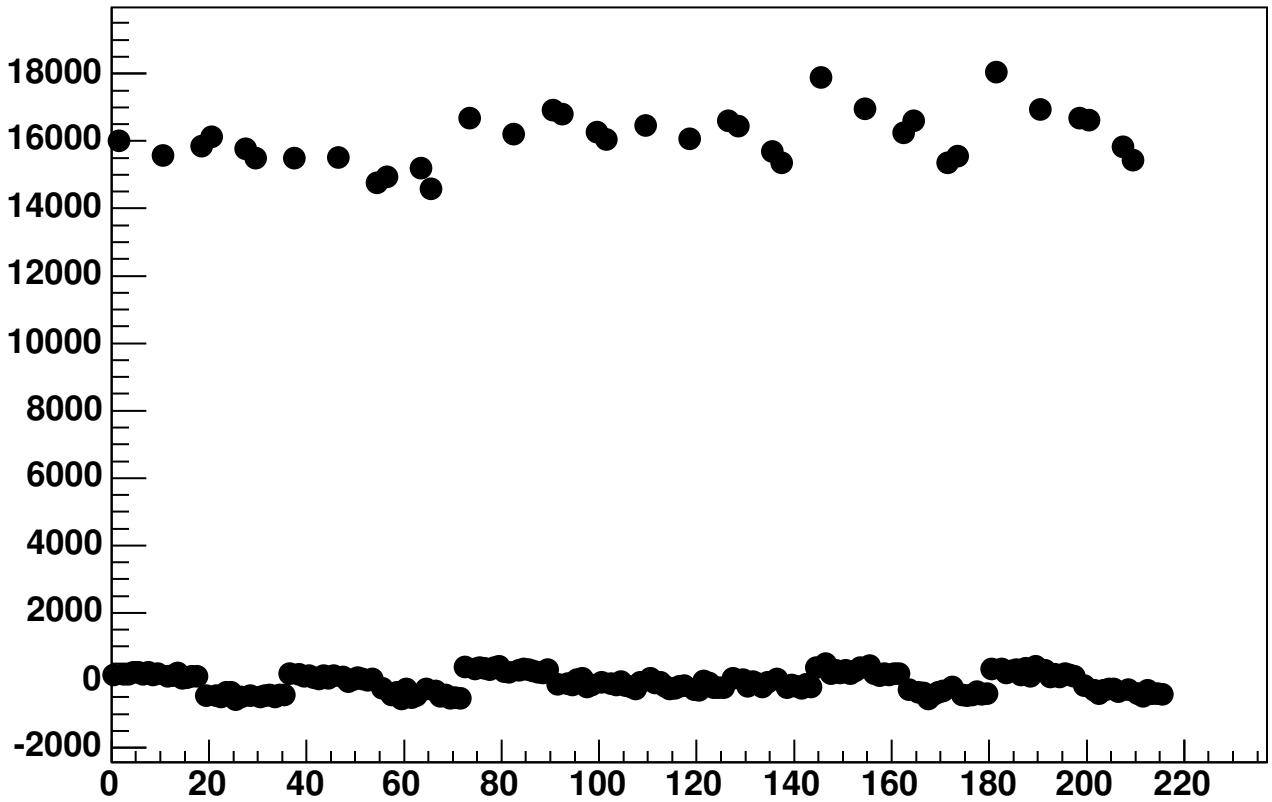
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



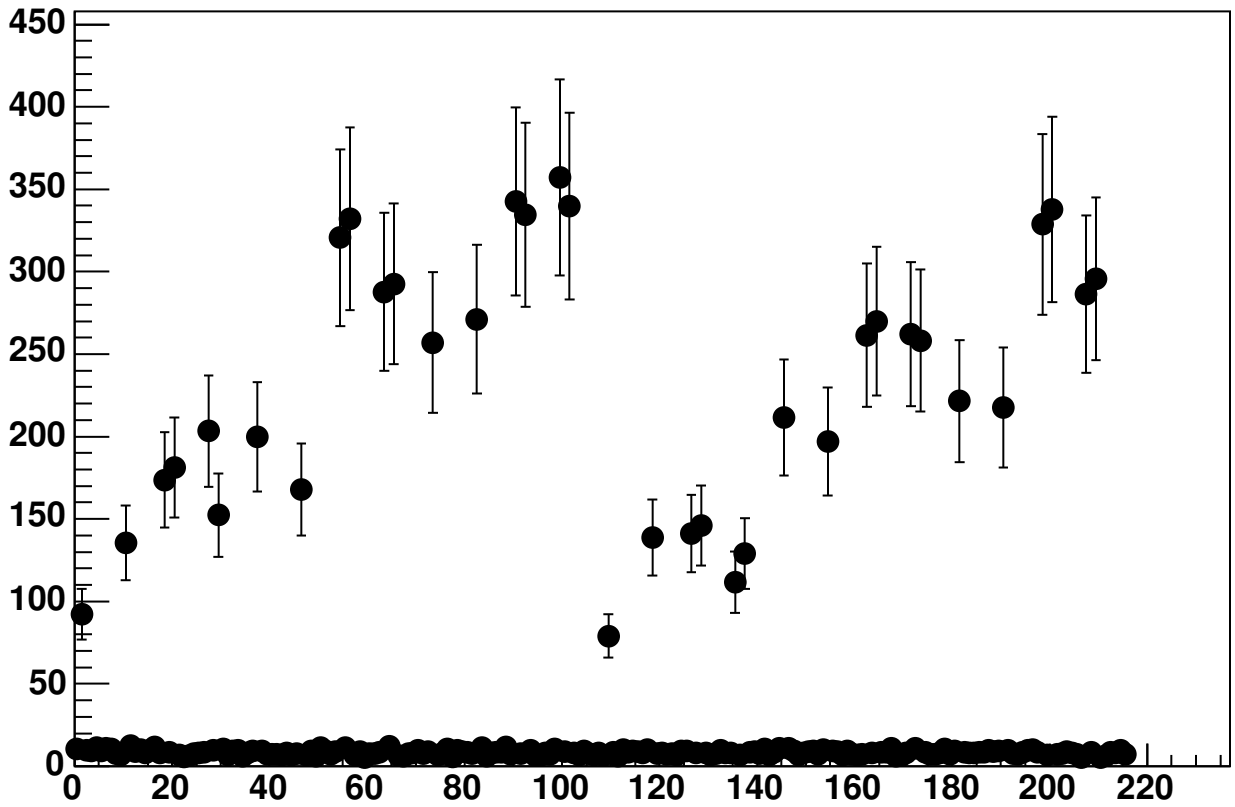
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



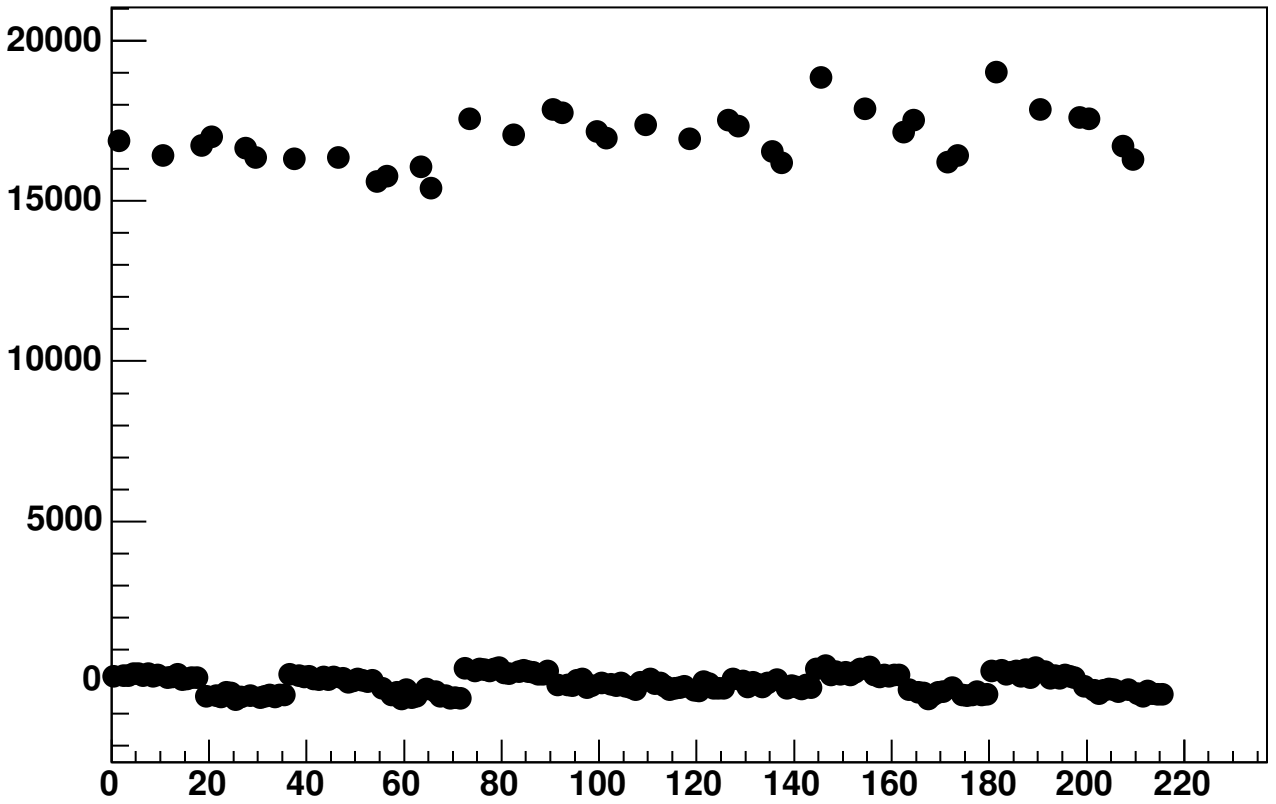
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



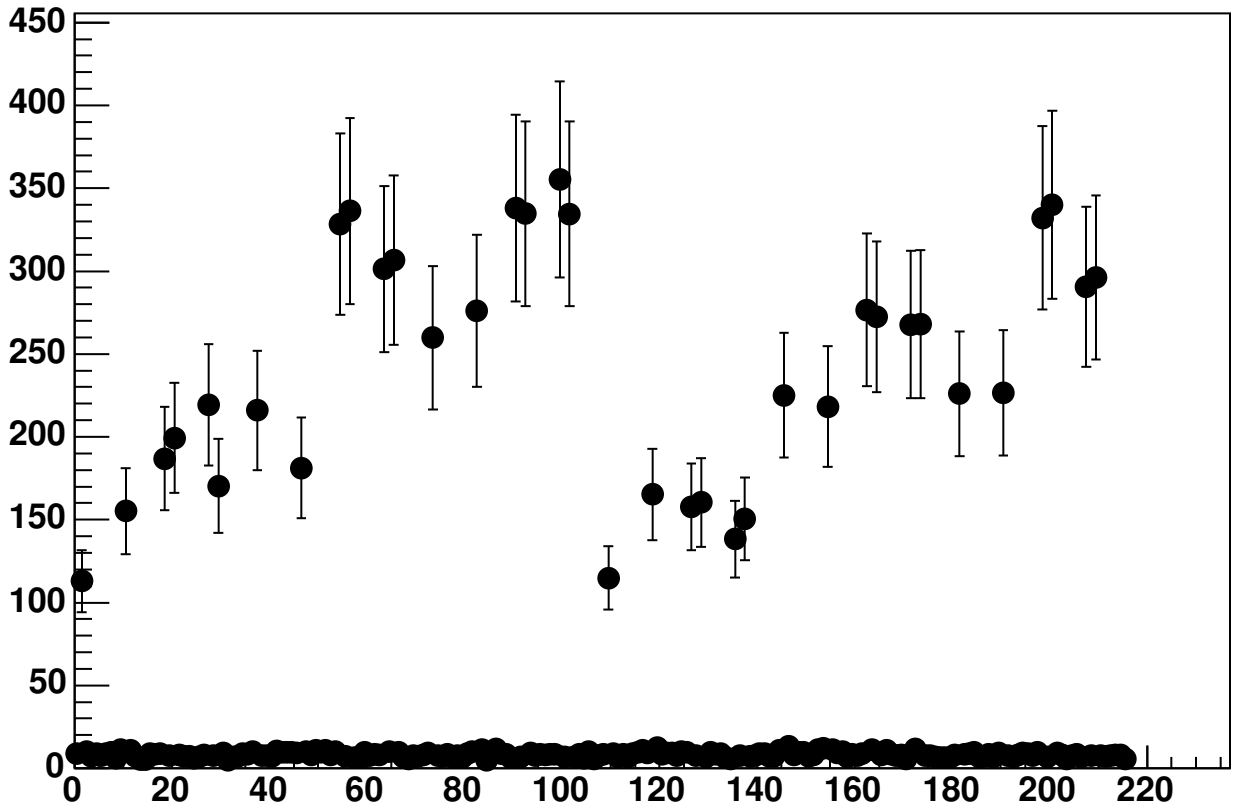
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



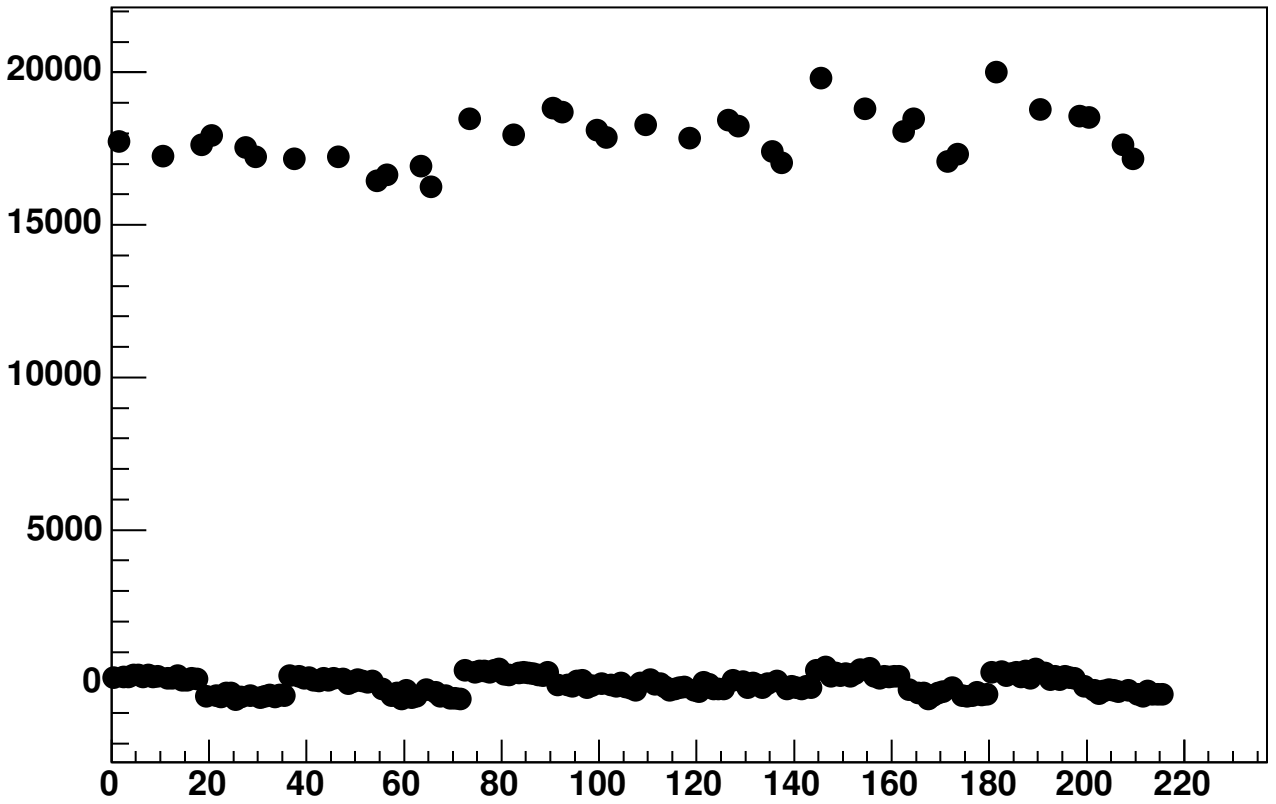
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



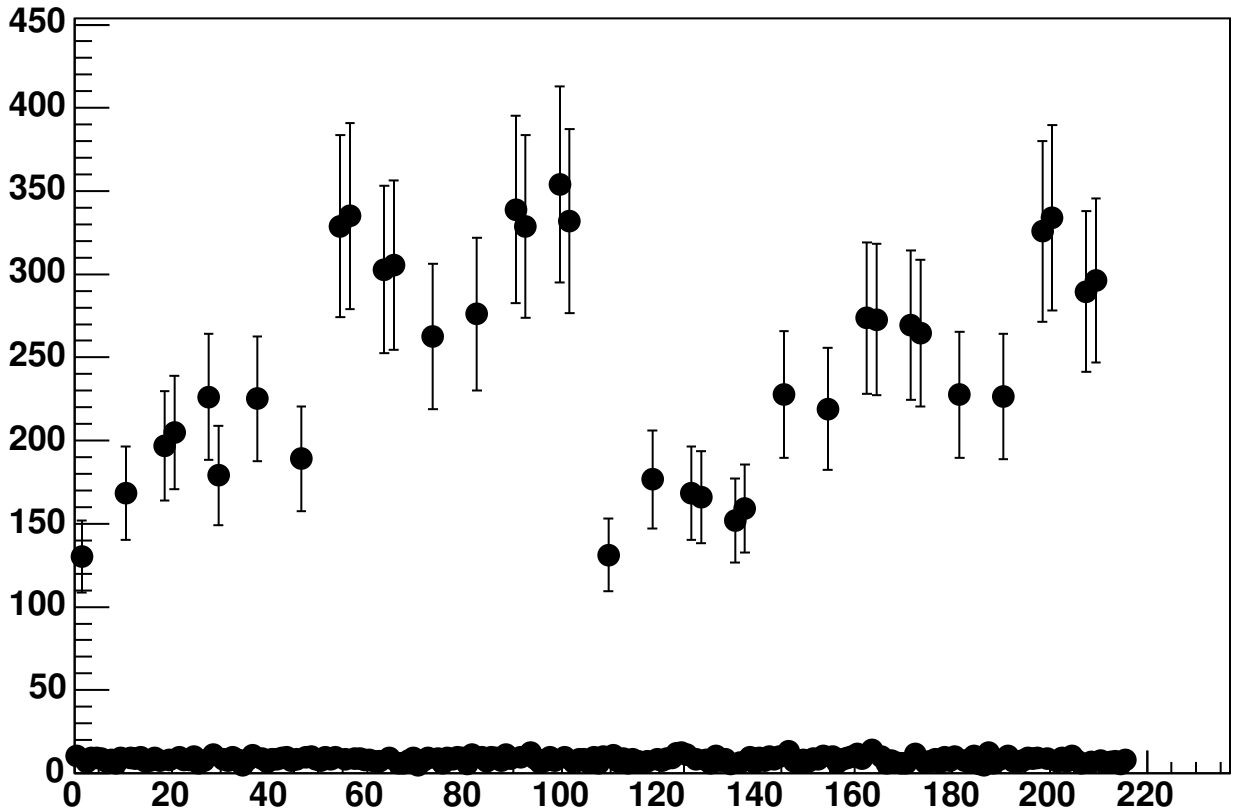
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



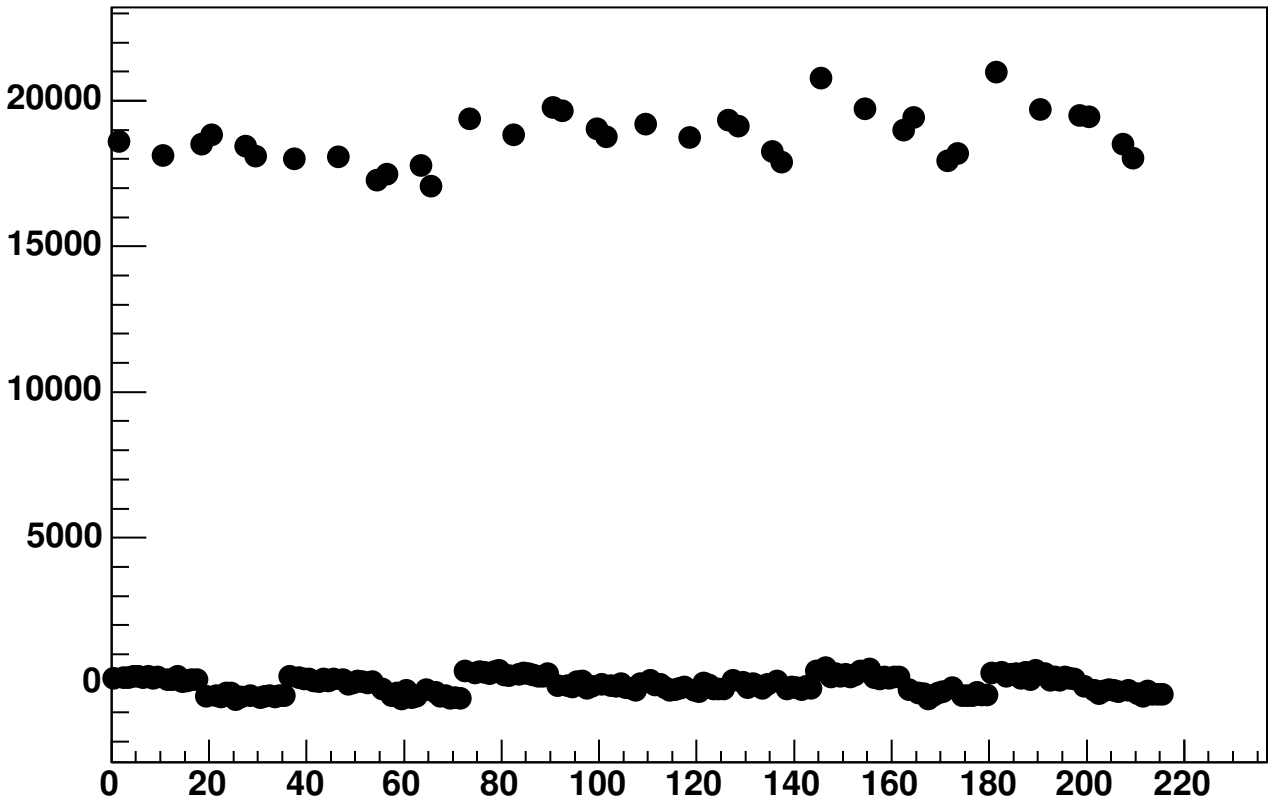
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



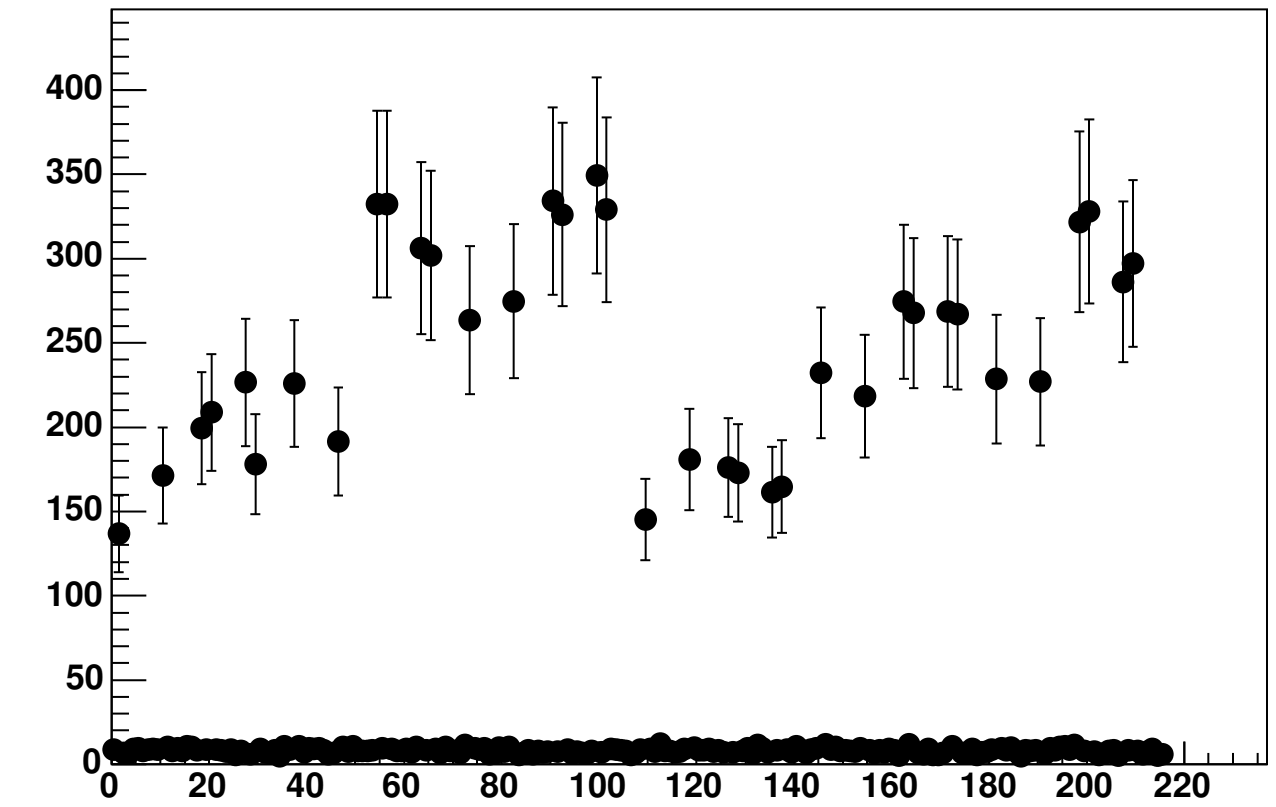
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



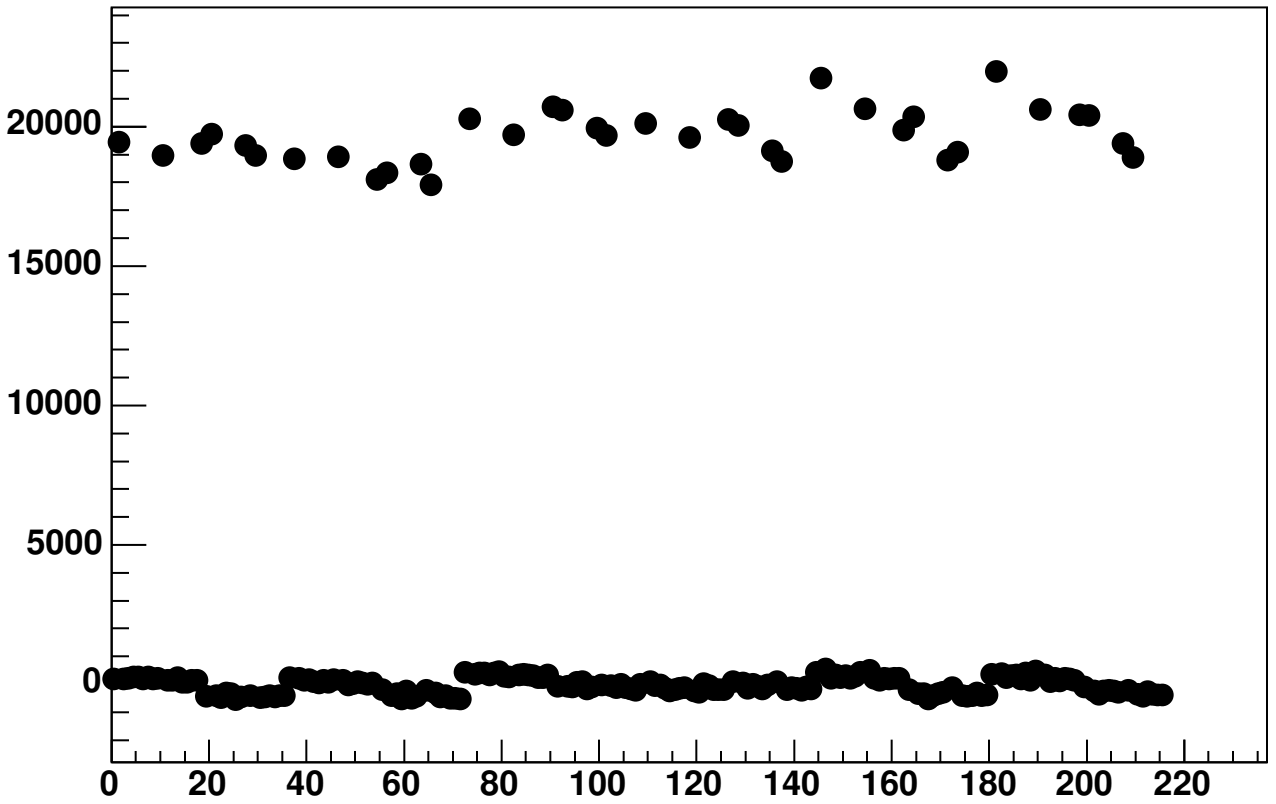
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



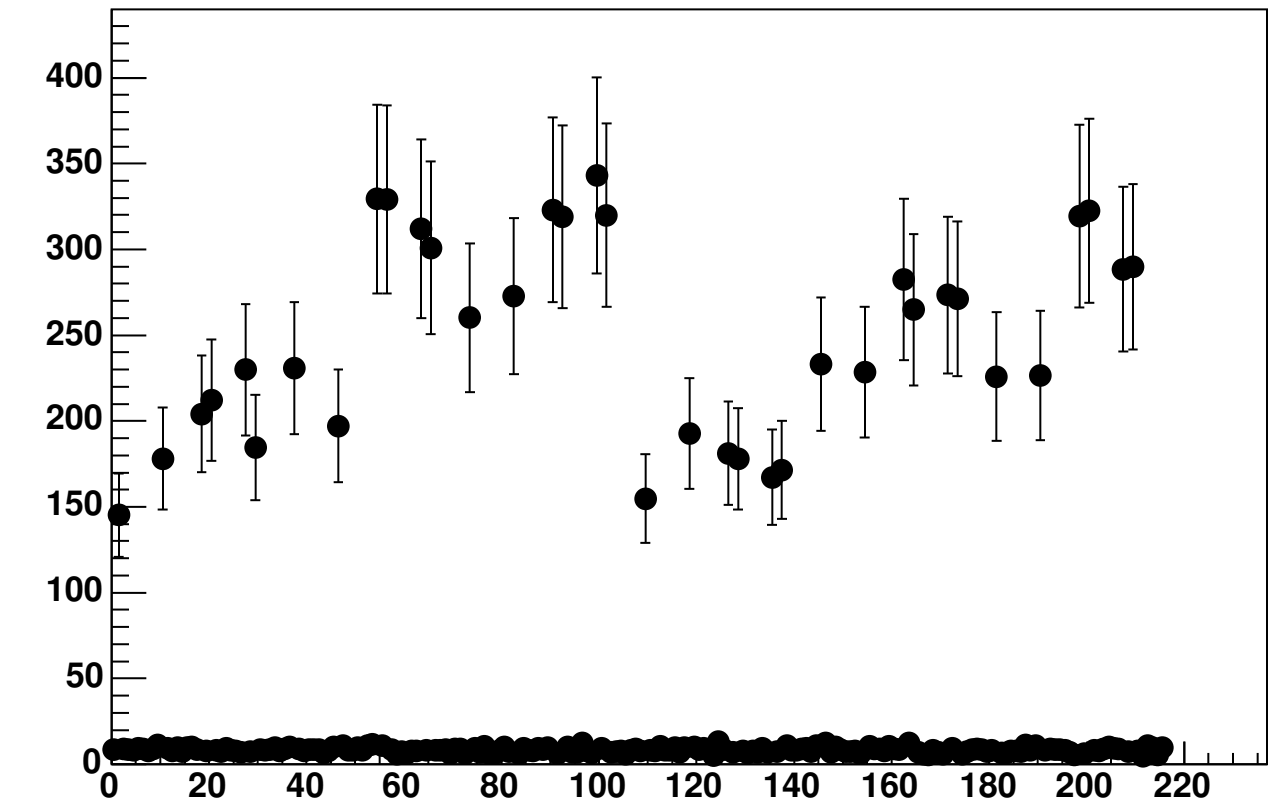
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



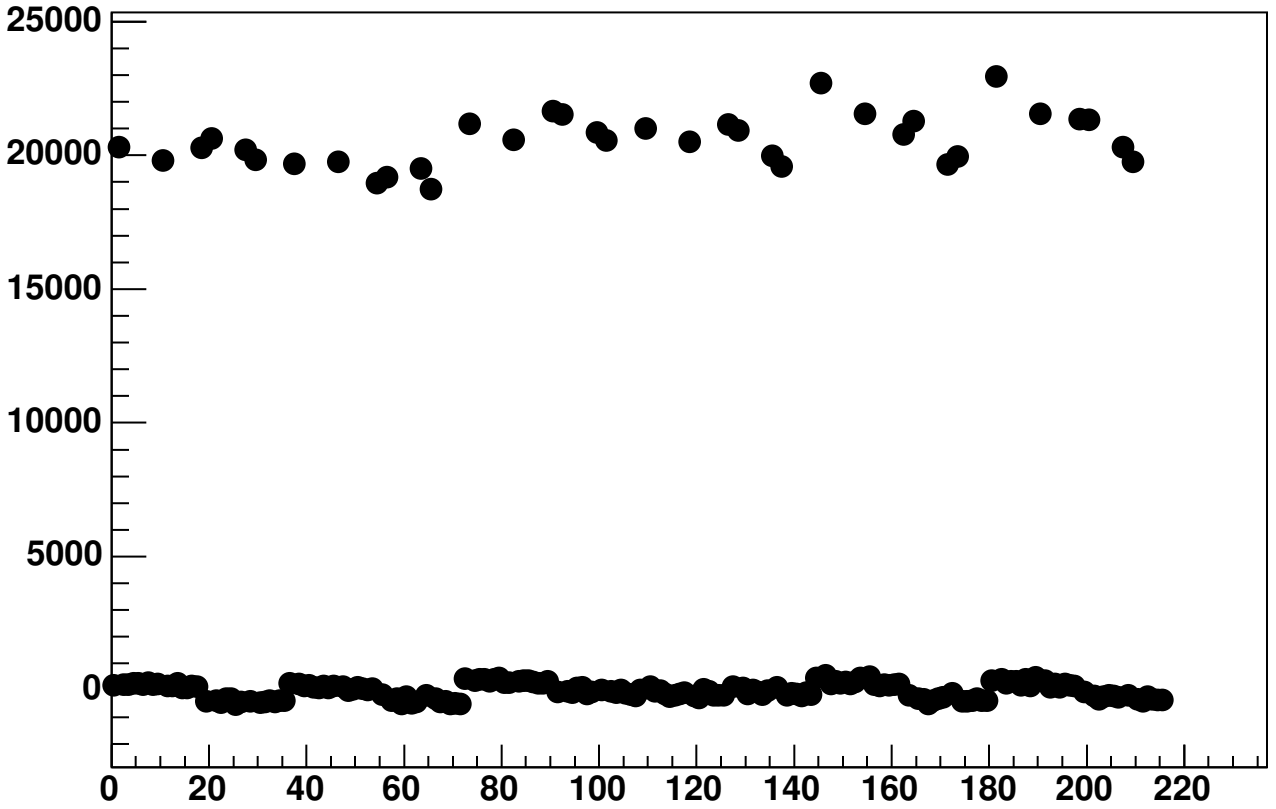
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



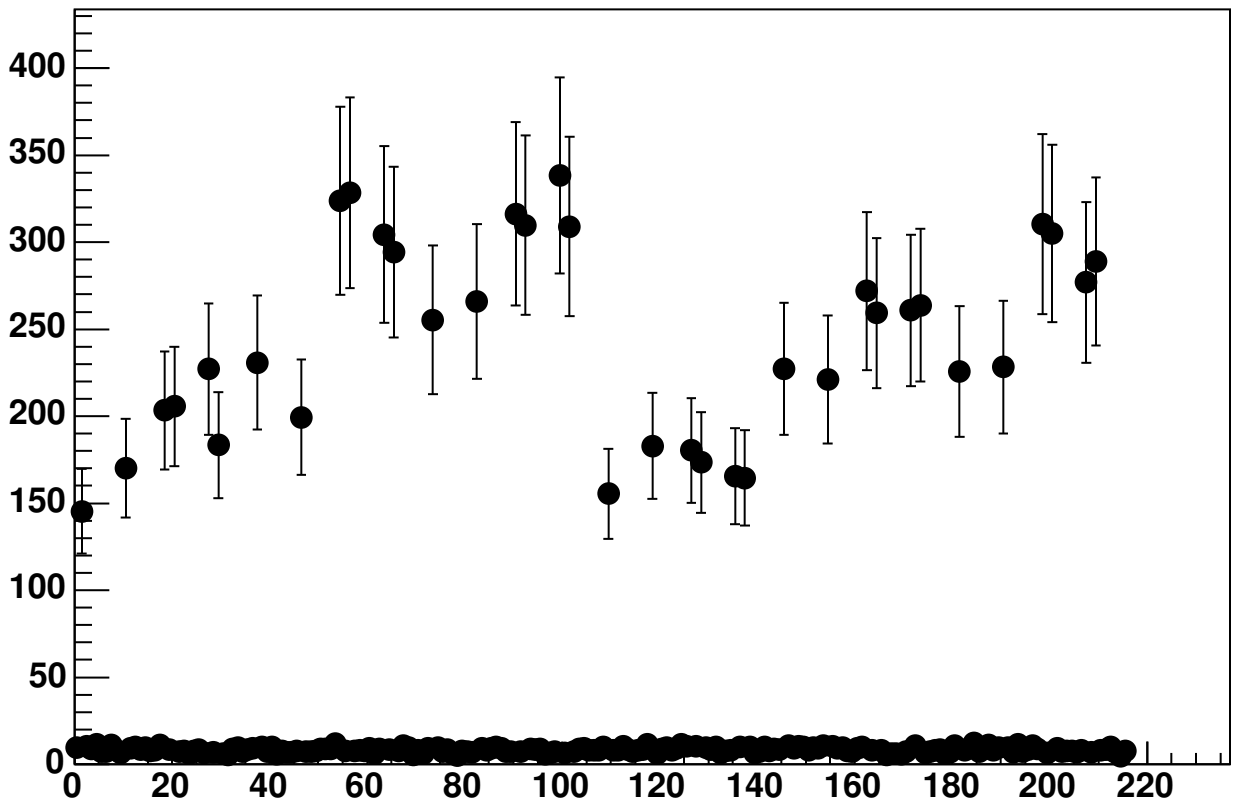
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



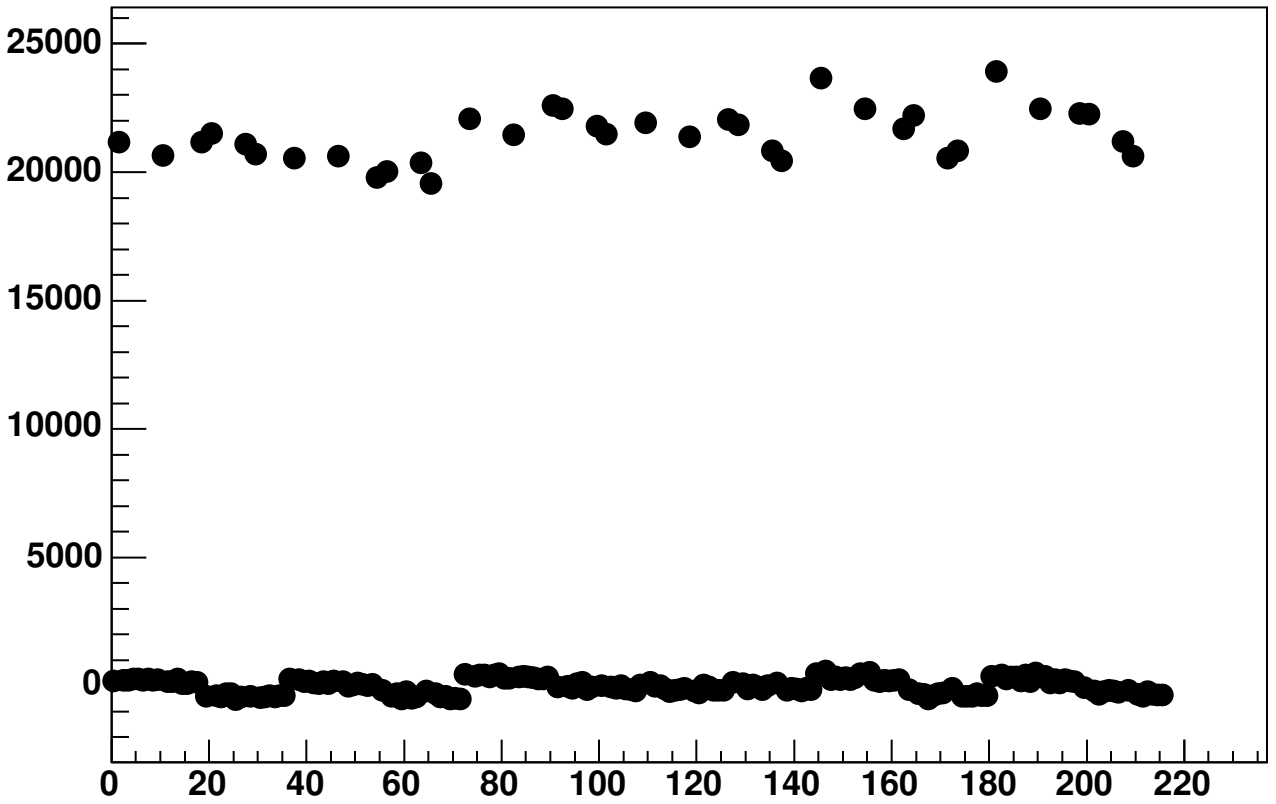
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



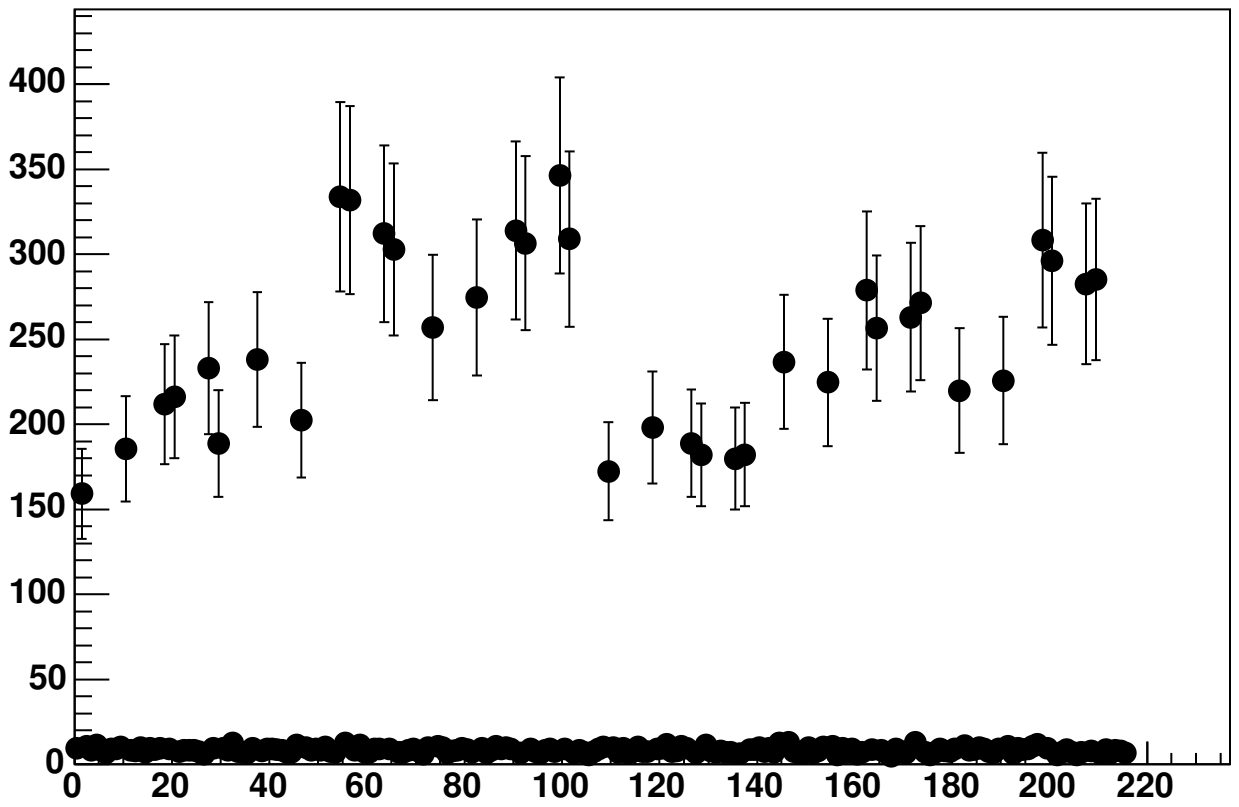
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



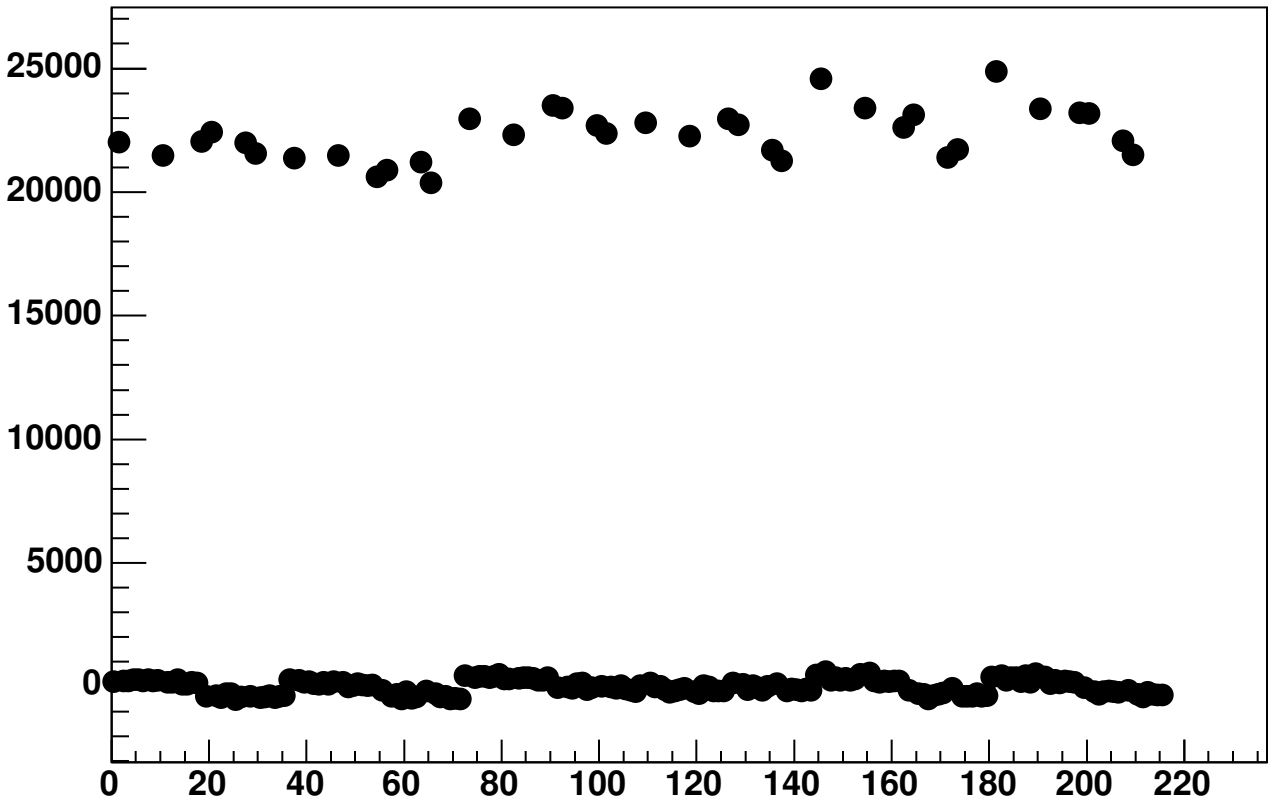
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



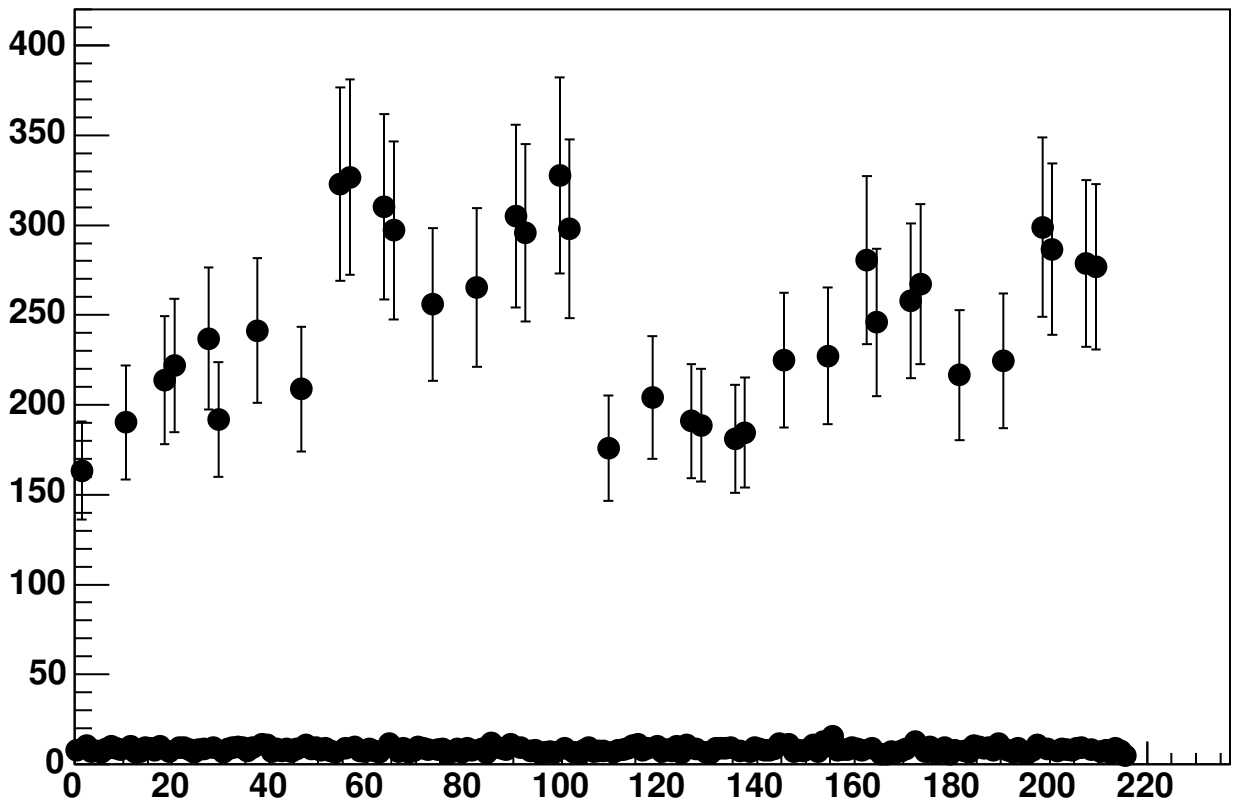
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



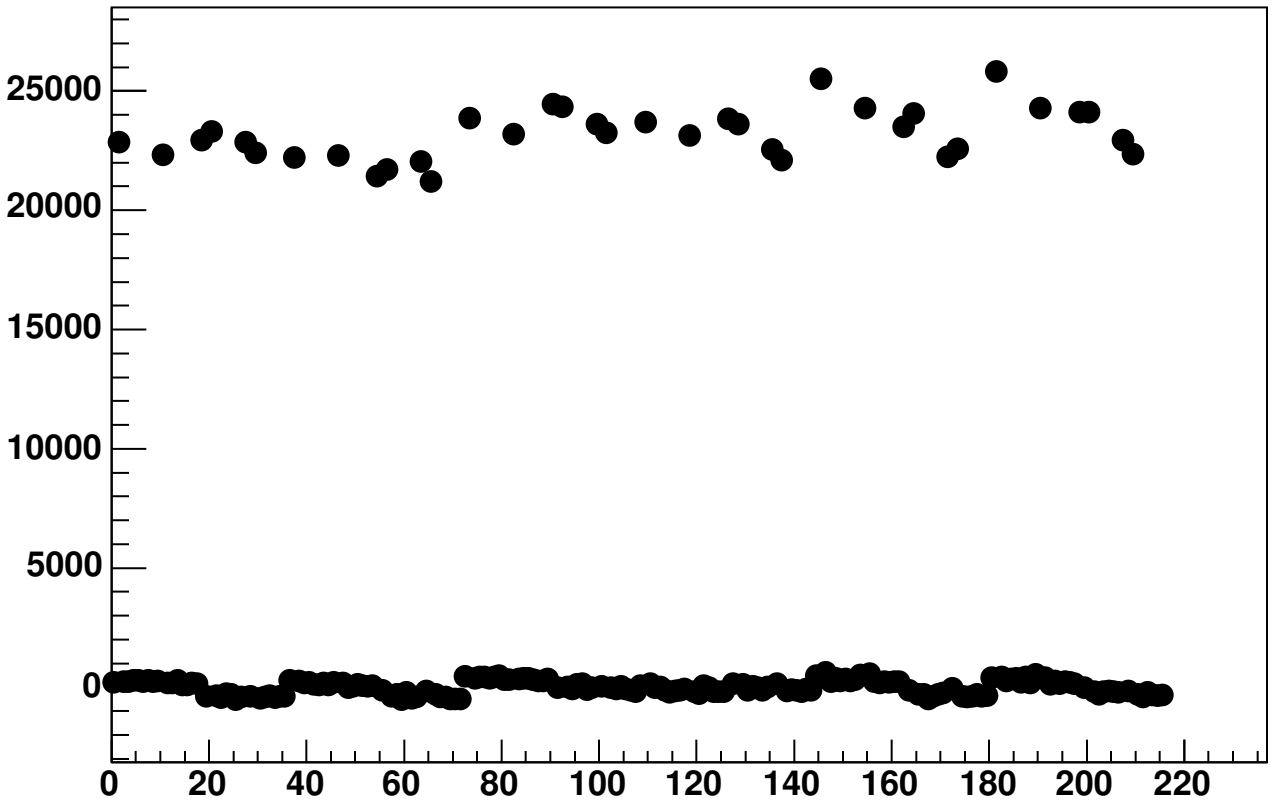
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



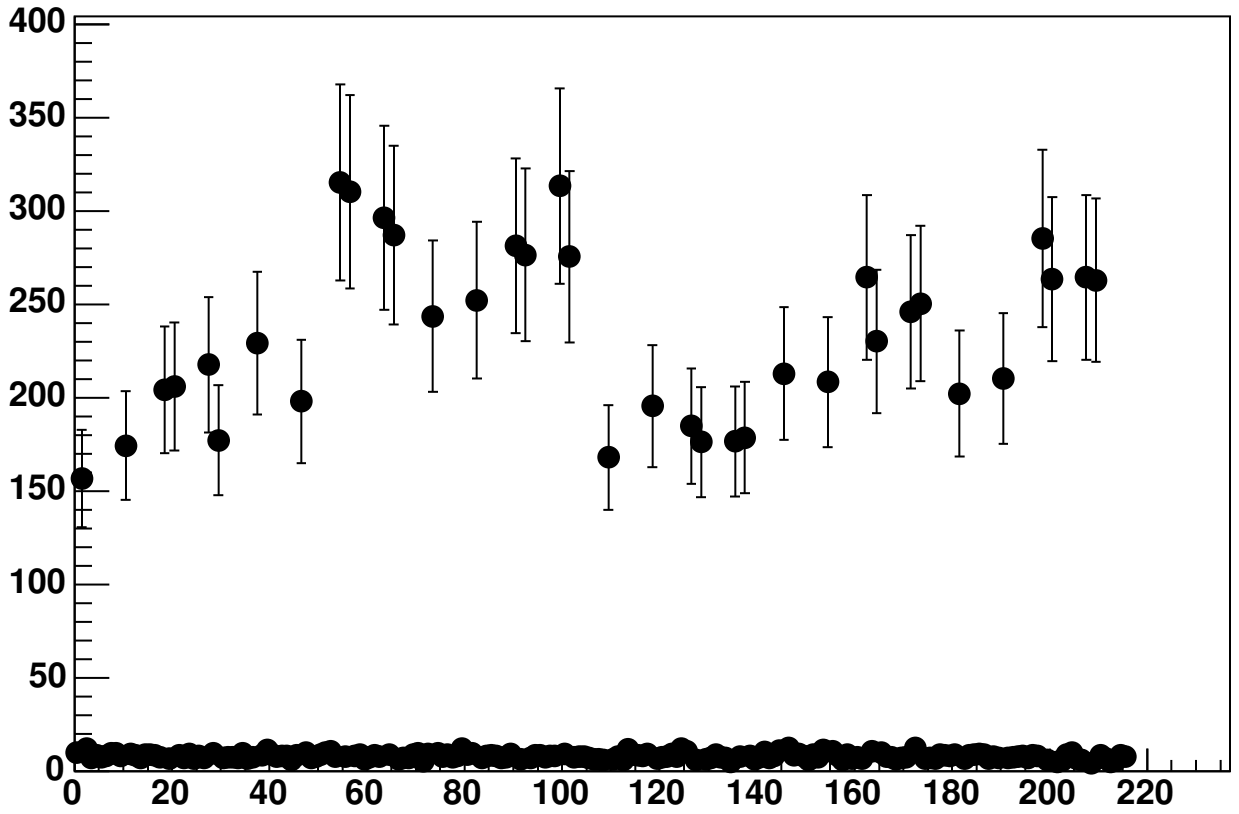
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



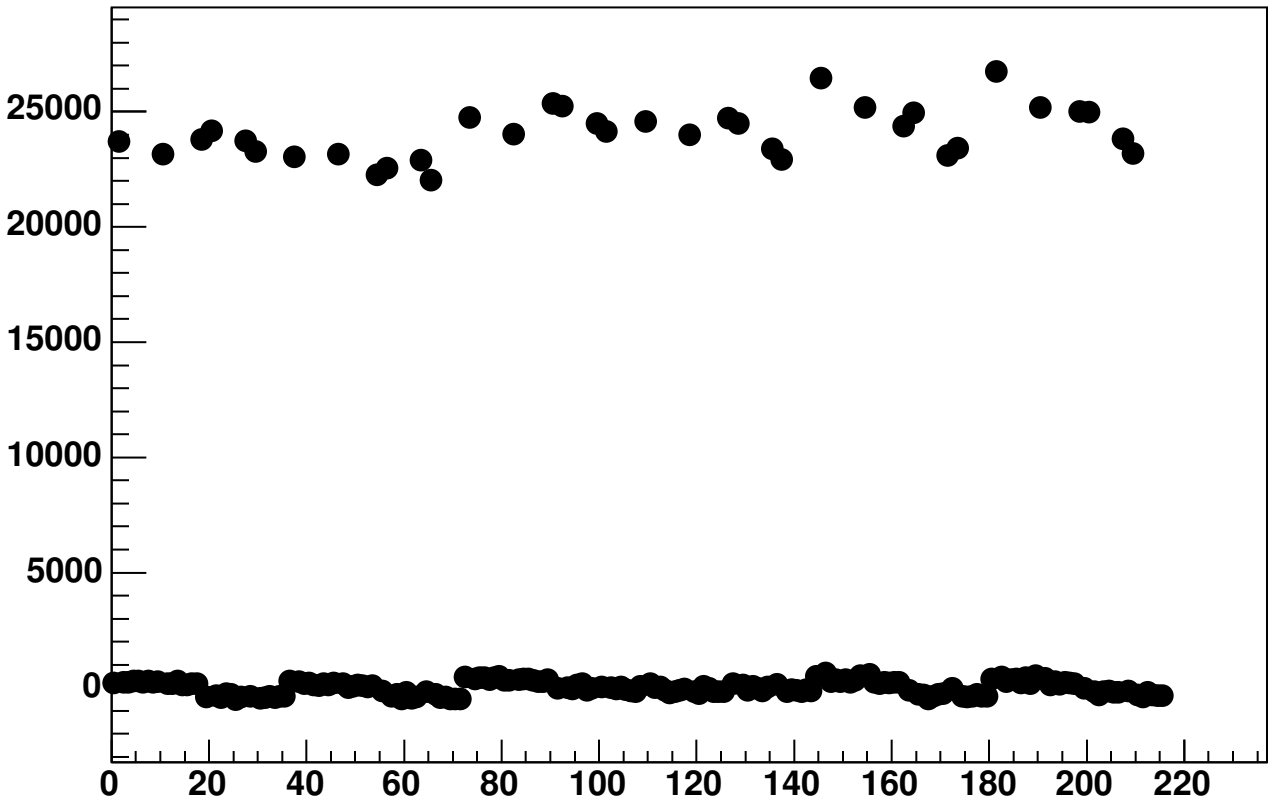
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



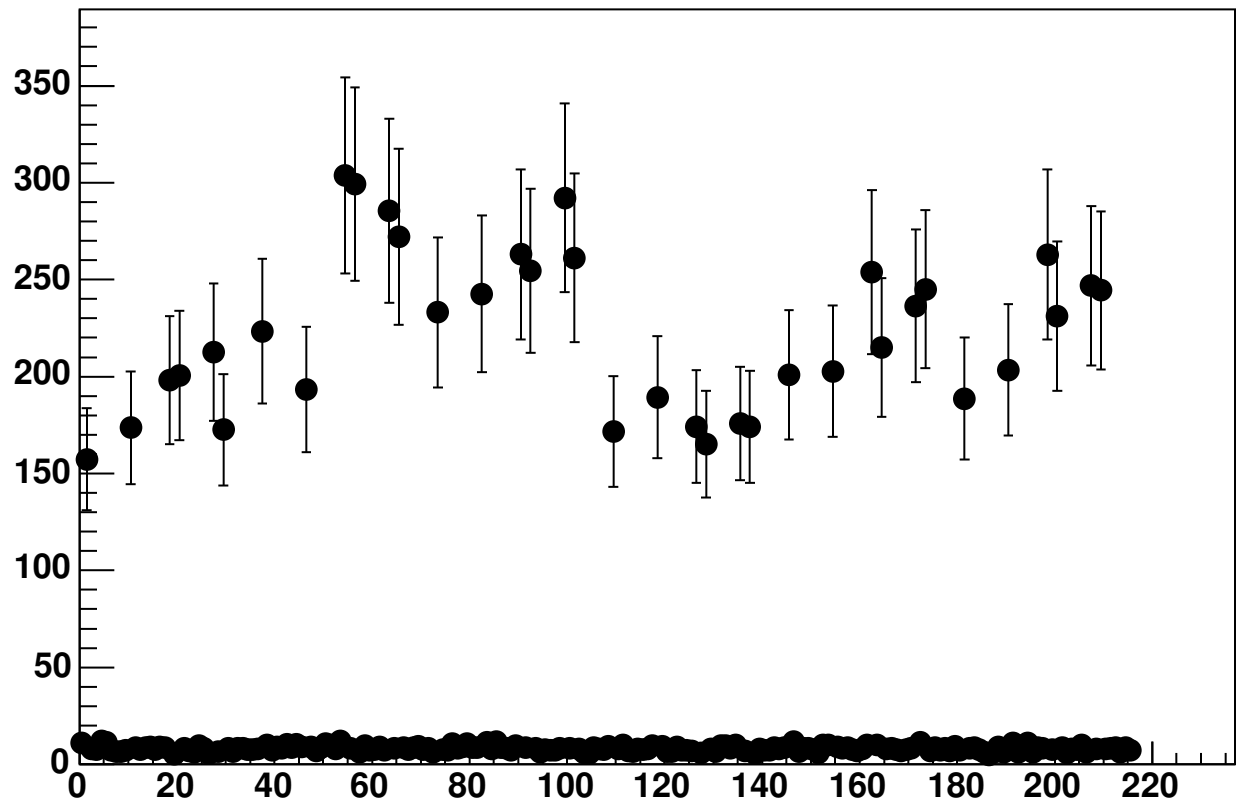
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



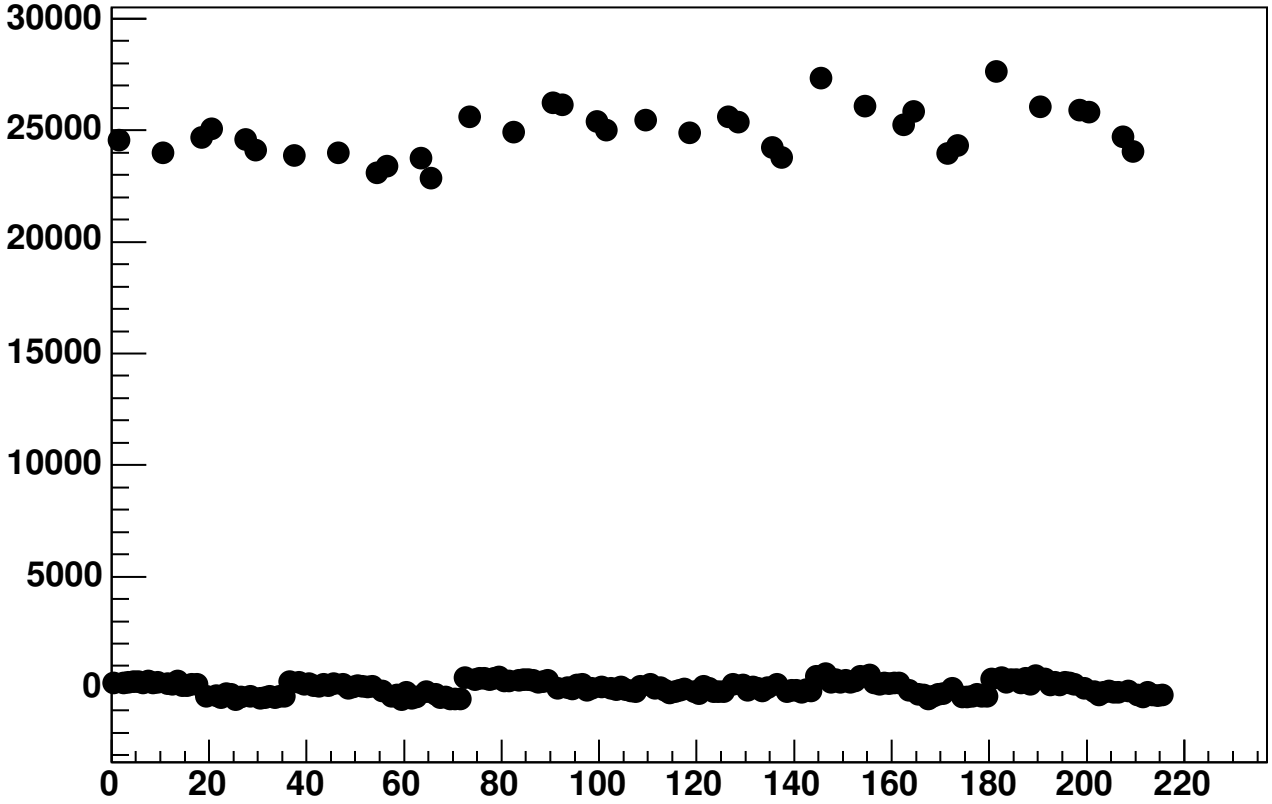
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



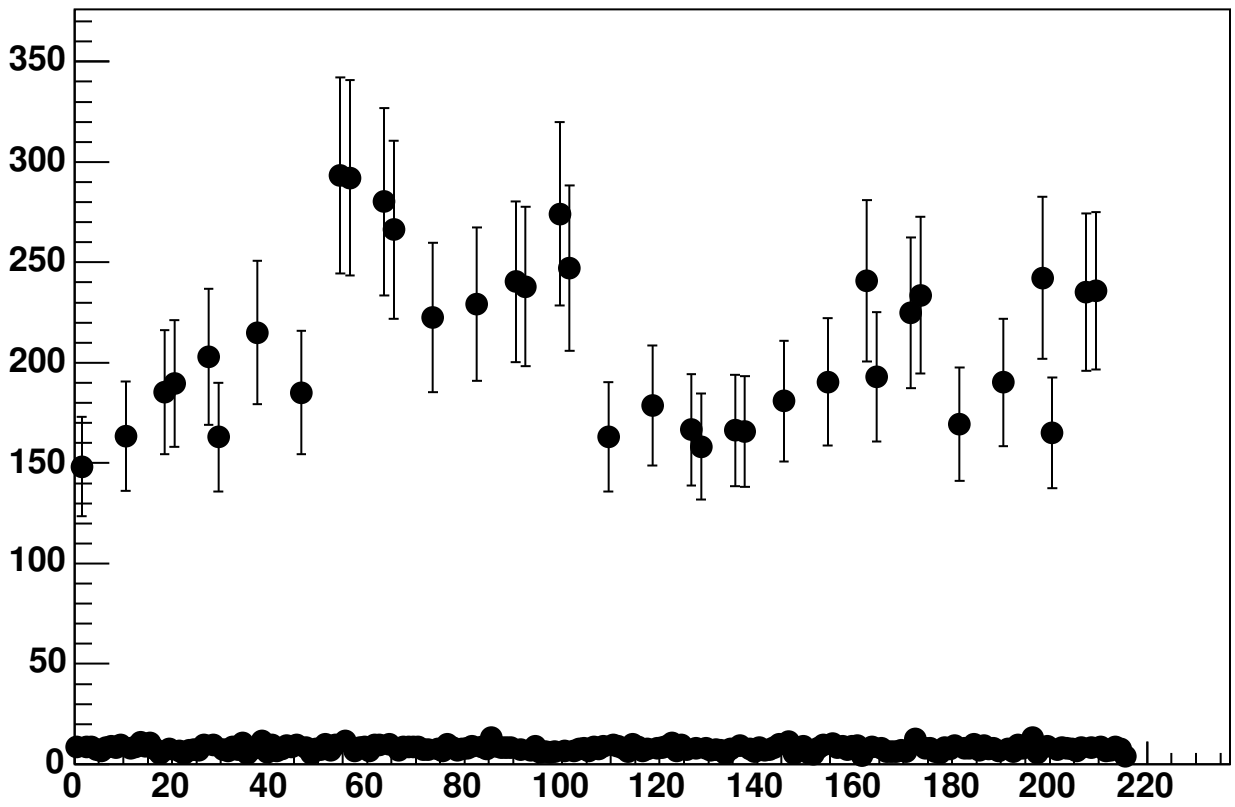
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



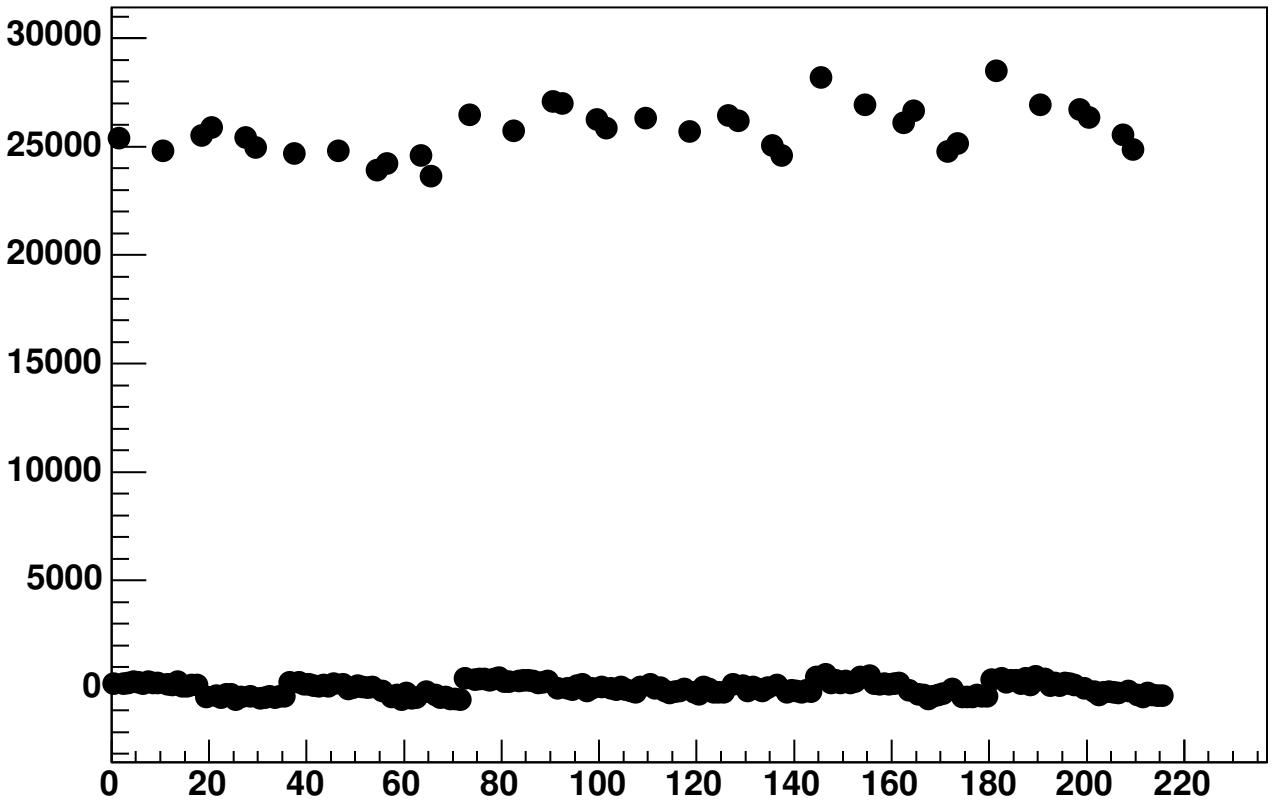
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



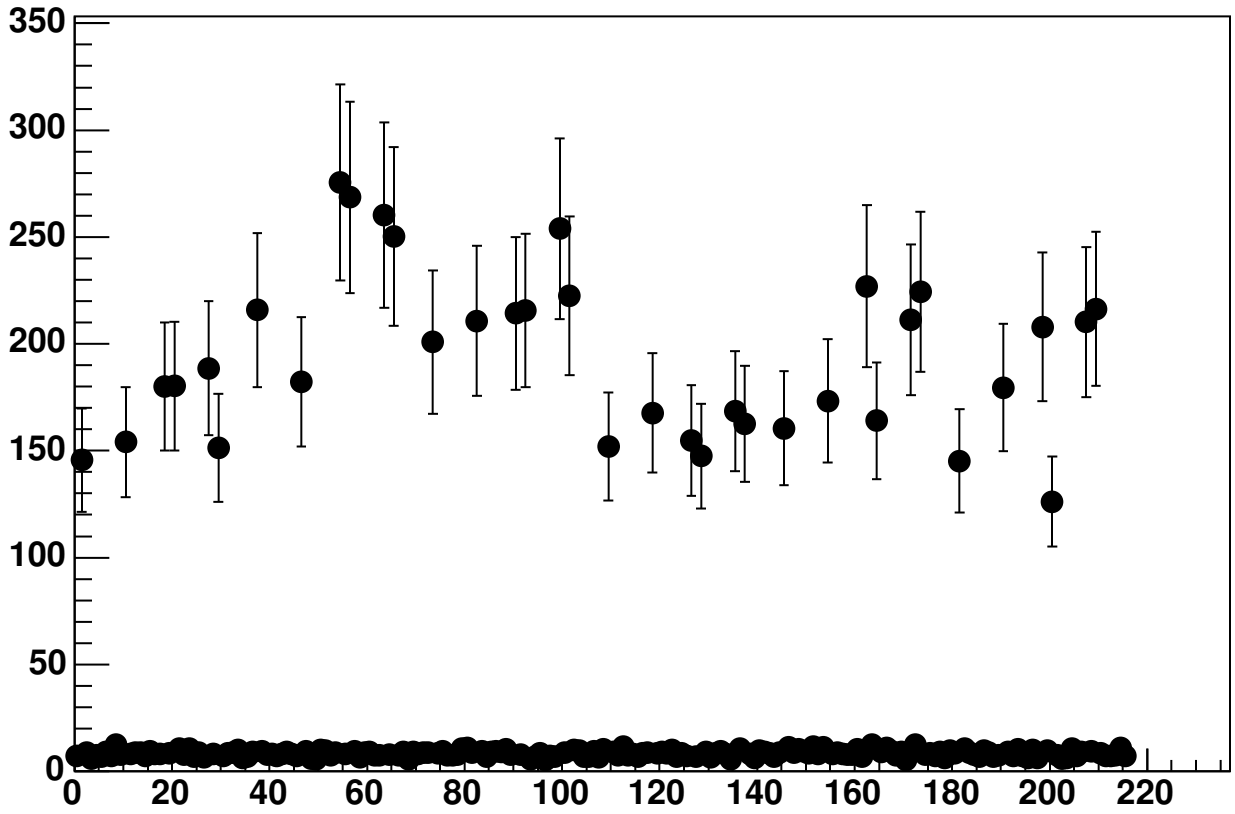
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



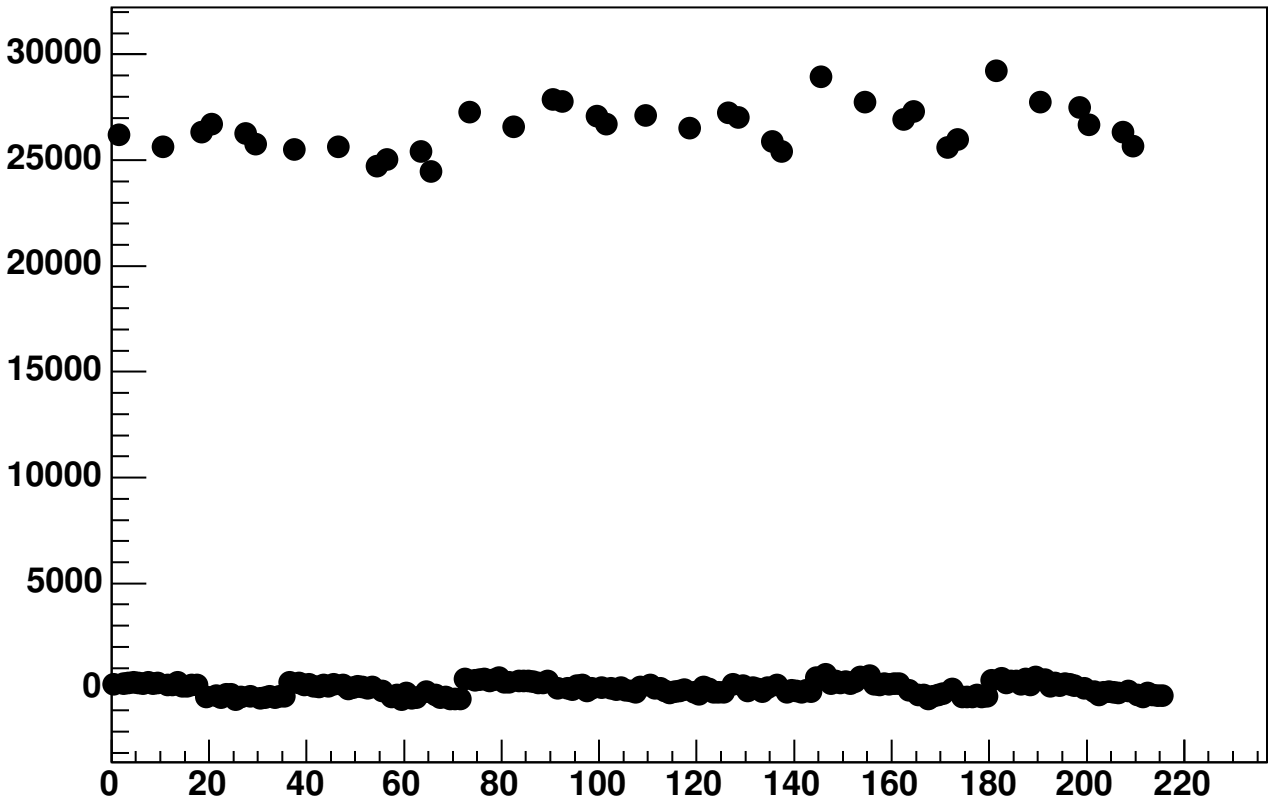
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



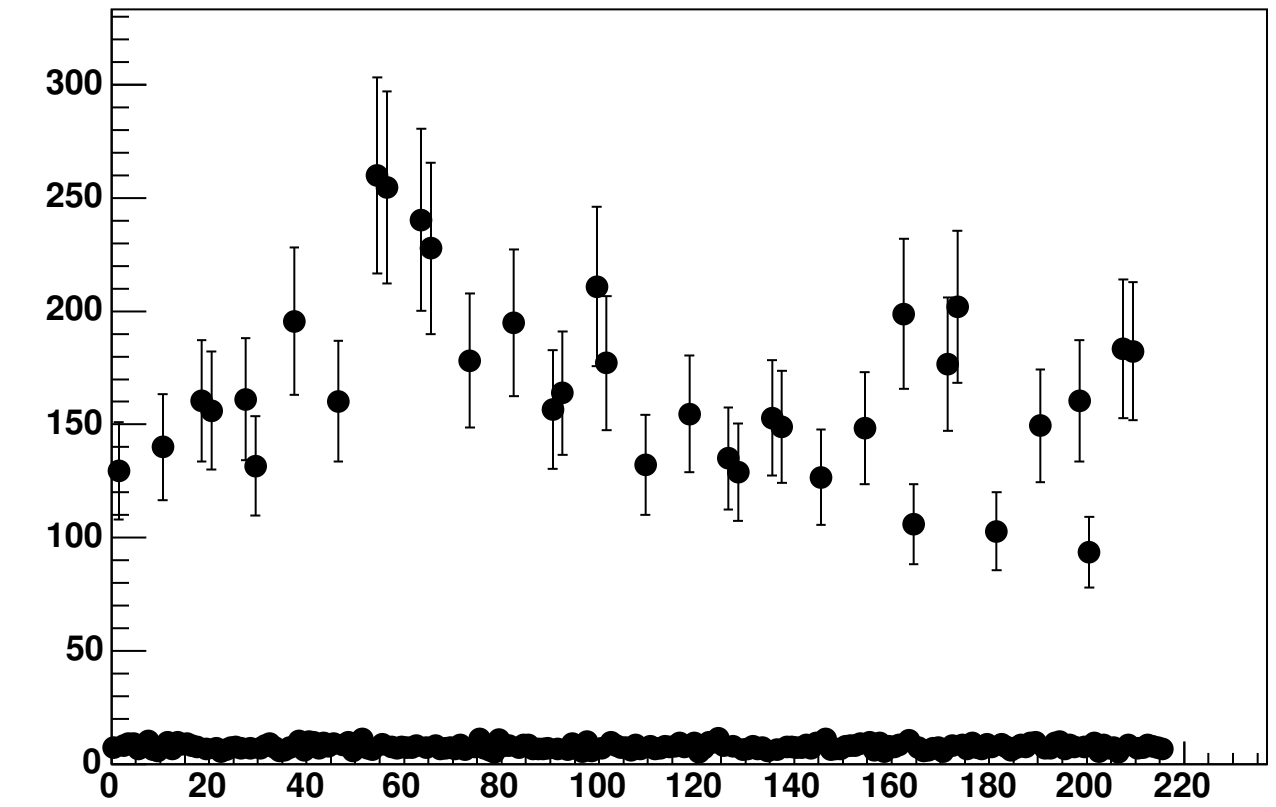
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



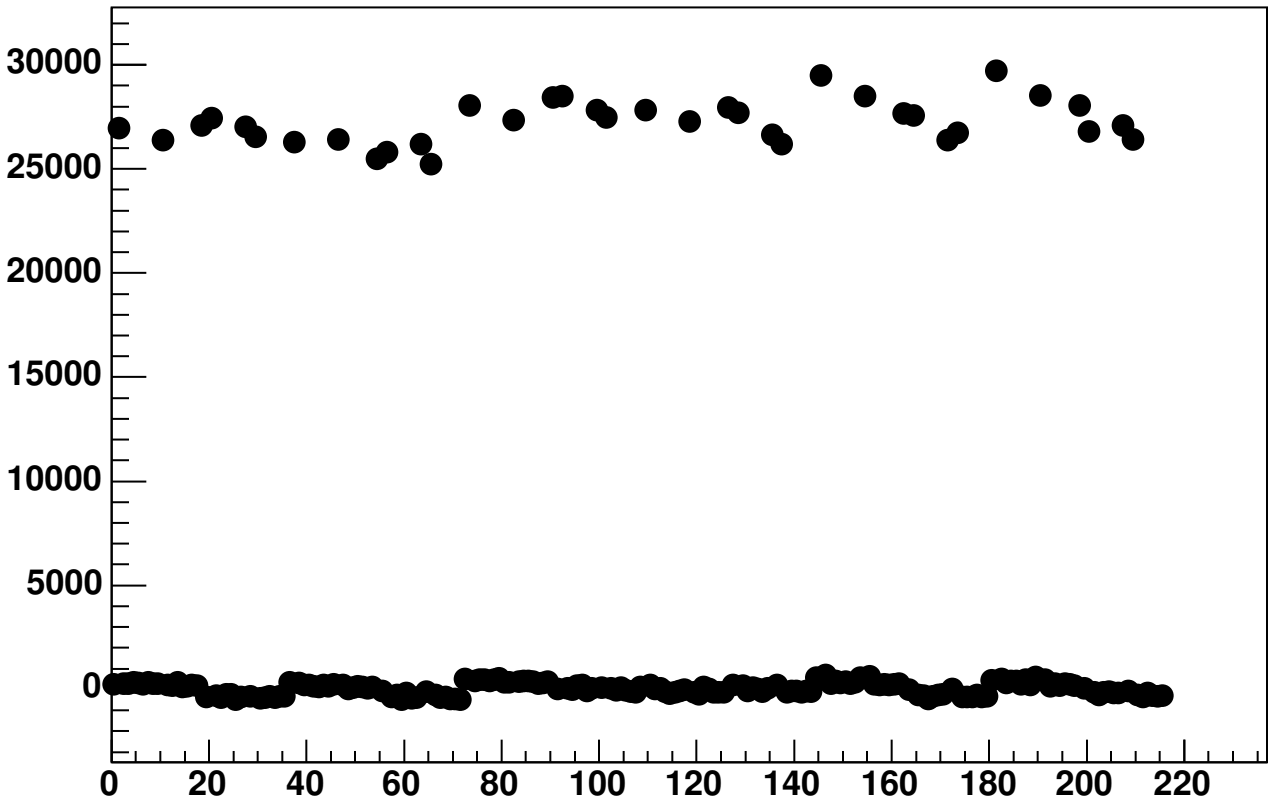
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



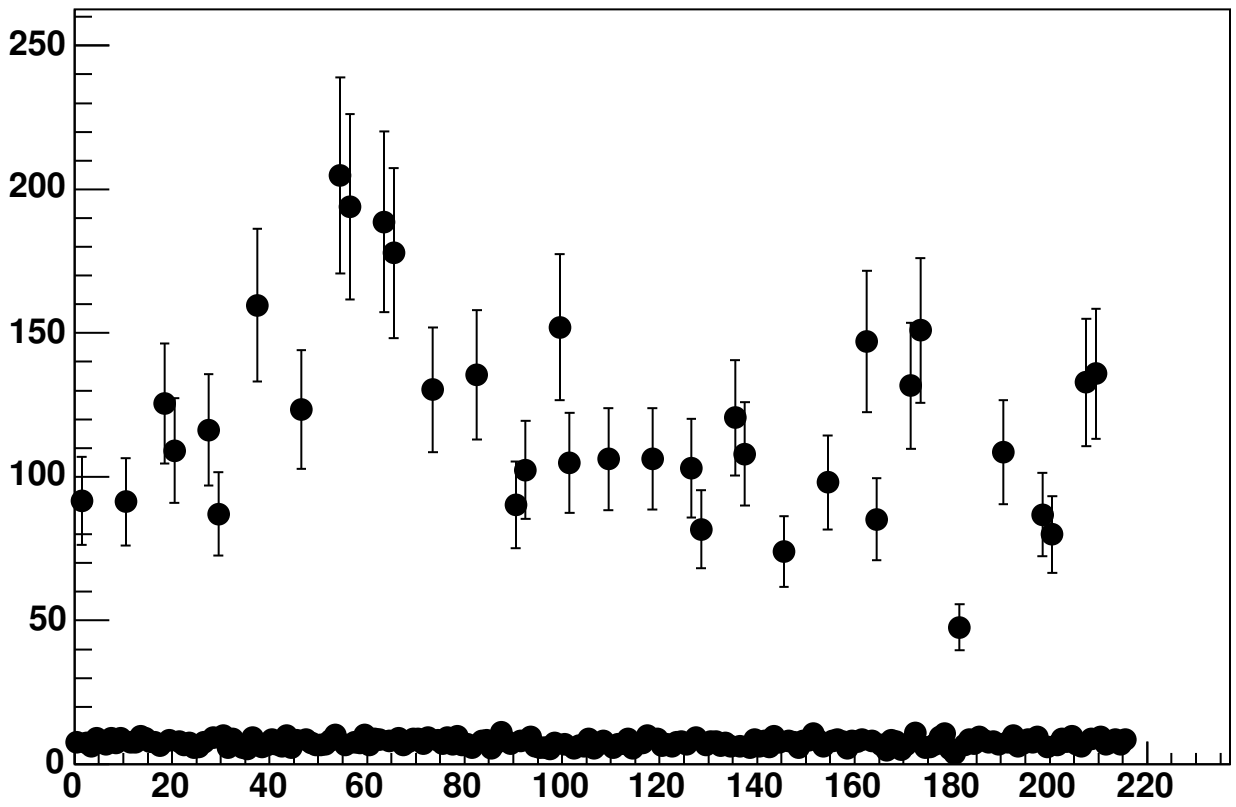
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



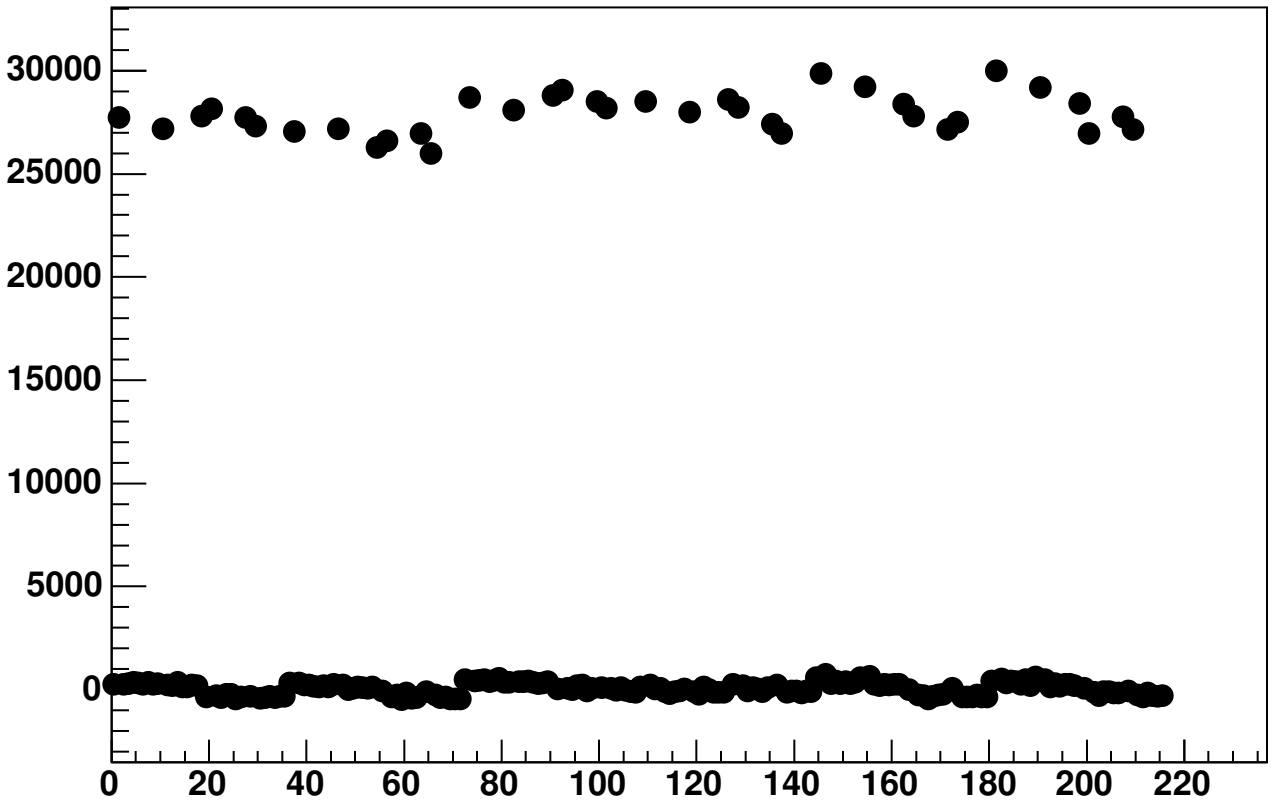
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



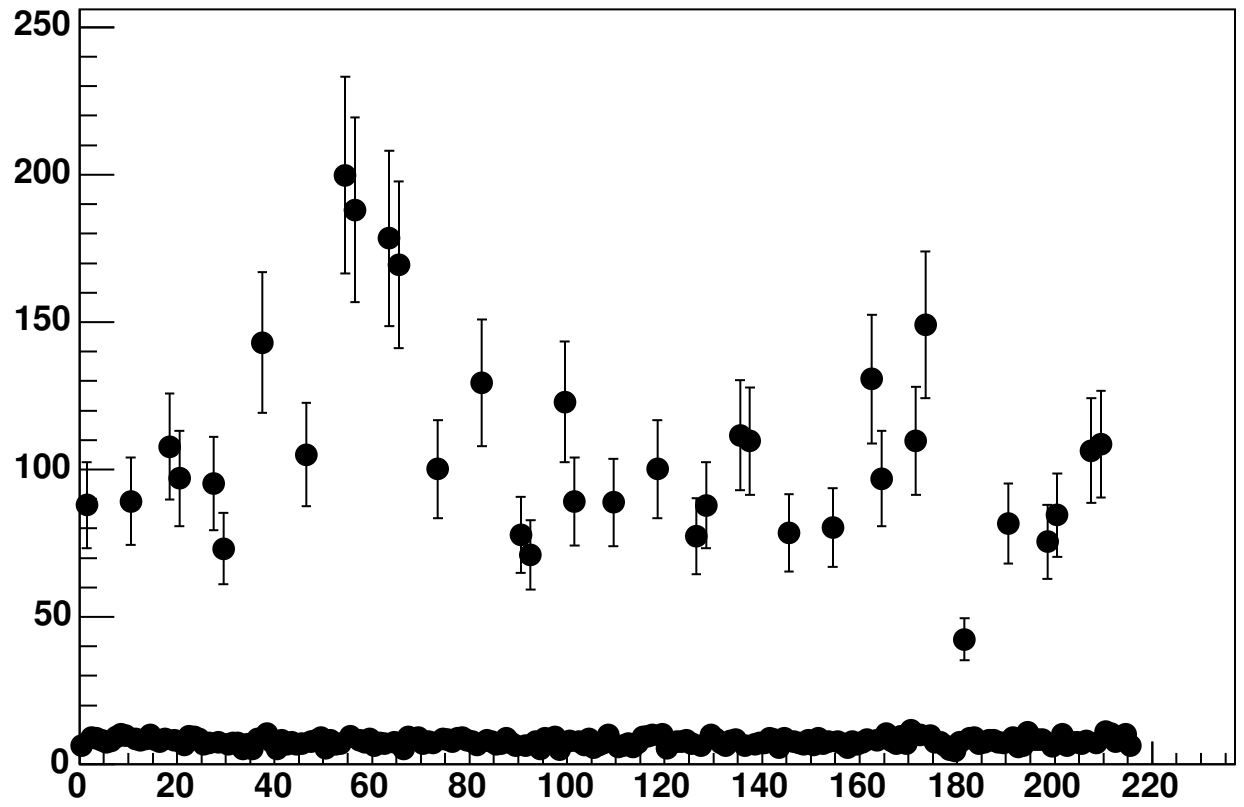
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



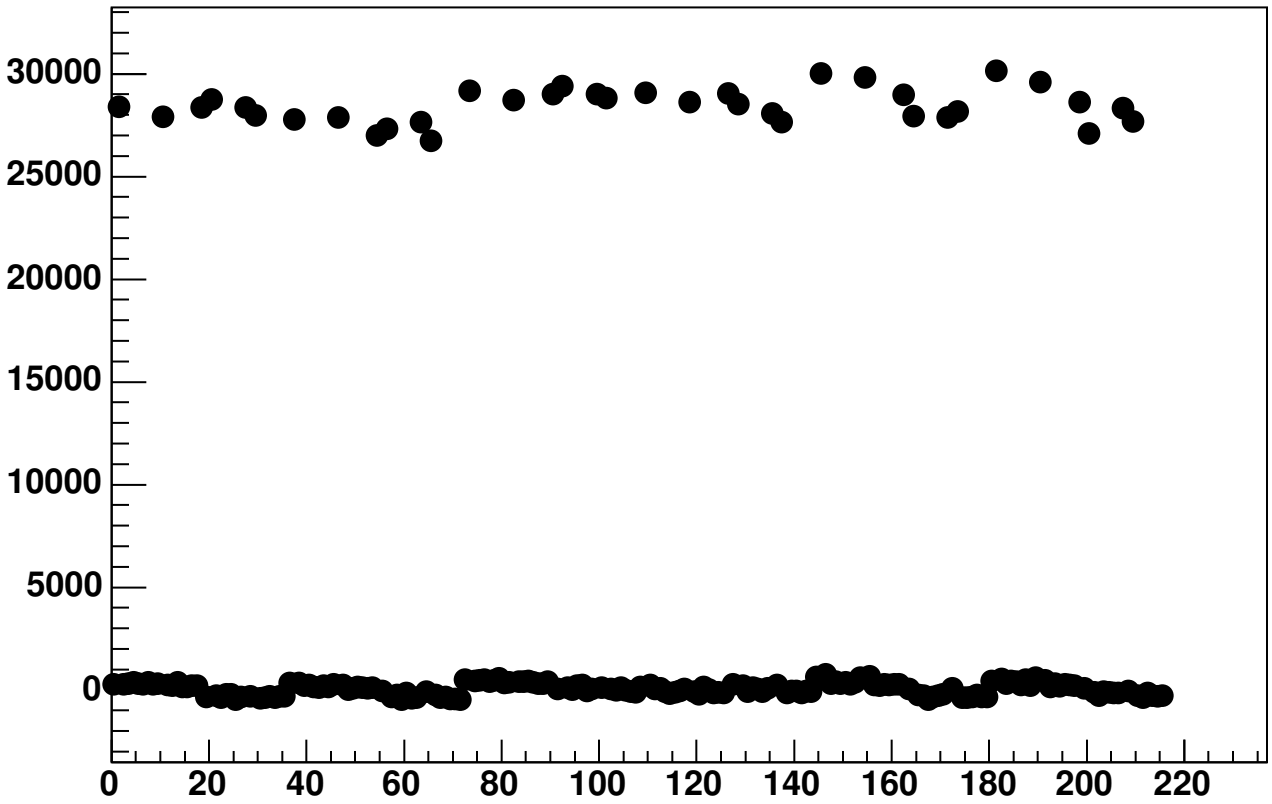
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



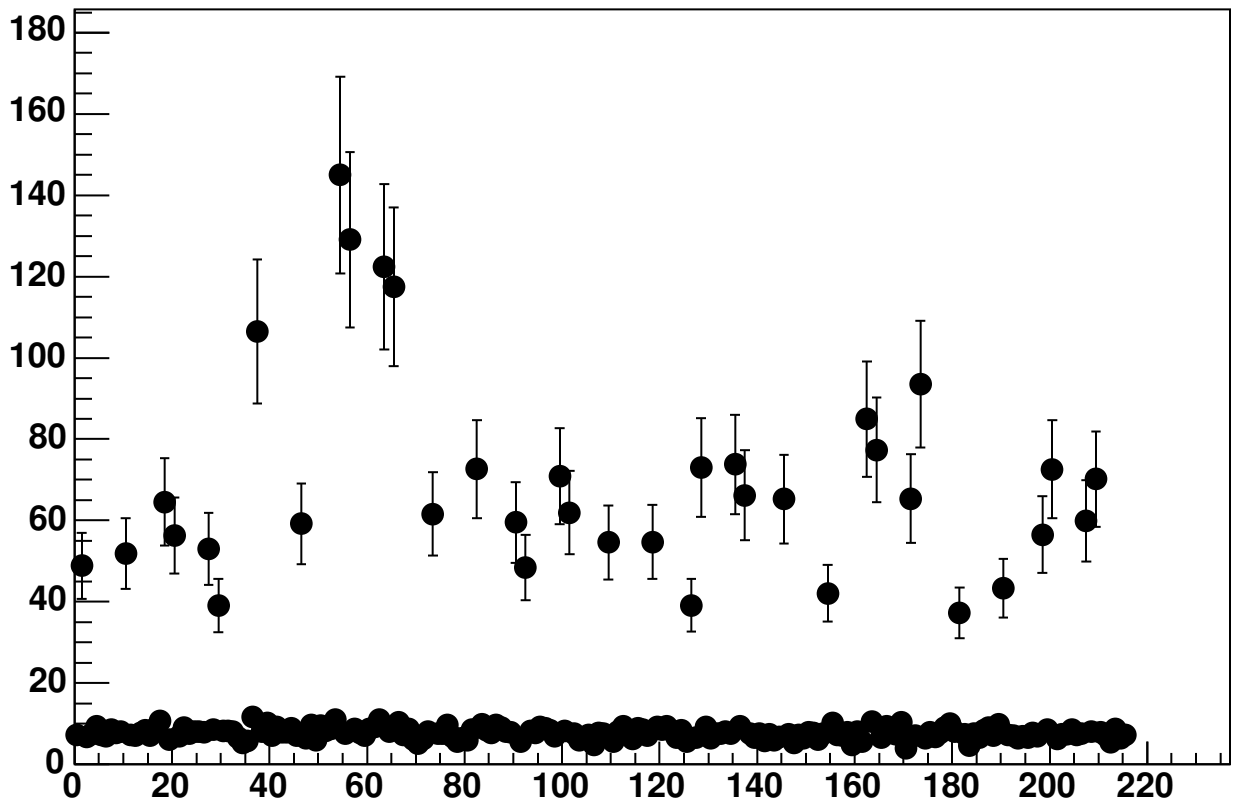
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



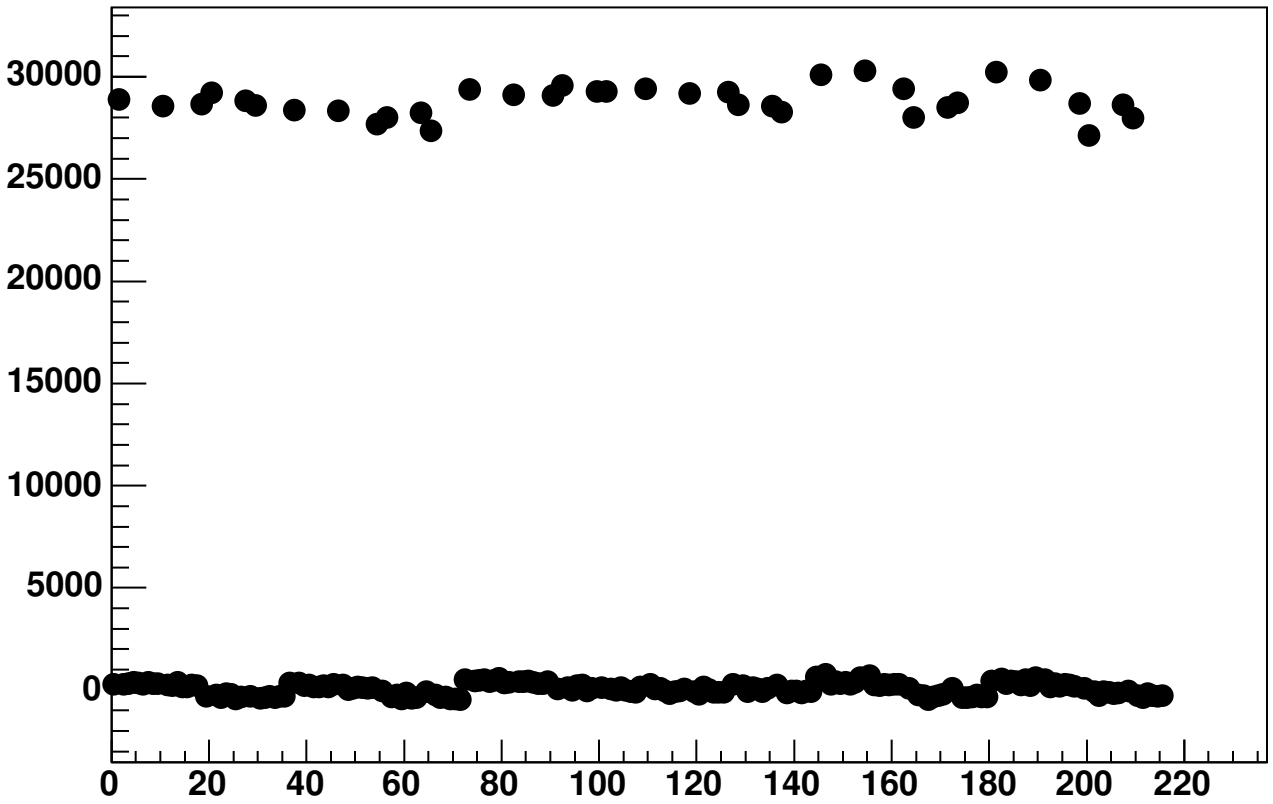
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



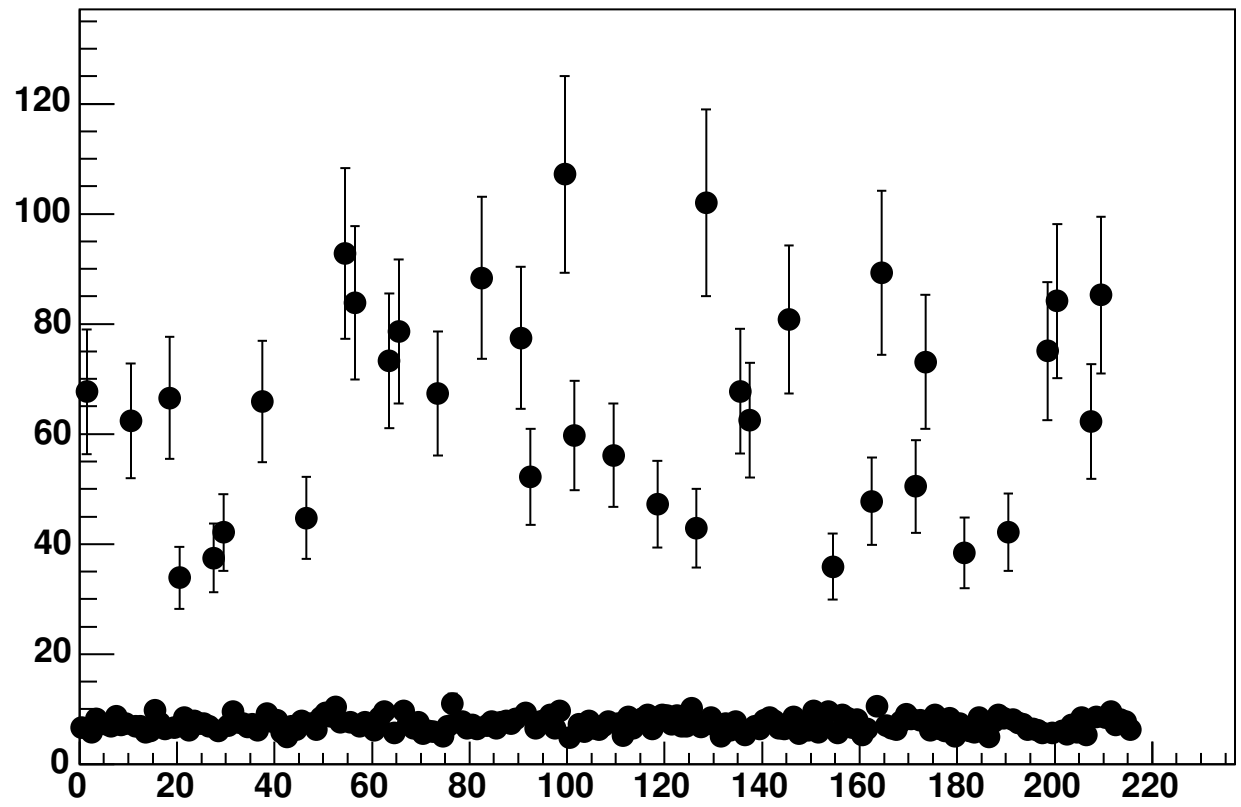
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



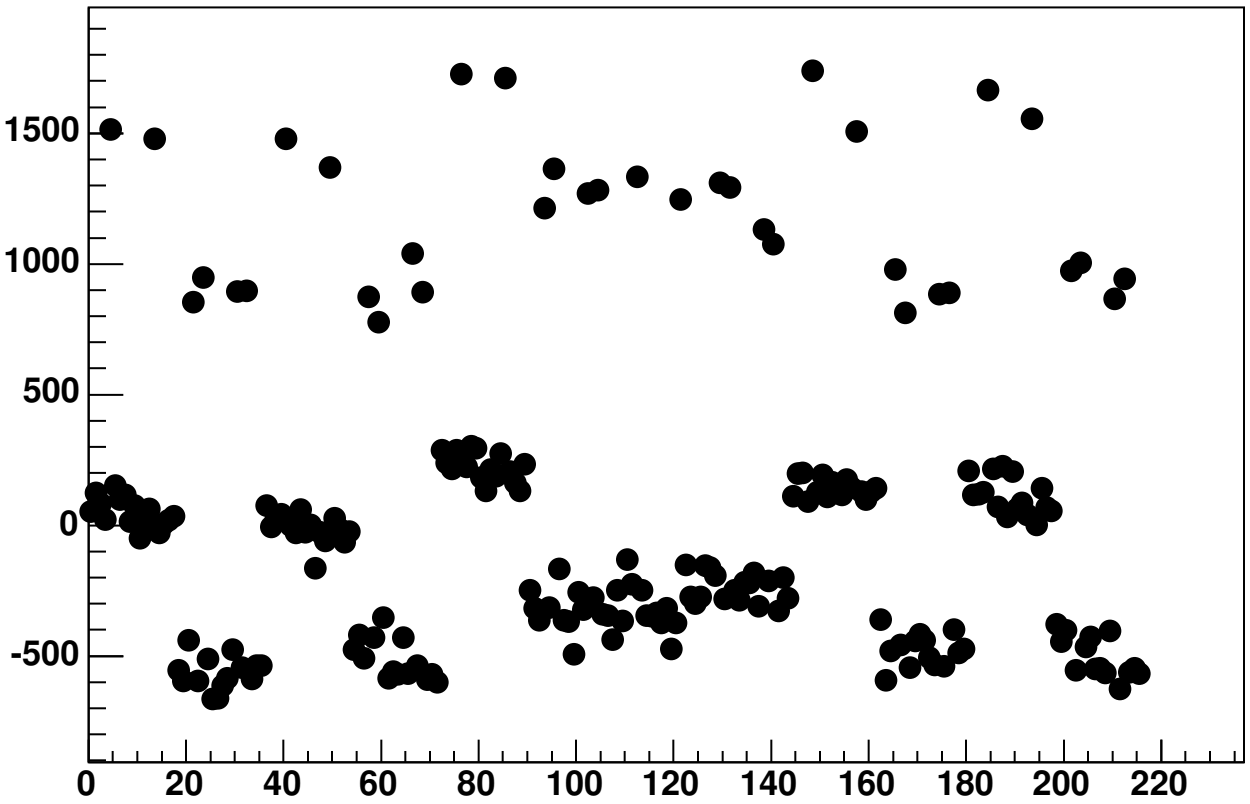
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



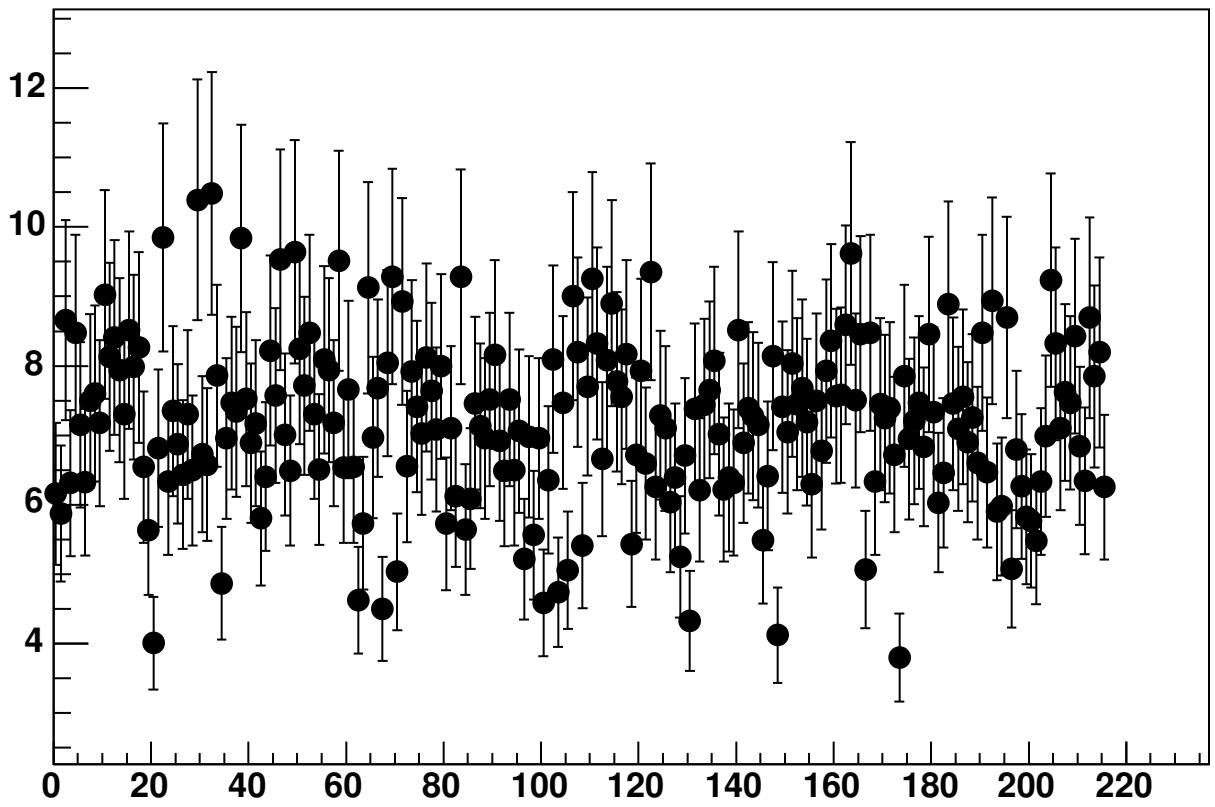
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



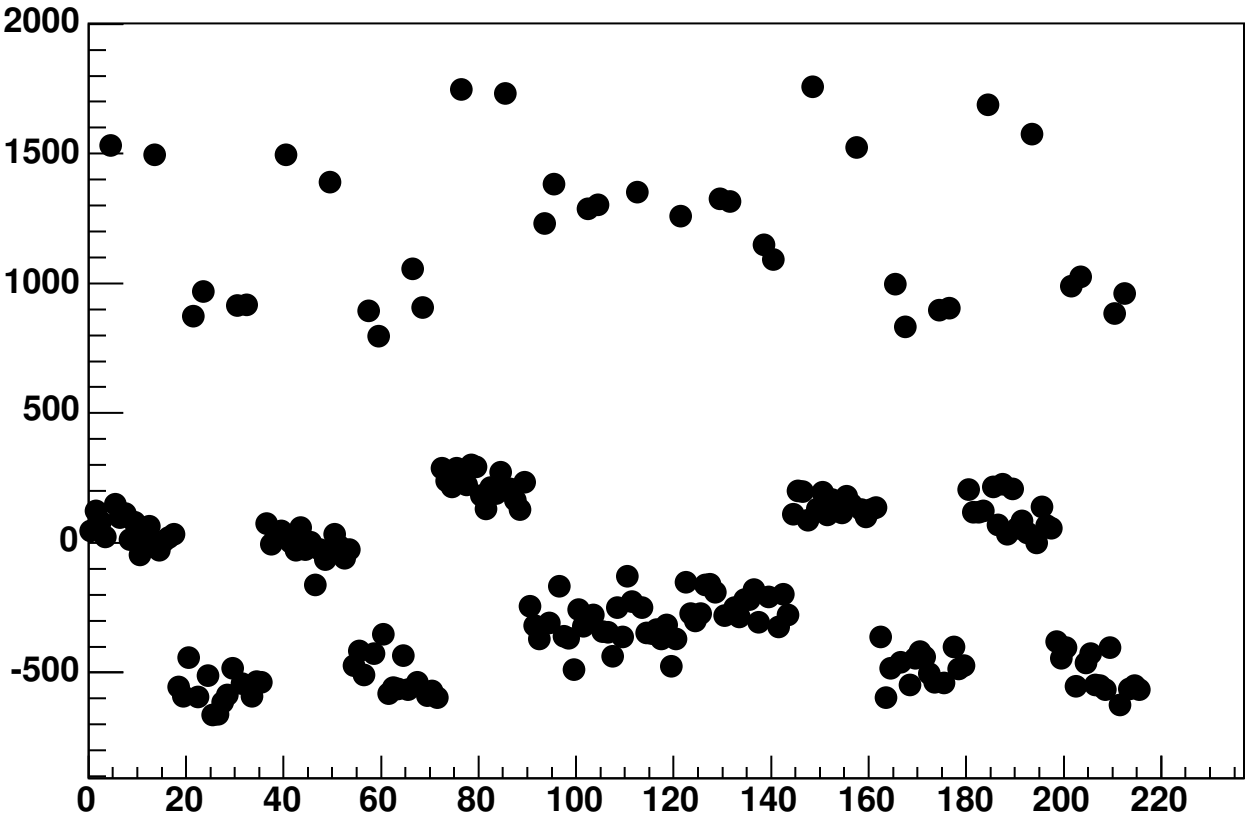
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



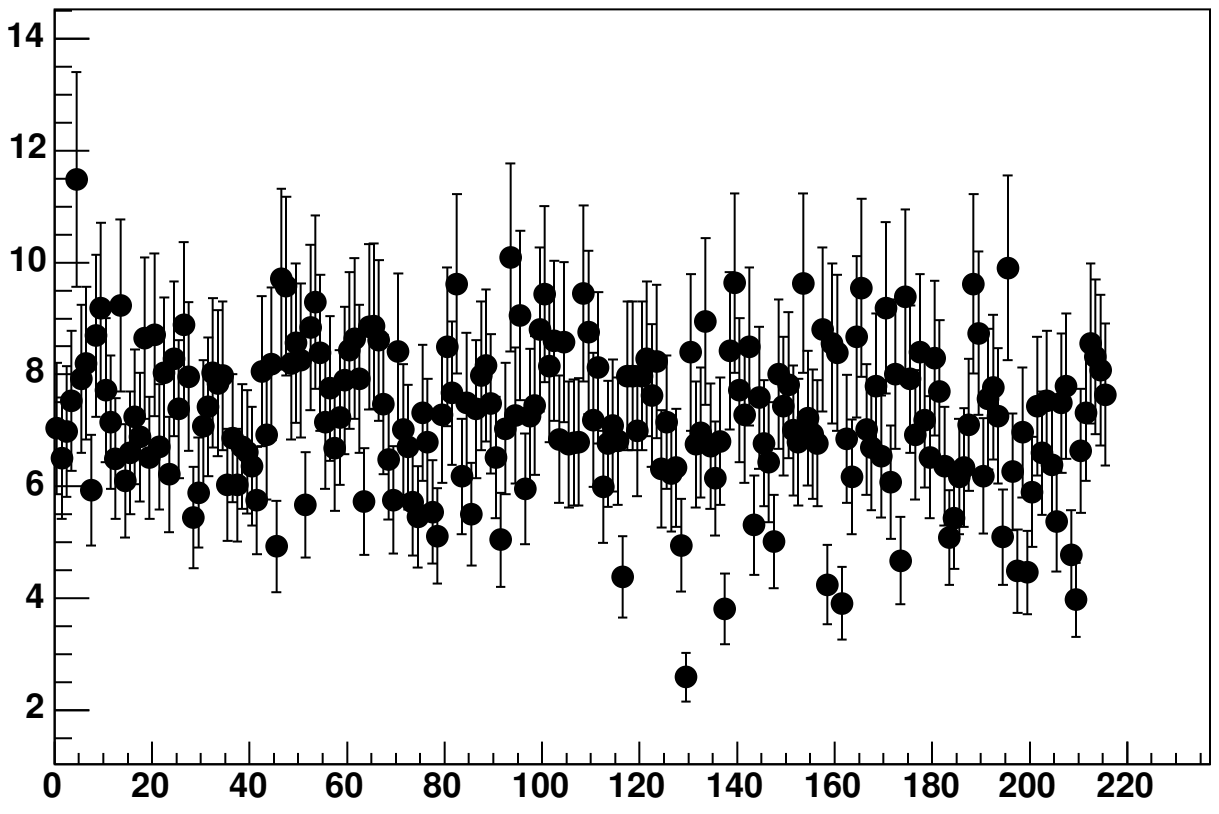
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



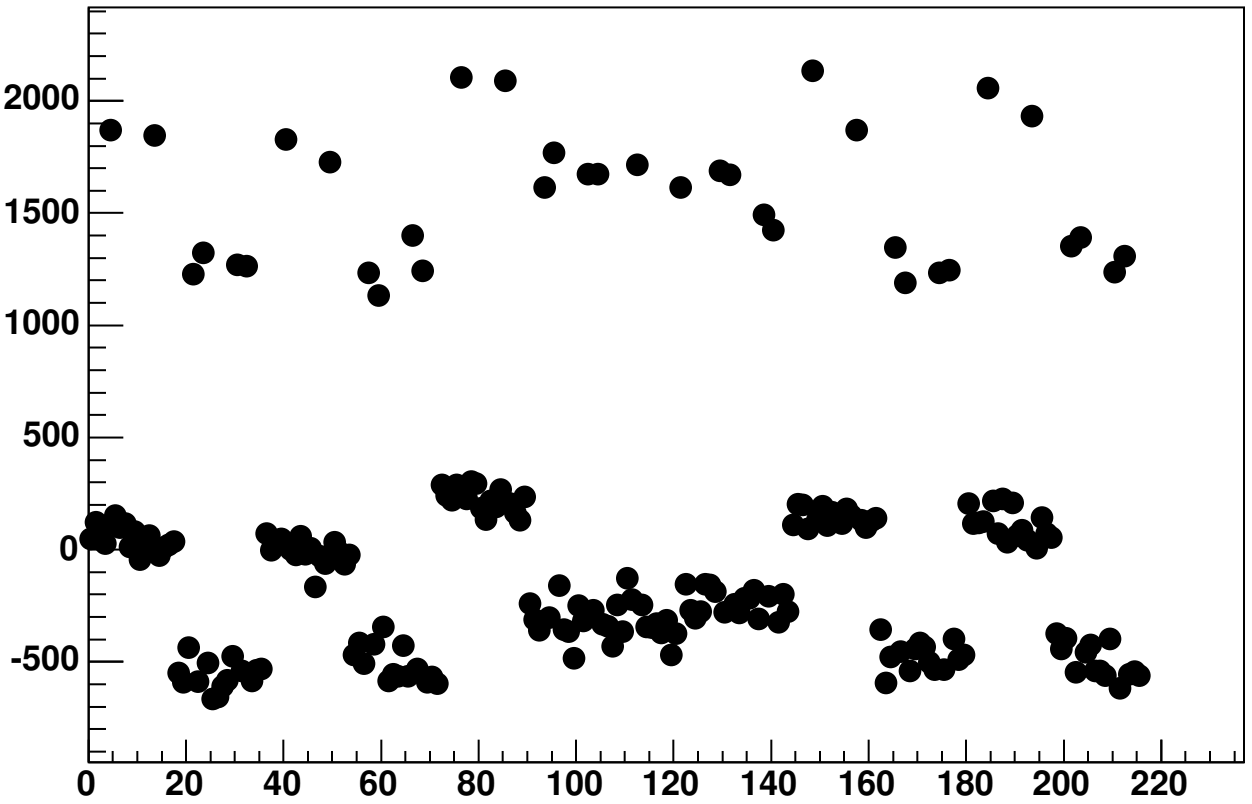
Enable 4, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



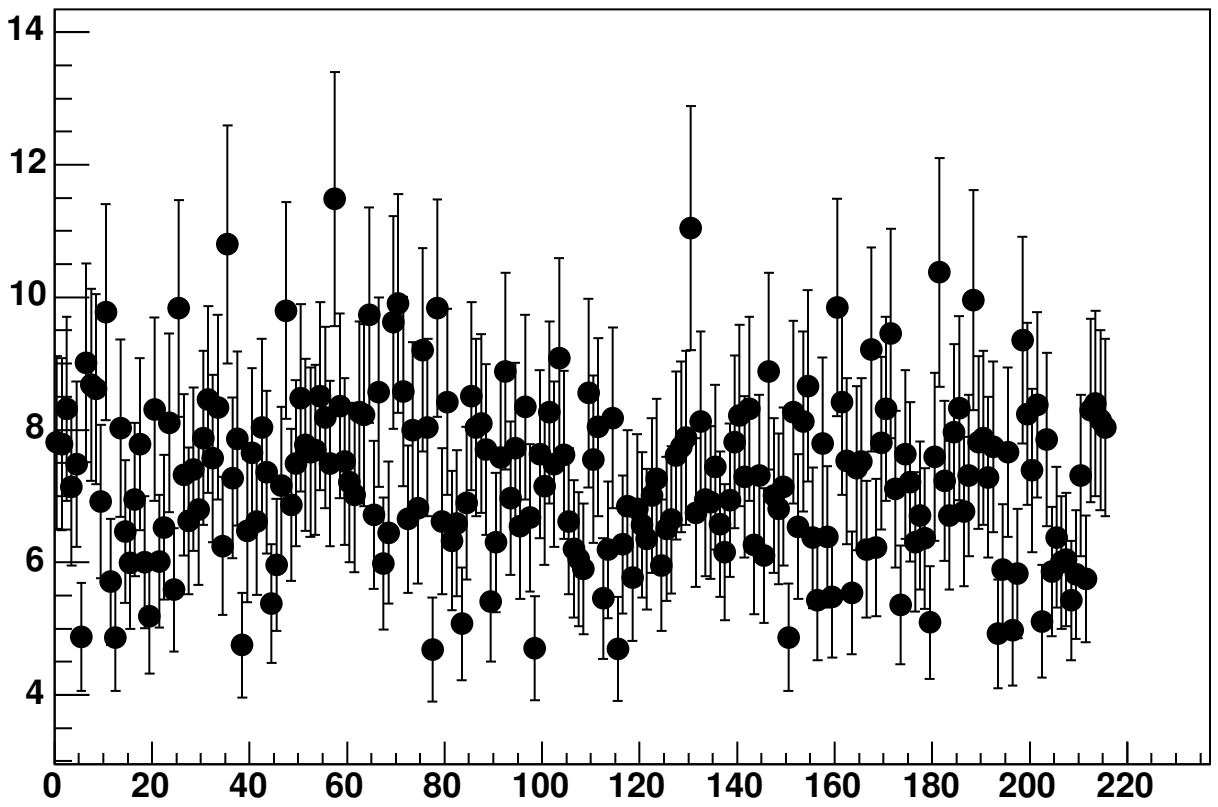
Enable 4, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



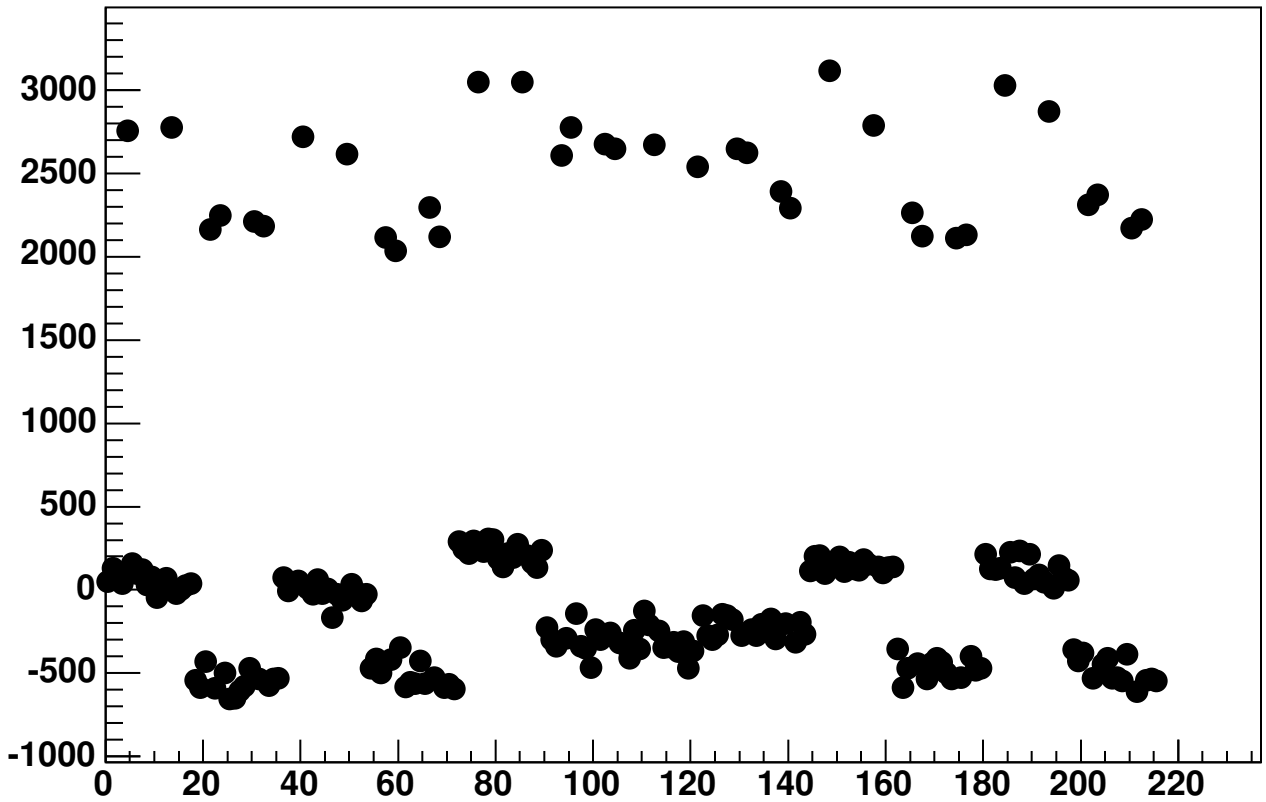
Enable 4, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



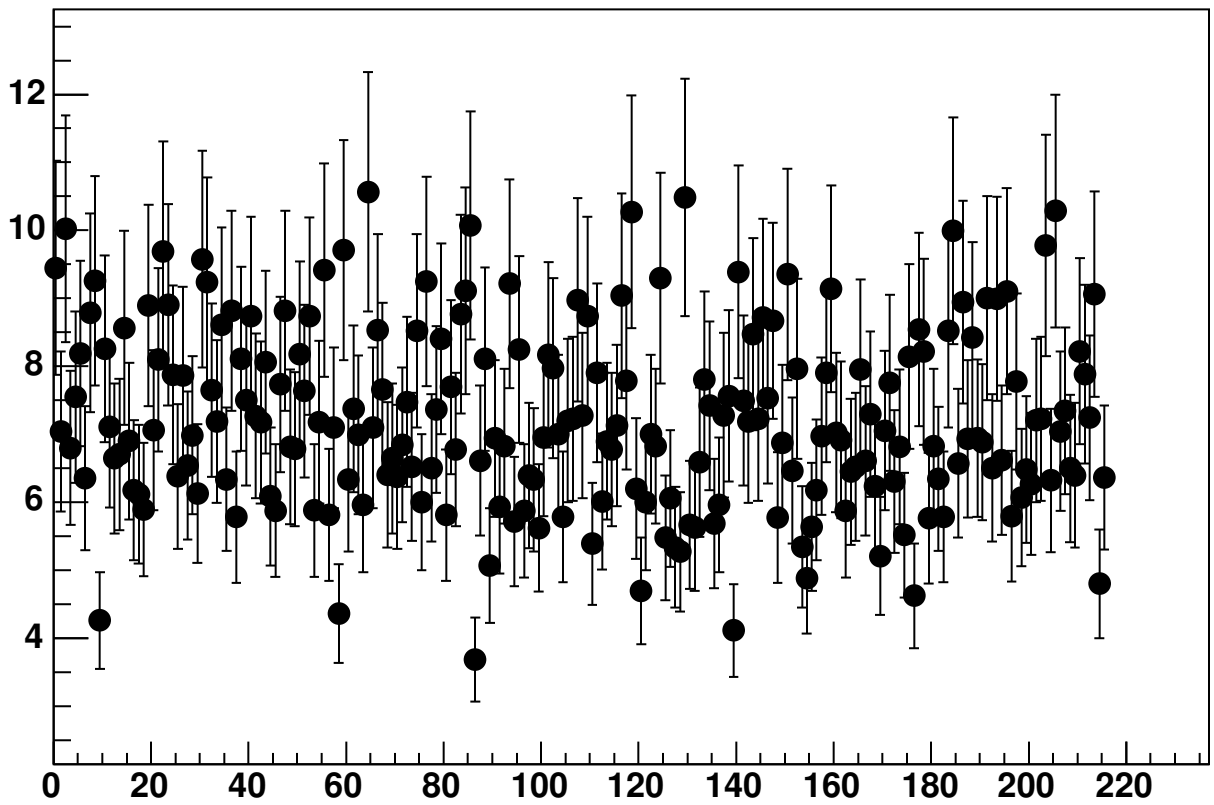
Enable 4, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



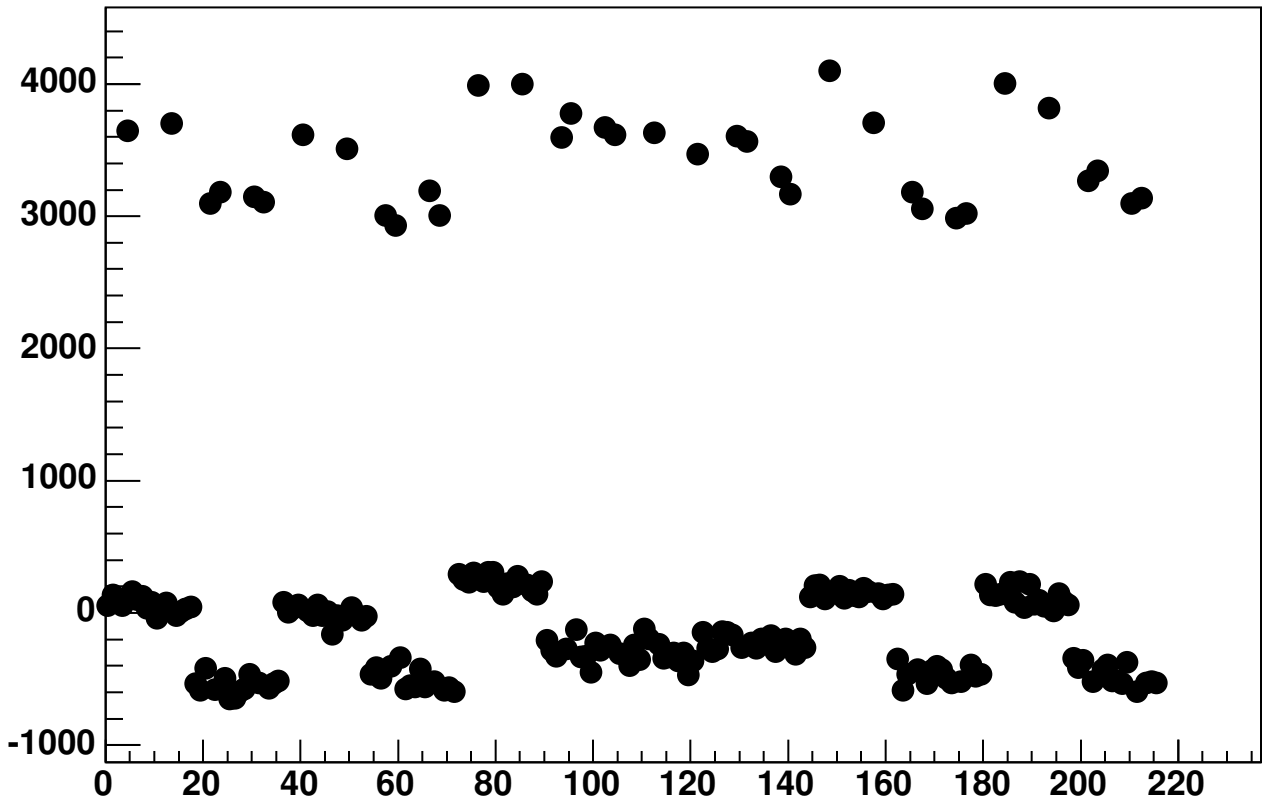
Enable 4, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



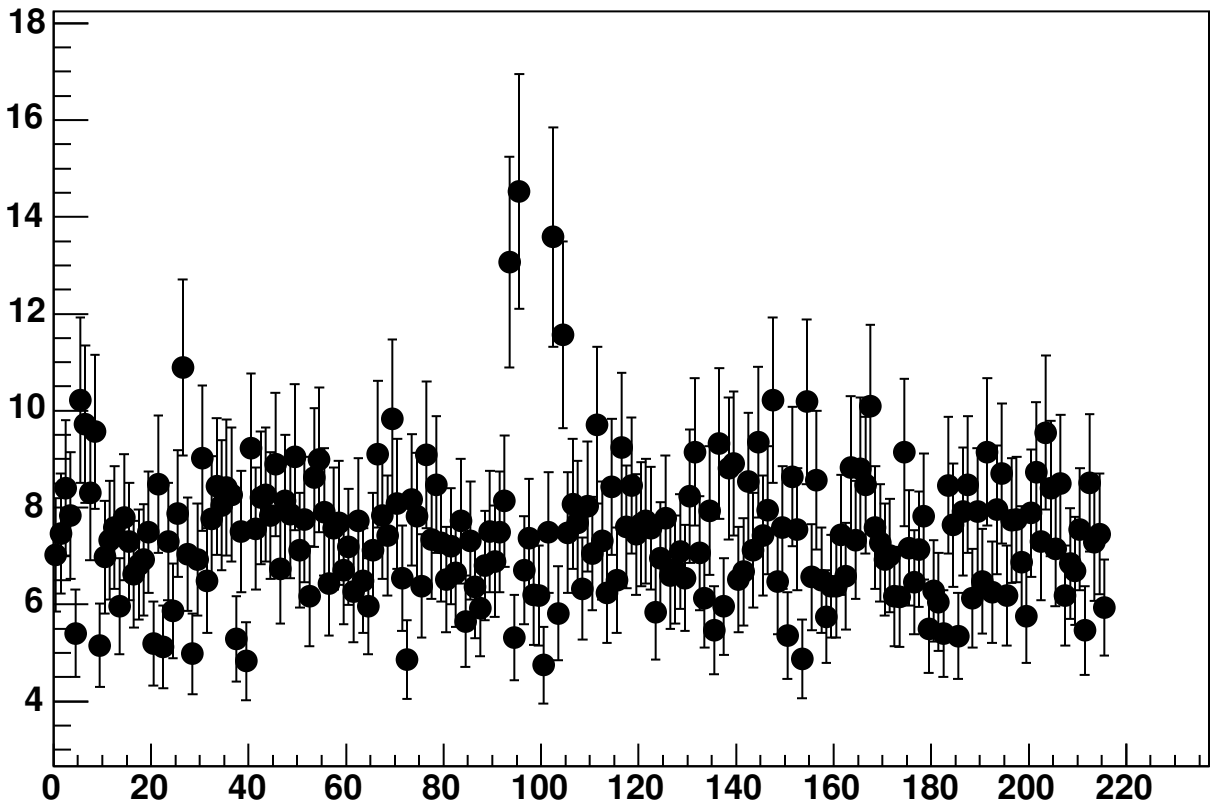
Enable 4, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



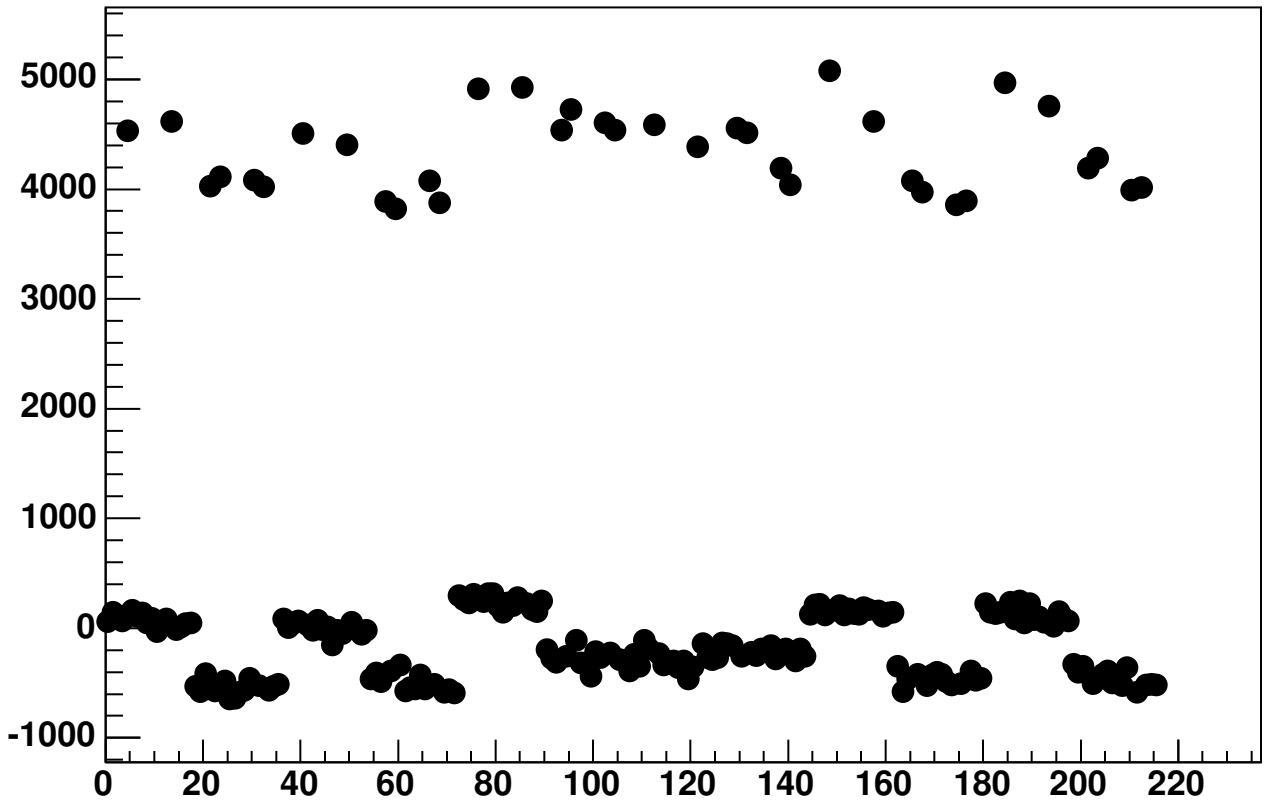
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



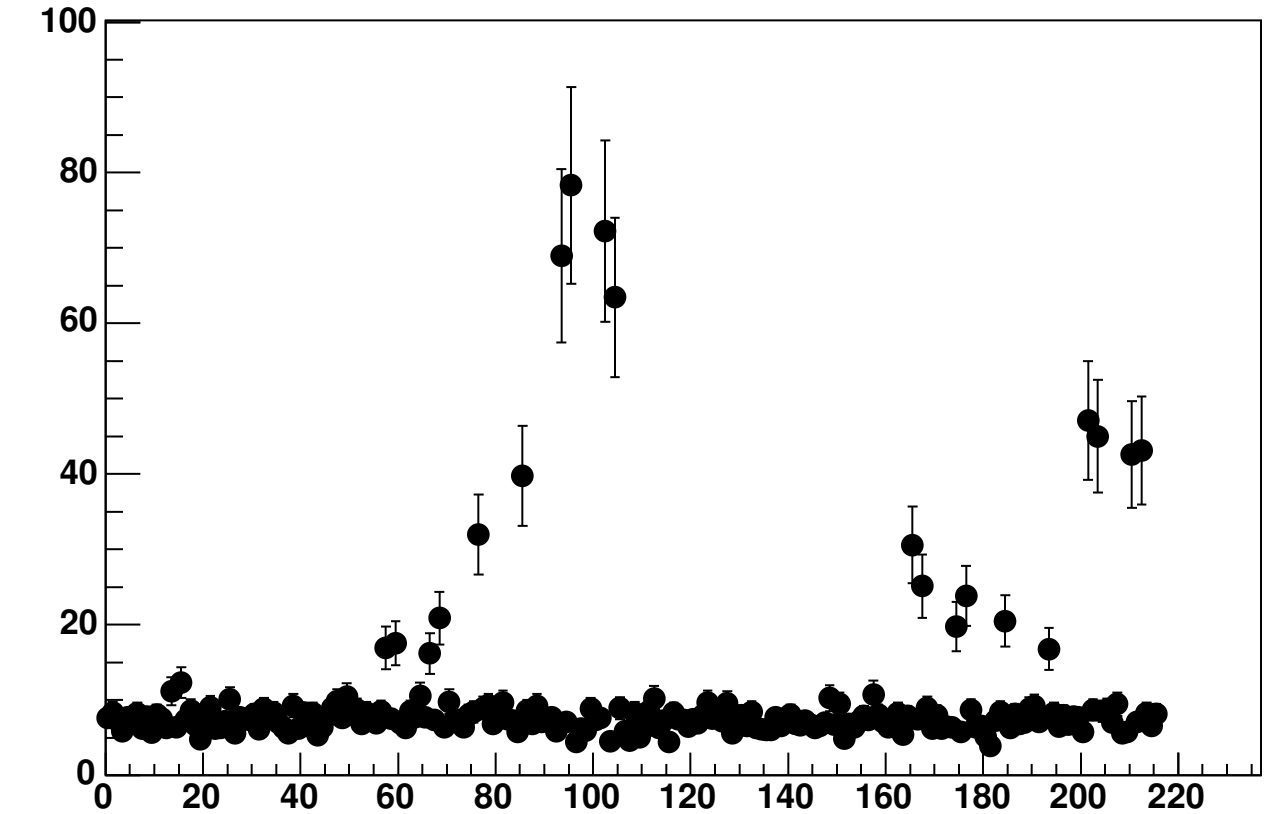
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



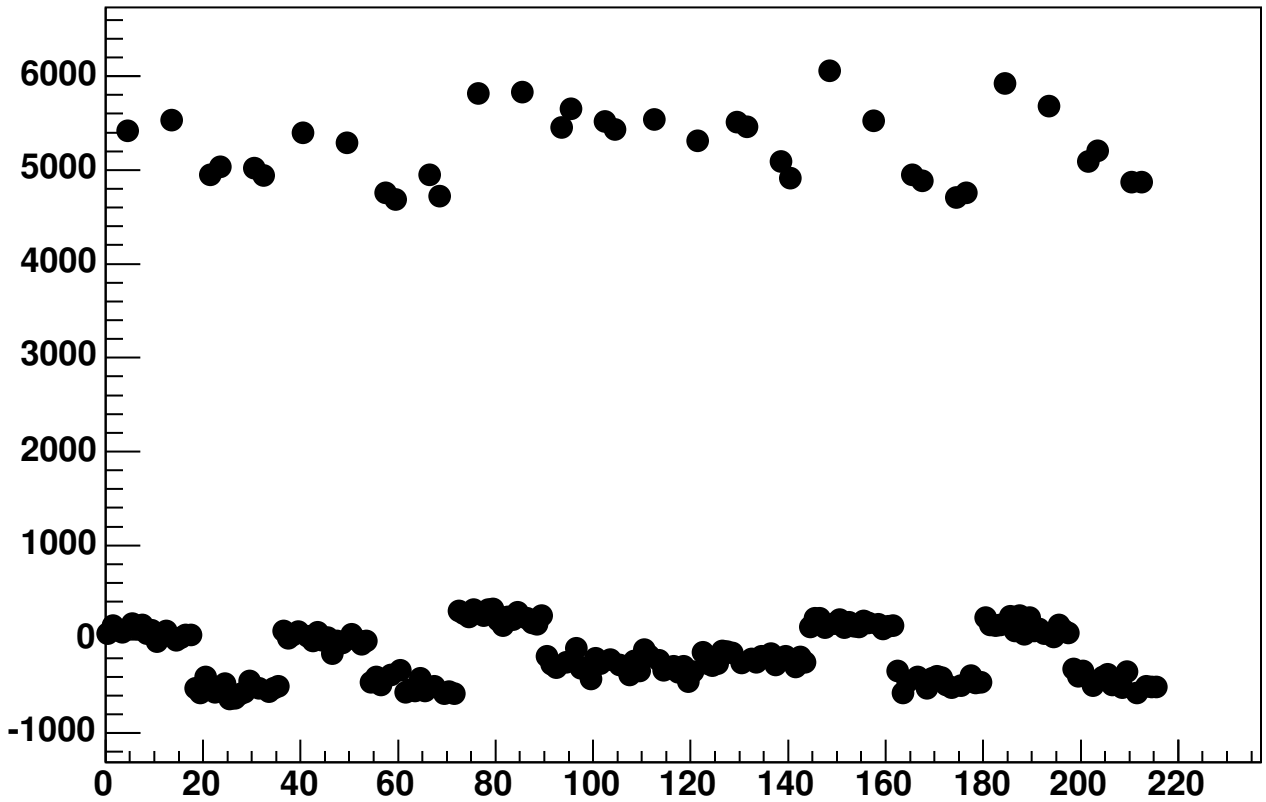
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



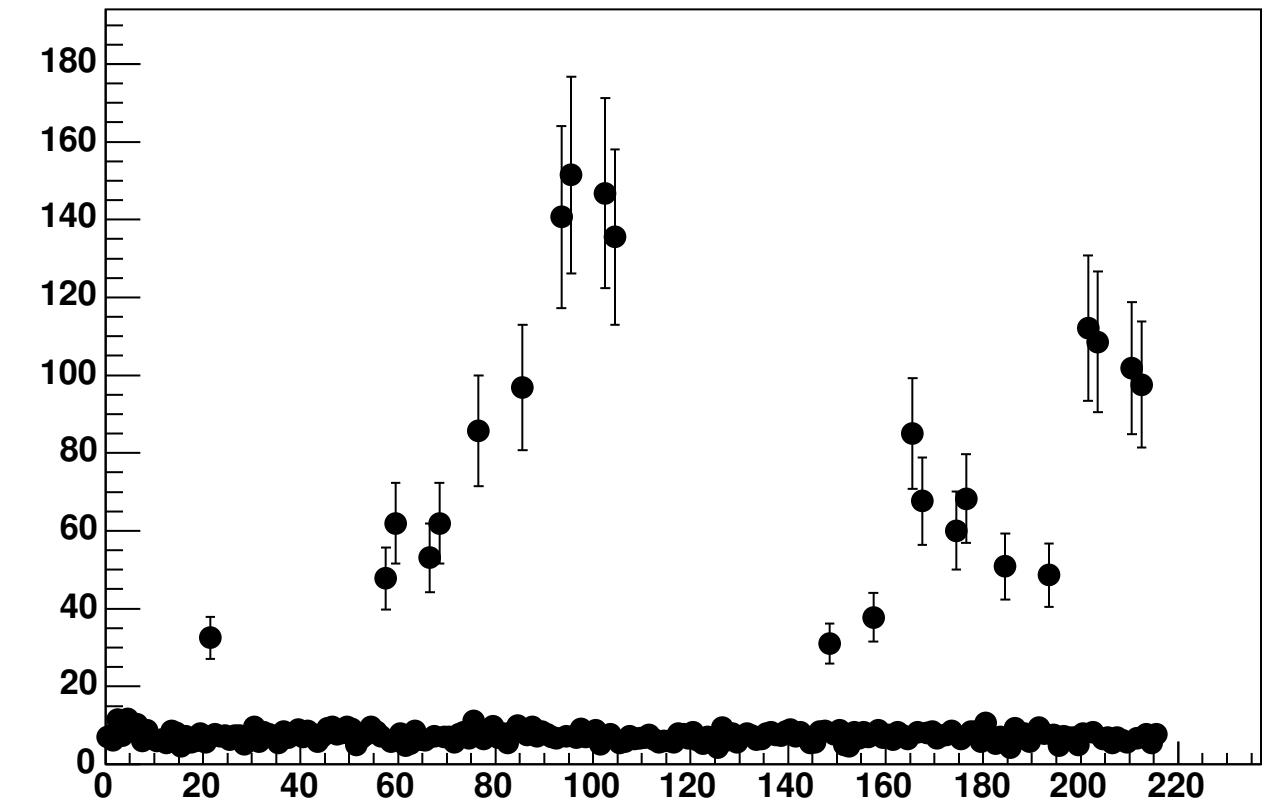
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



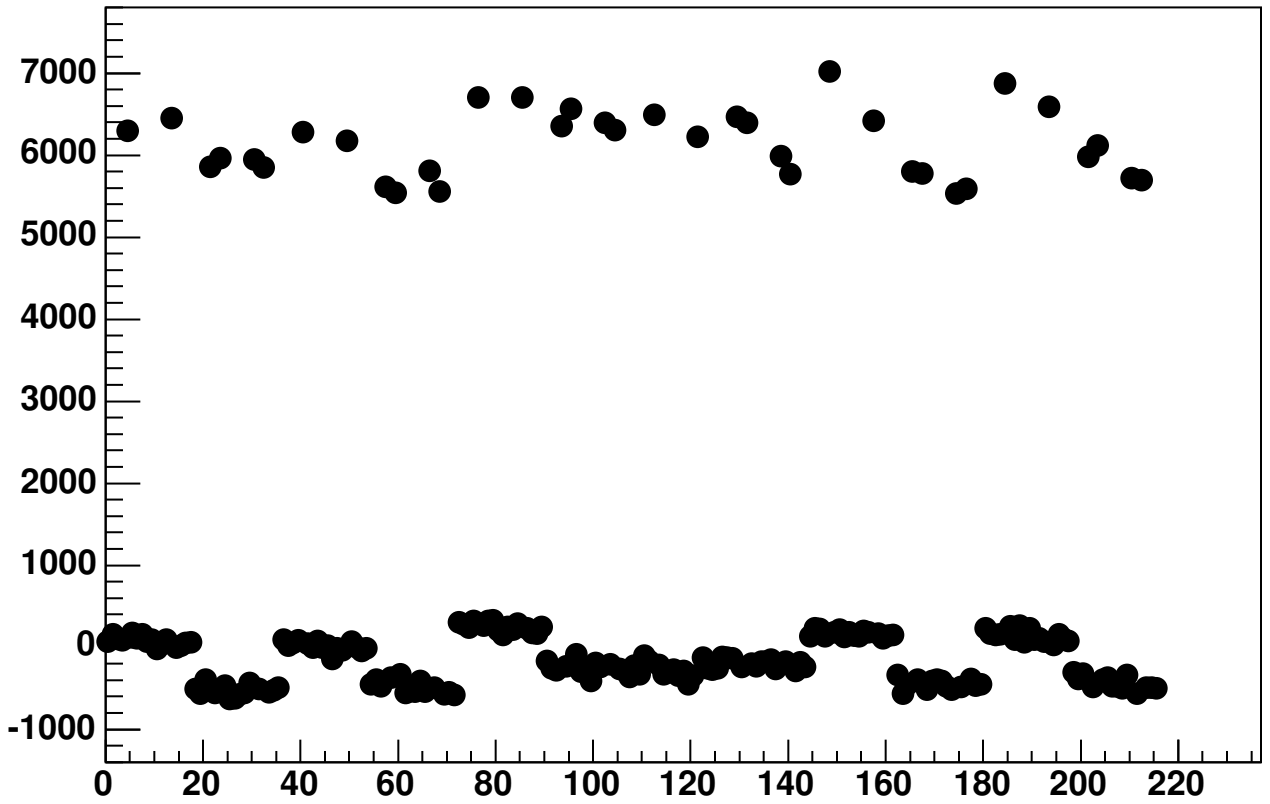
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



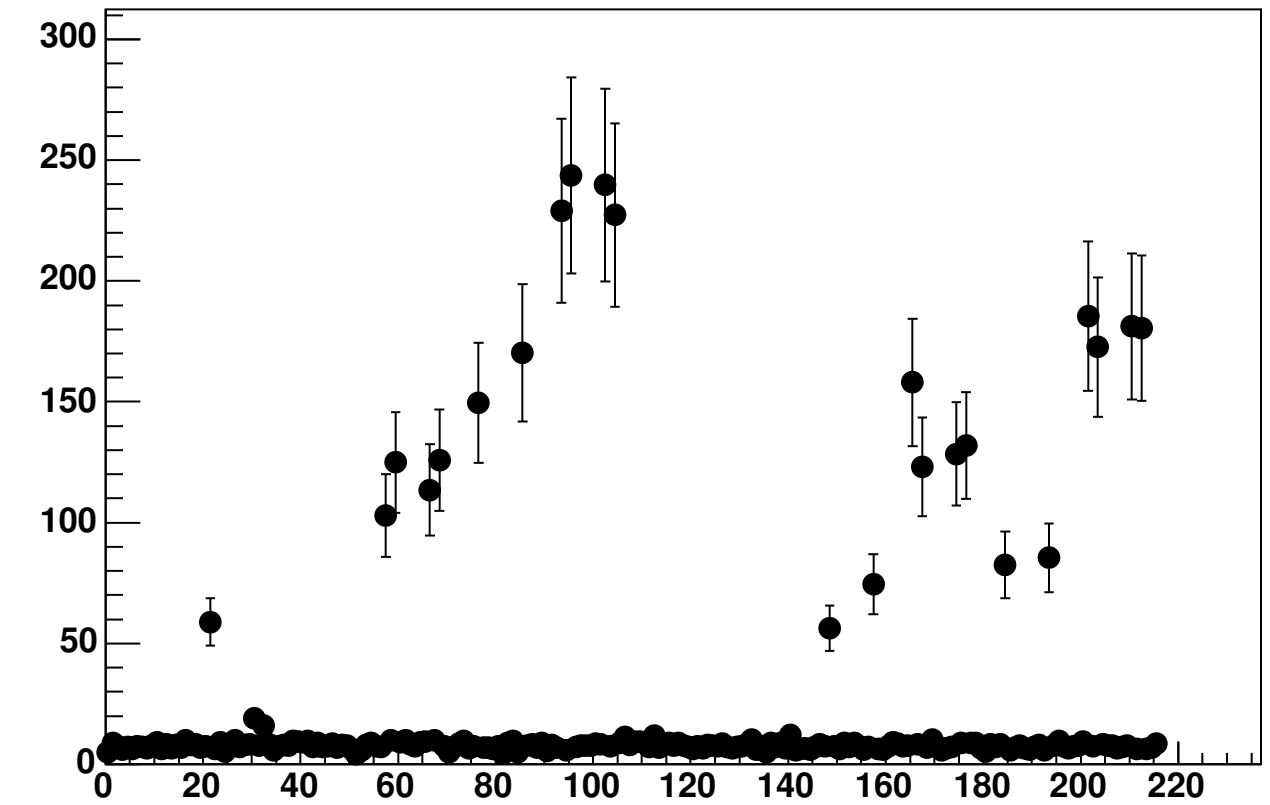
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



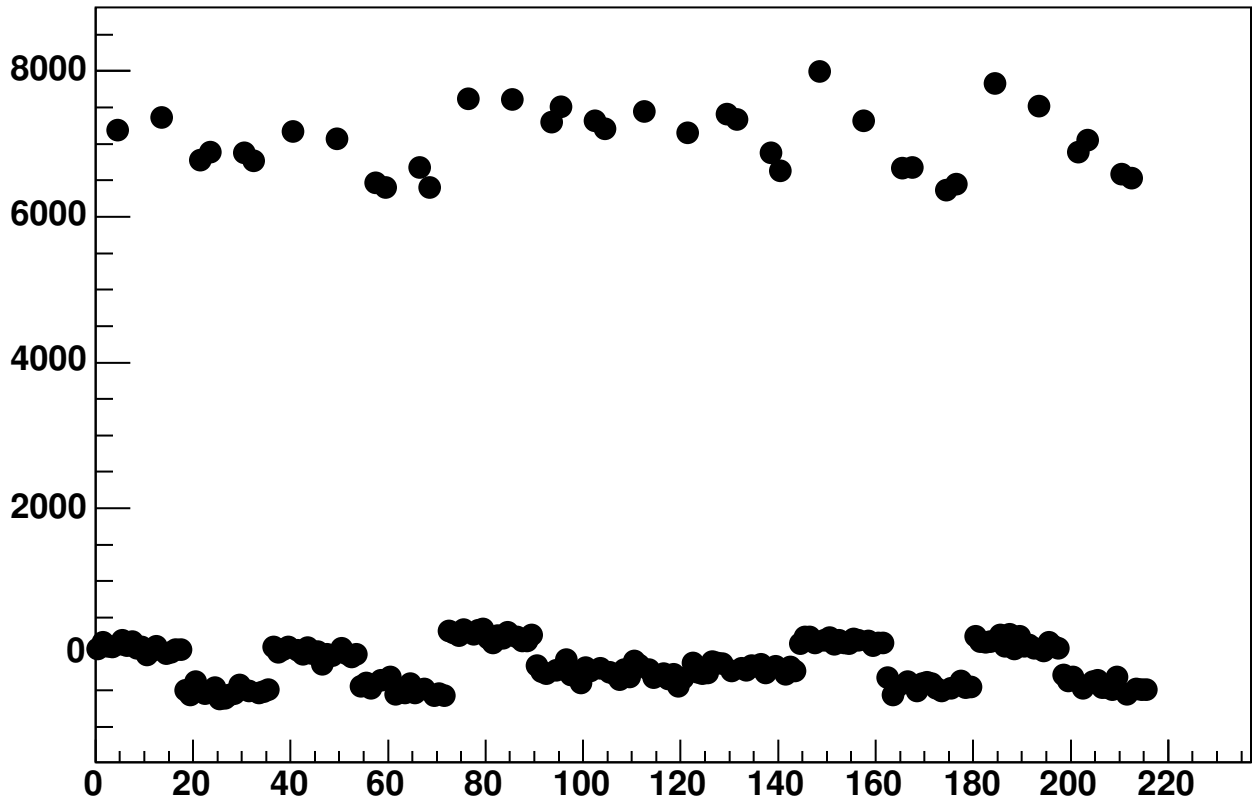
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



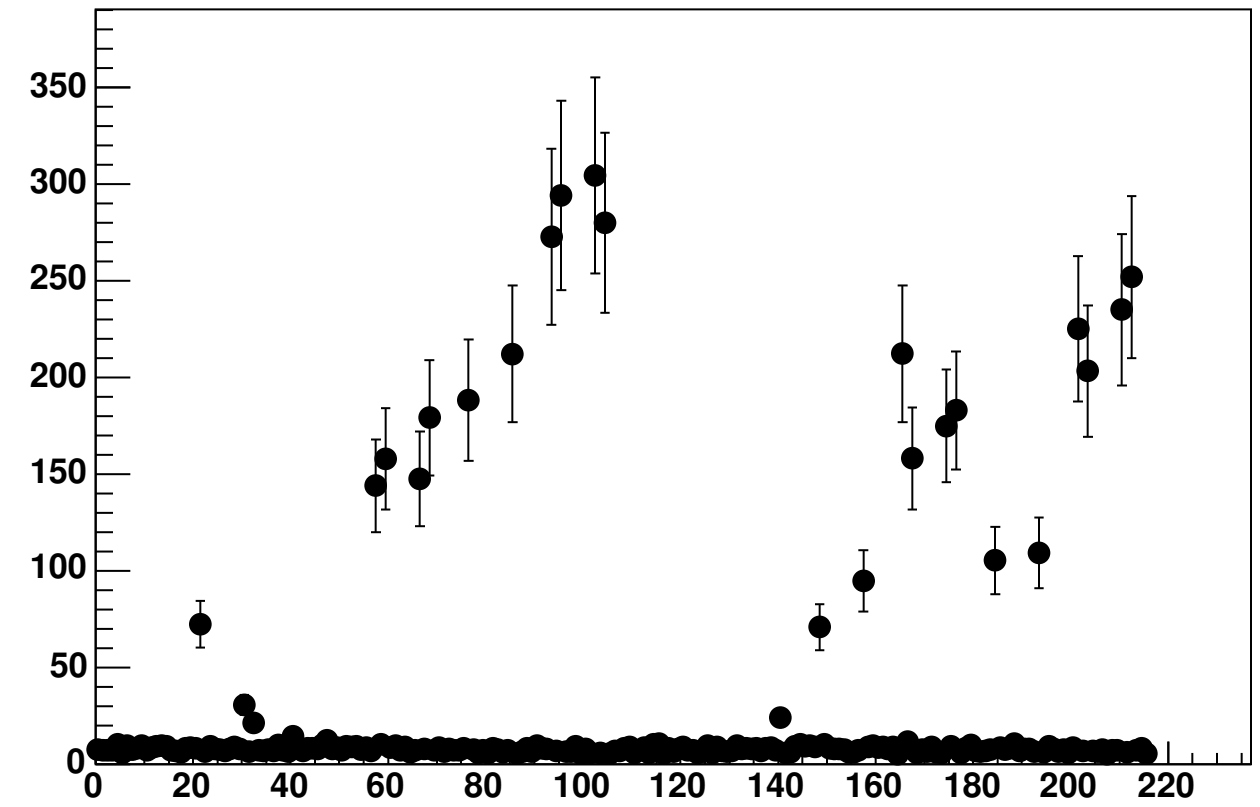
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



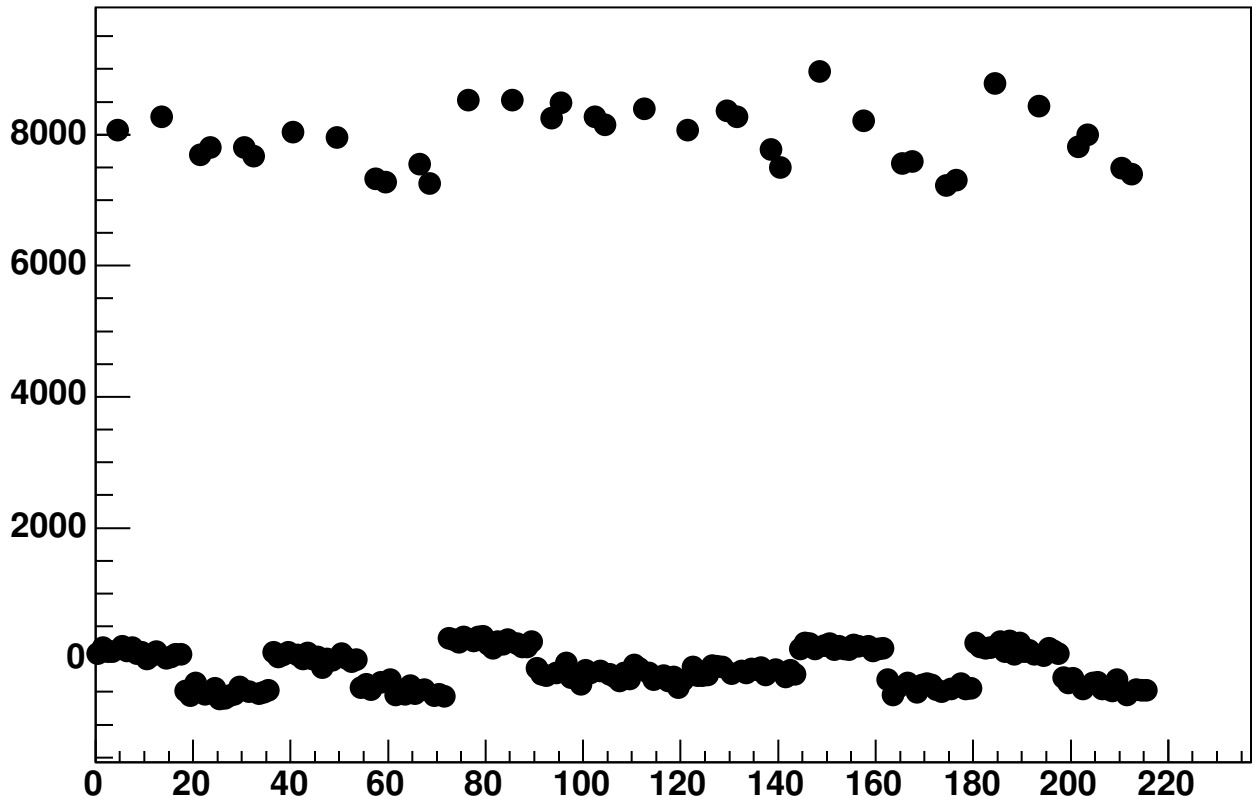
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



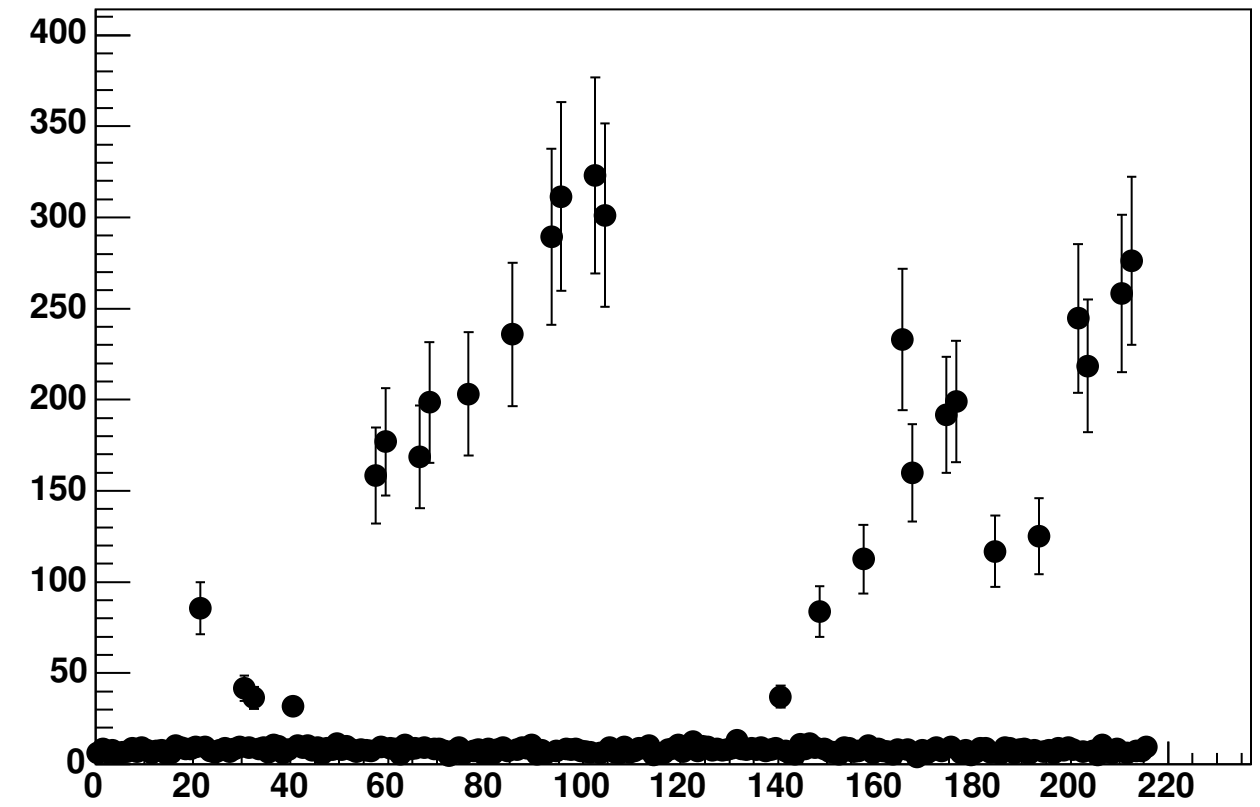
Enable 4, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



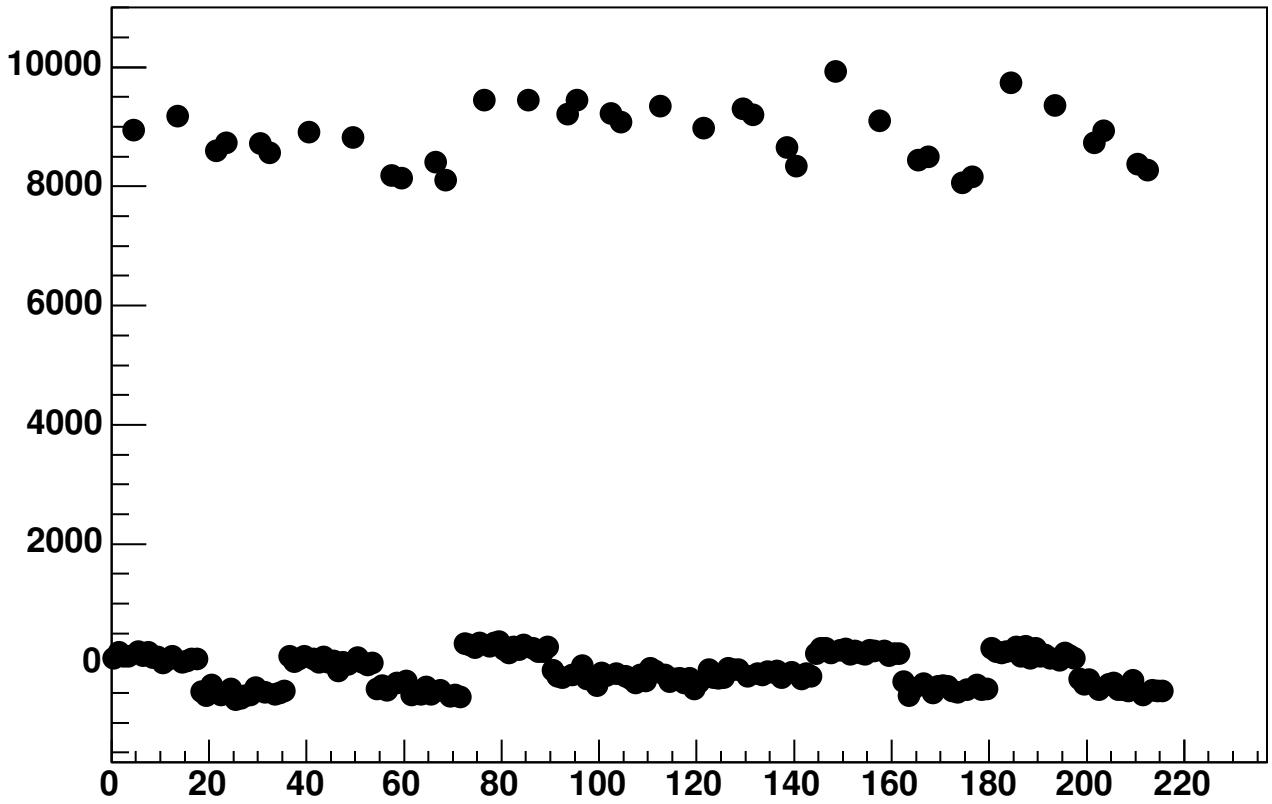
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



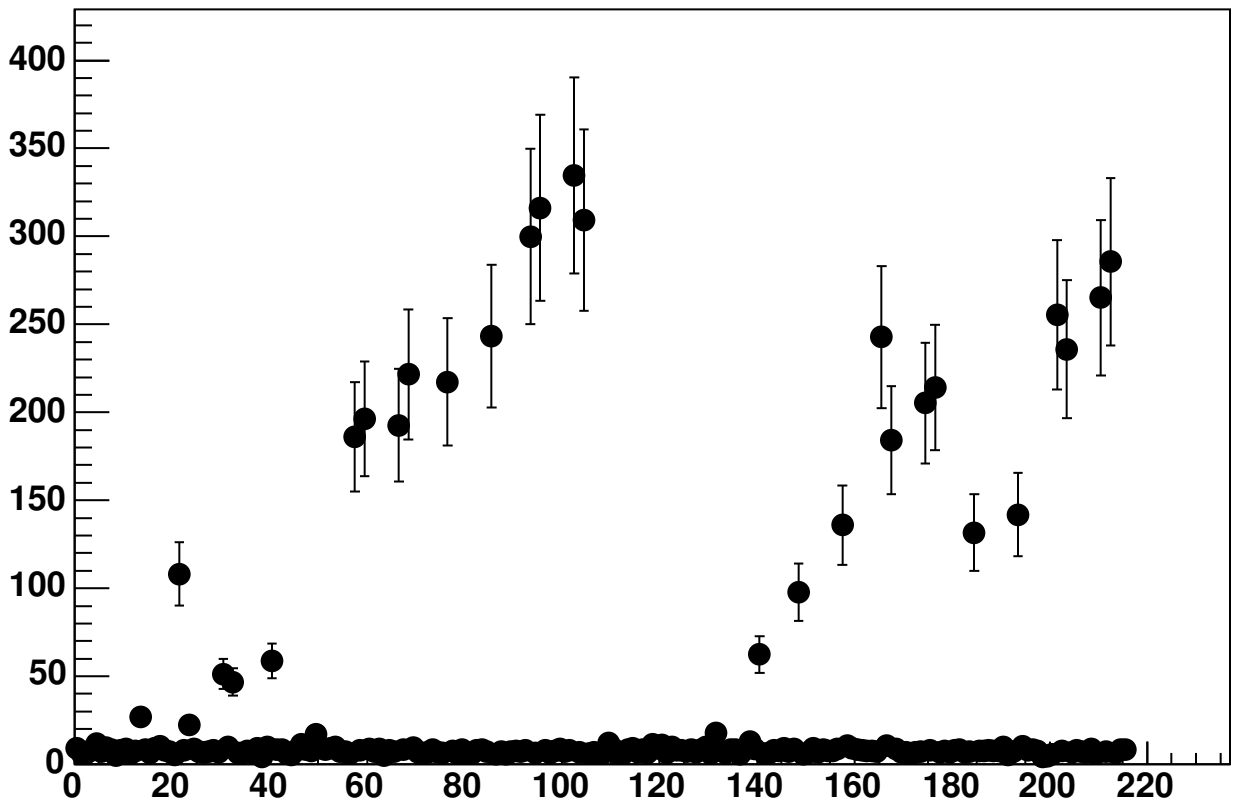
Enable 4, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



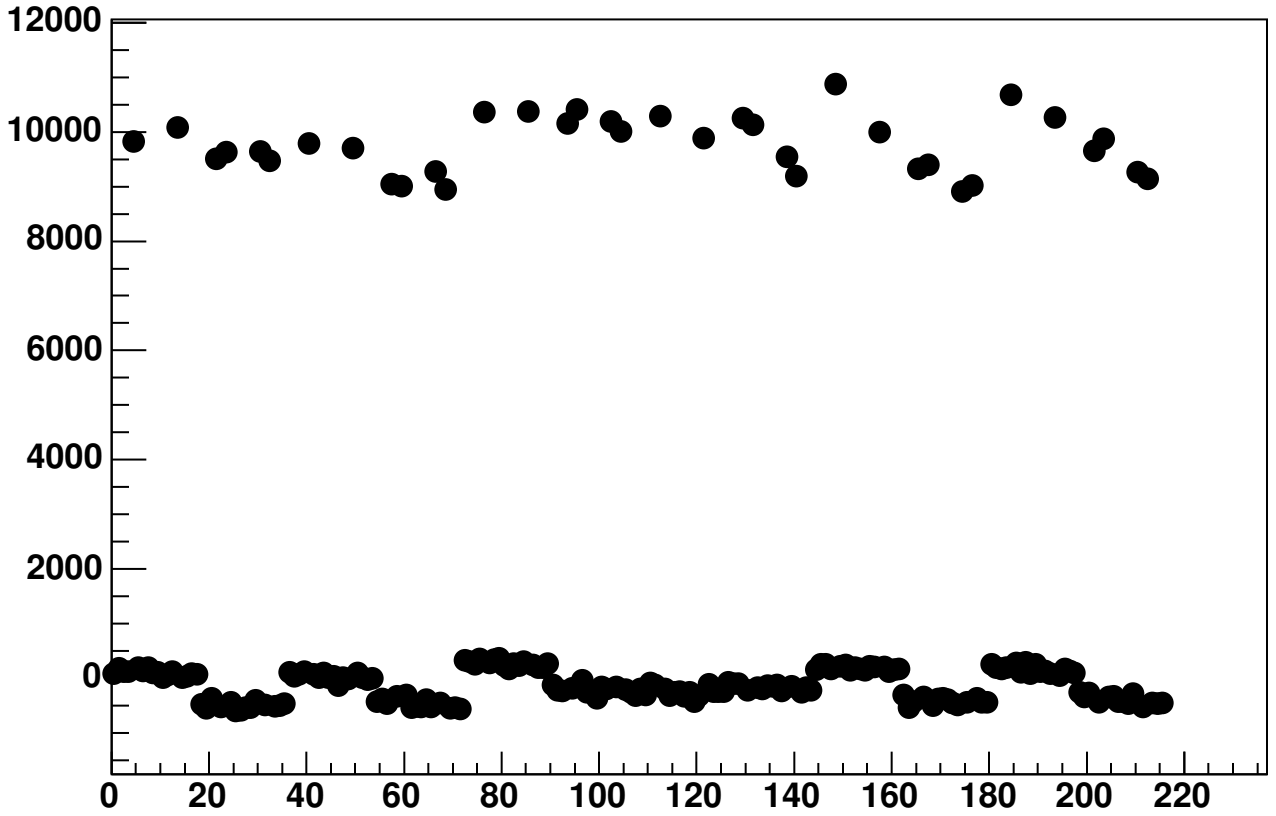
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



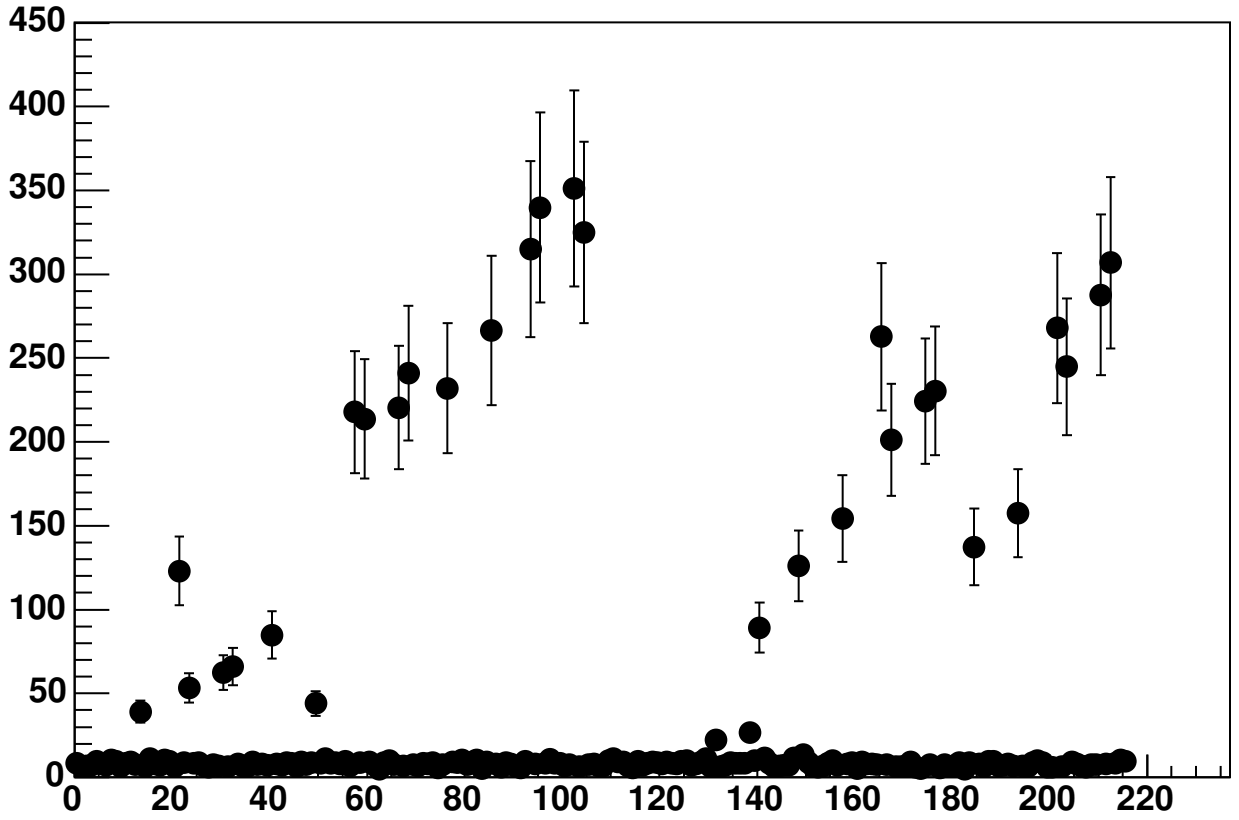
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



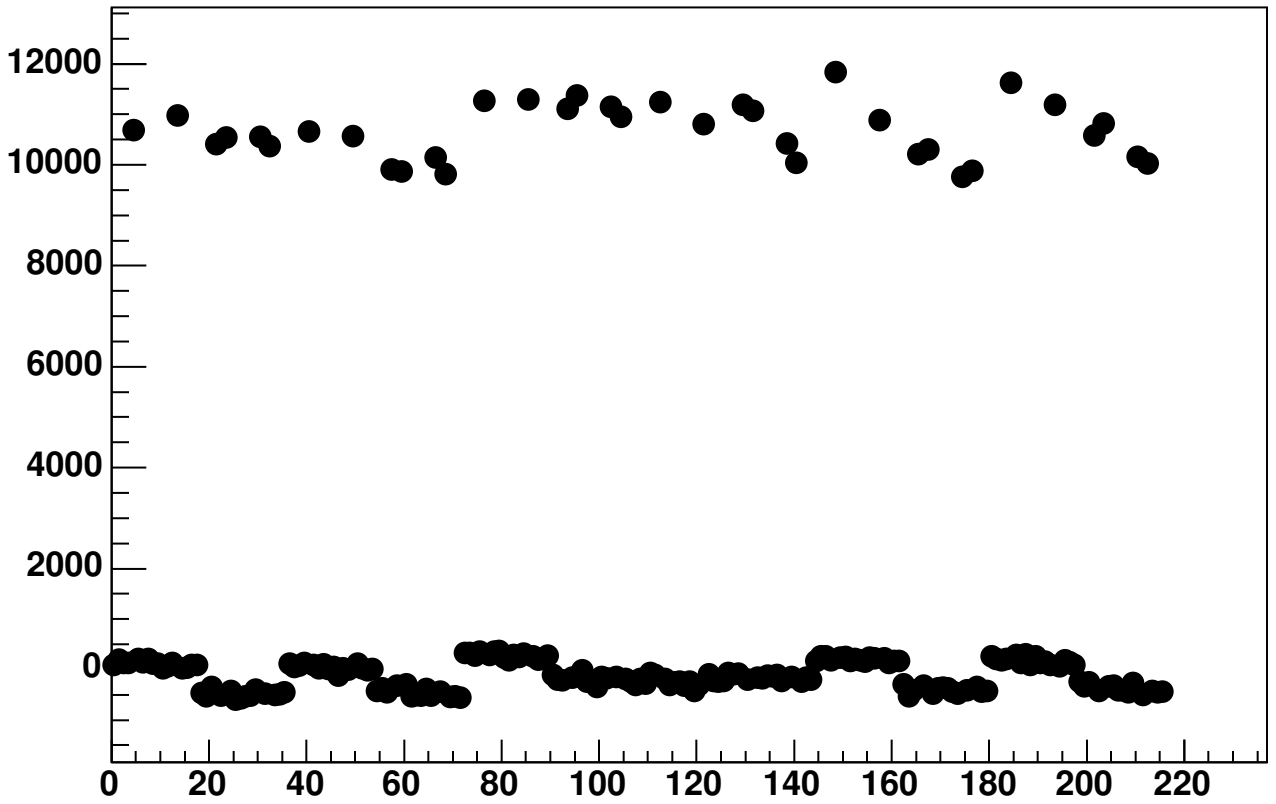
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



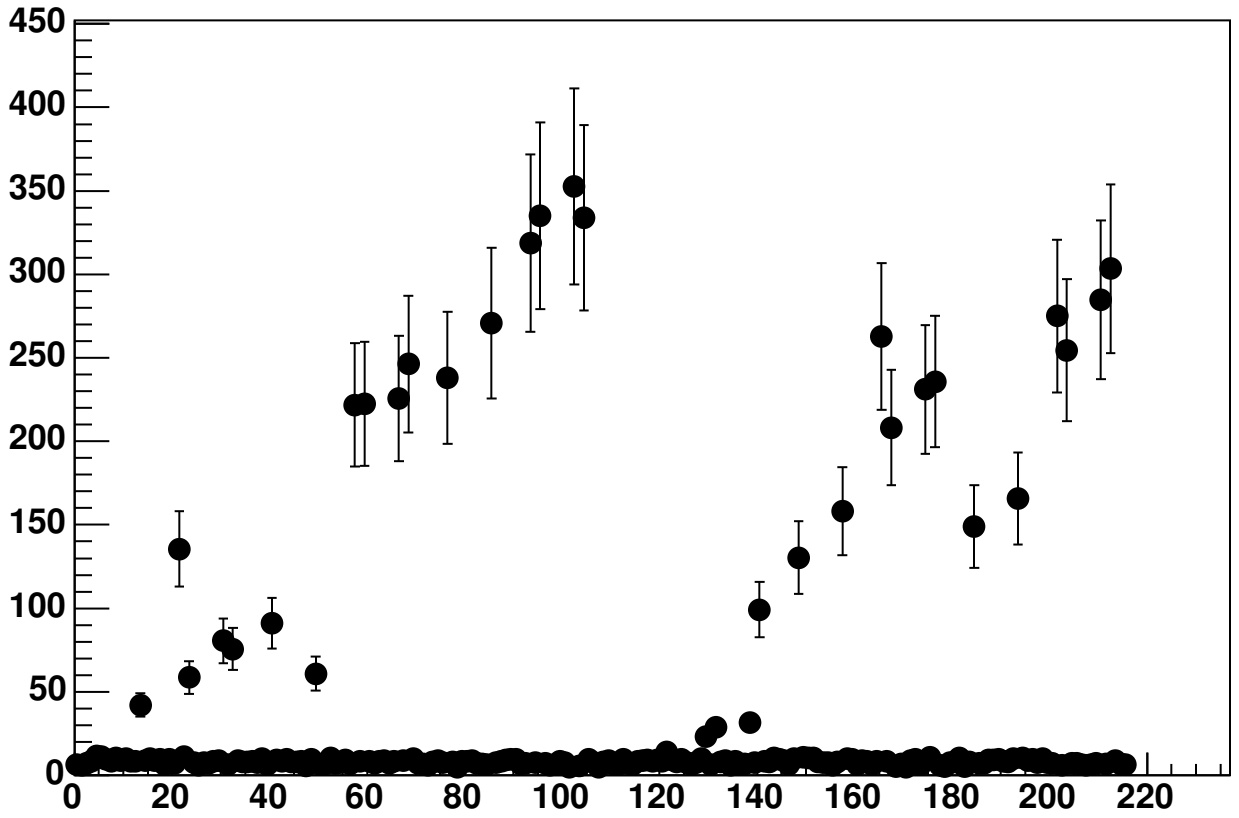
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



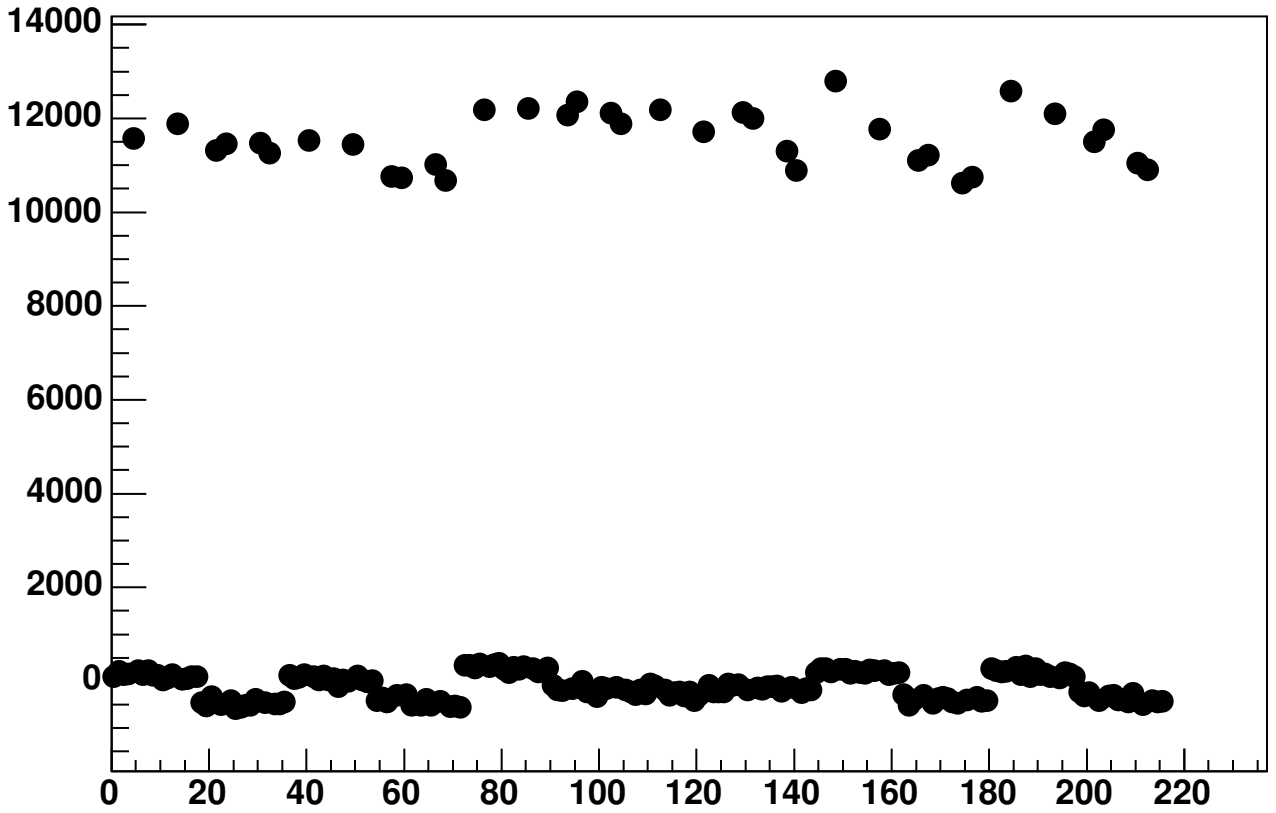
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



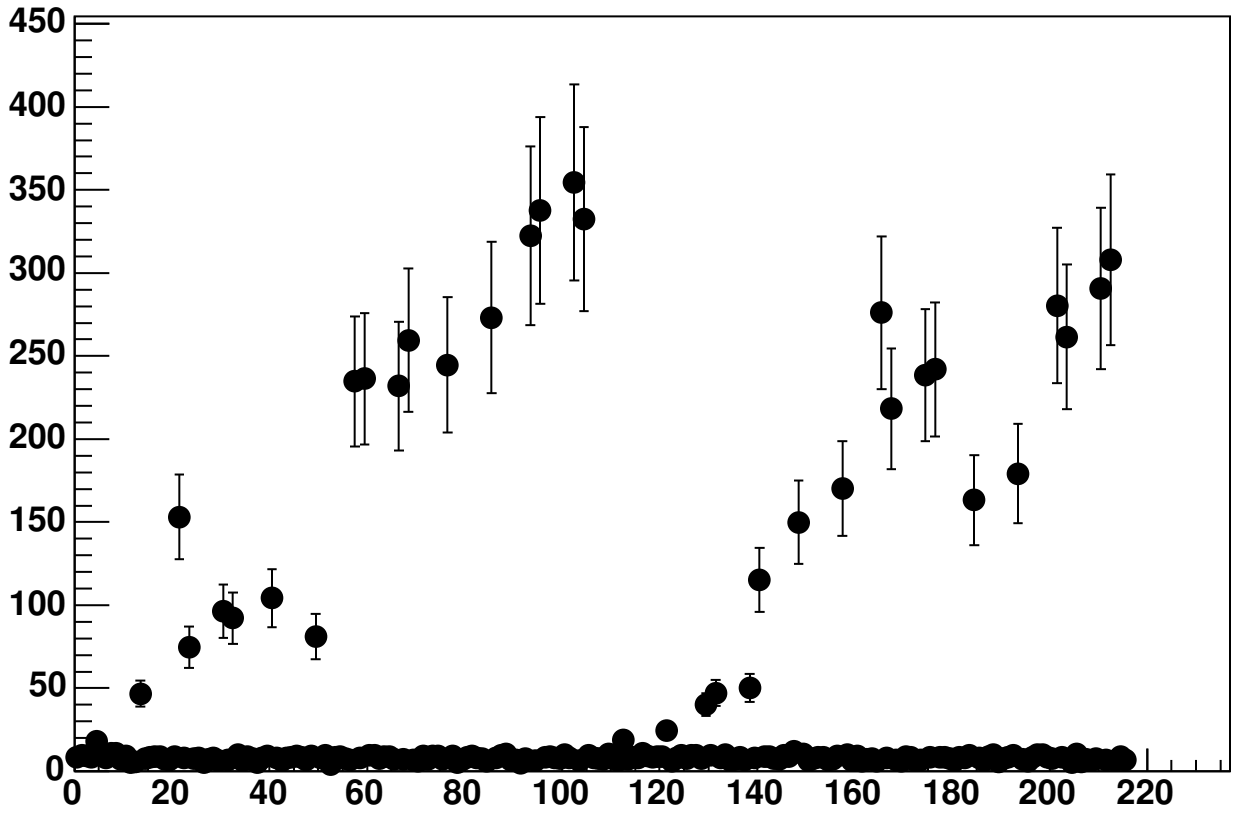
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



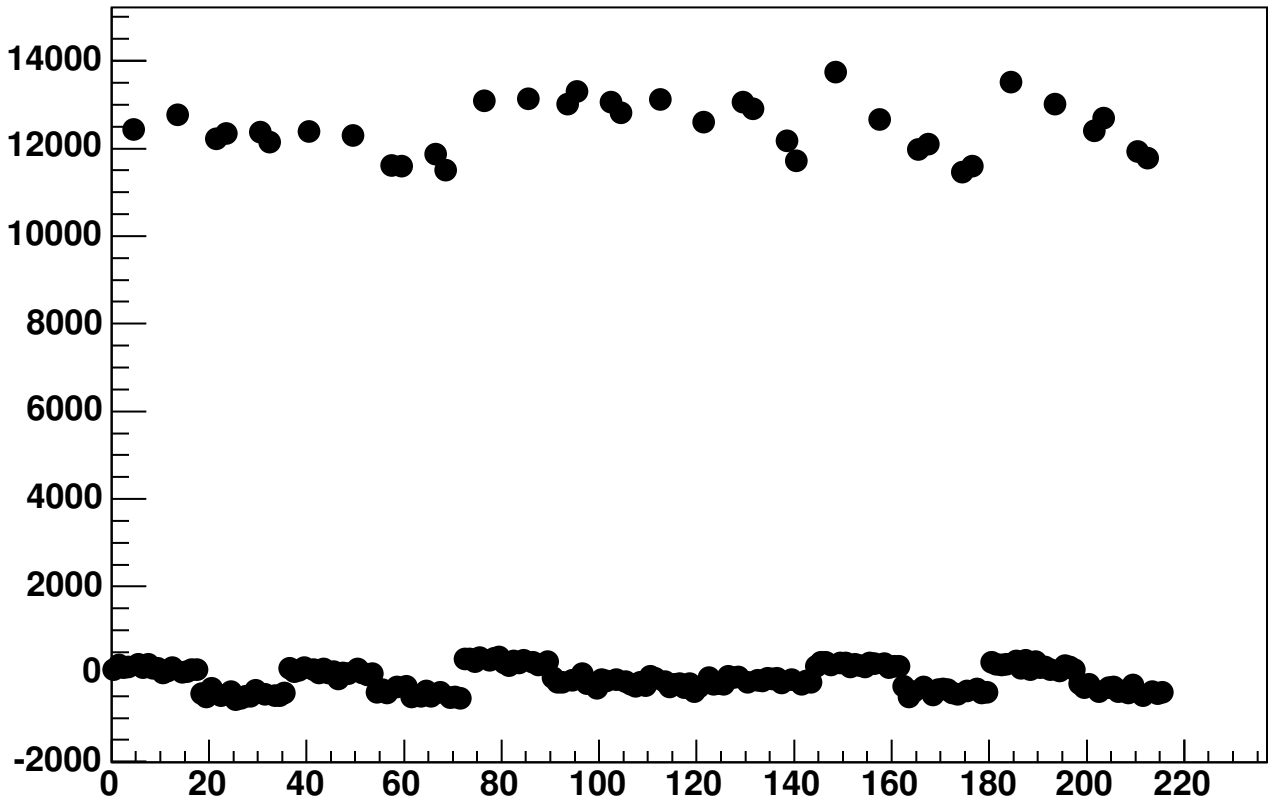
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



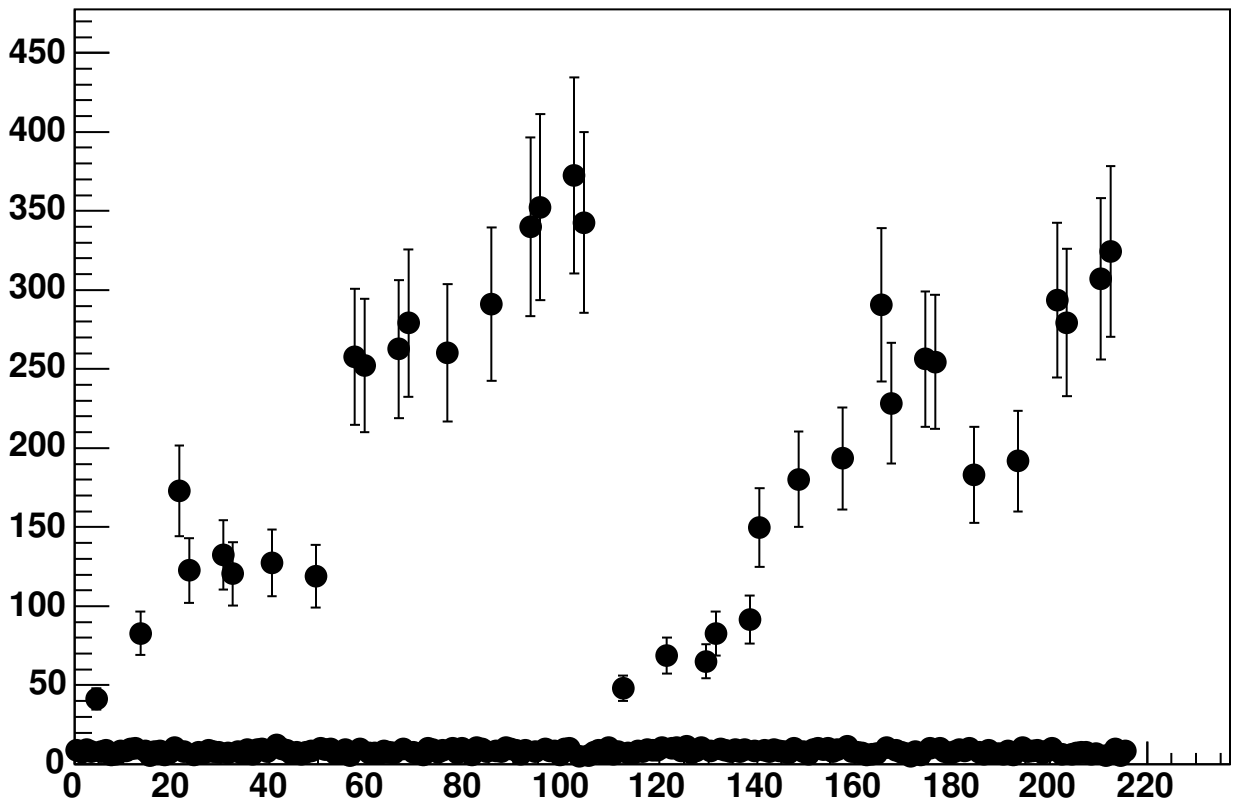
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



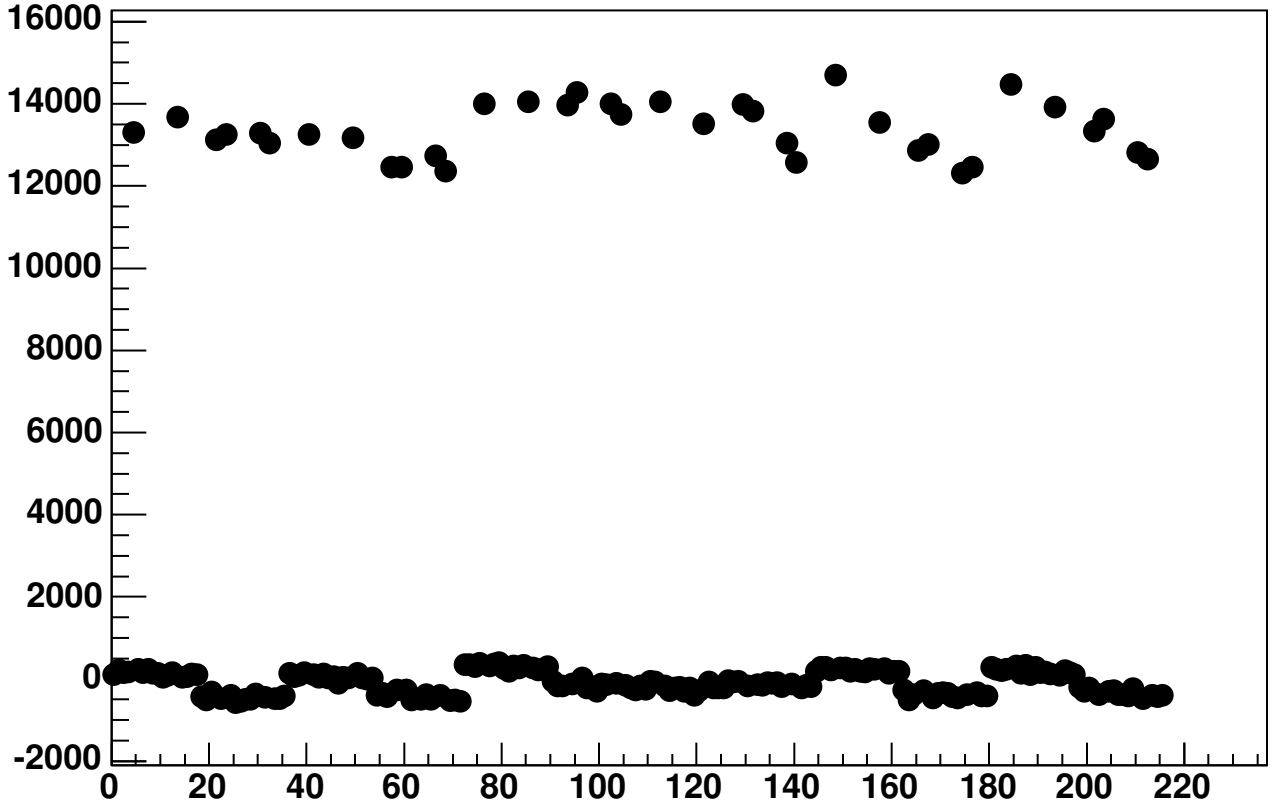
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



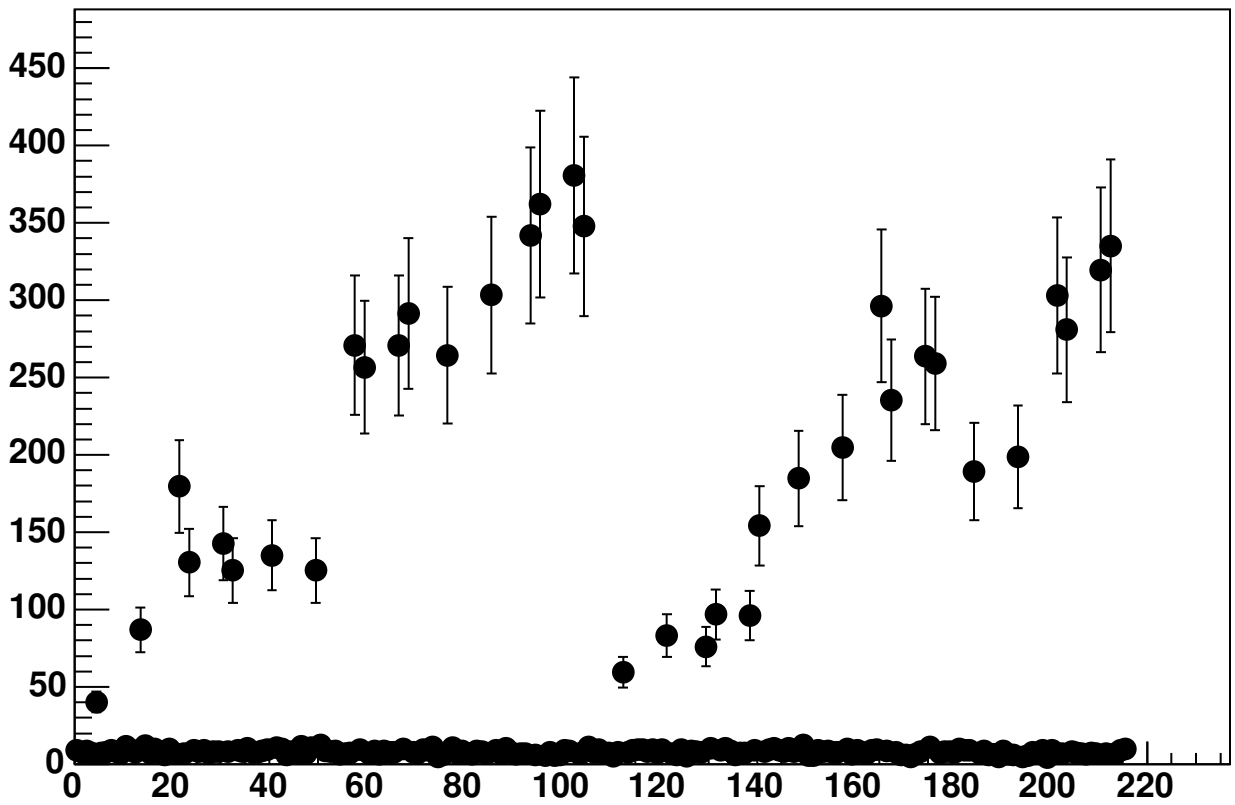
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



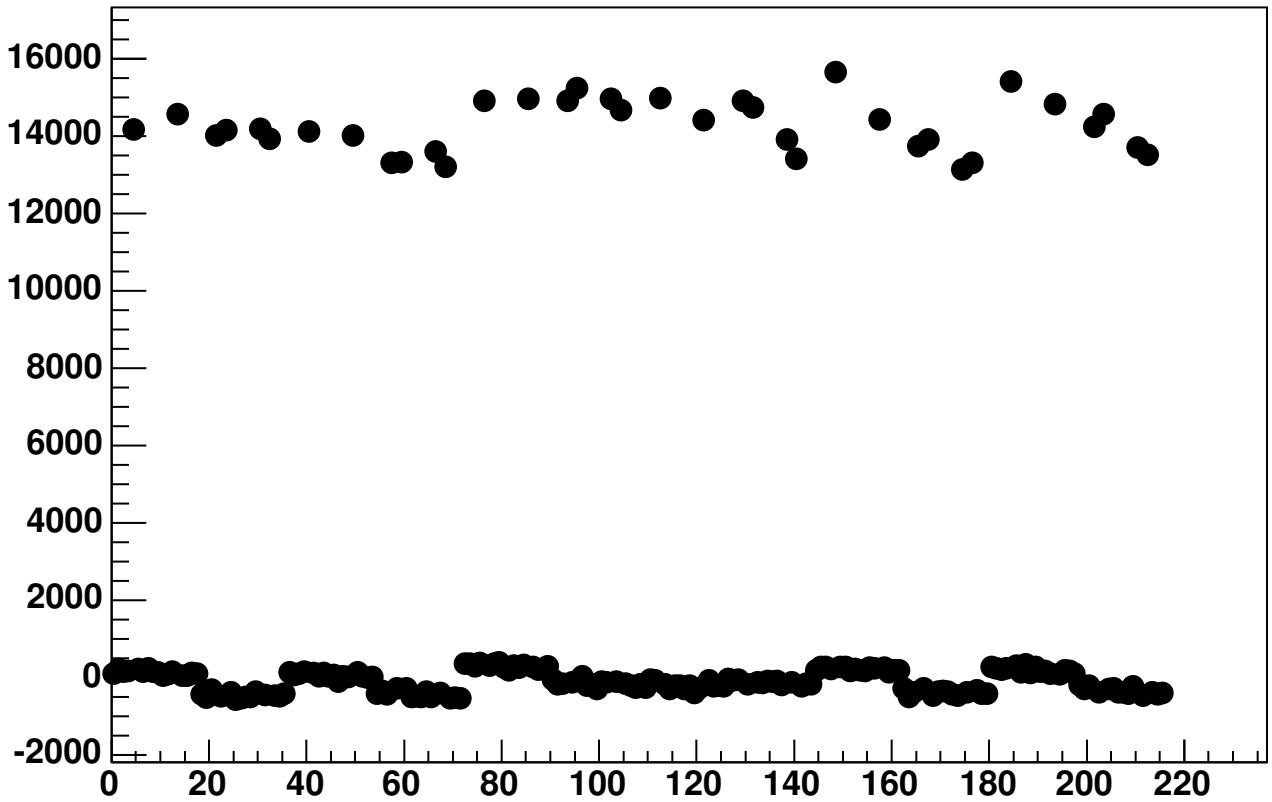
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



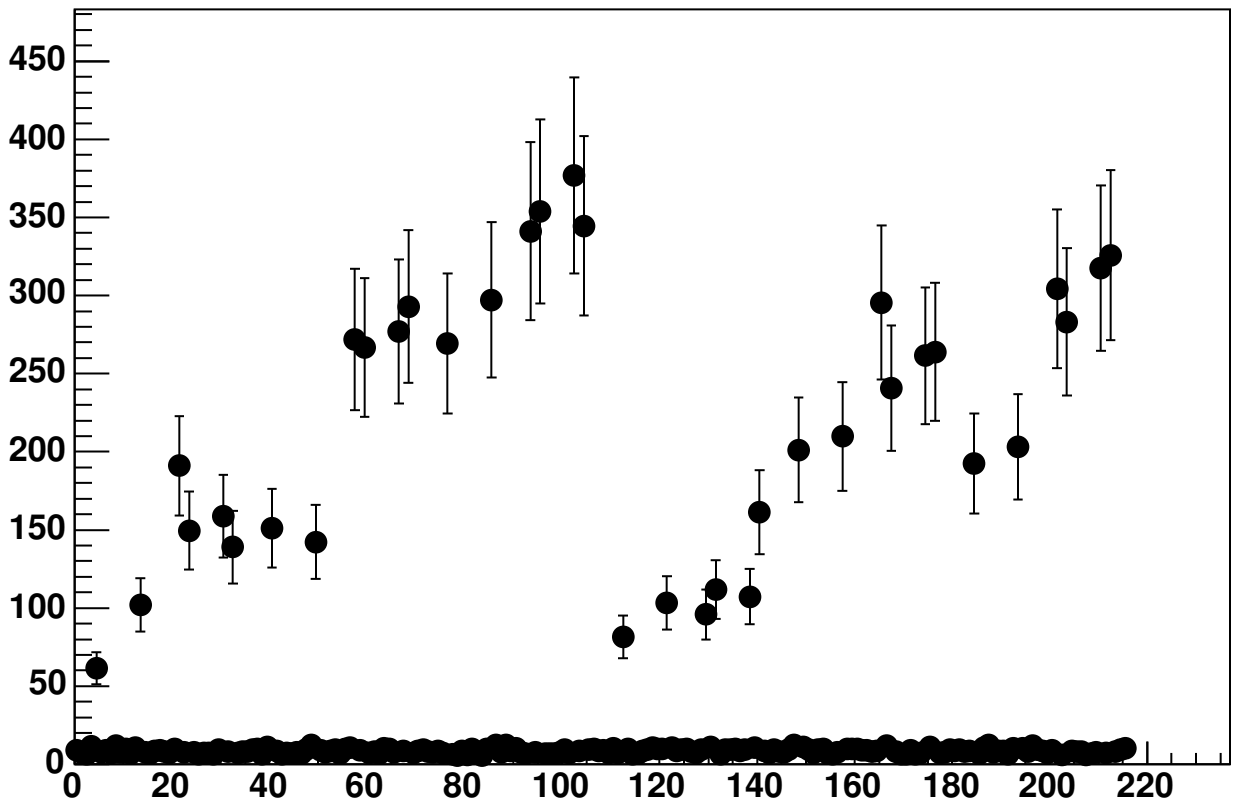
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



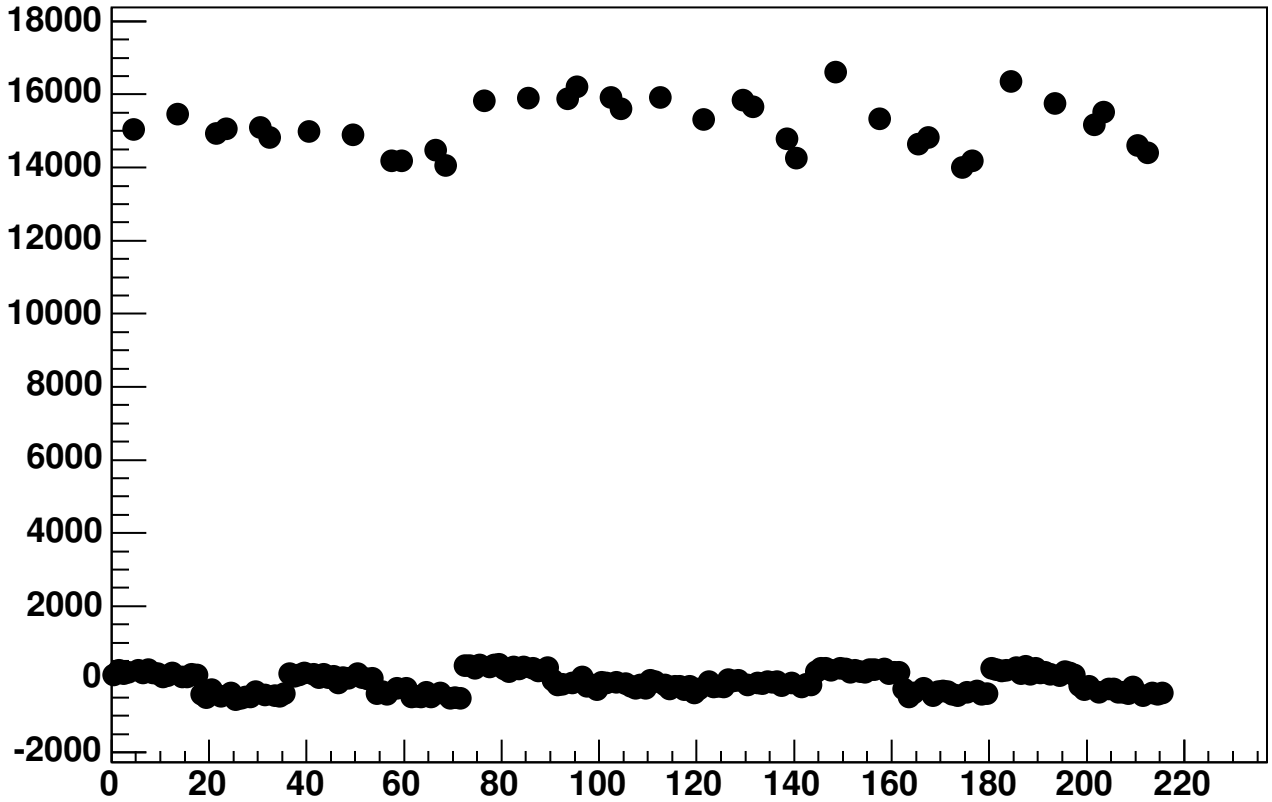
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



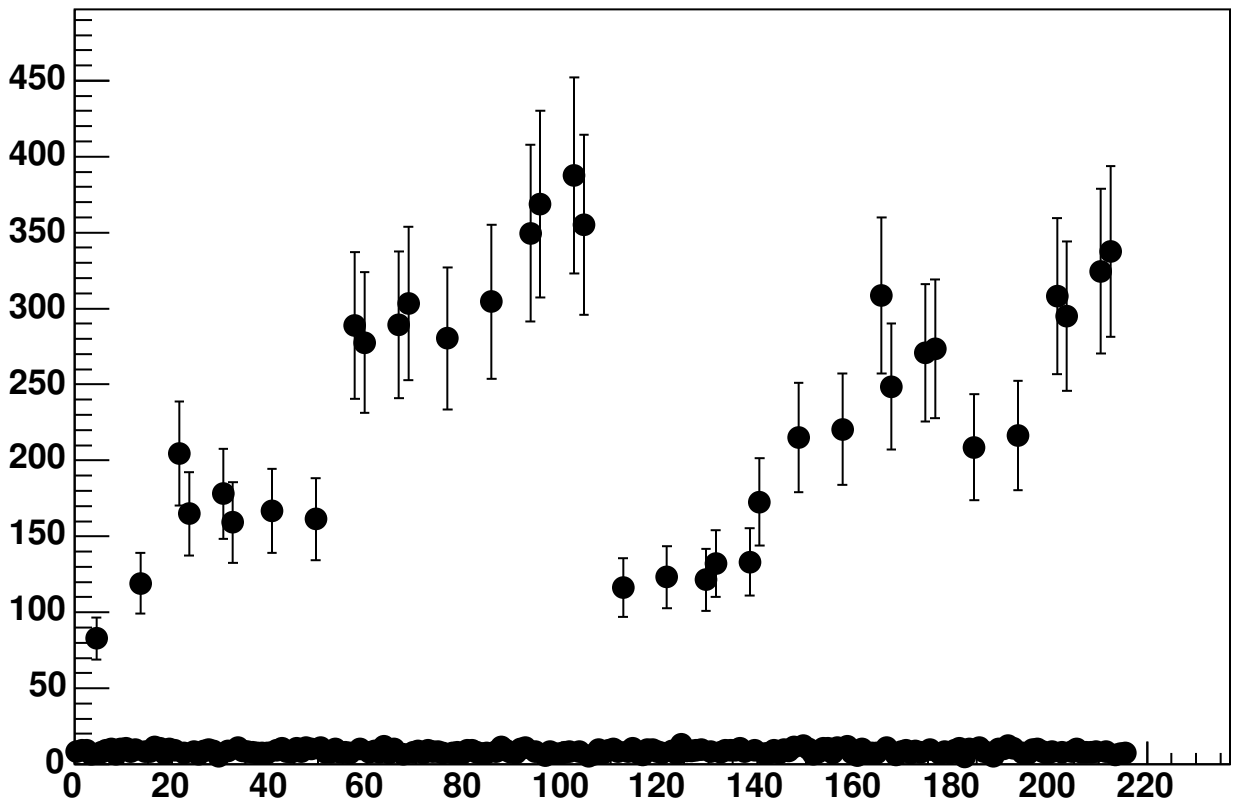
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



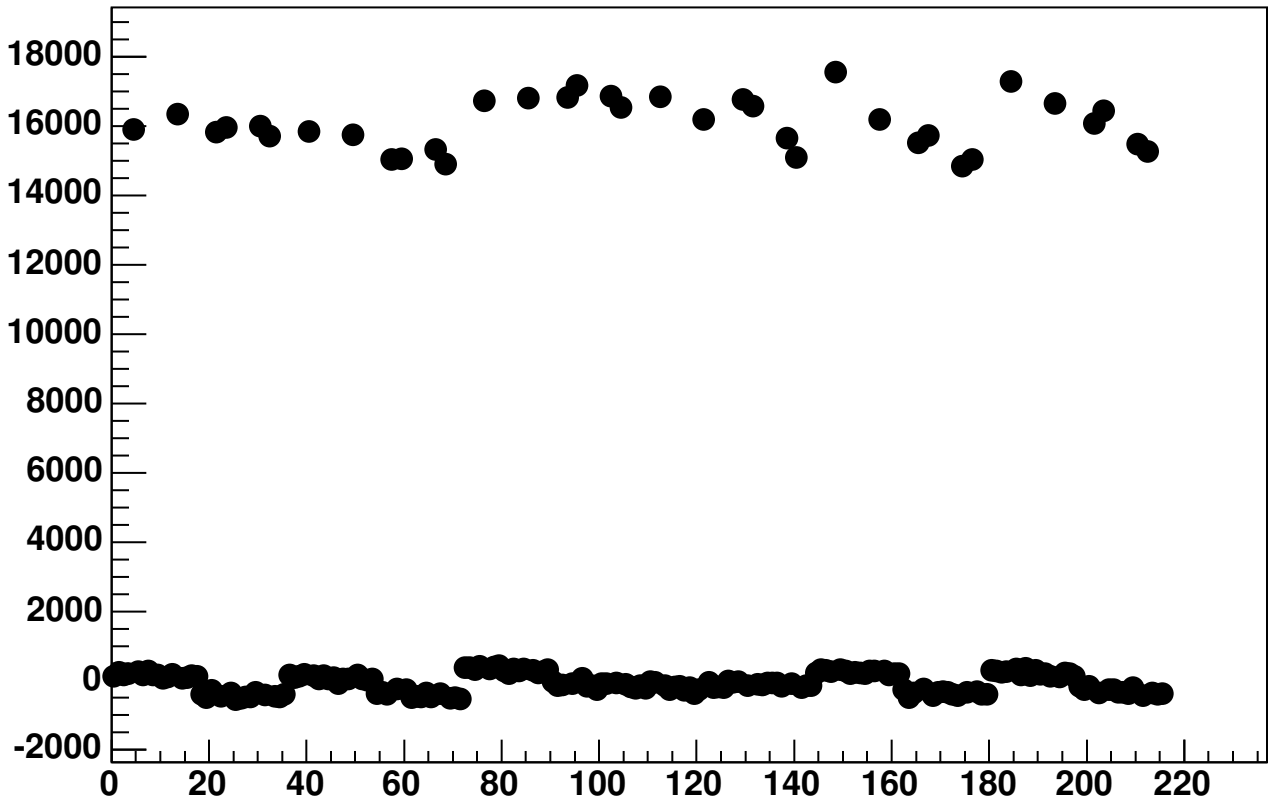
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



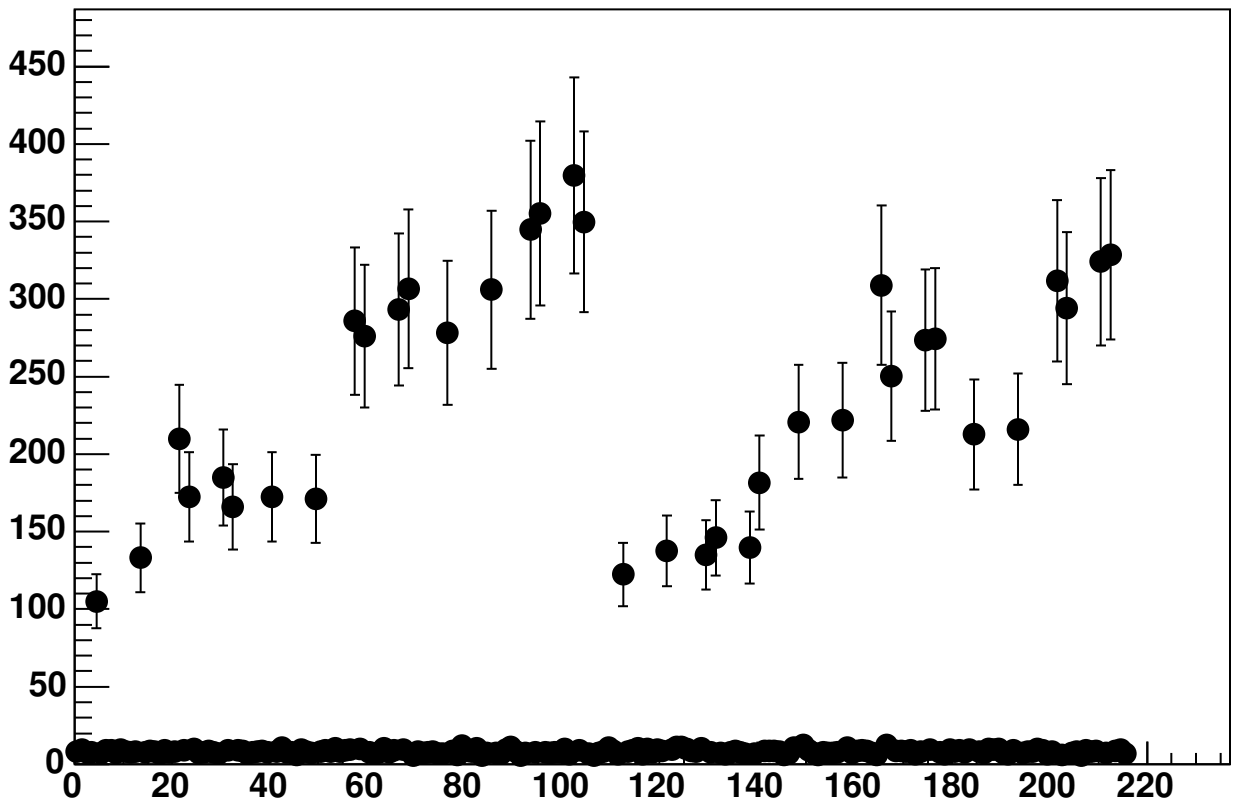
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



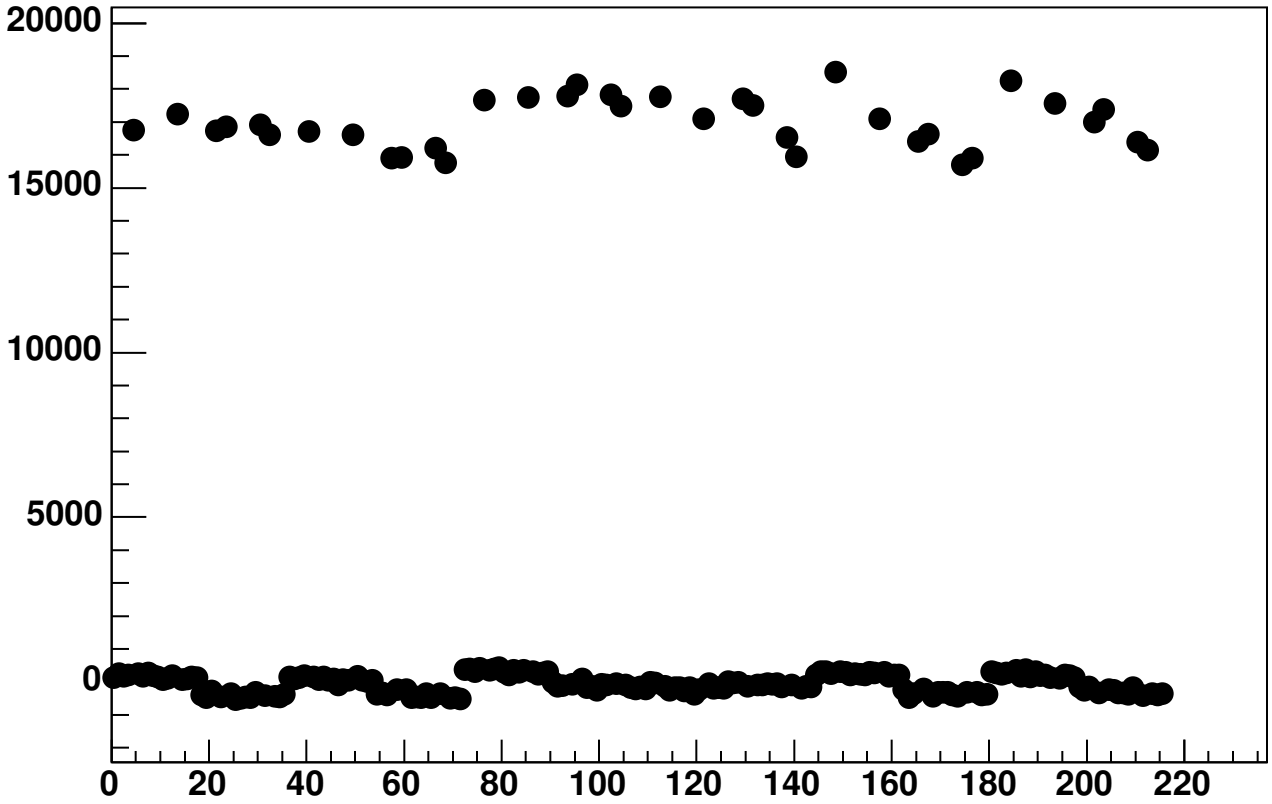
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



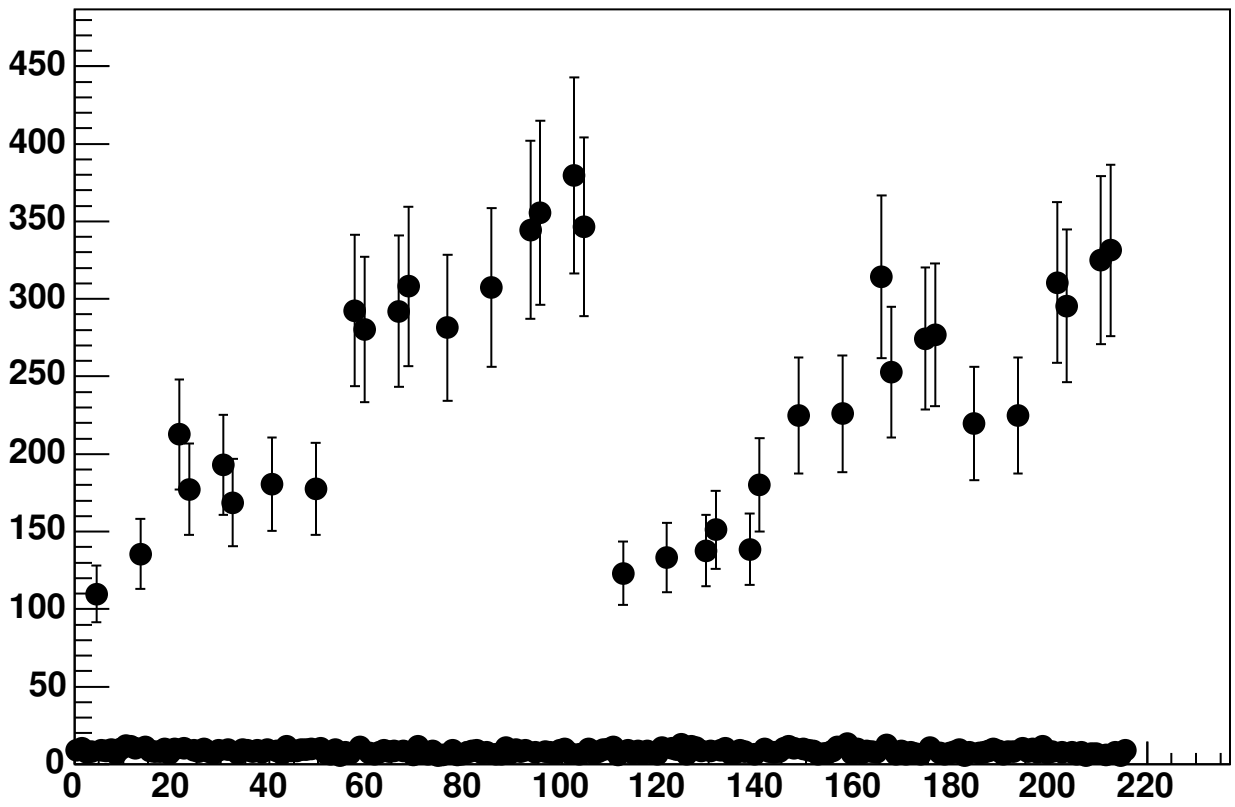
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



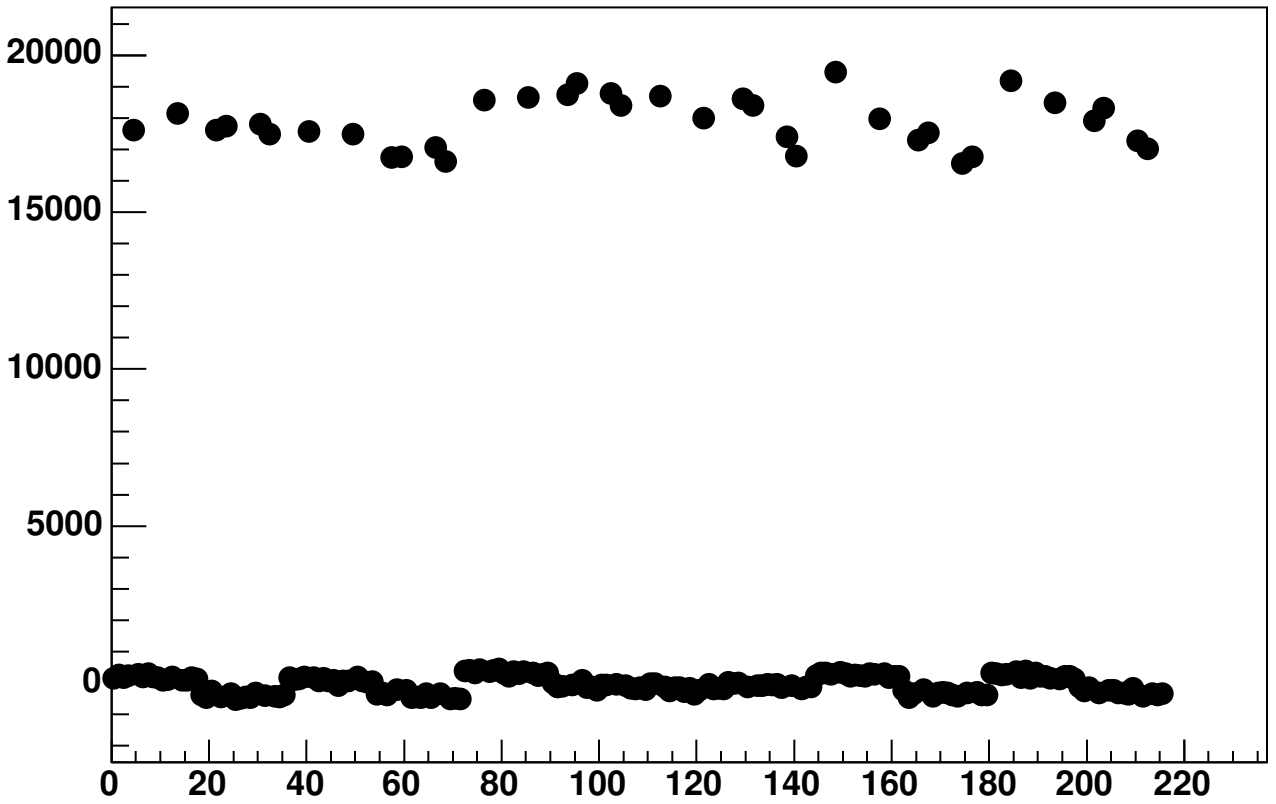
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



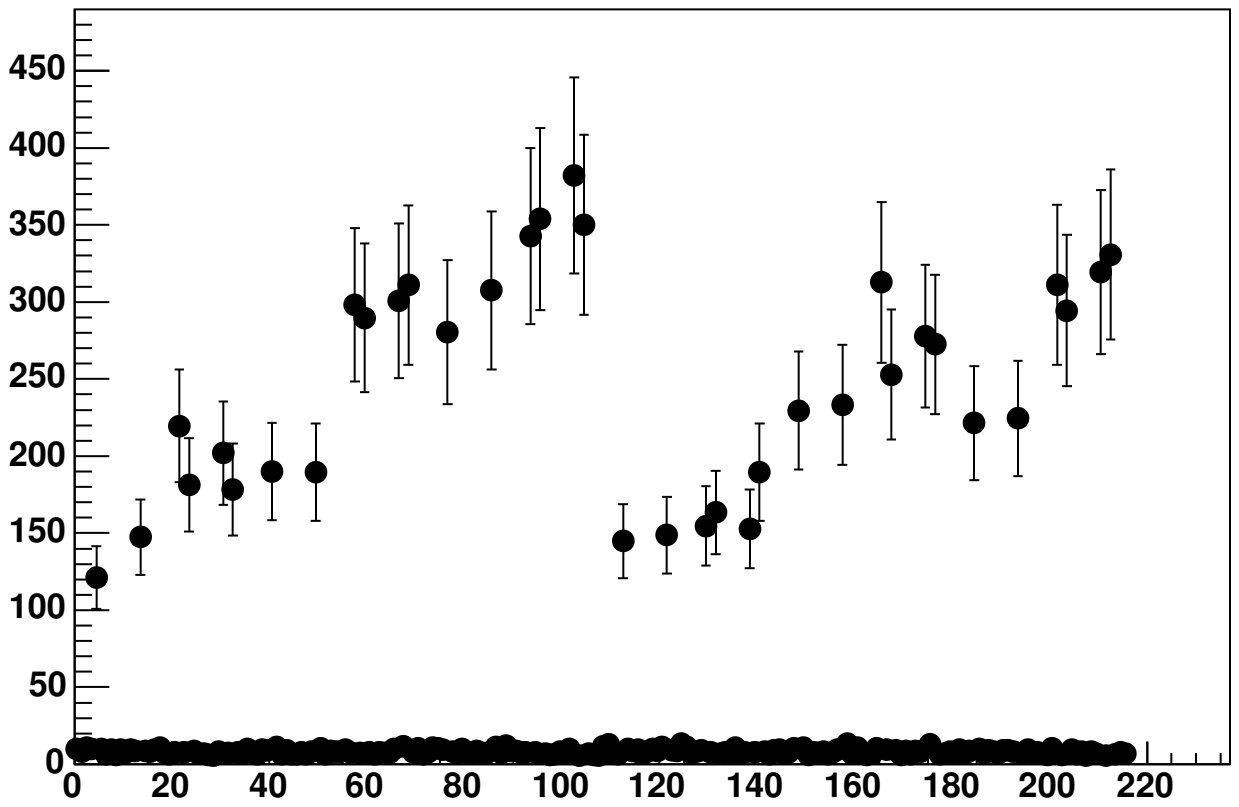
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



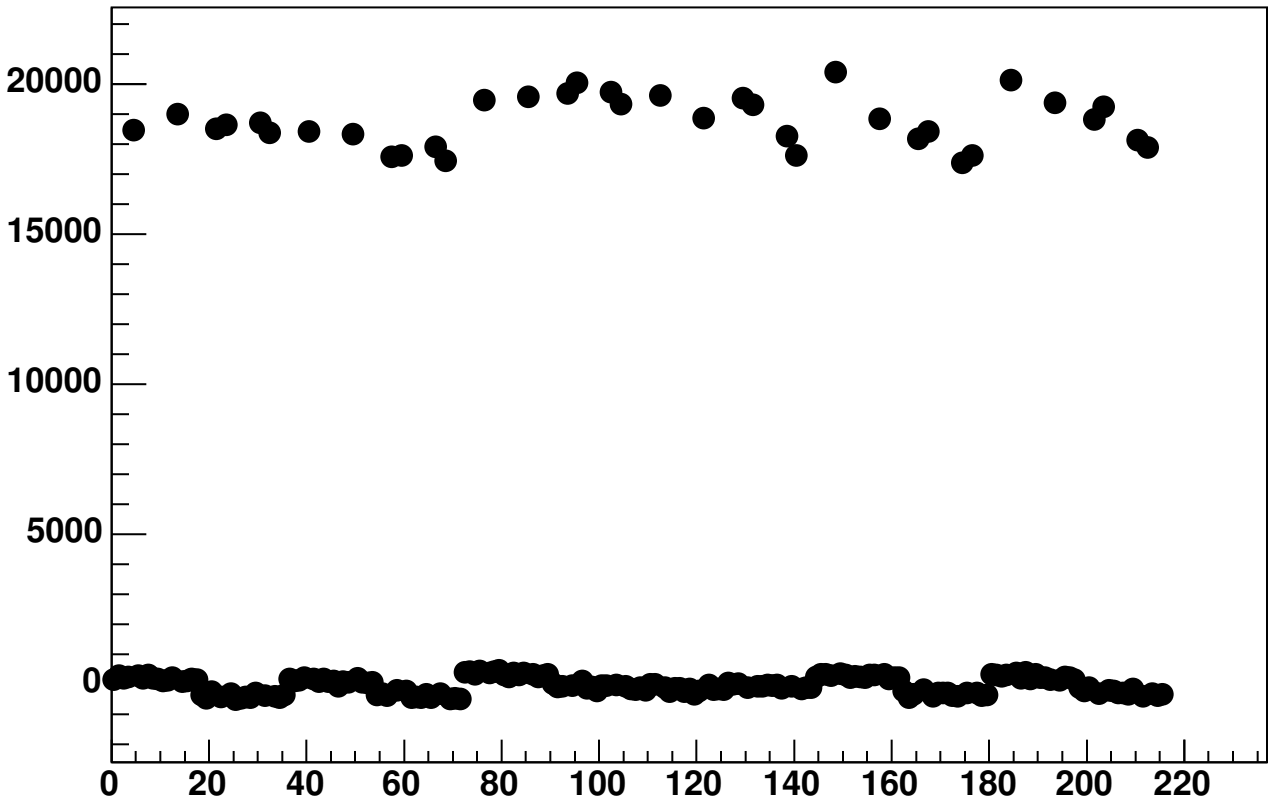
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



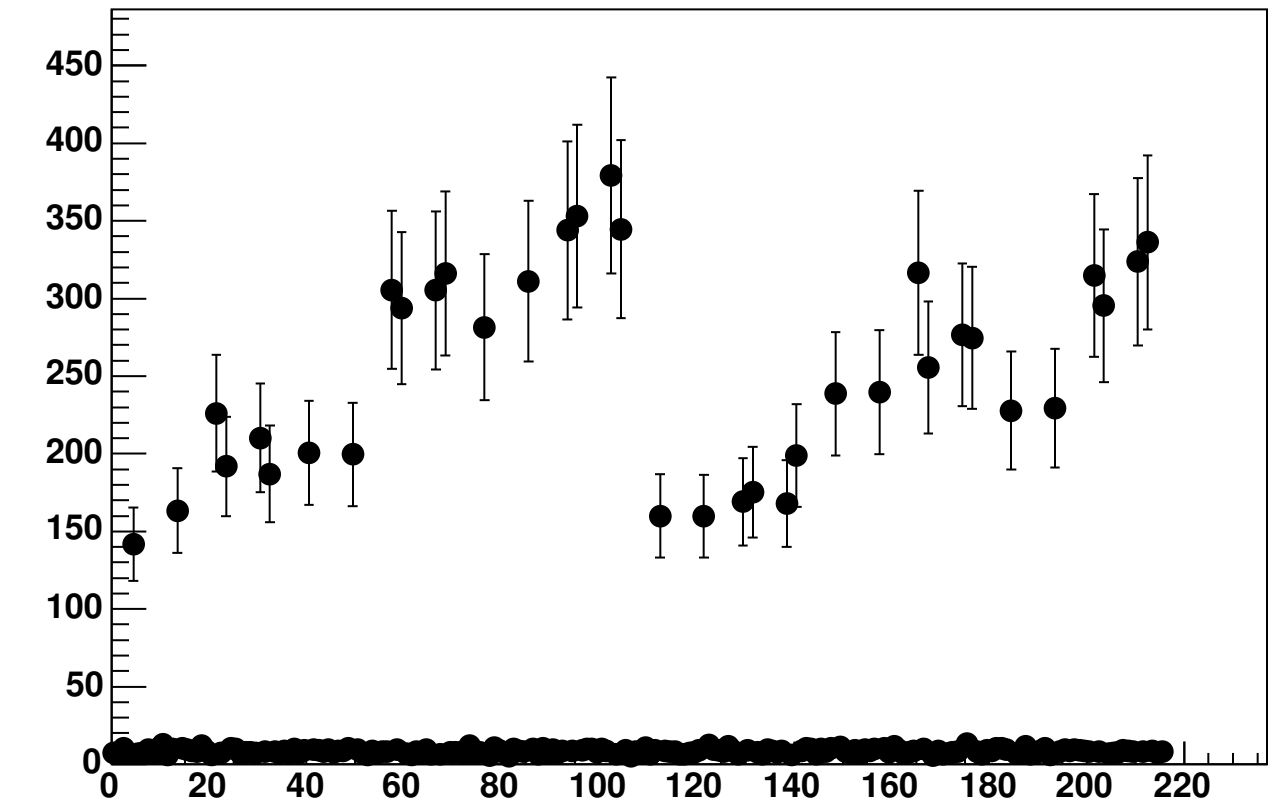
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



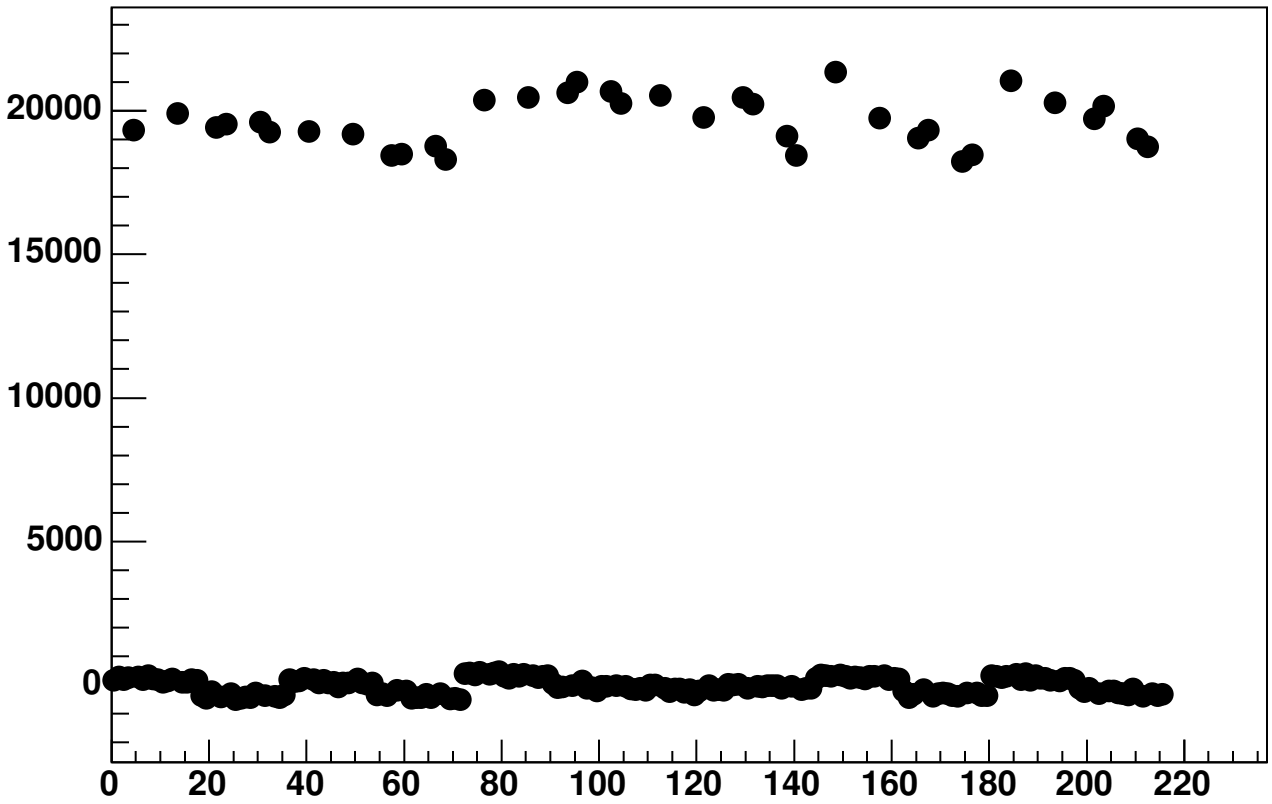
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



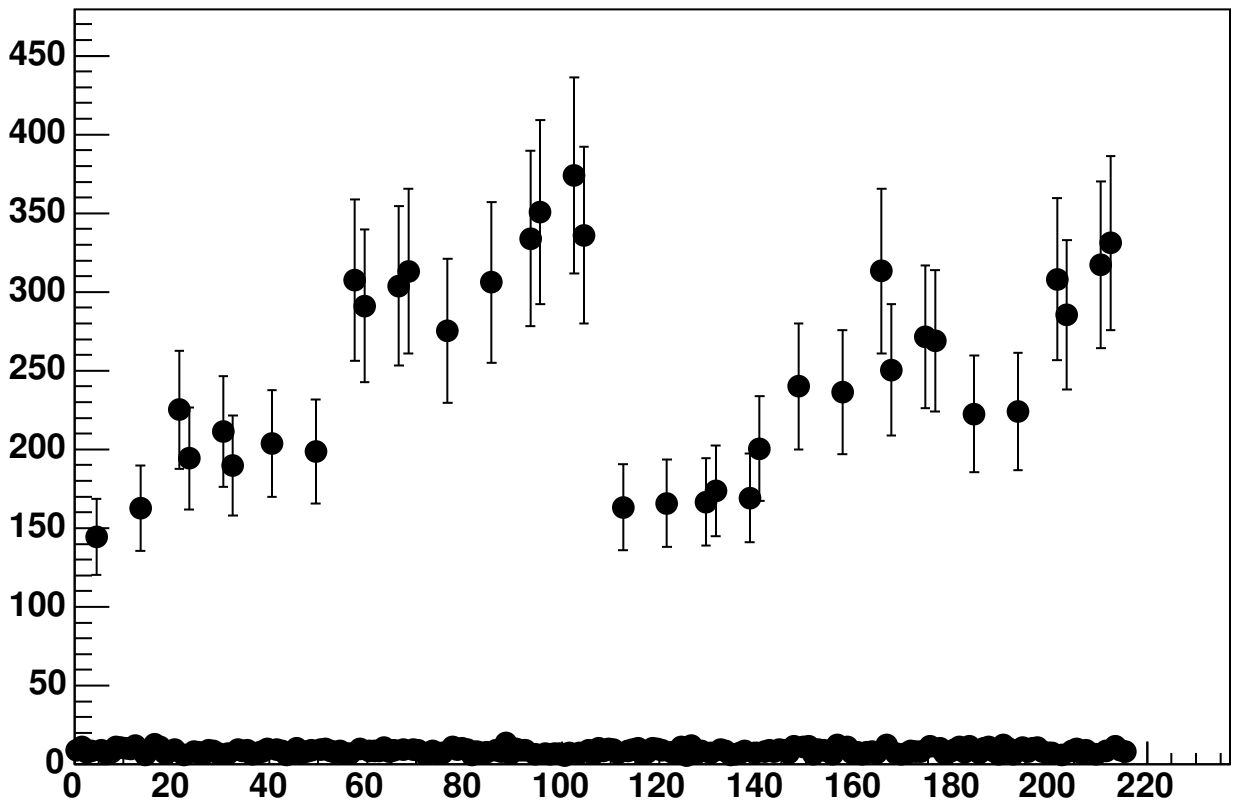
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



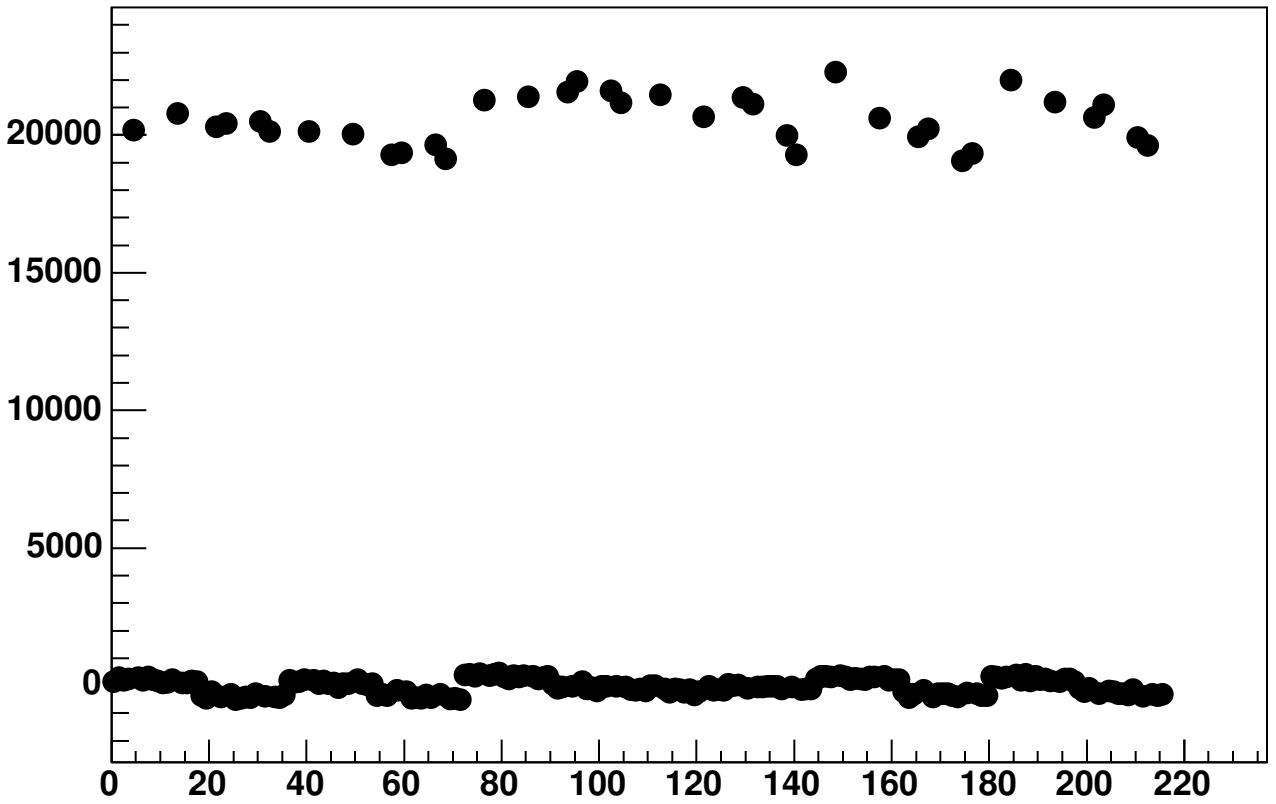
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



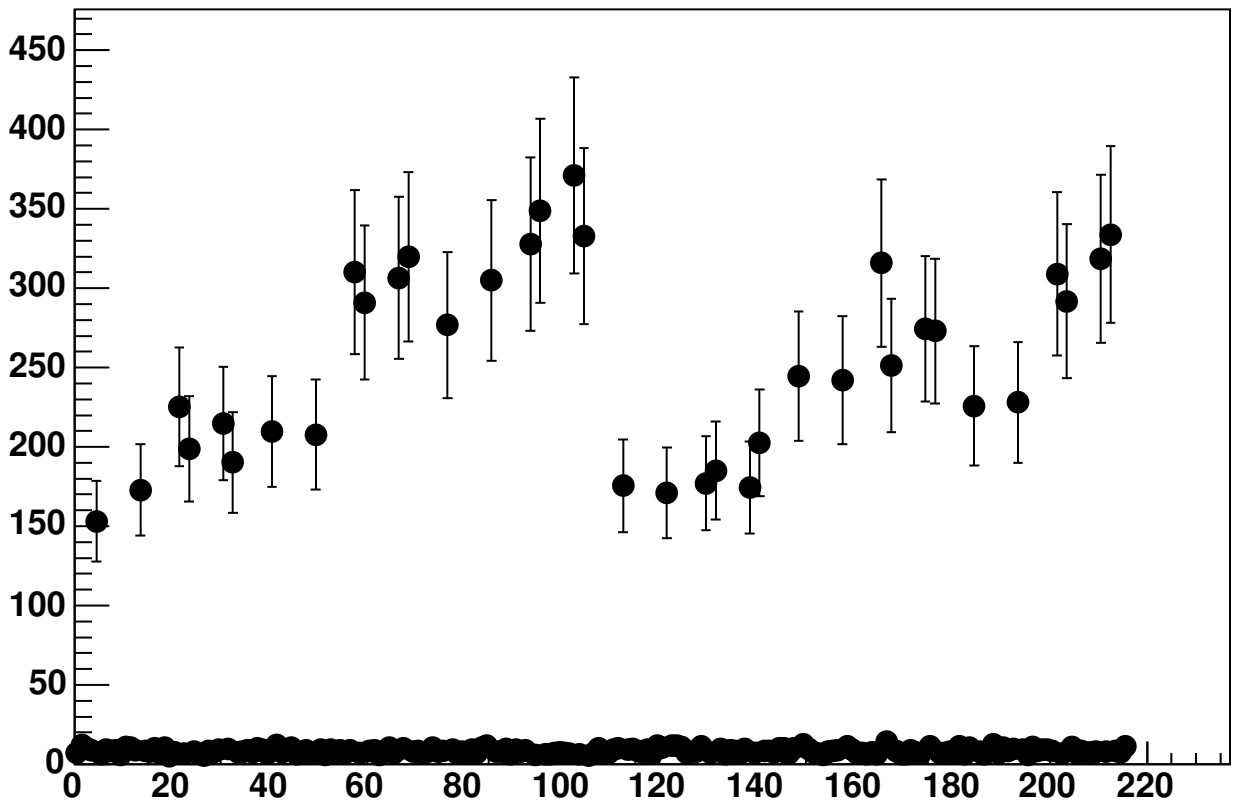
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



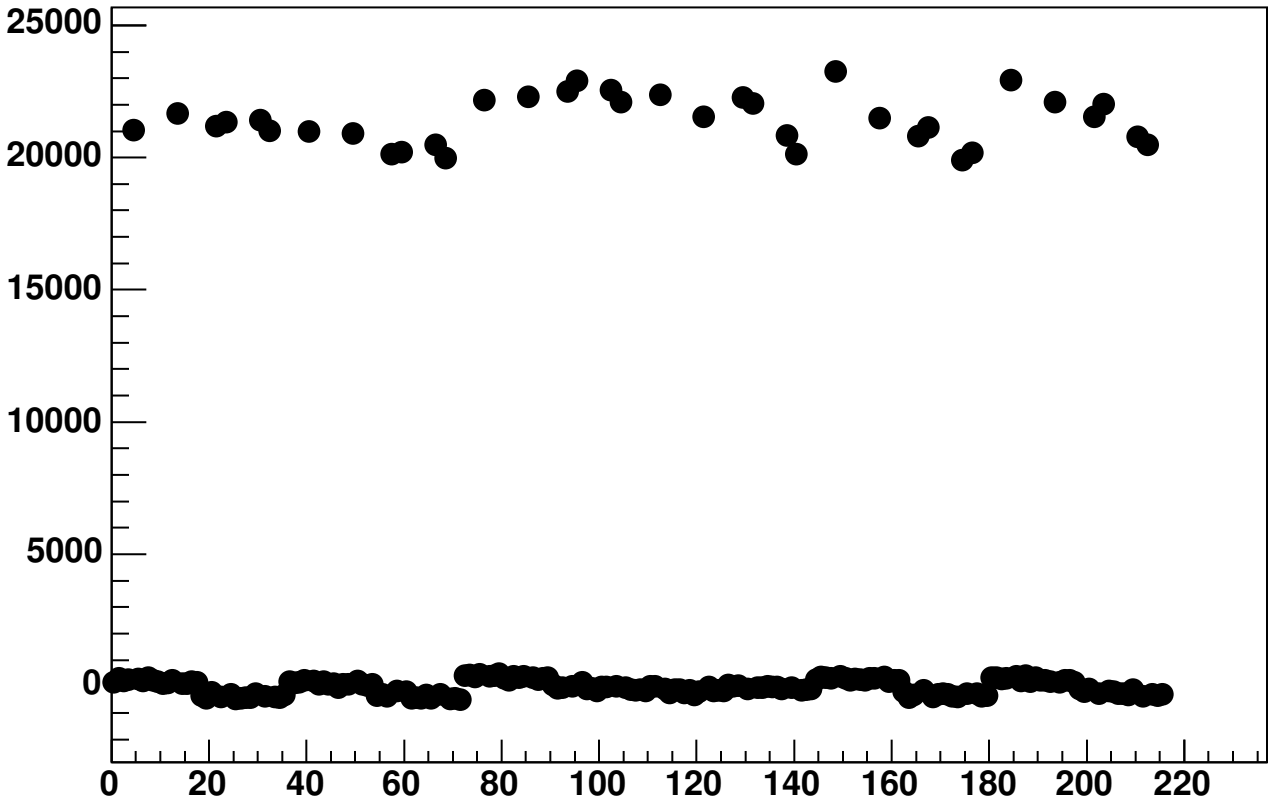
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



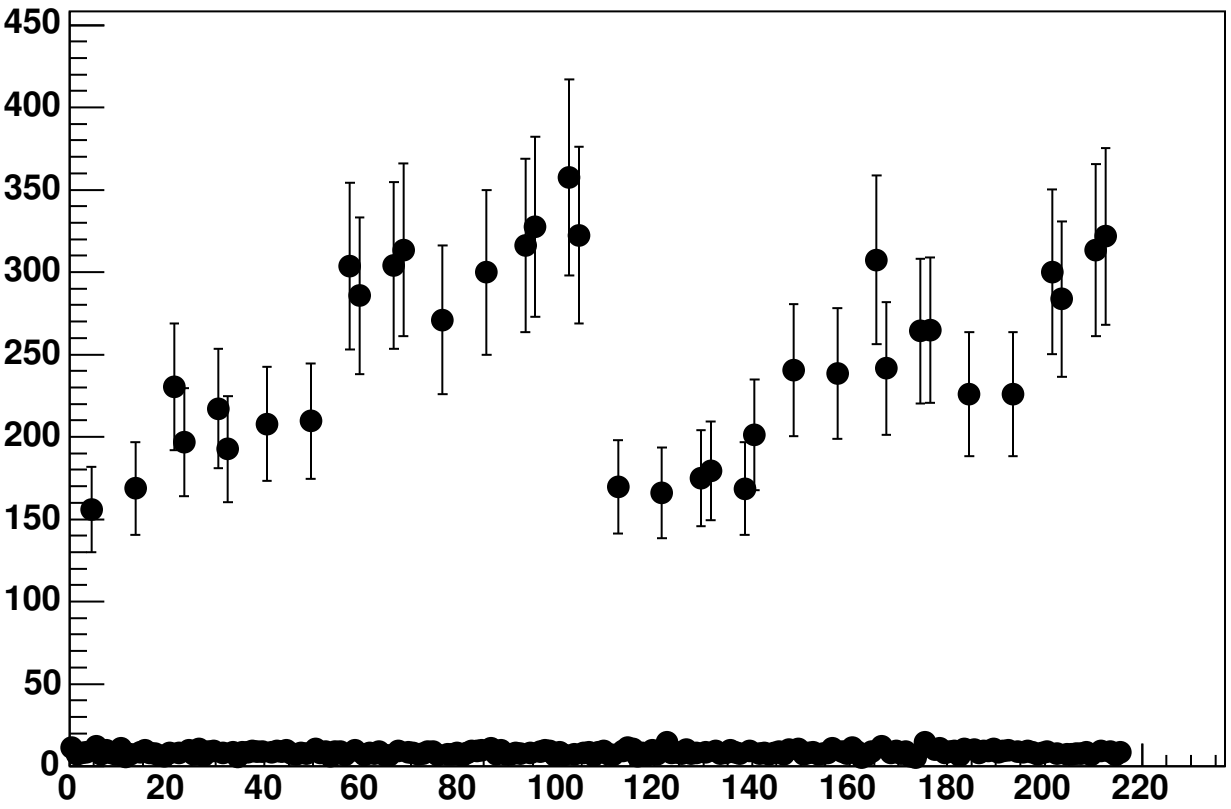
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



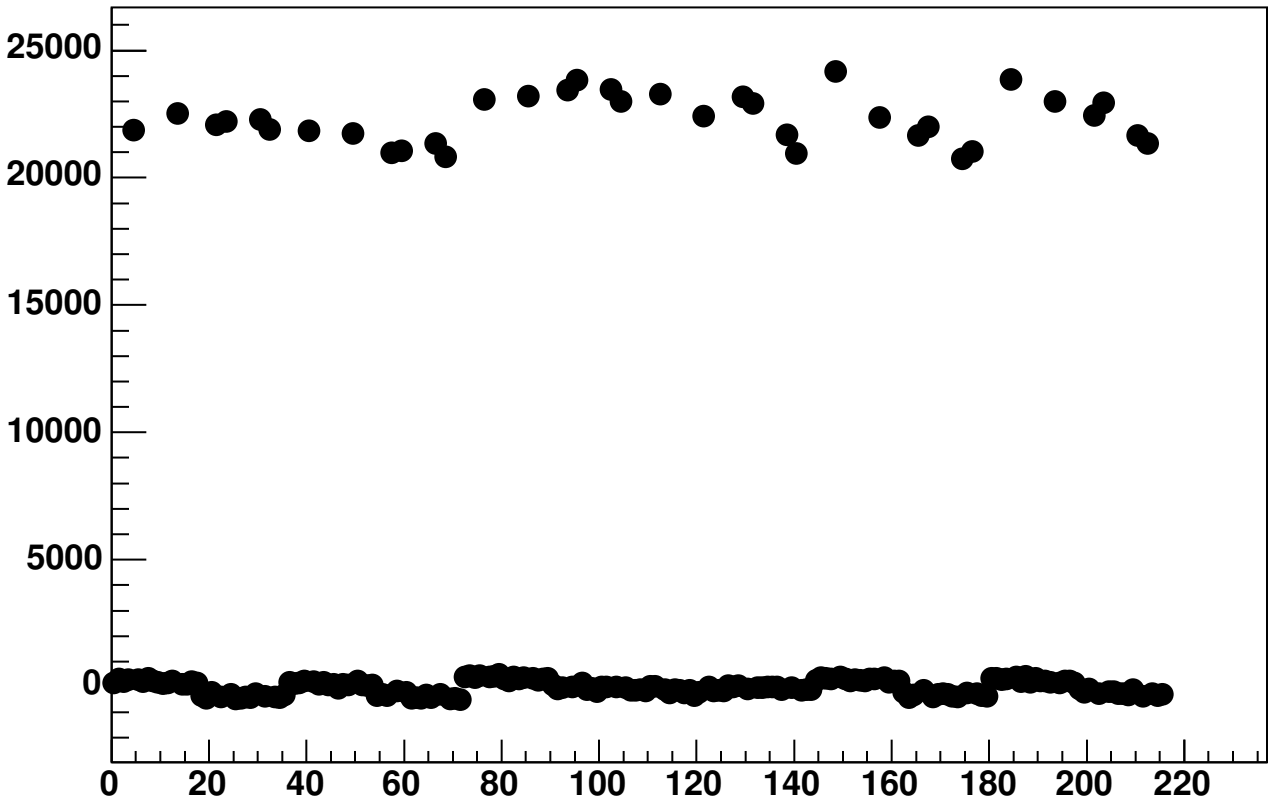
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



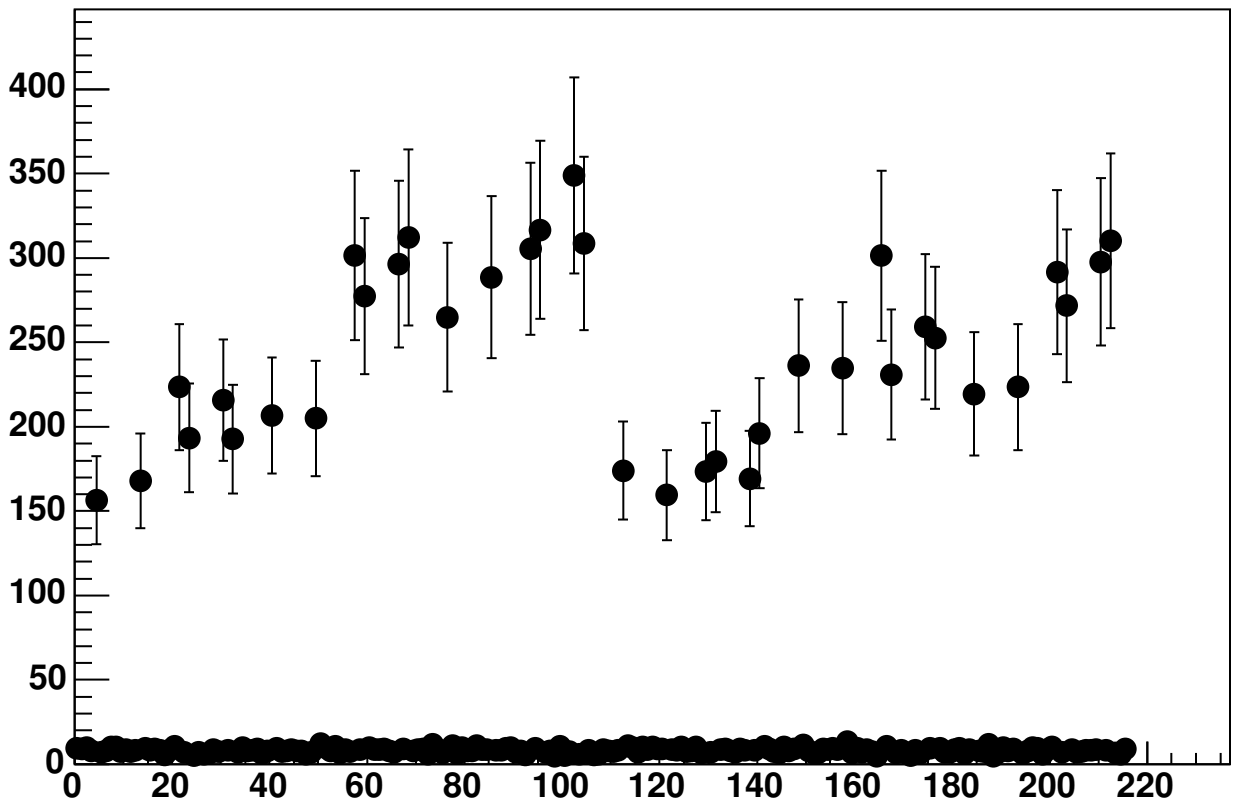
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



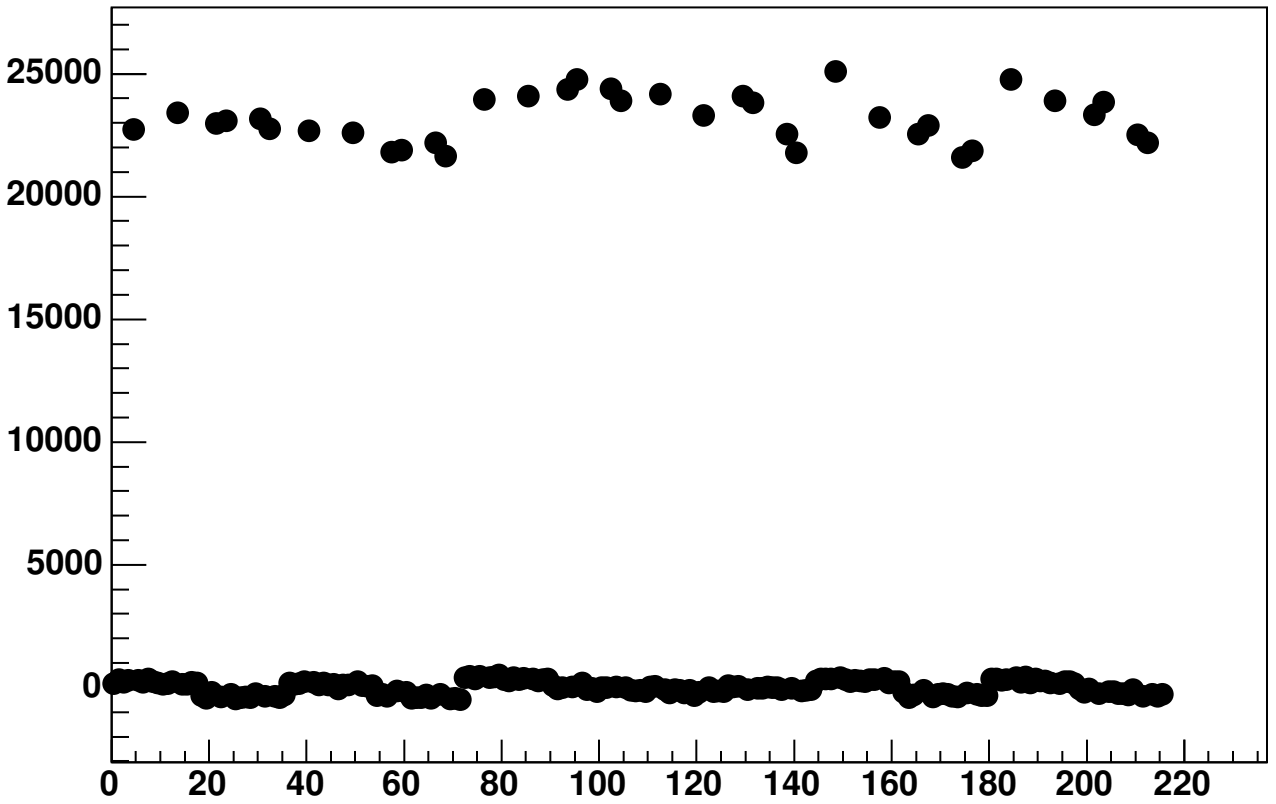
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



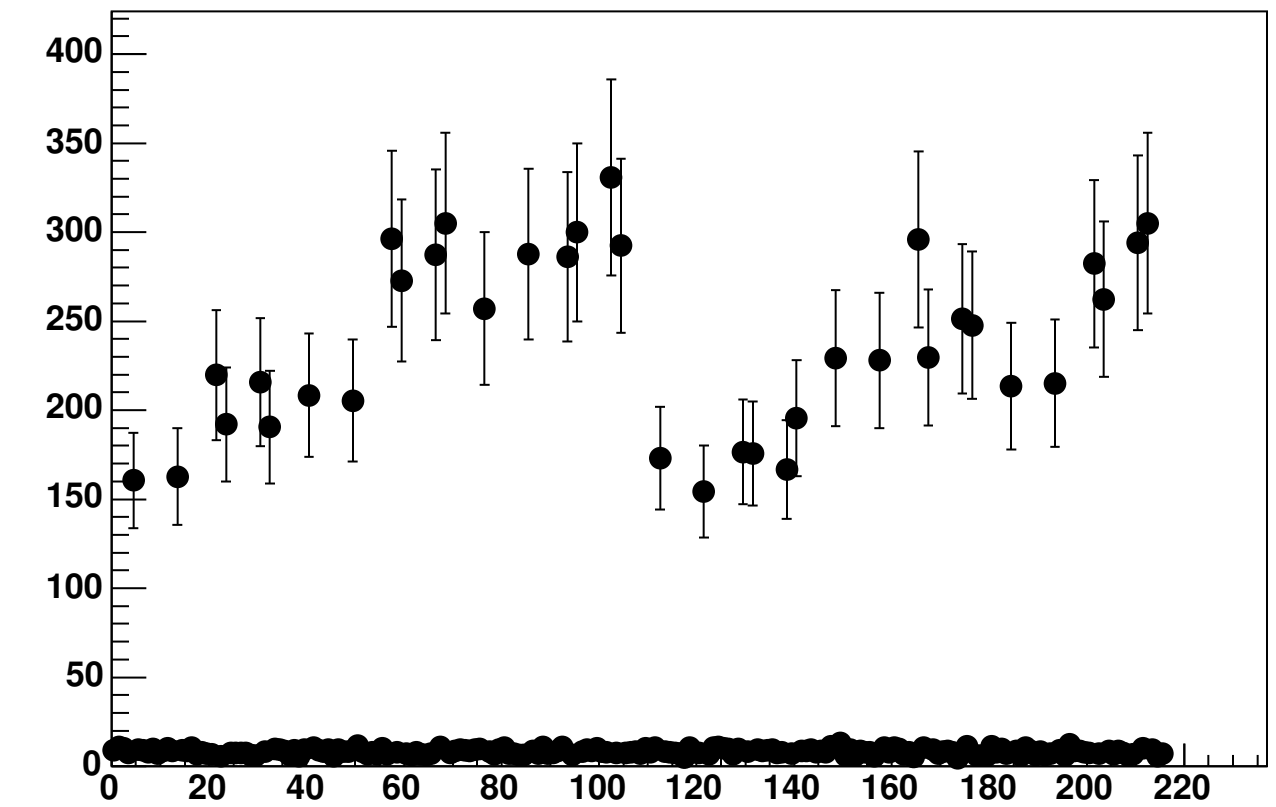
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



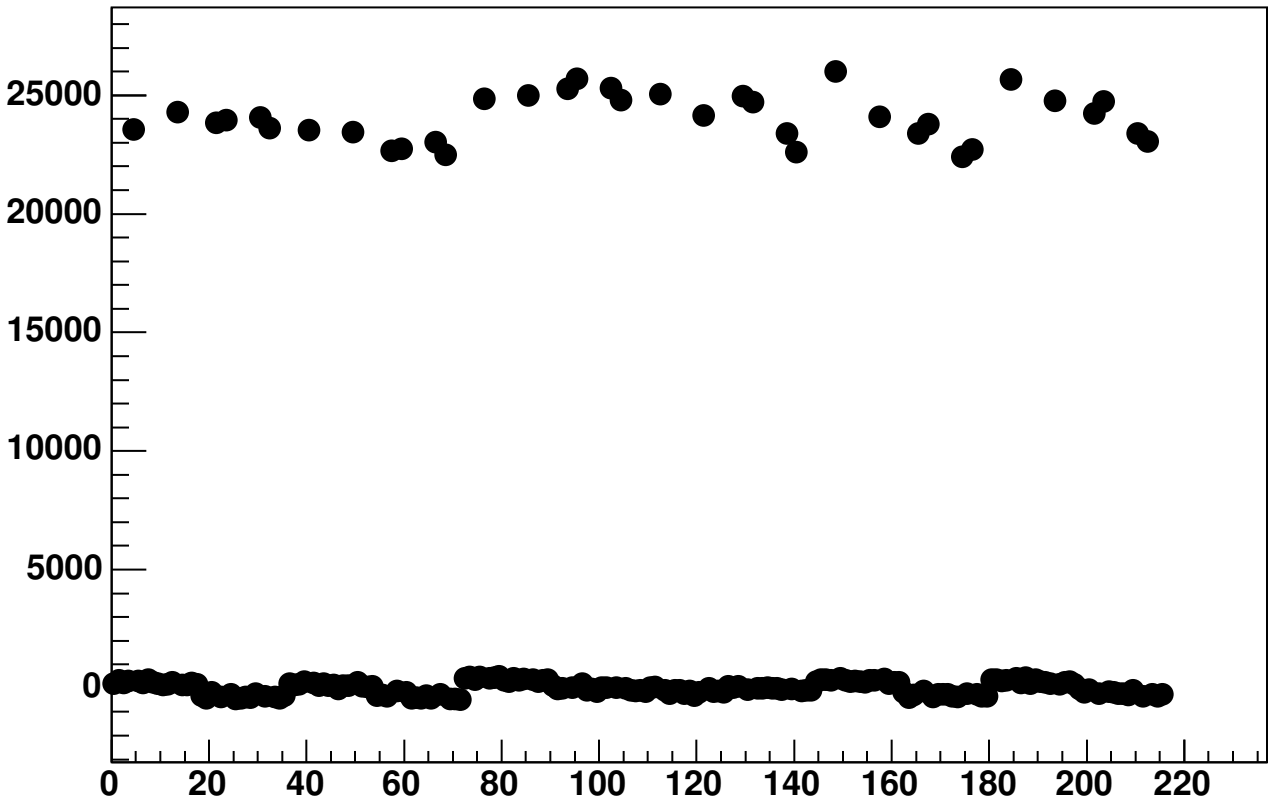
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



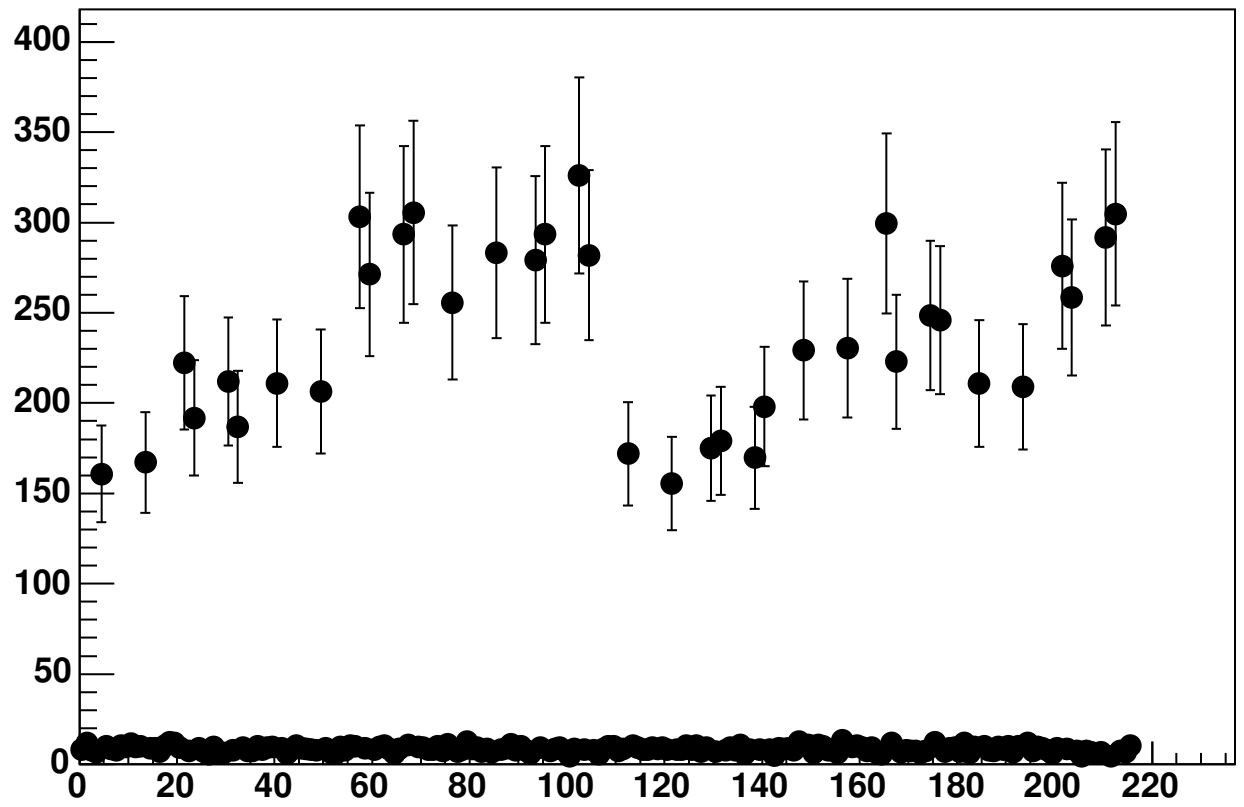
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



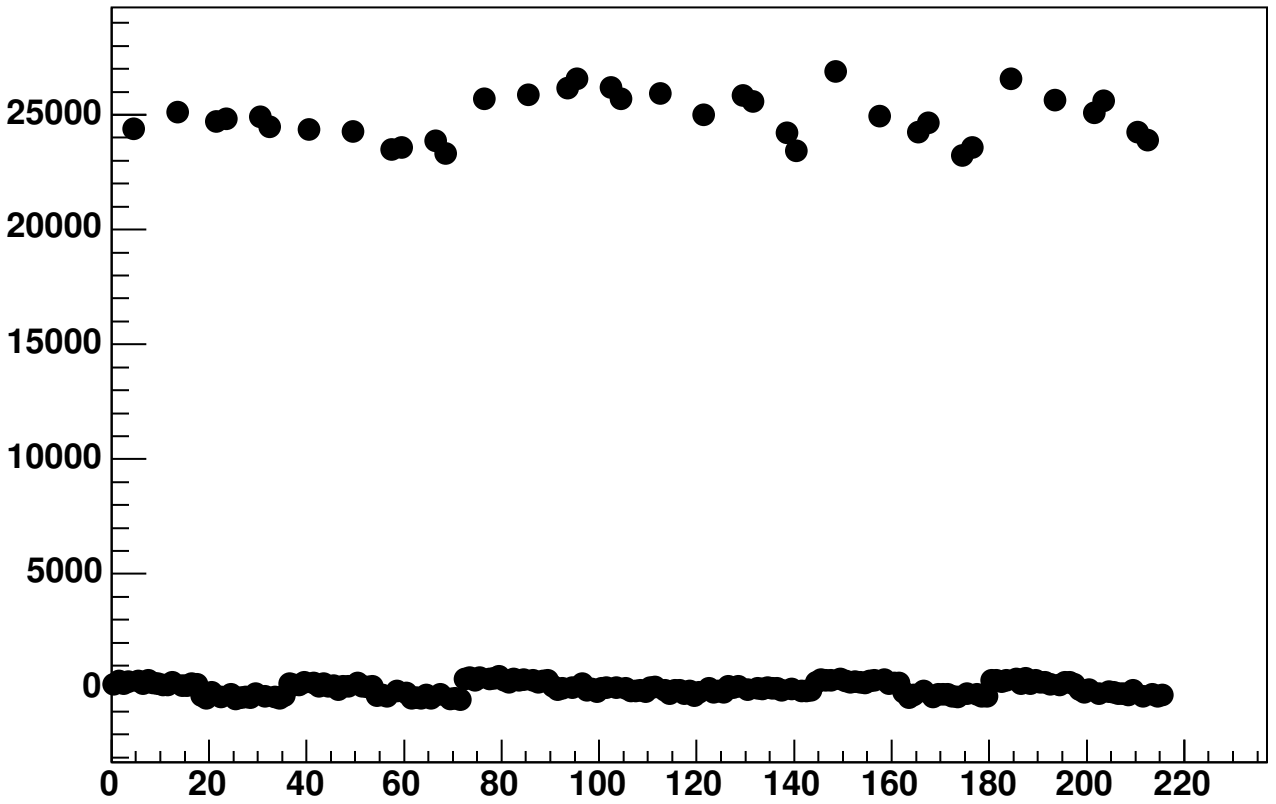
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



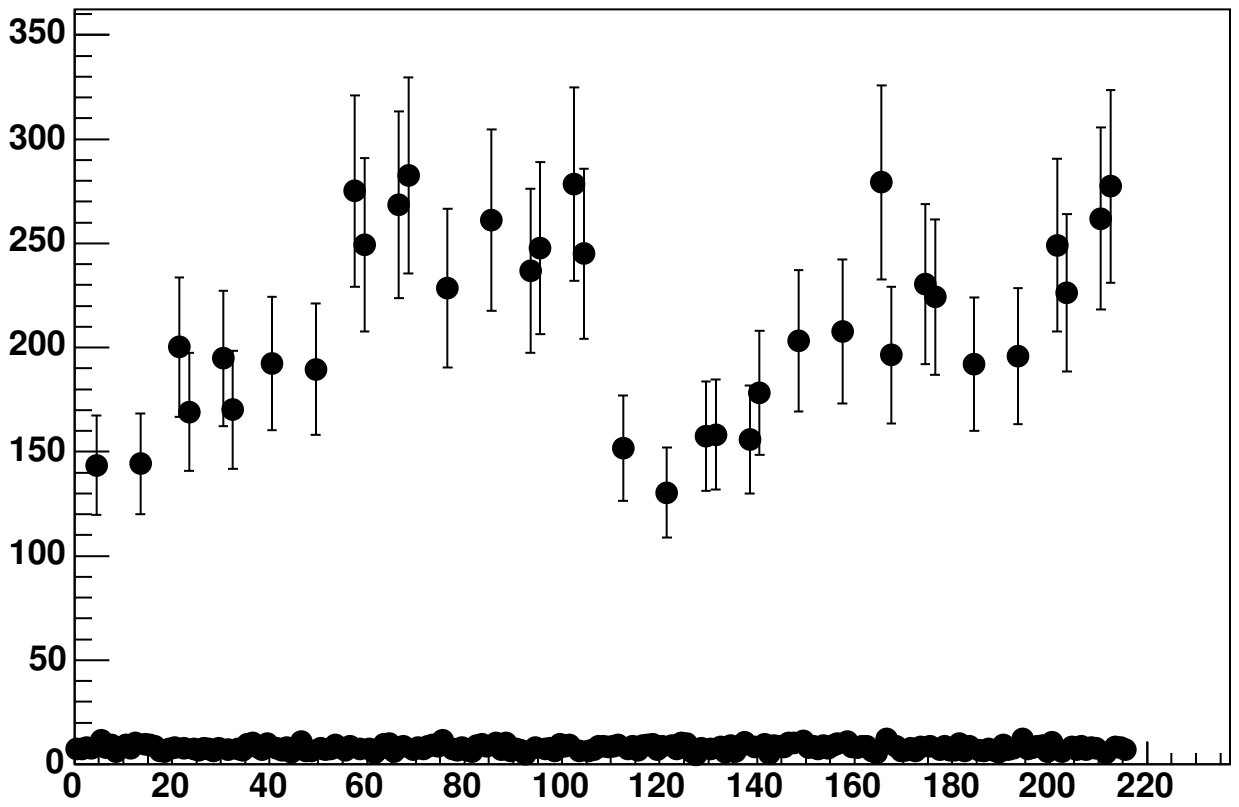
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



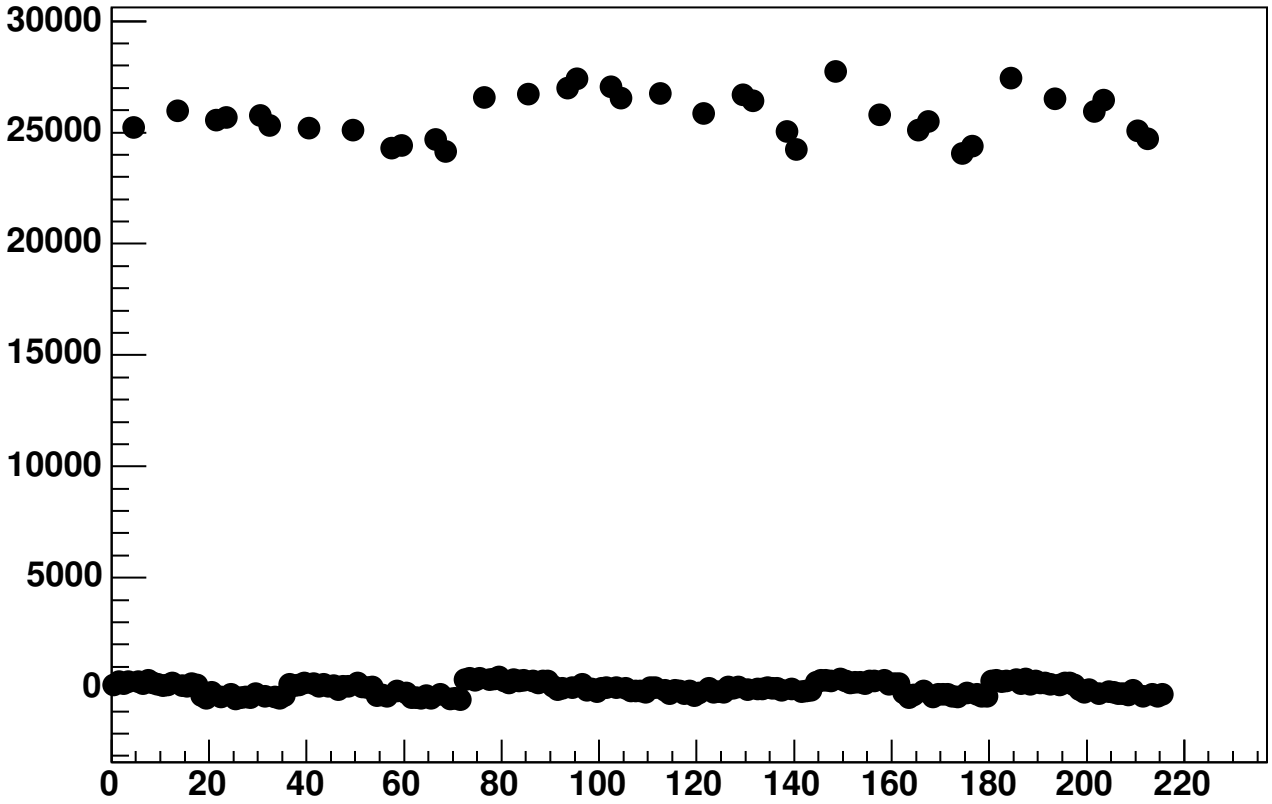
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



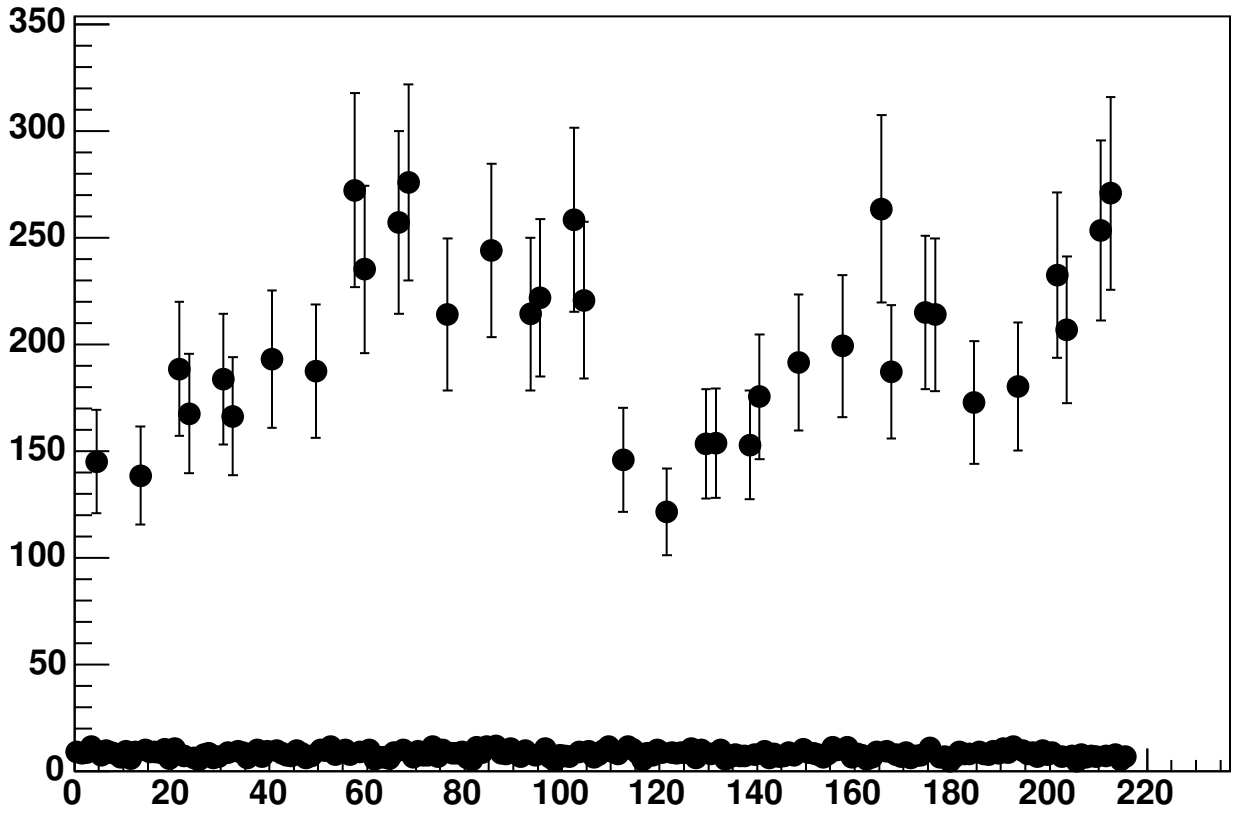
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



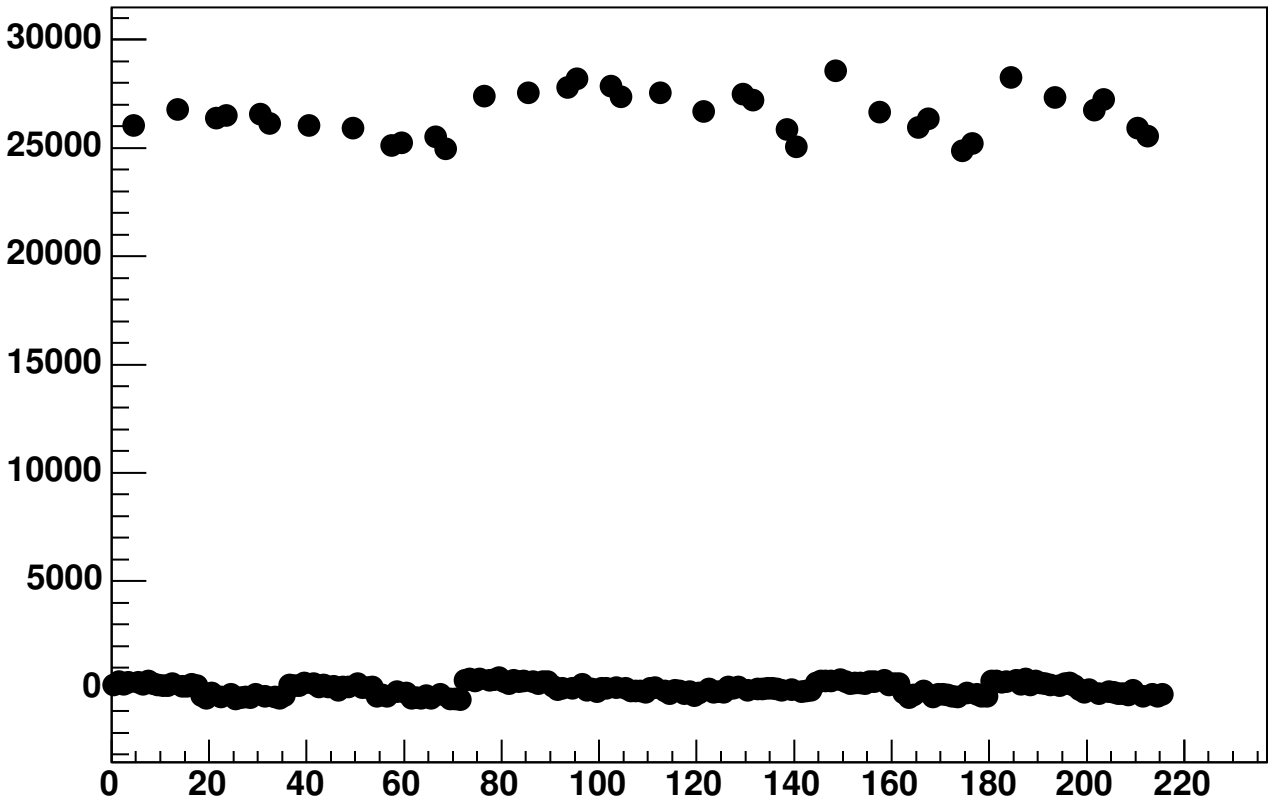
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



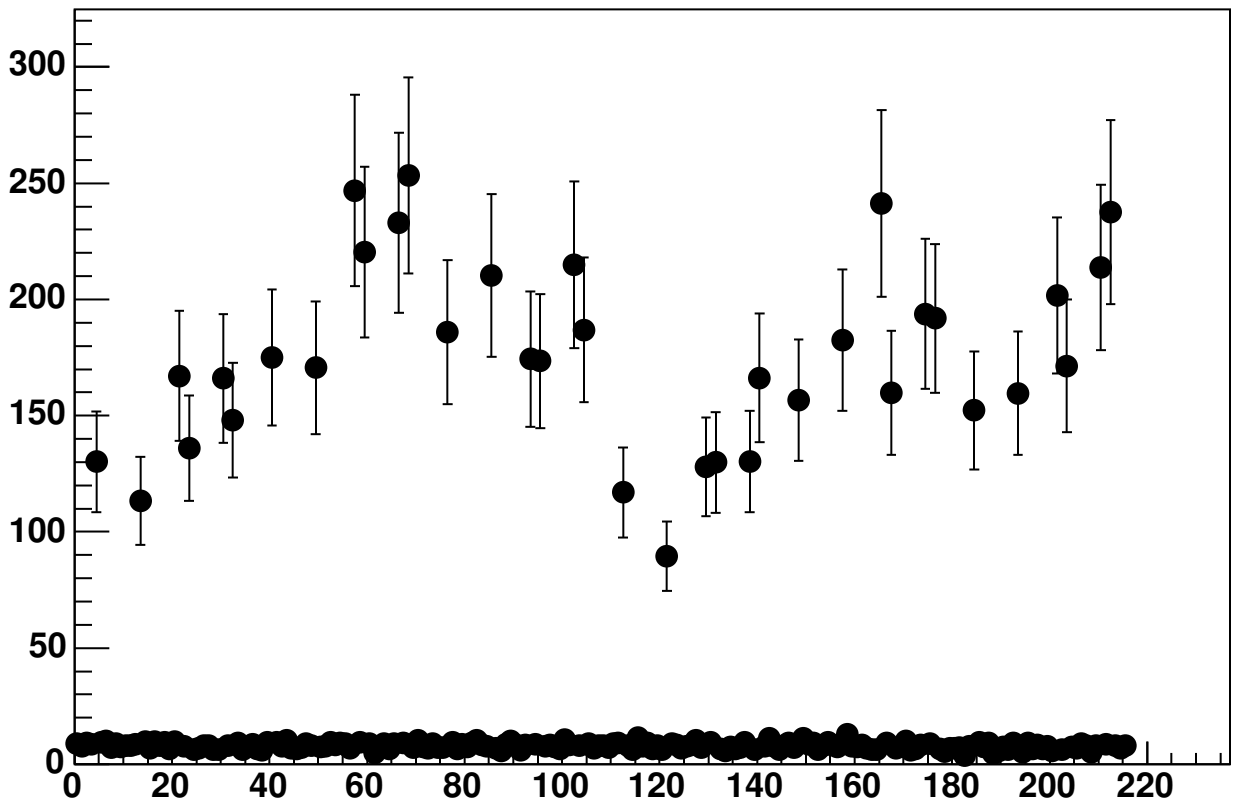
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



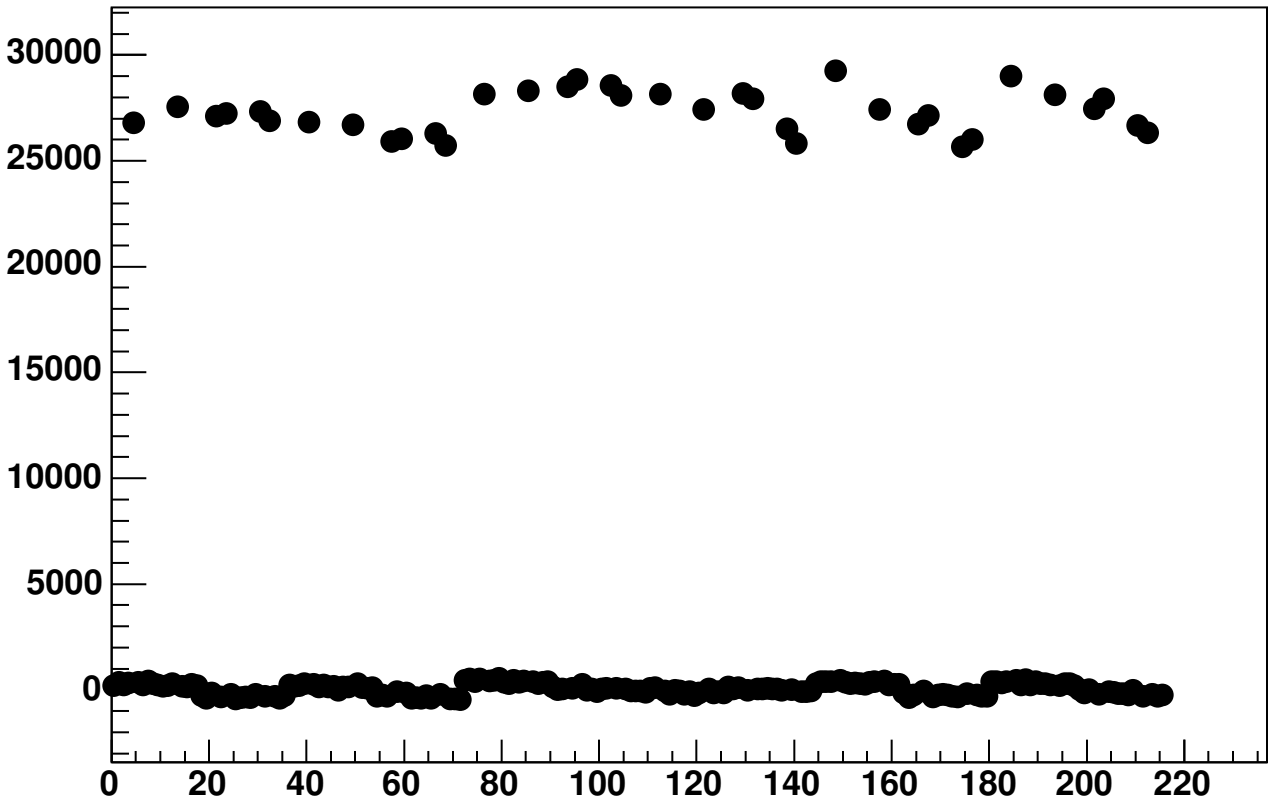
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



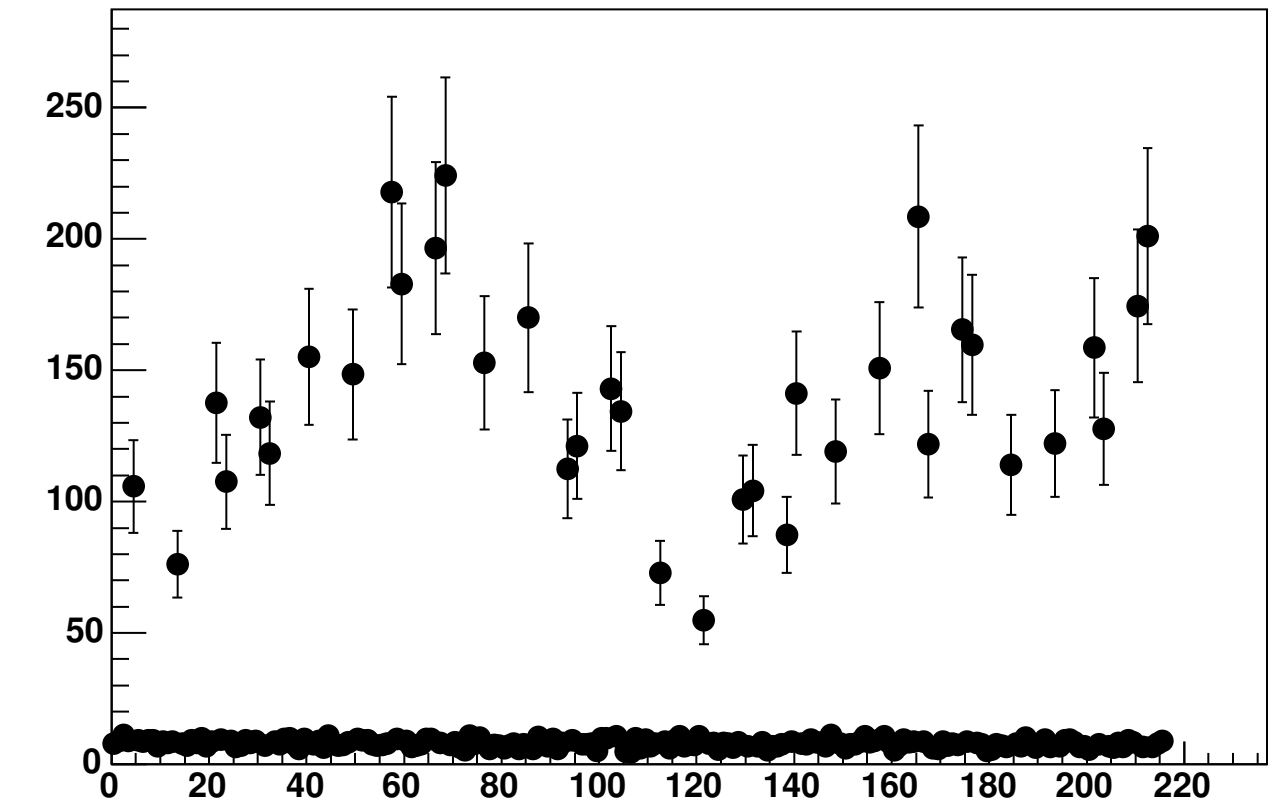
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



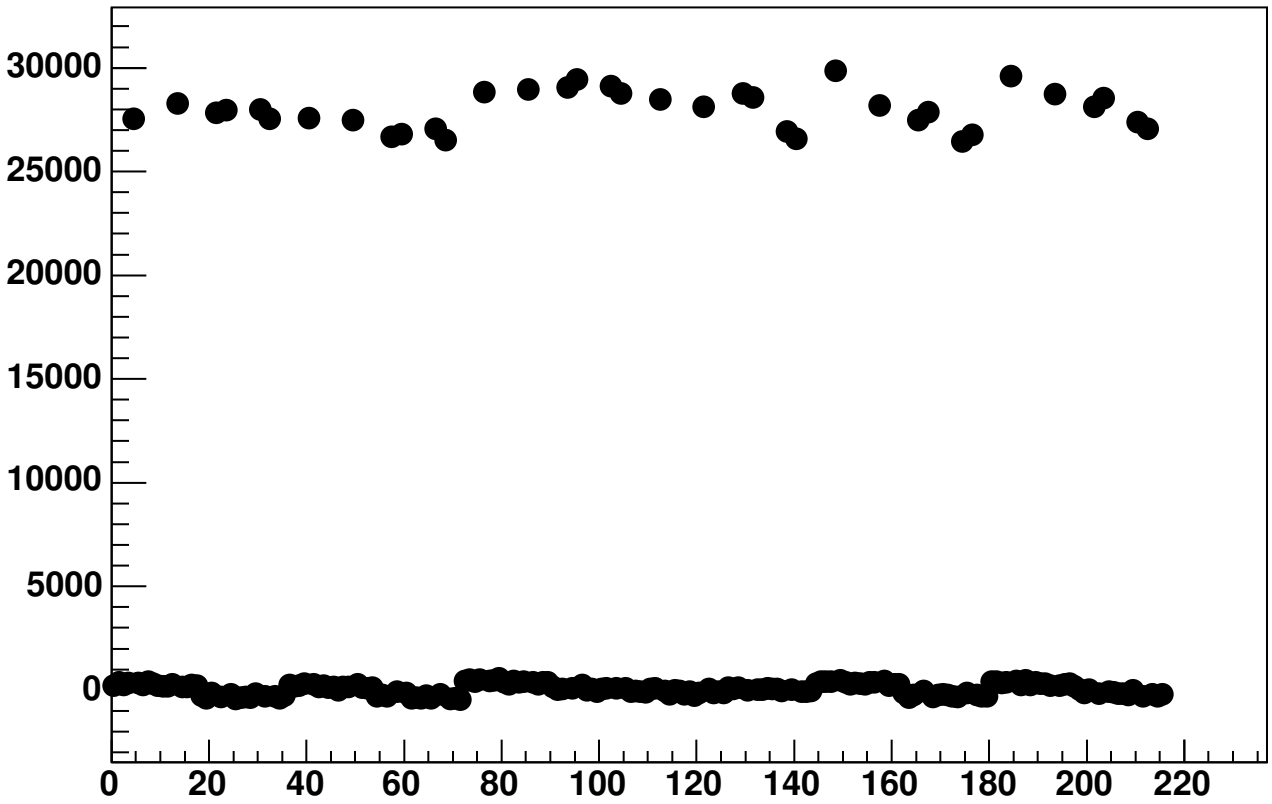
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



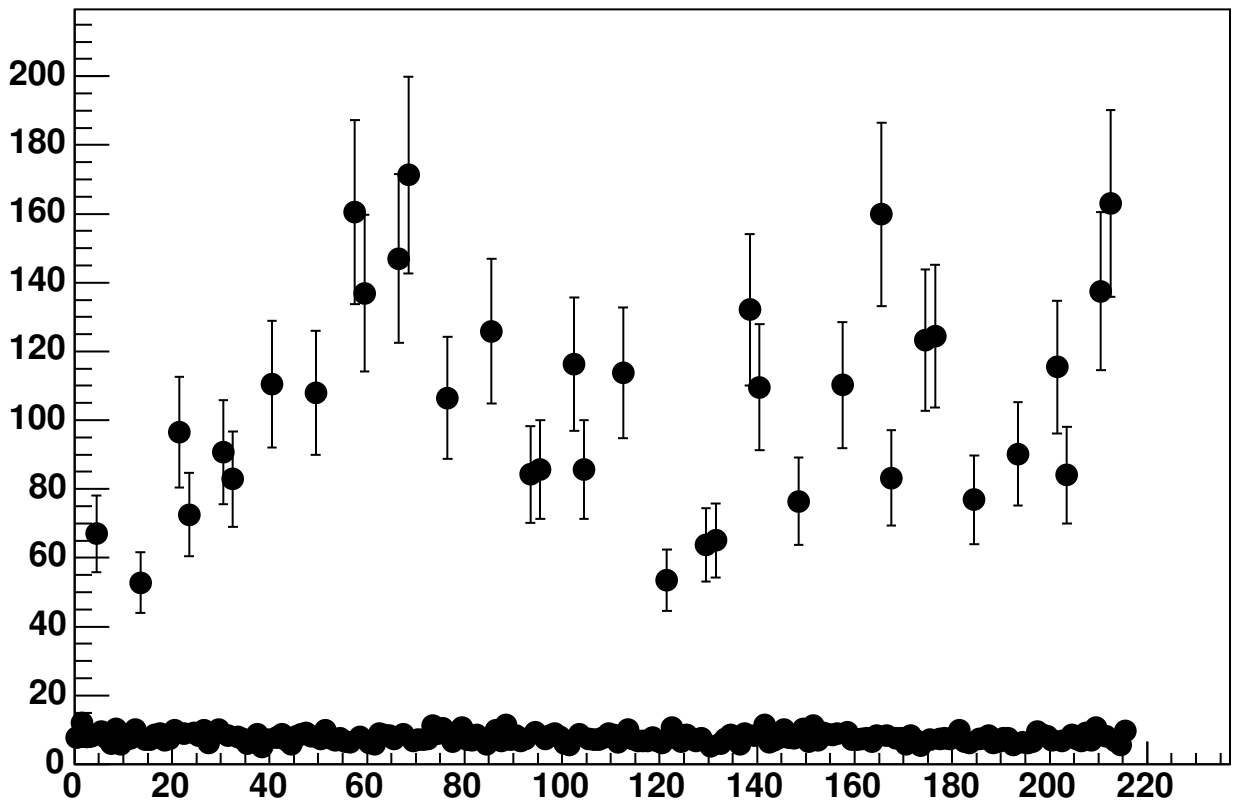
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



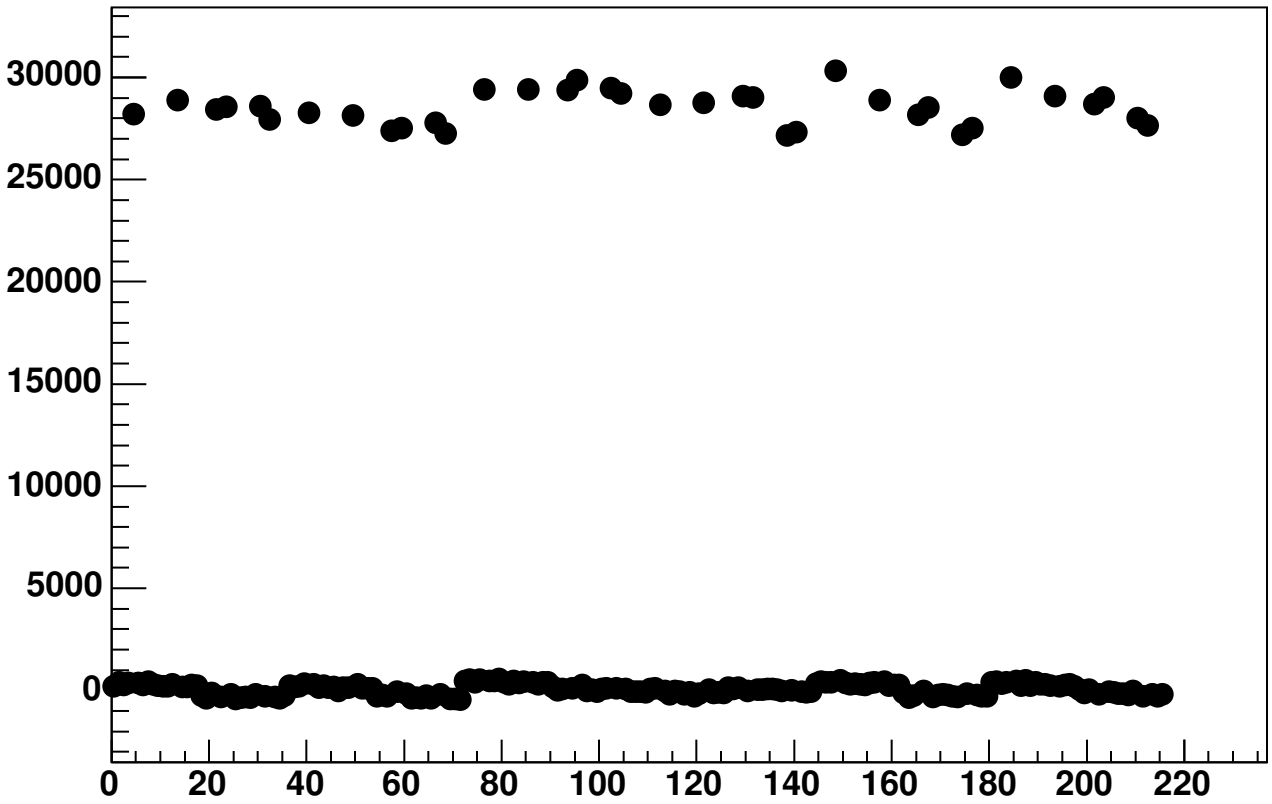
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



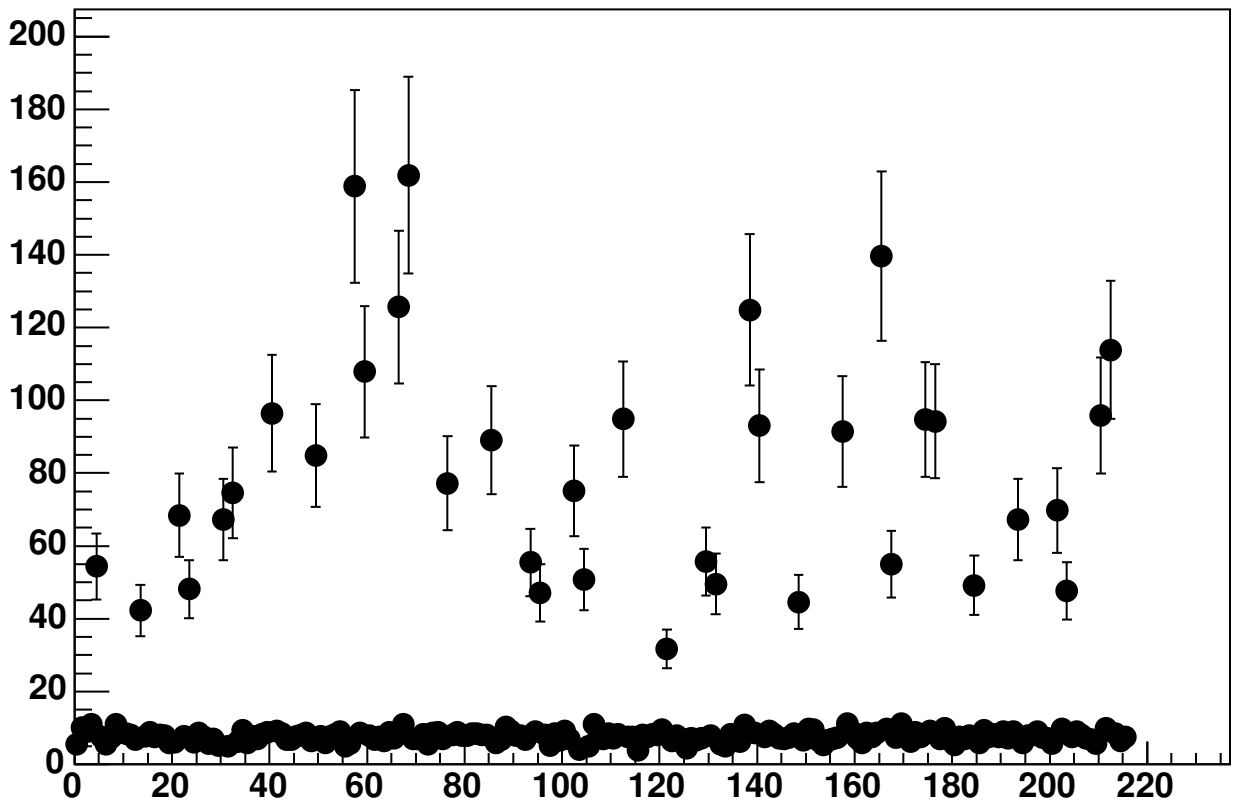
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



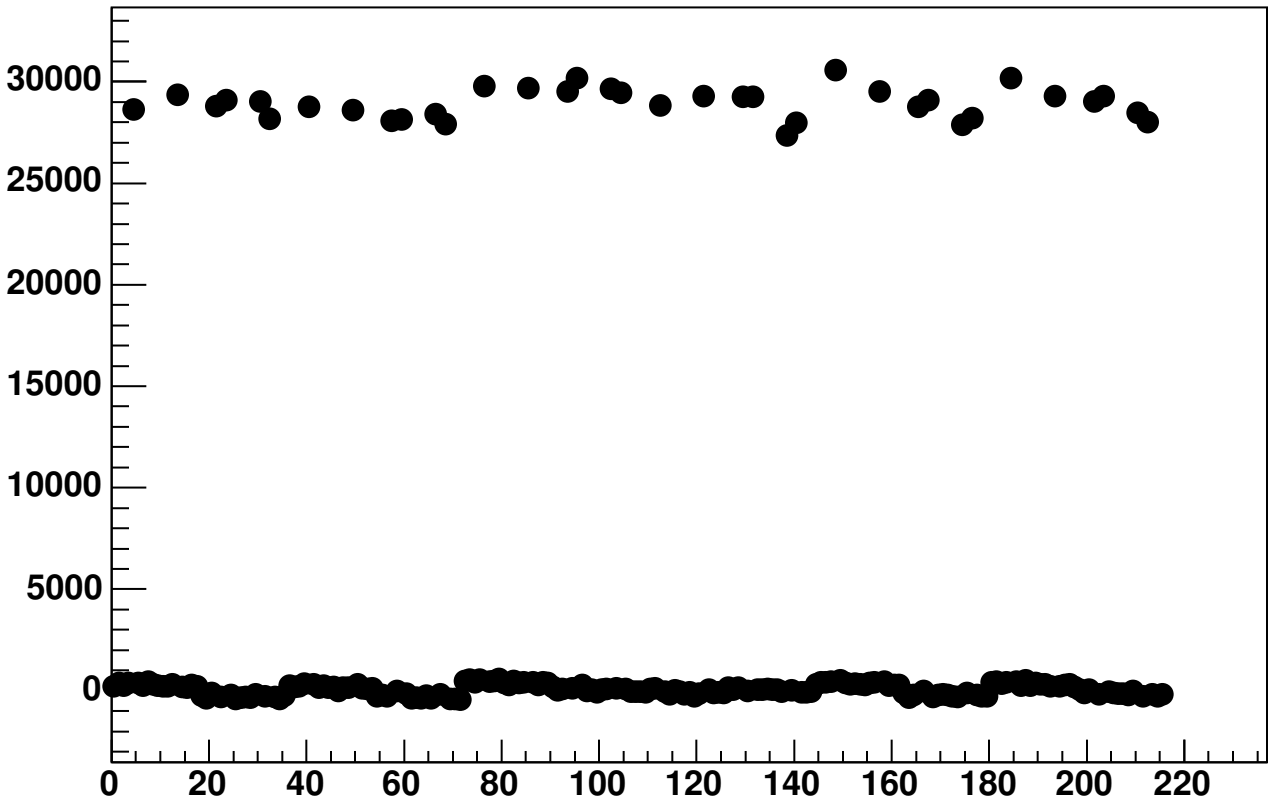
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



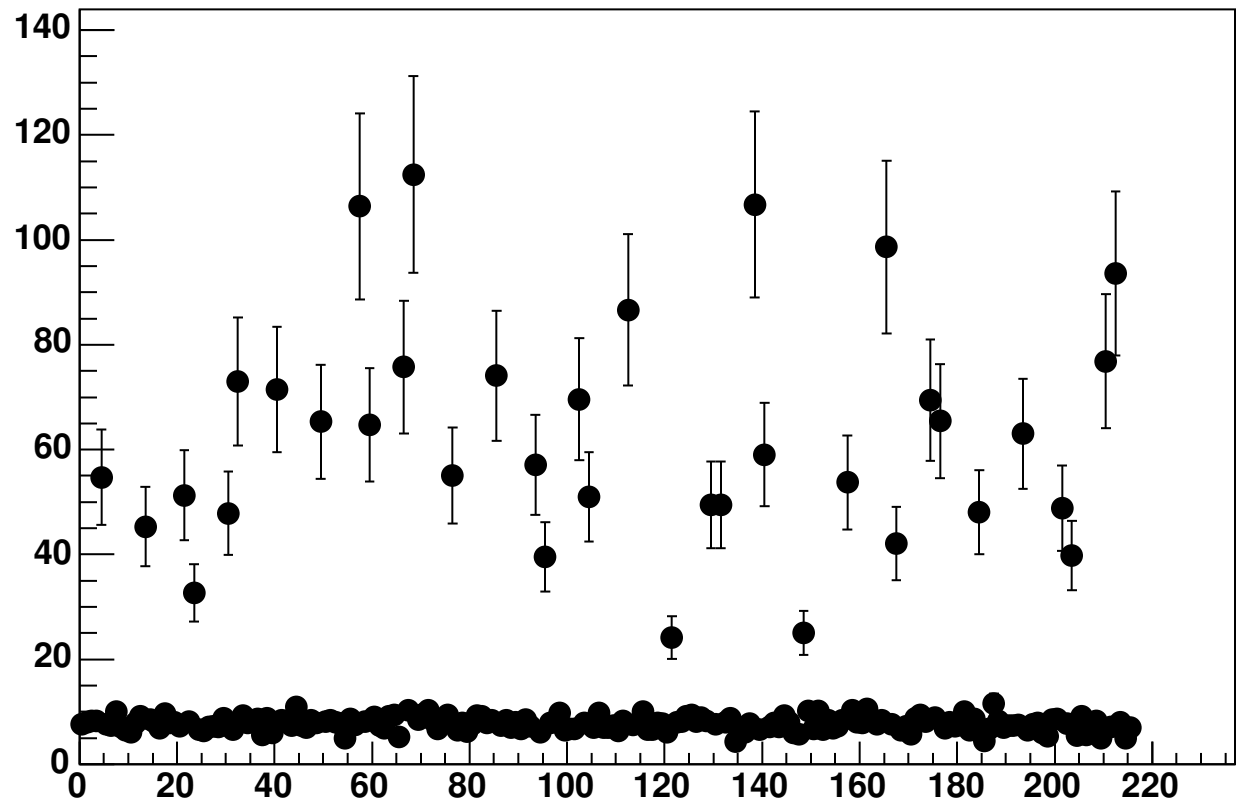
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



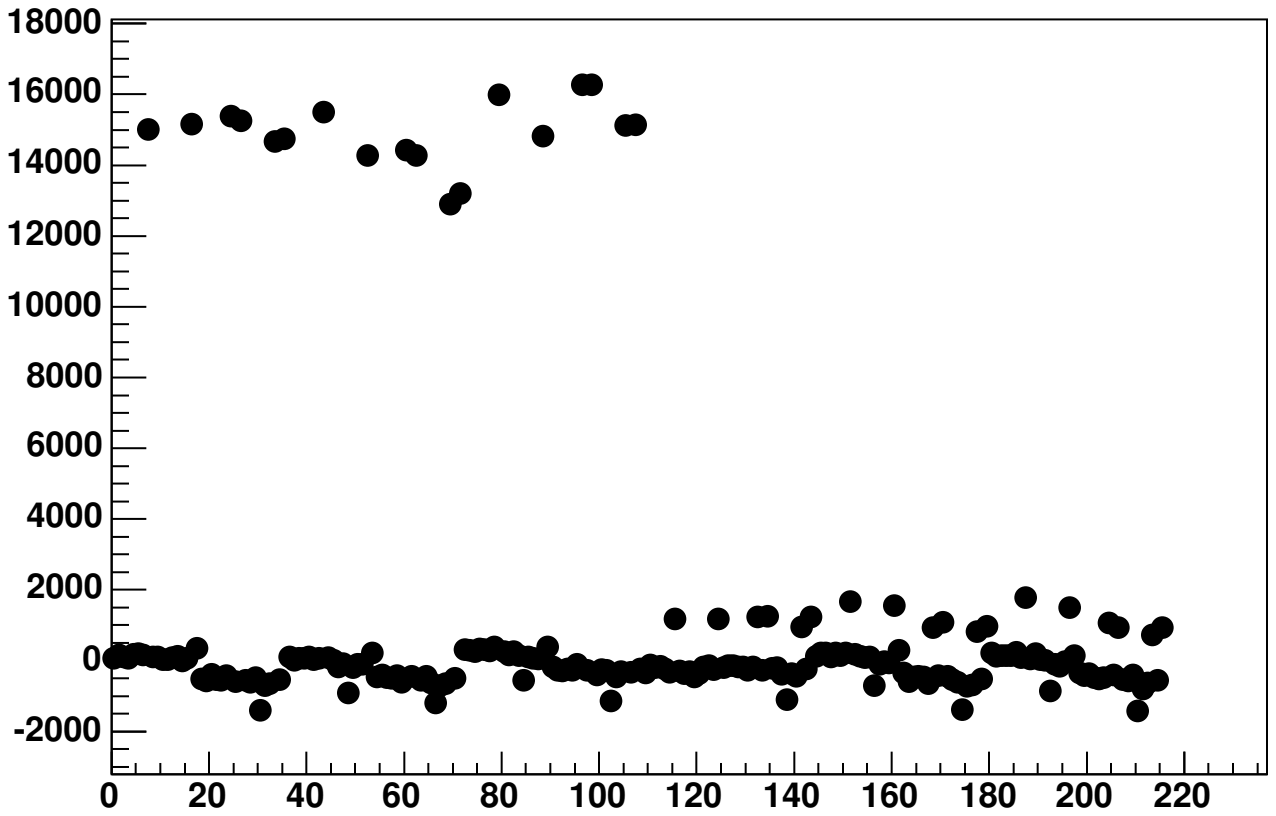
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



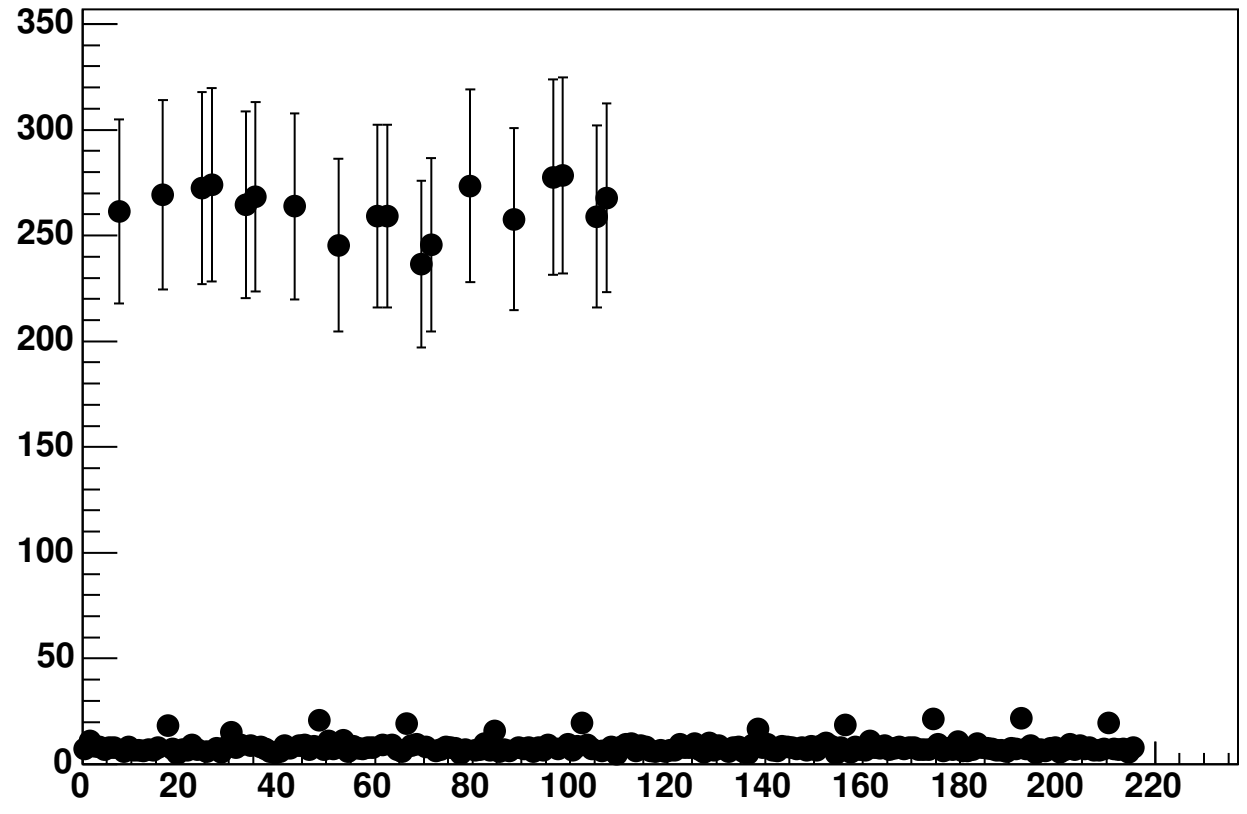
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



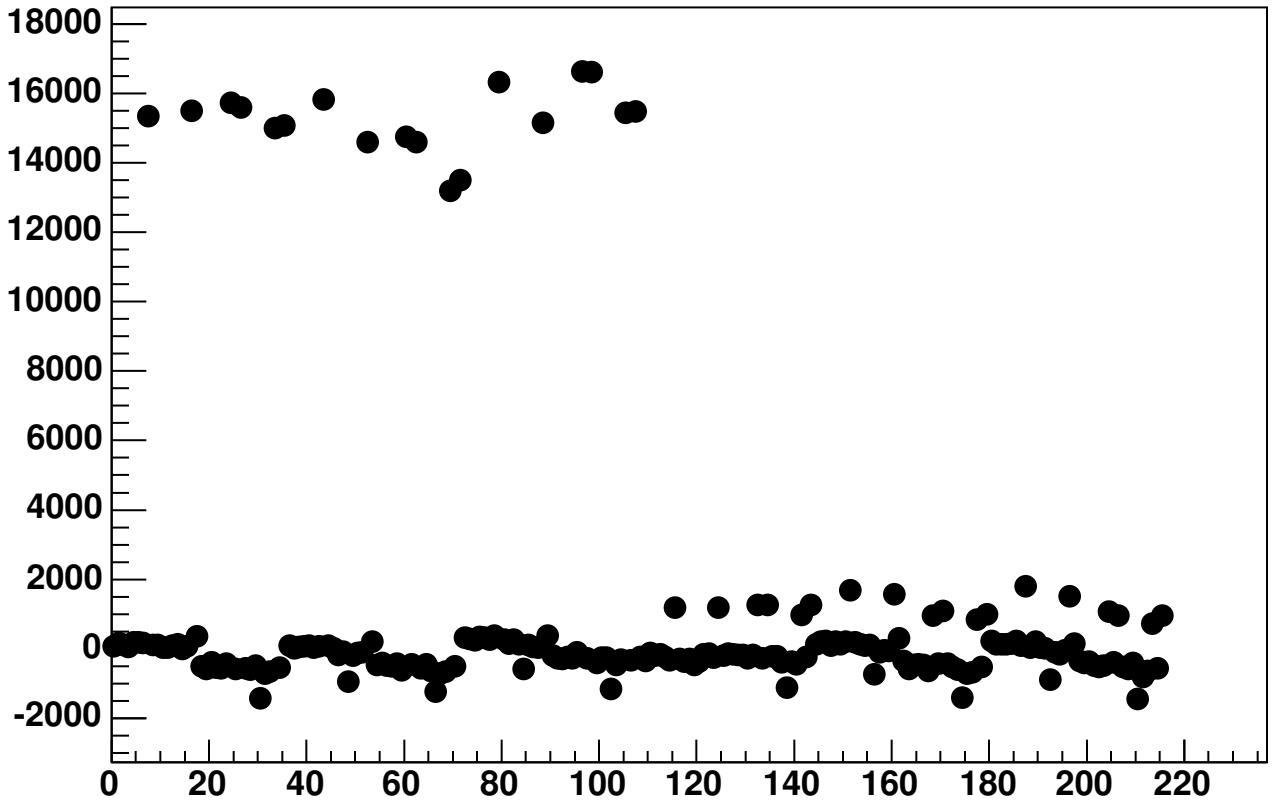
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



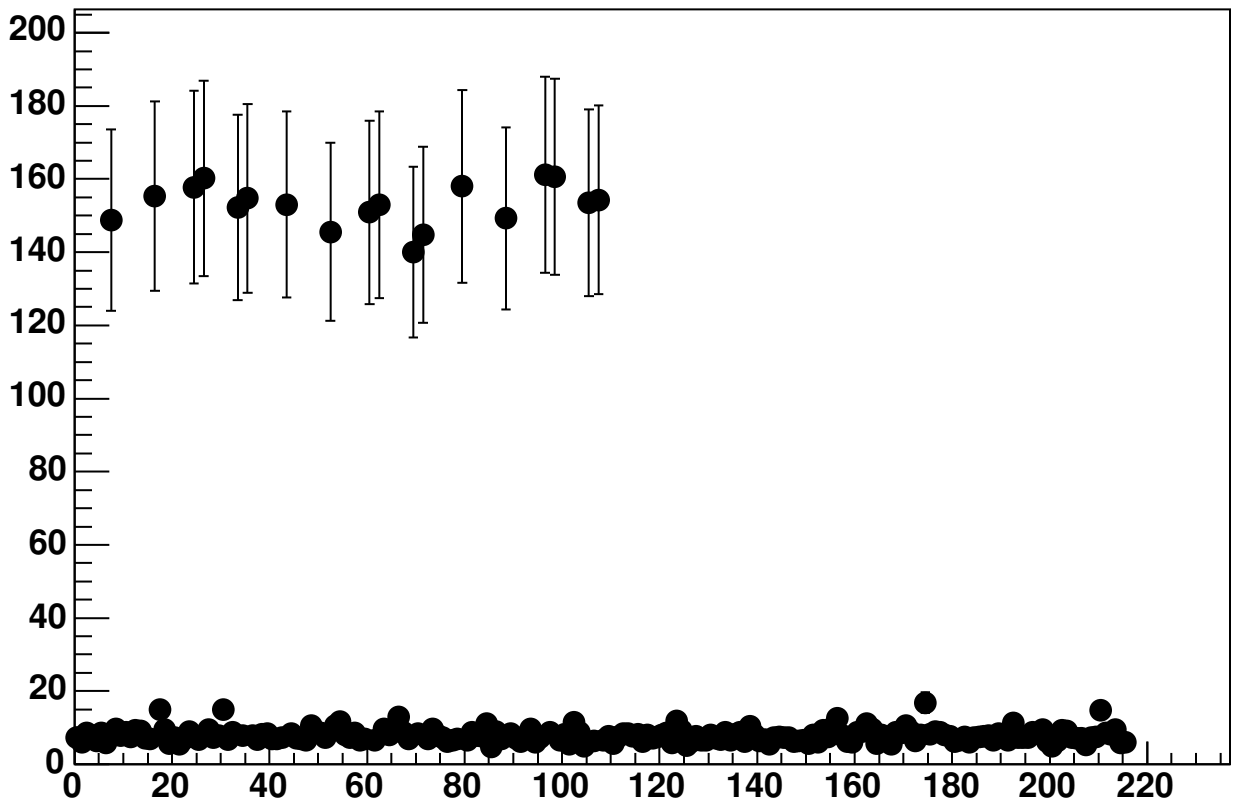
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



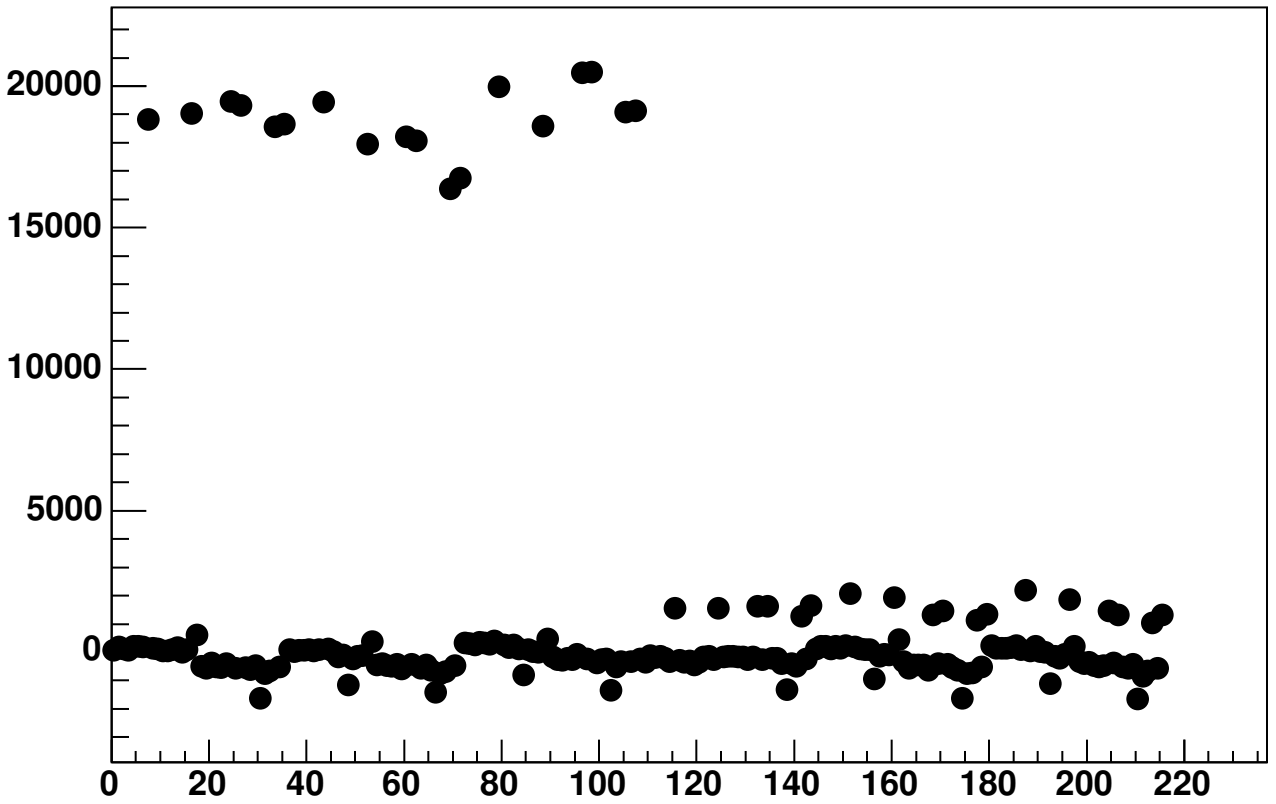
Enable 5, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



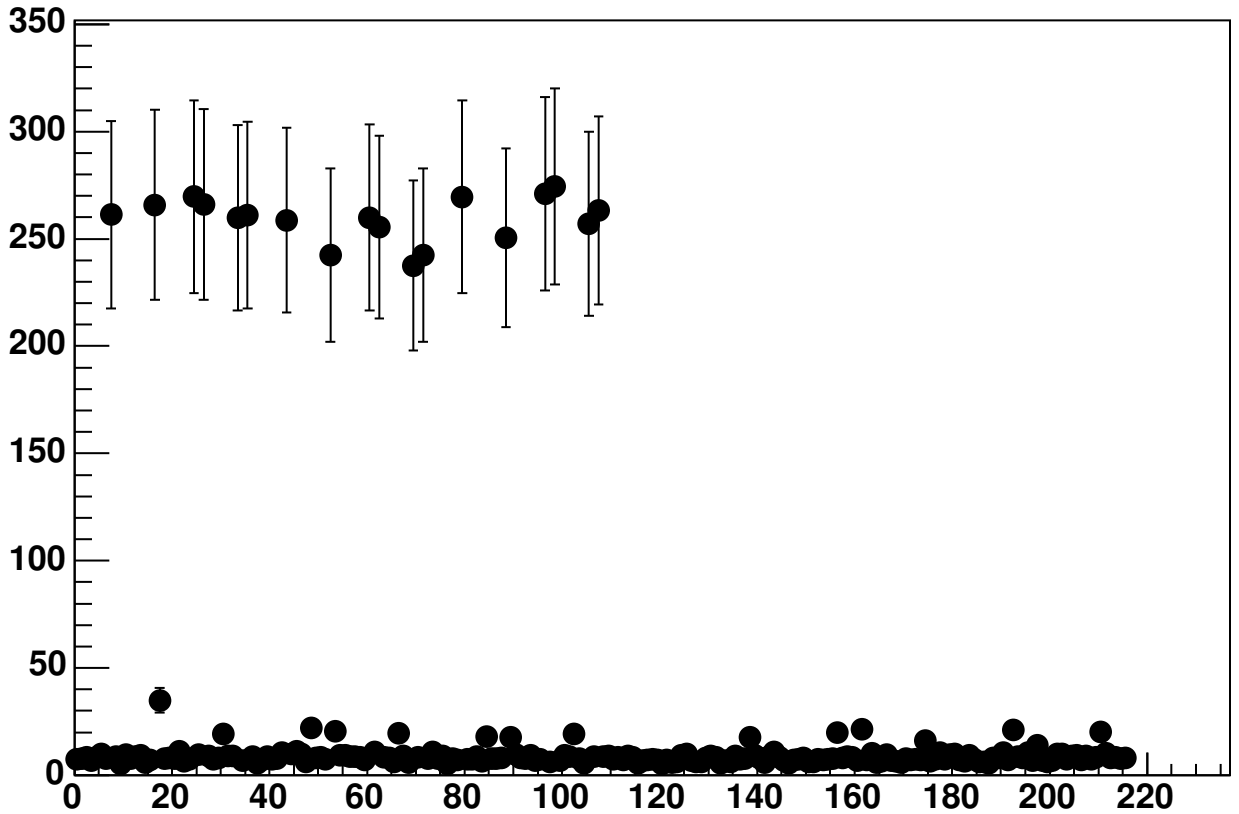
Enable 5, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



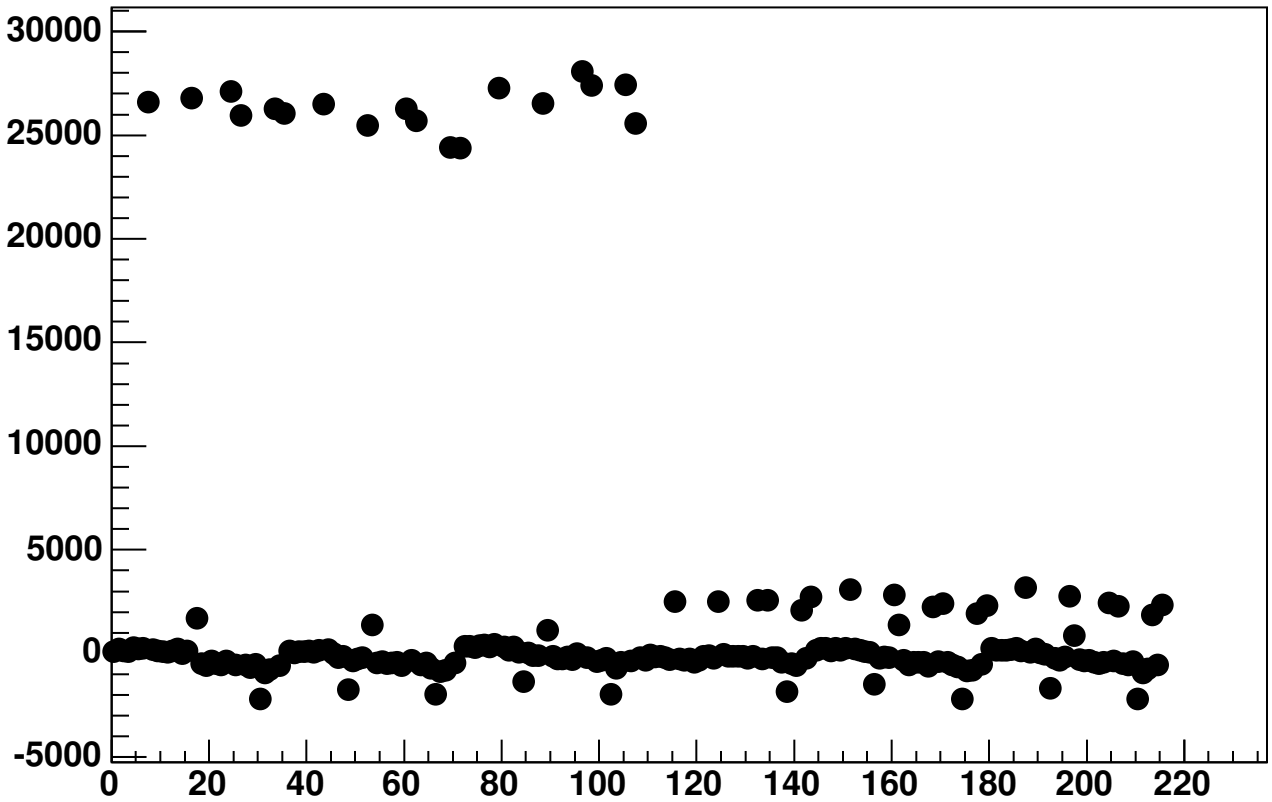
Enable 5, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



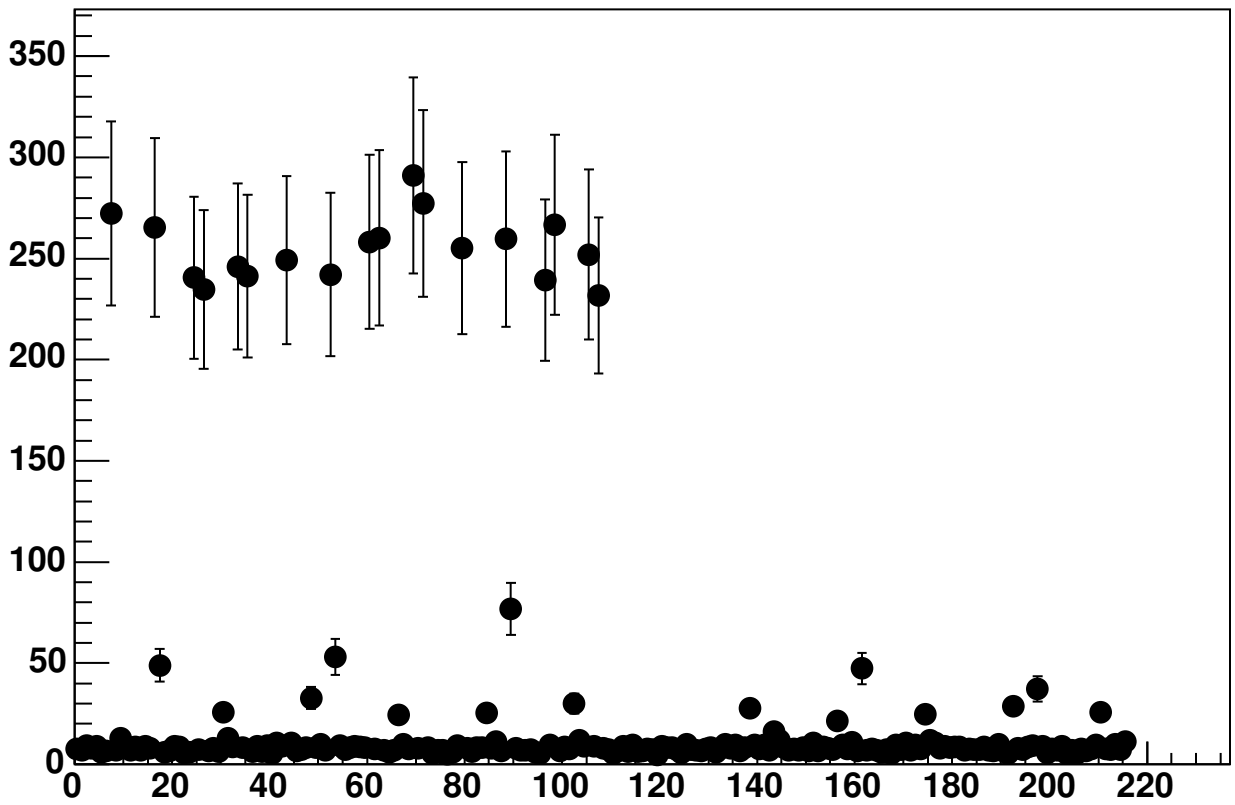
Enable 5, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



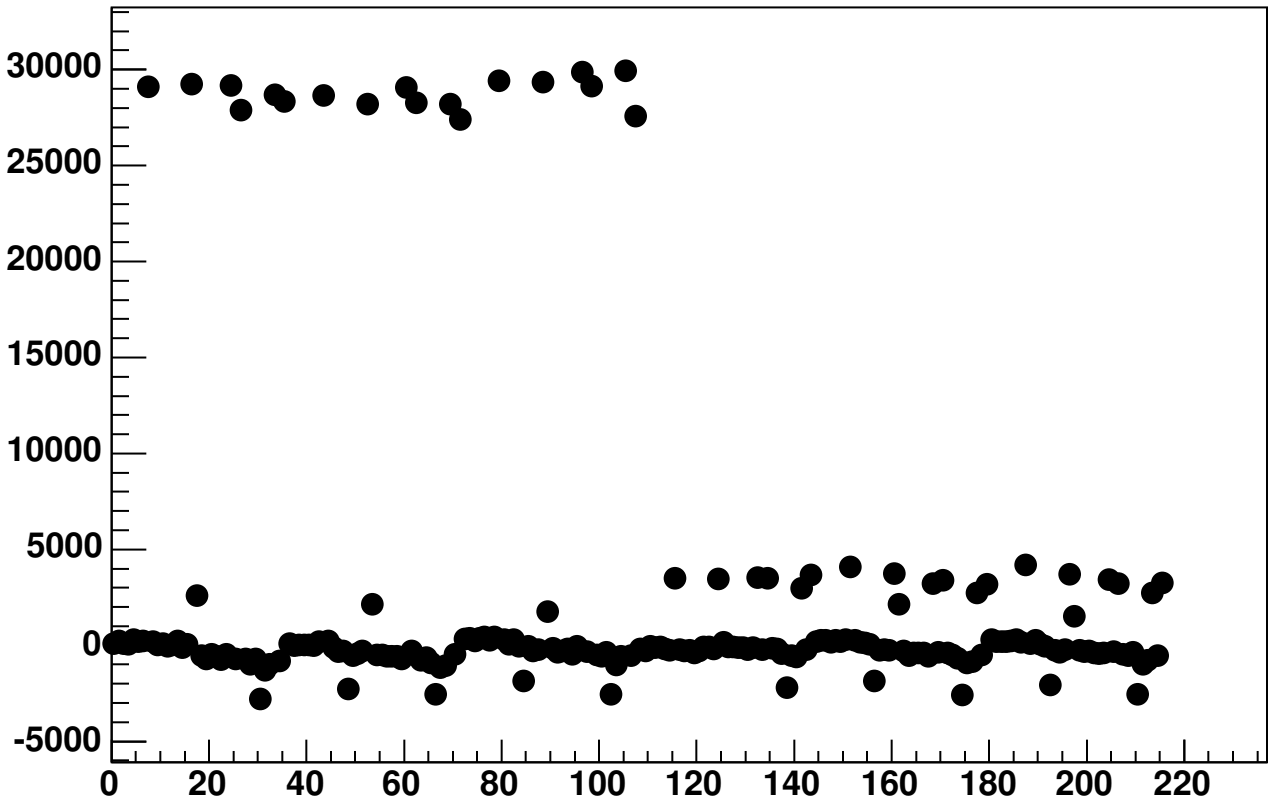
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



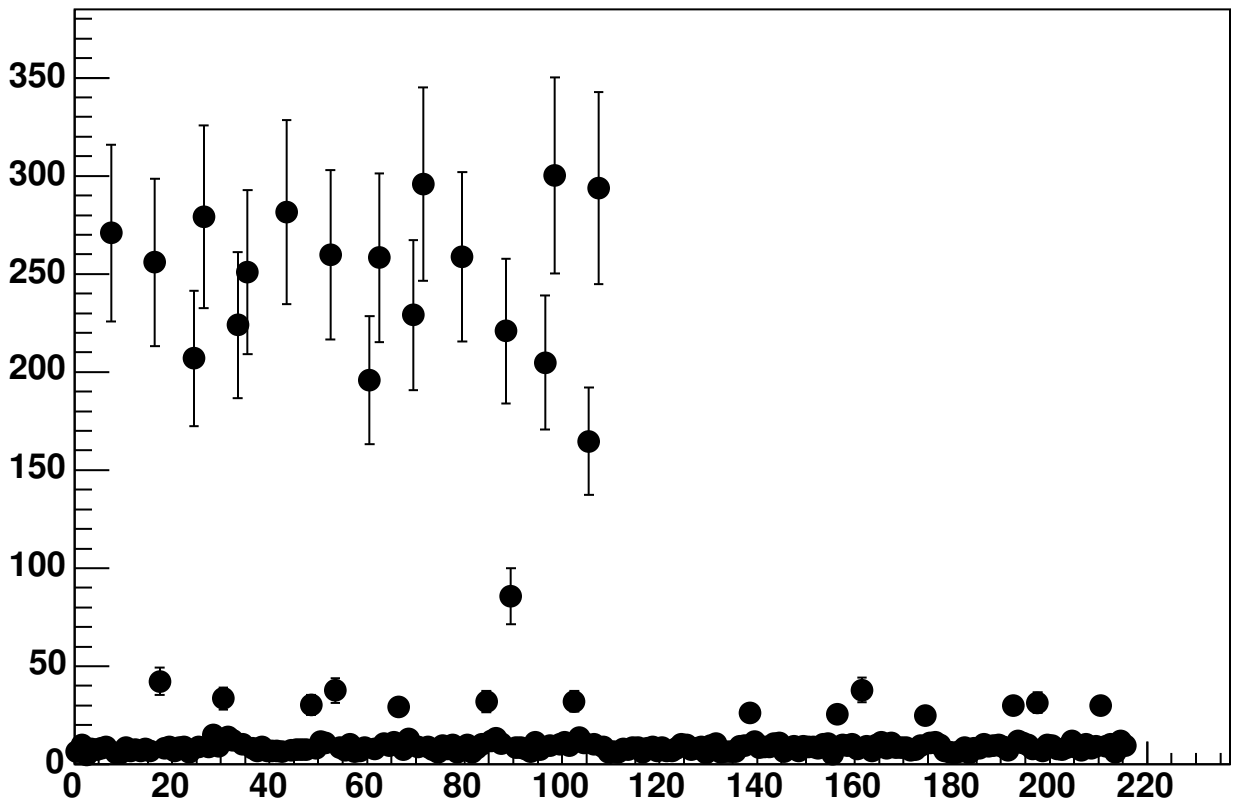
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



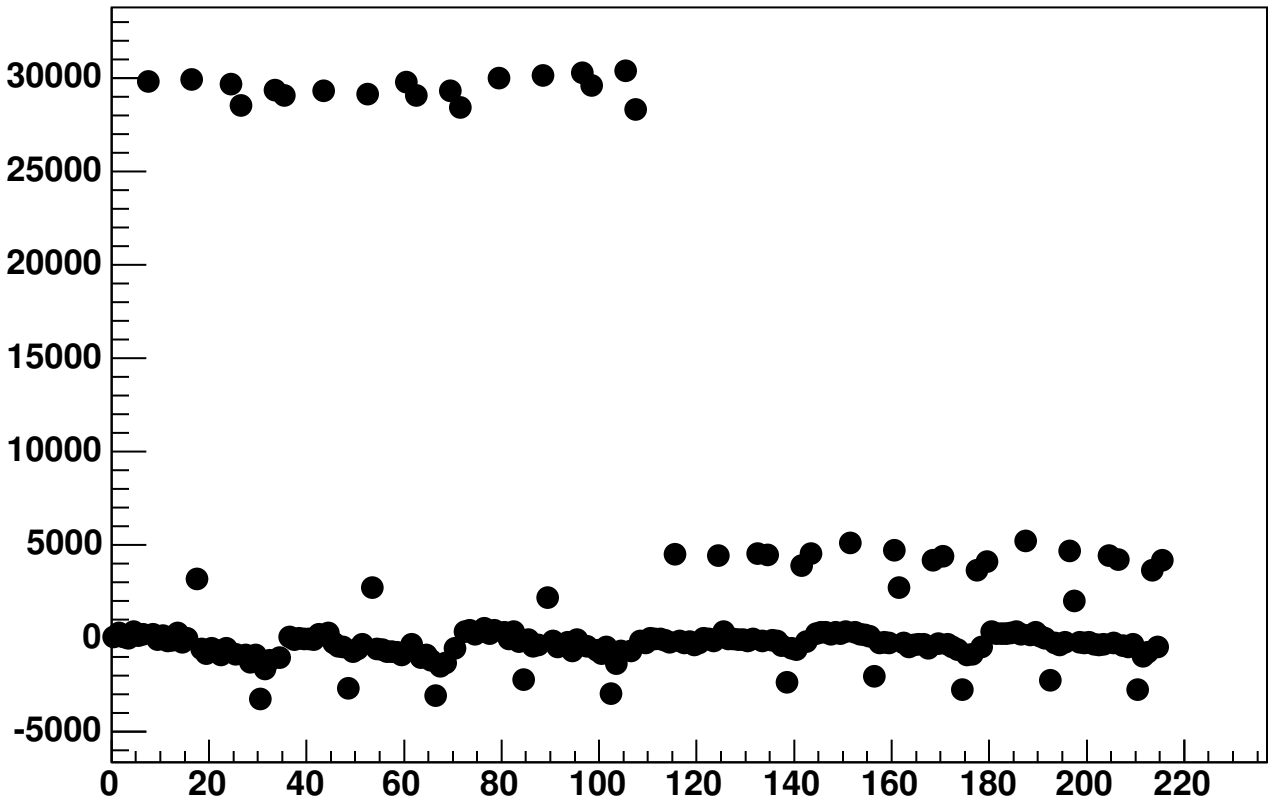
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



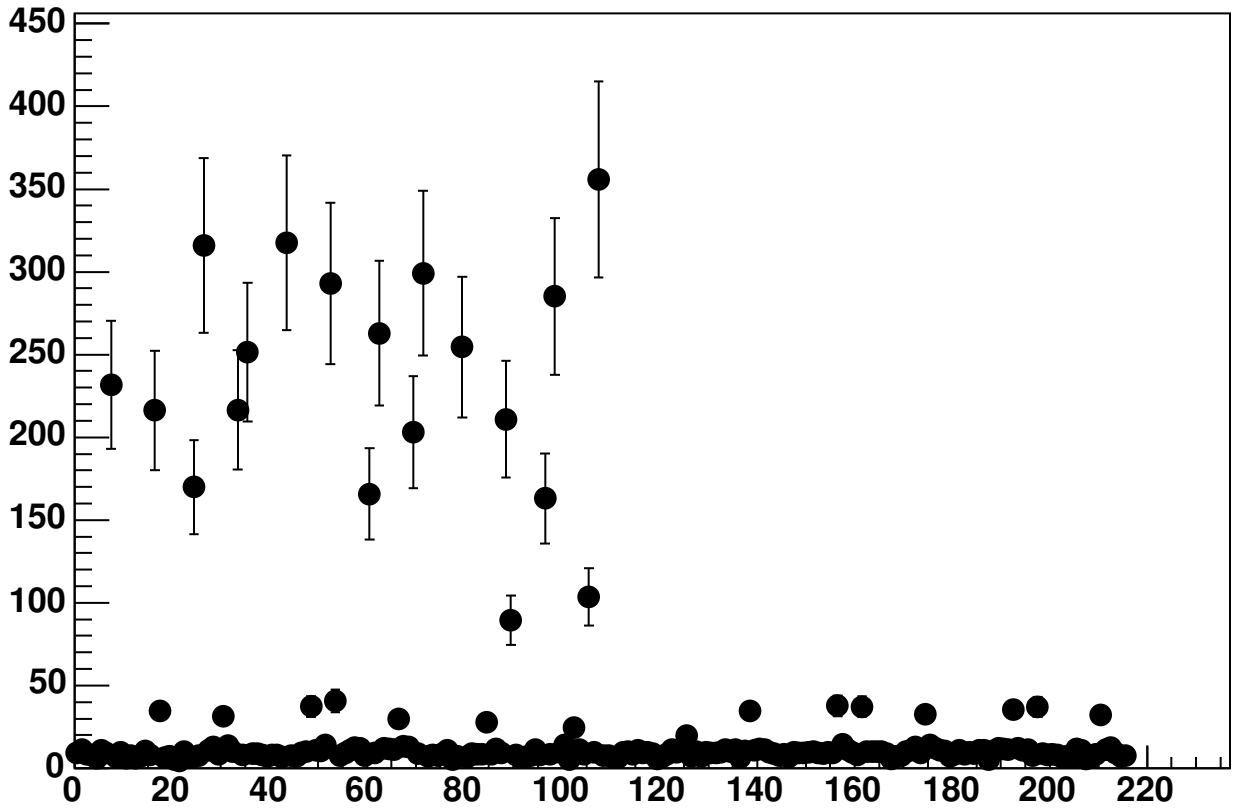
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



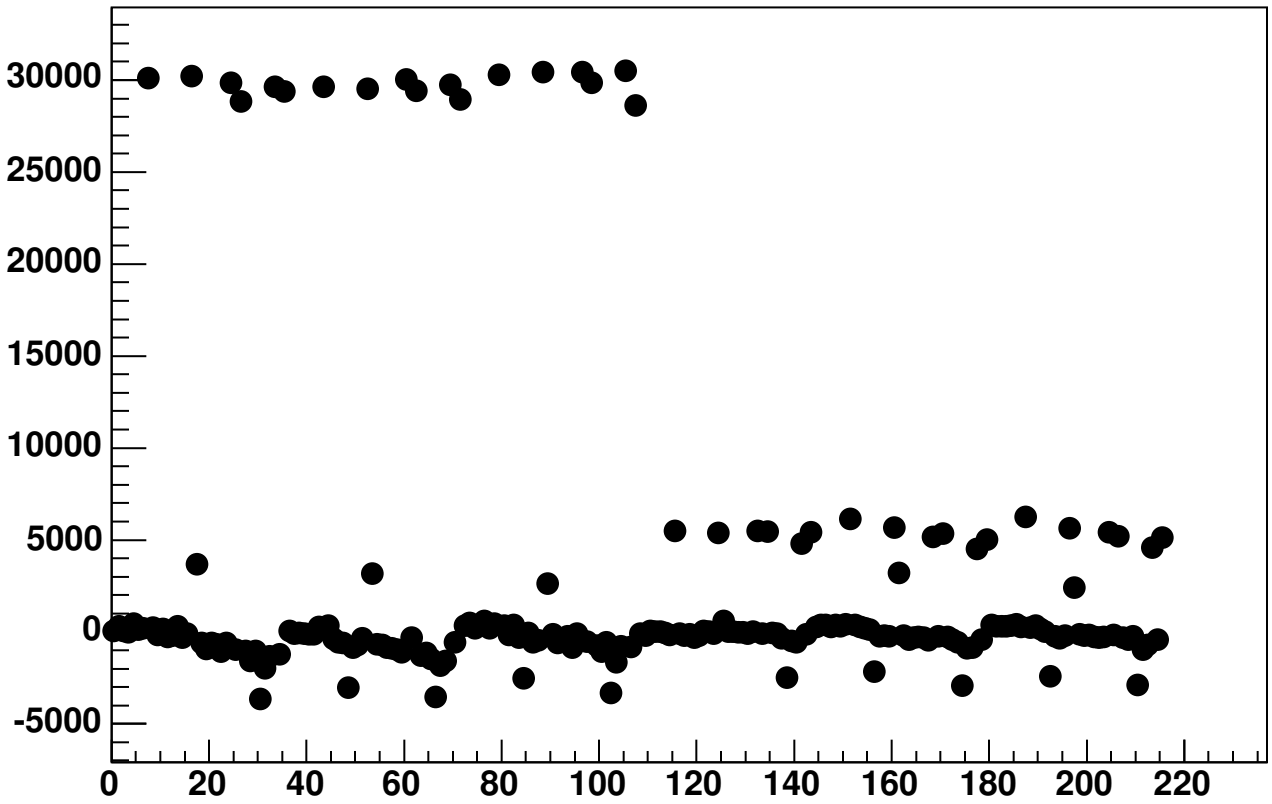
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



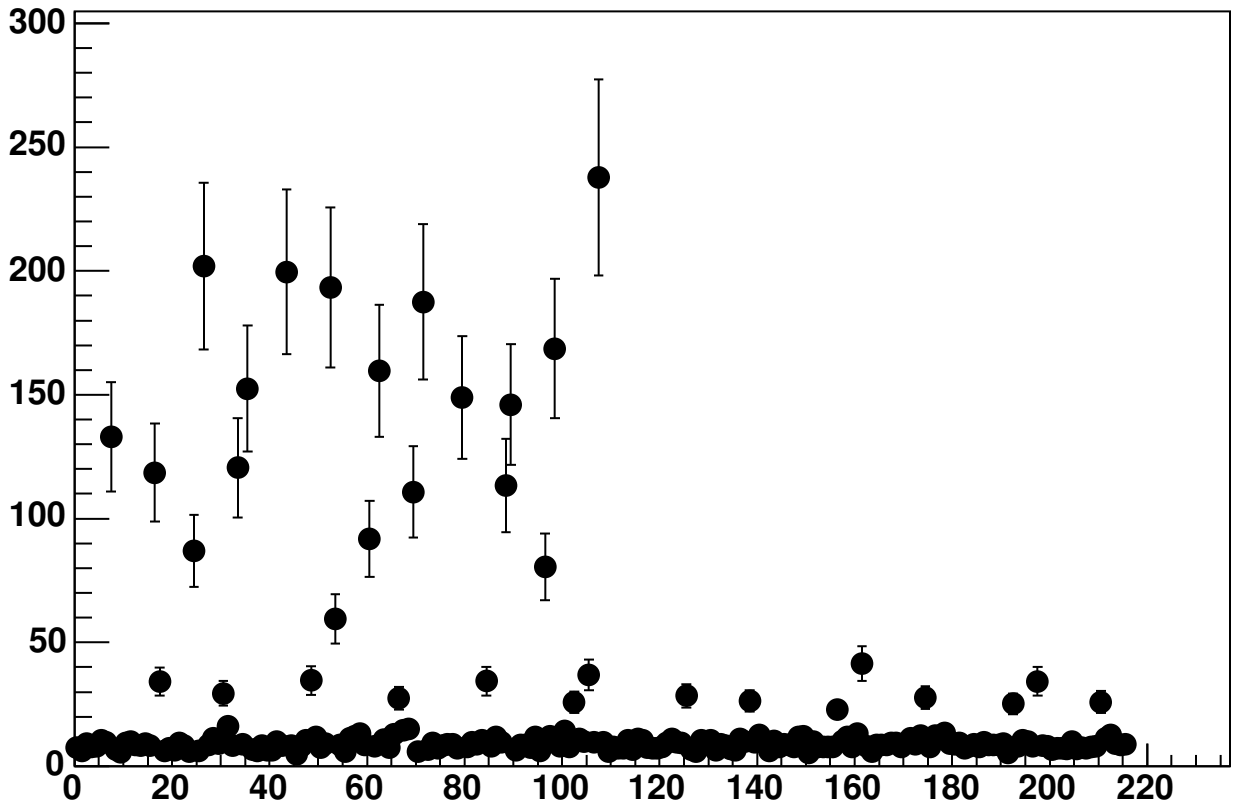
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



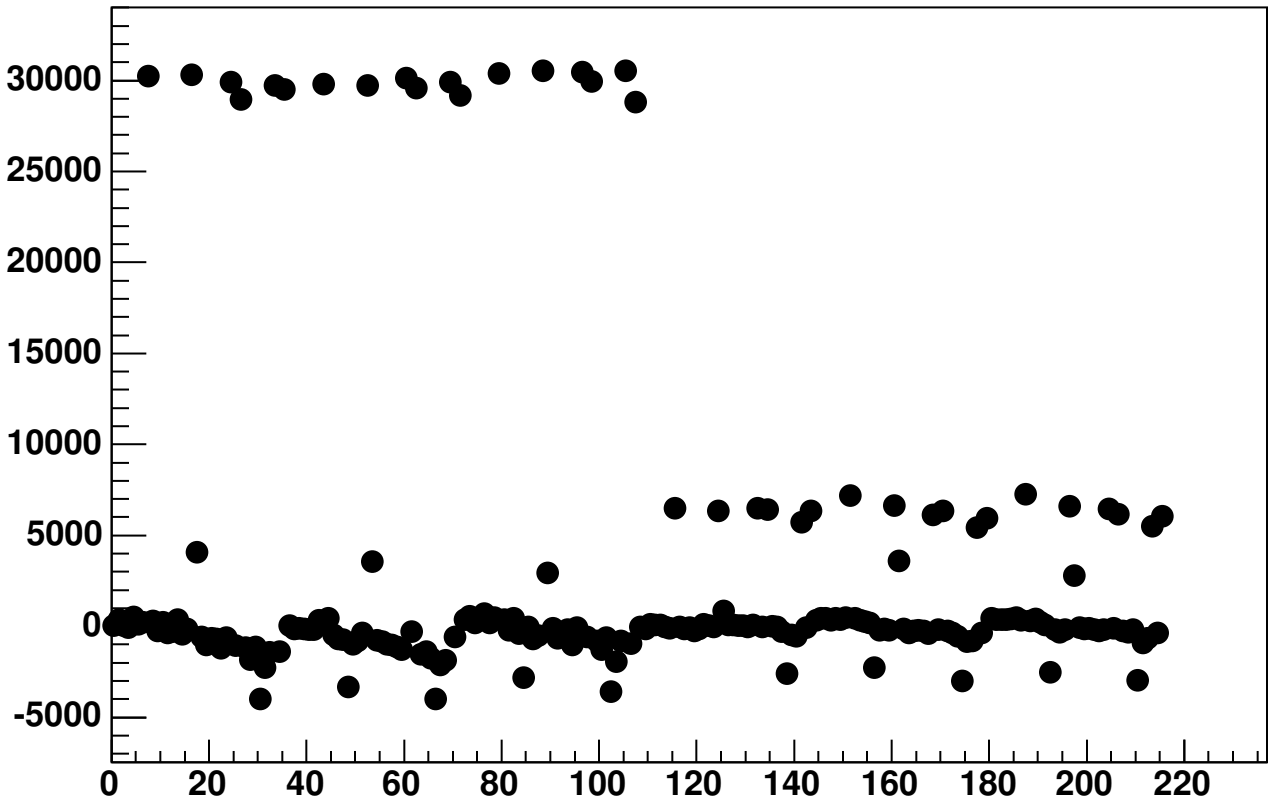
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



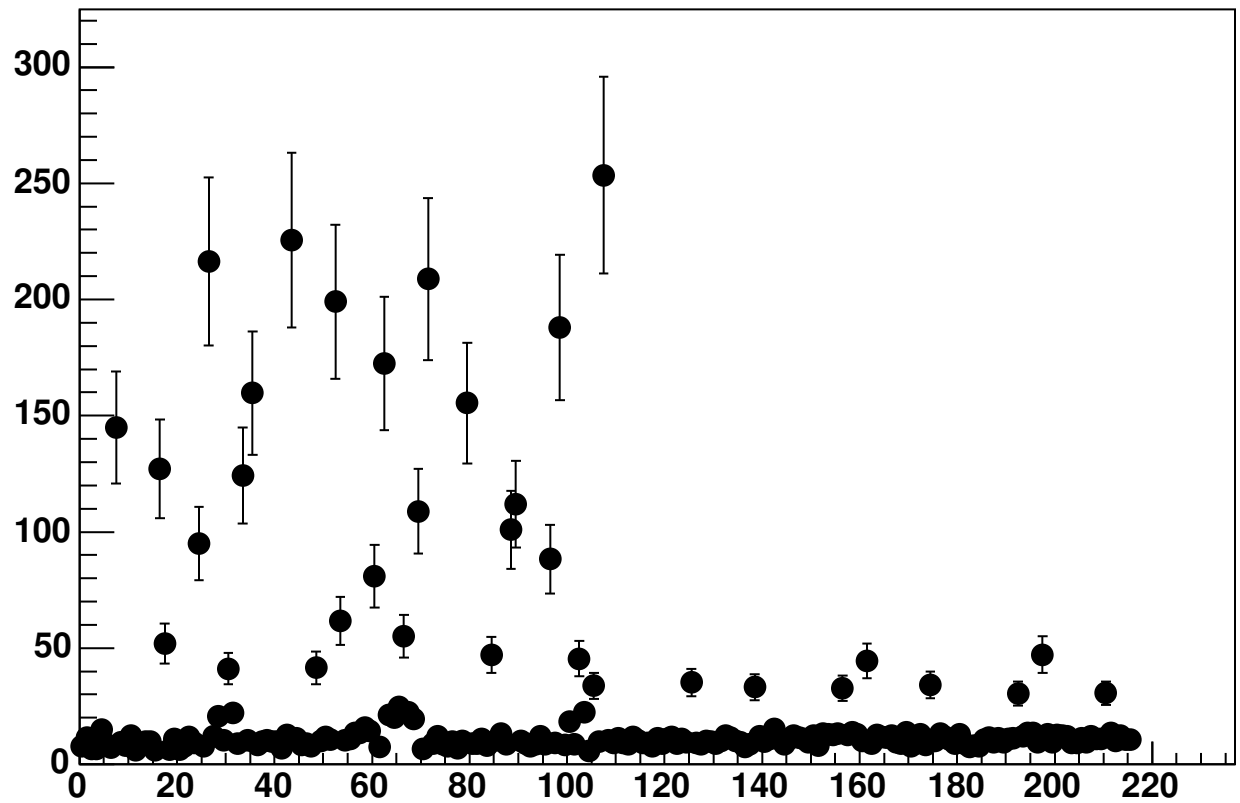
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



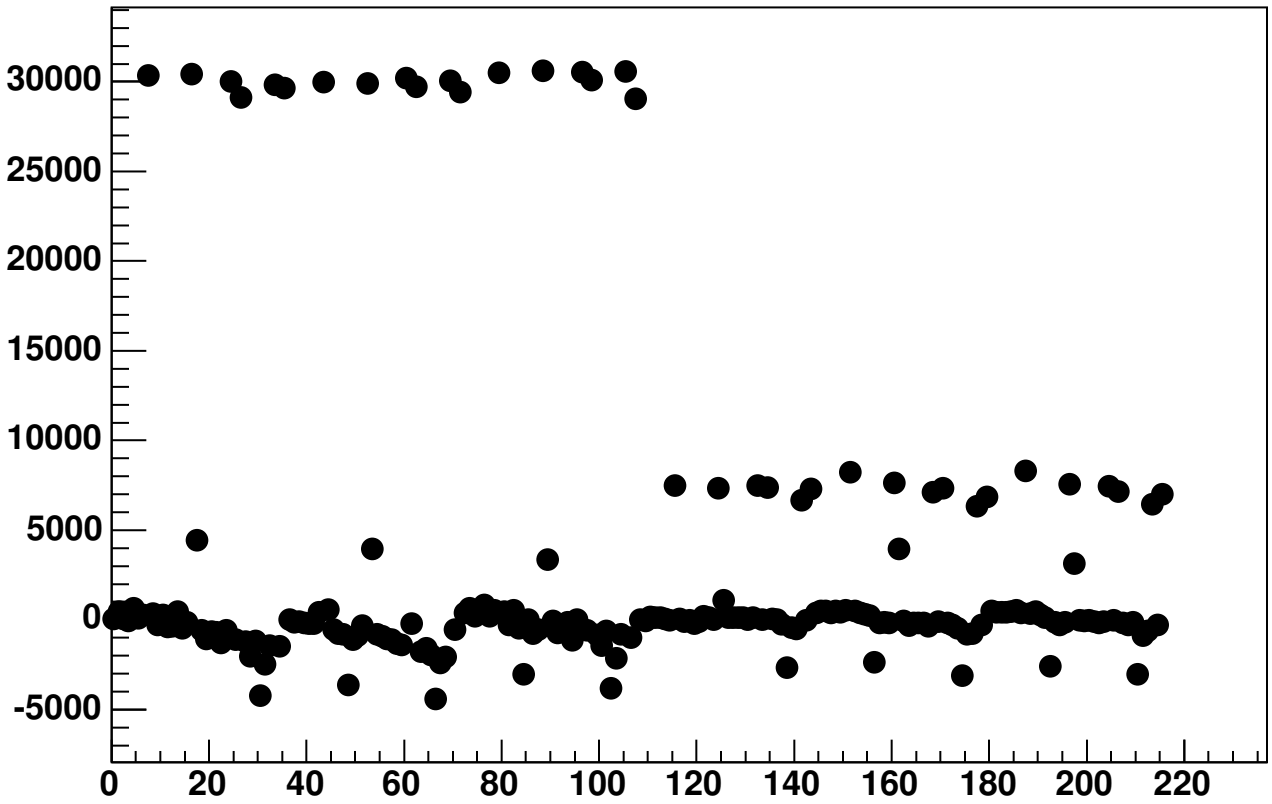
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



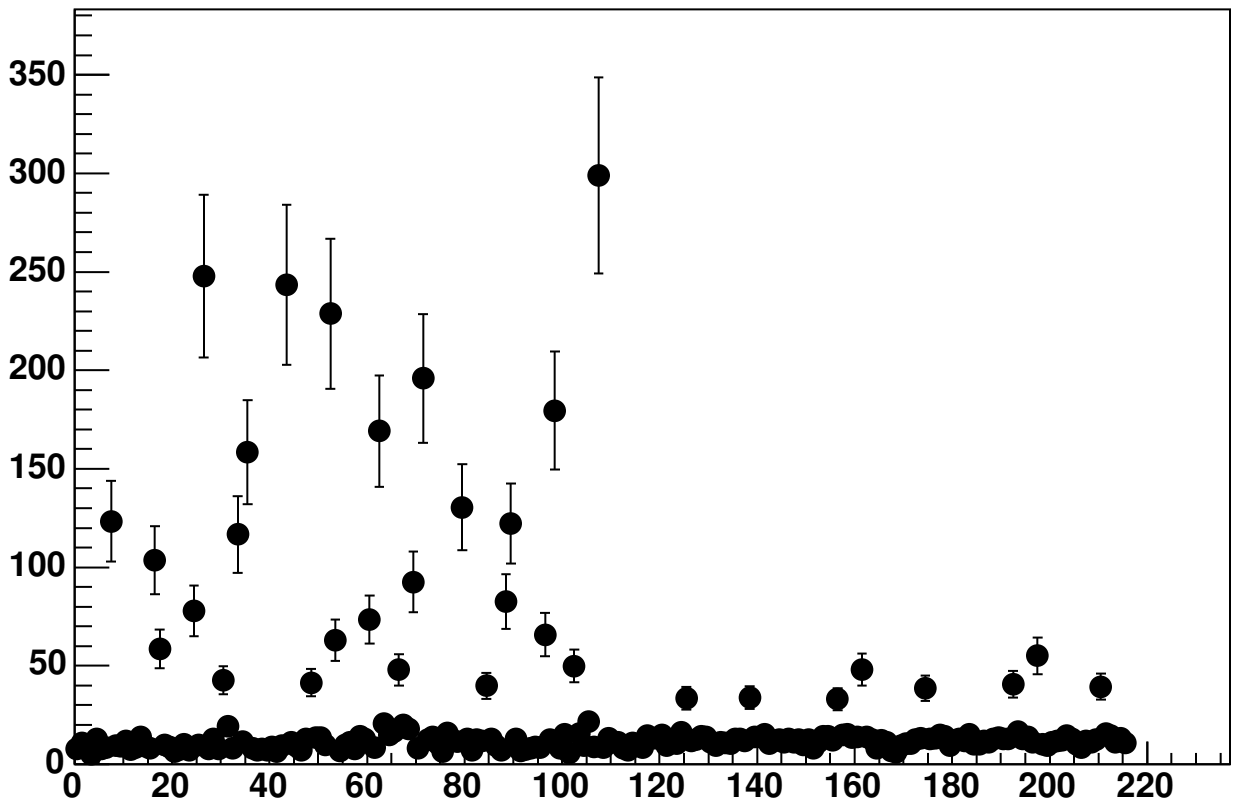
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



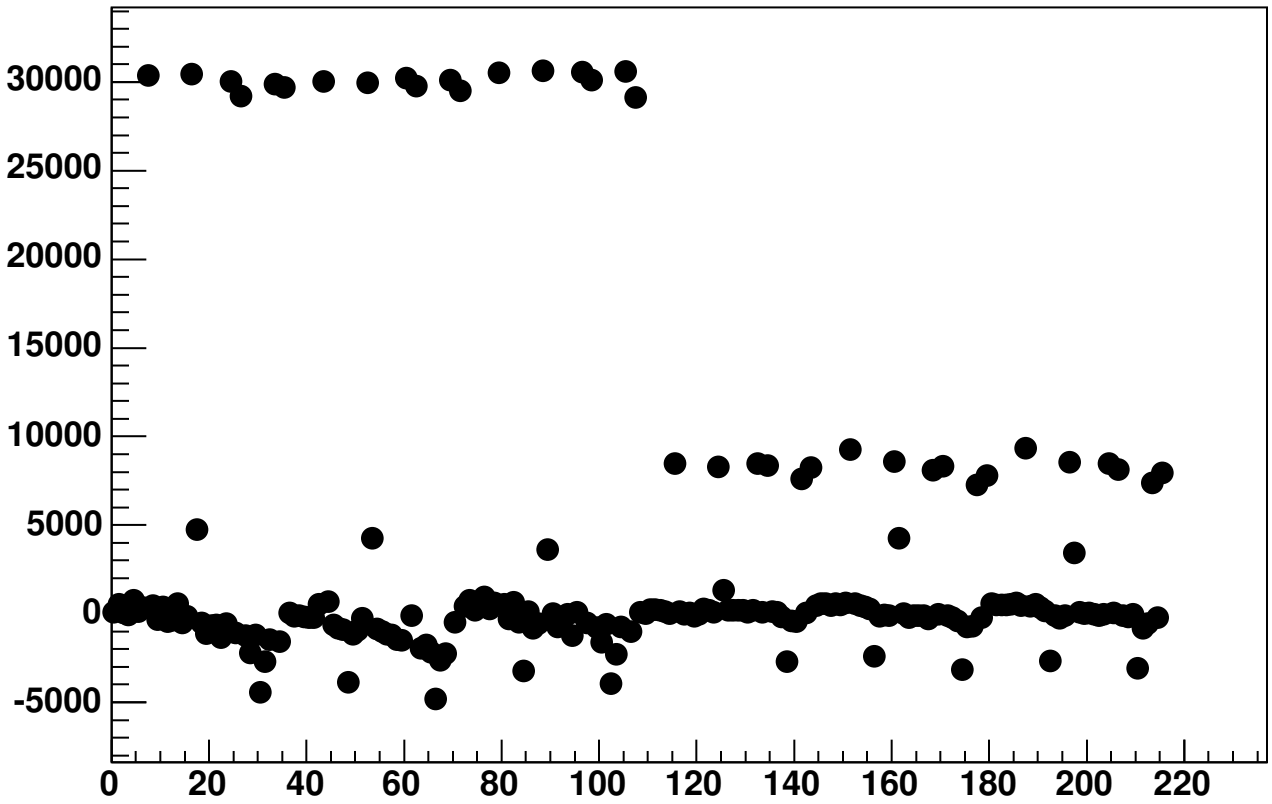
Enable 5, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



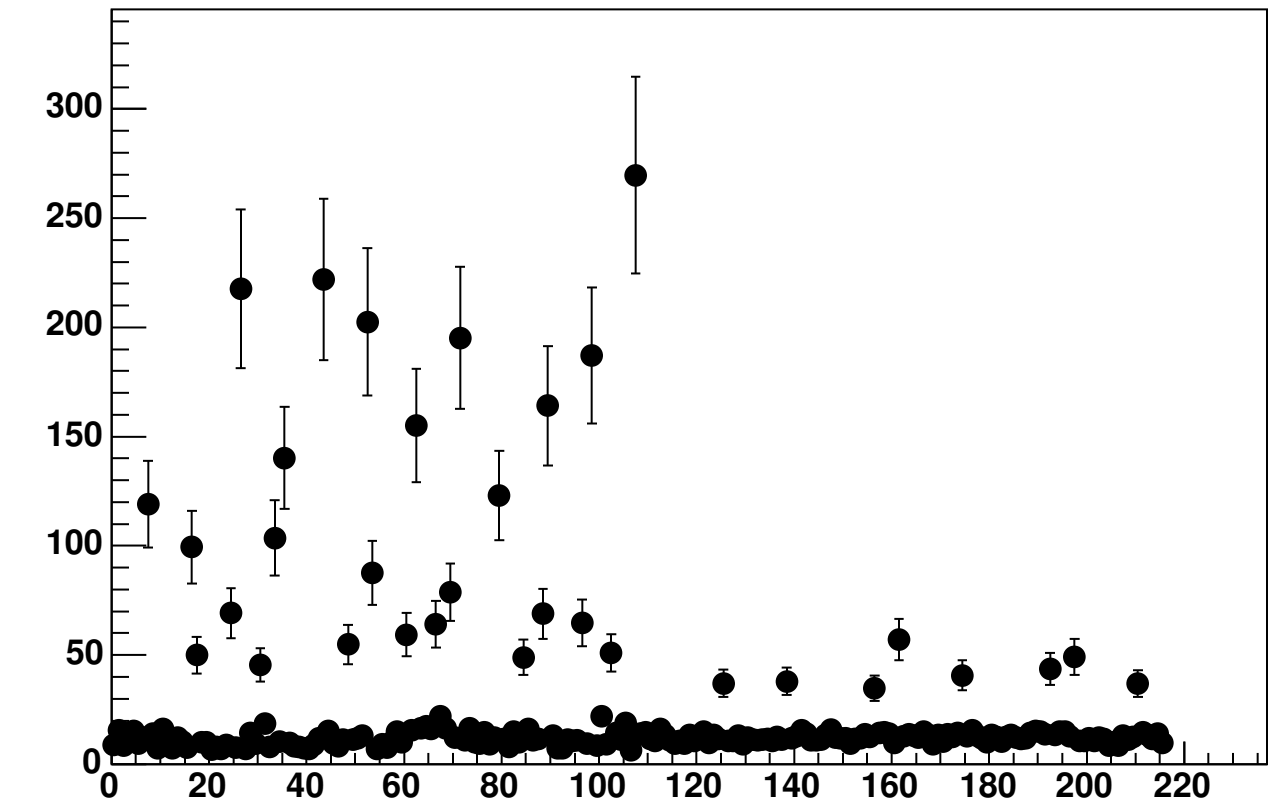
Enable 5, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



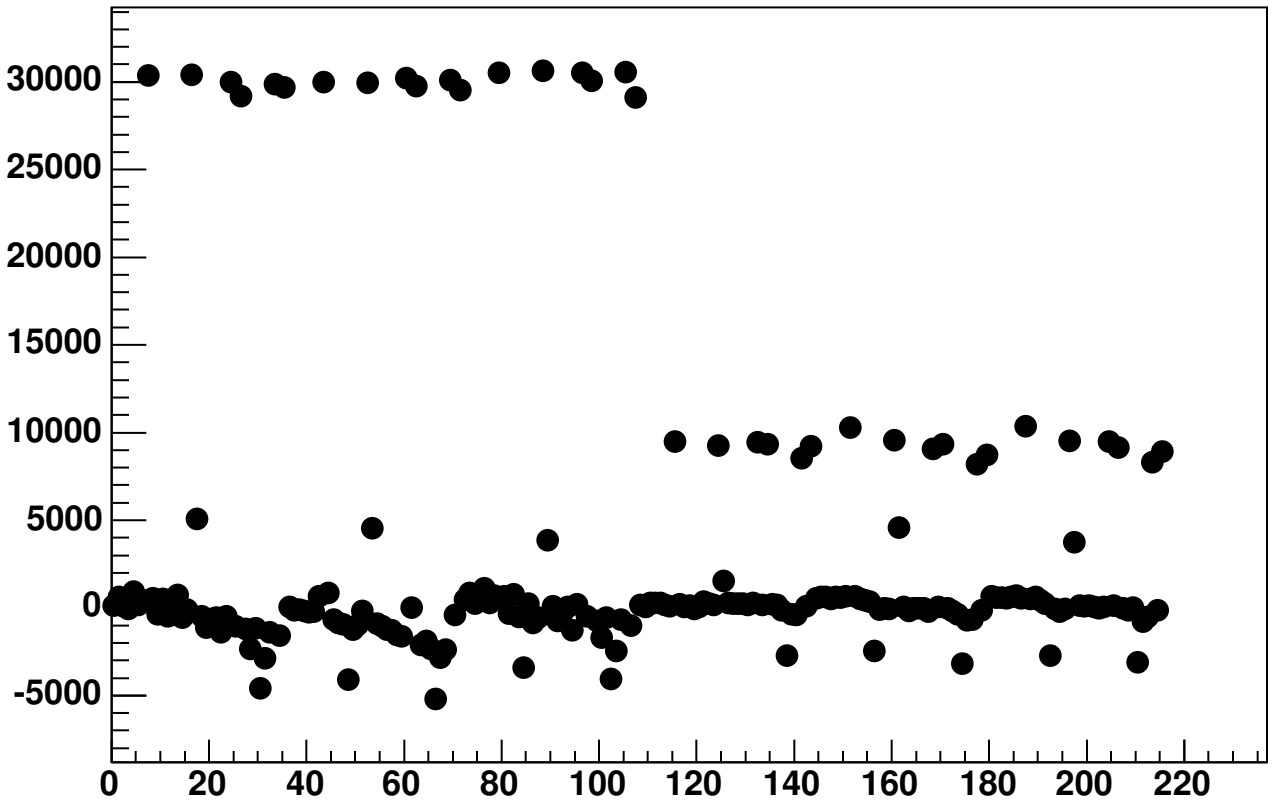
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



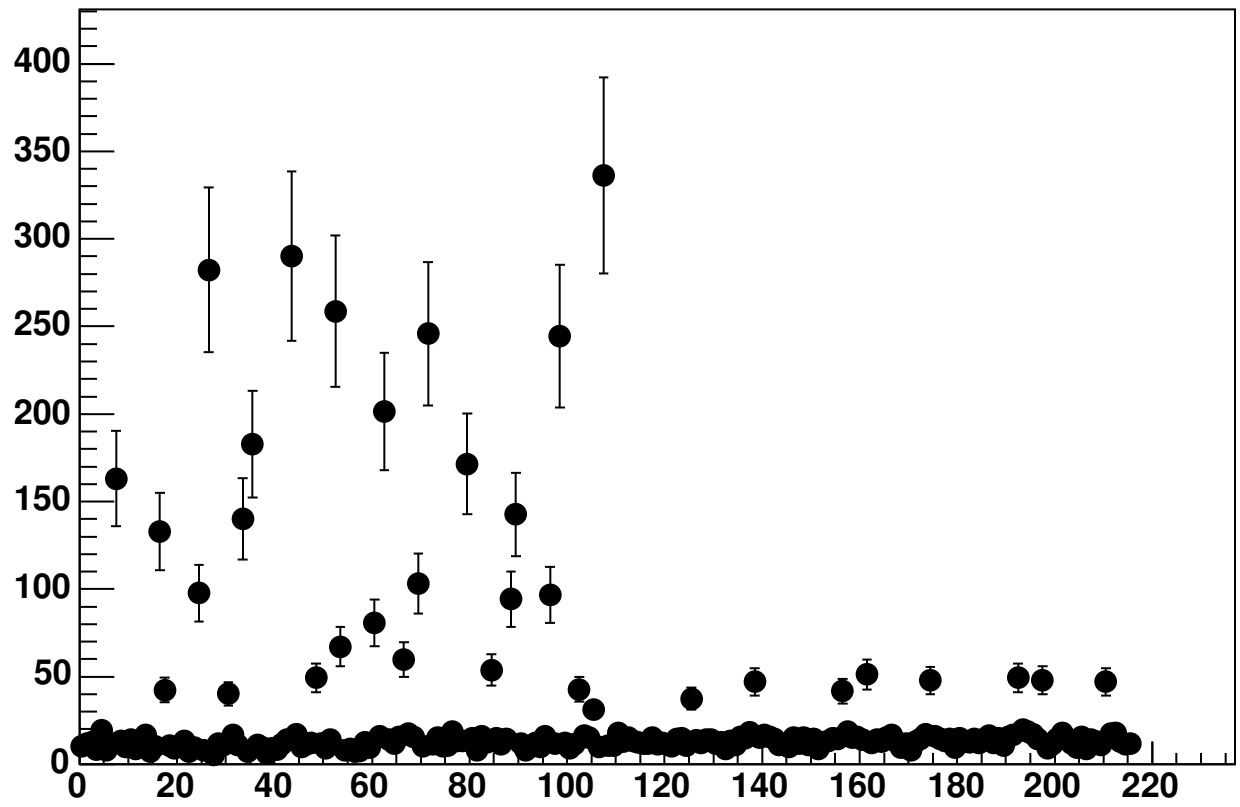
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



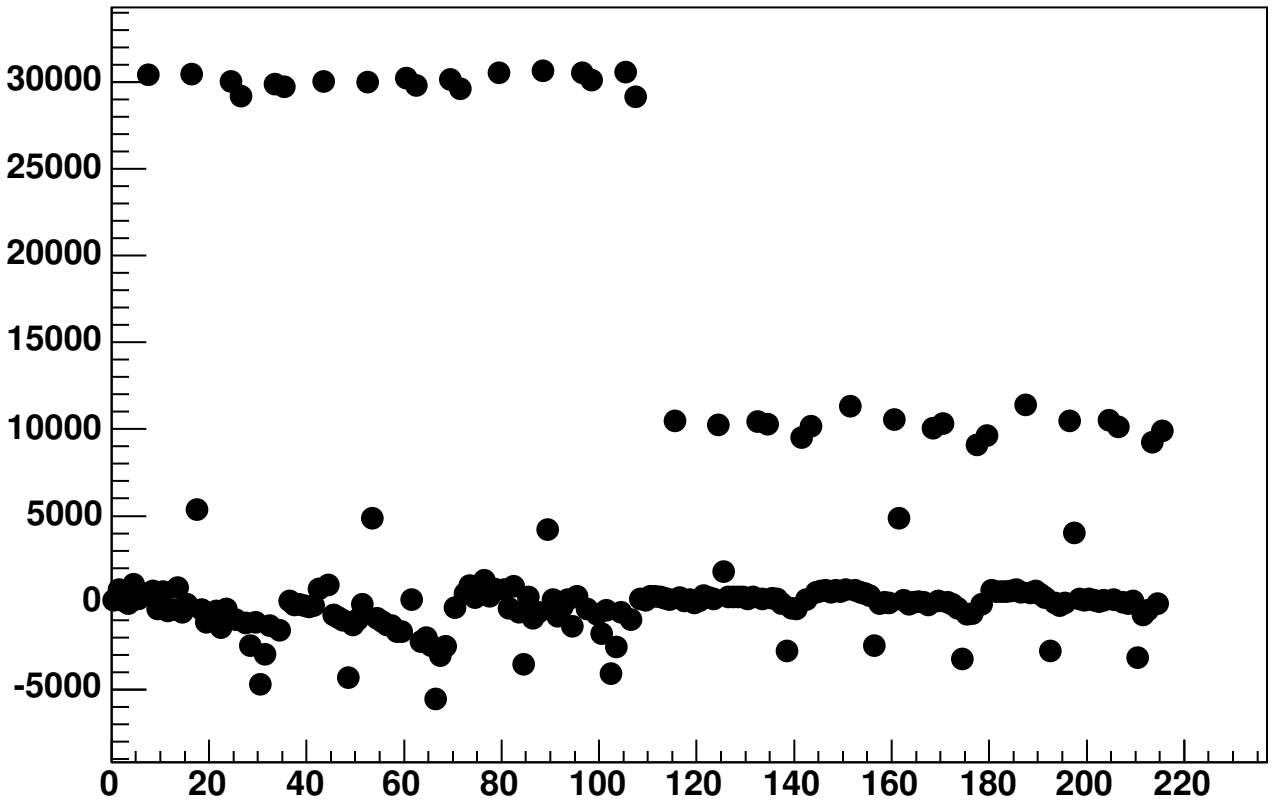
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



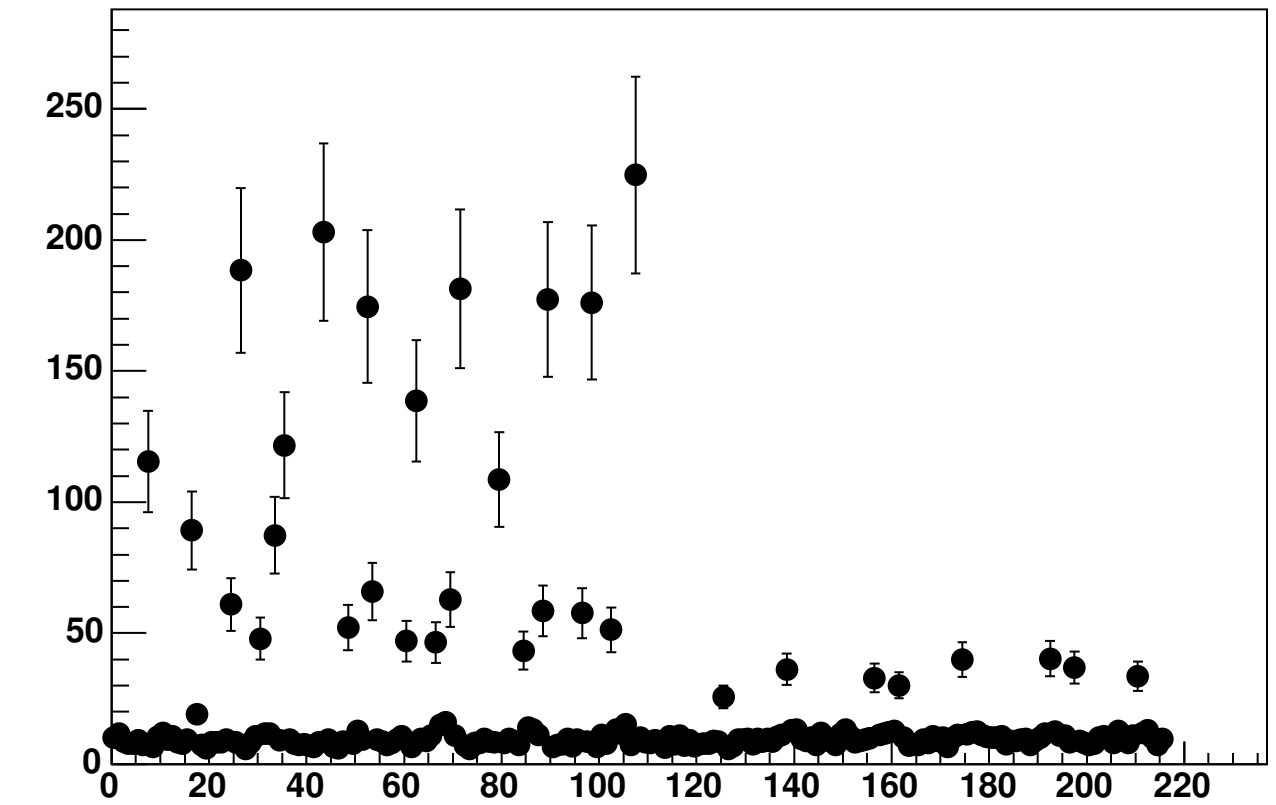
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



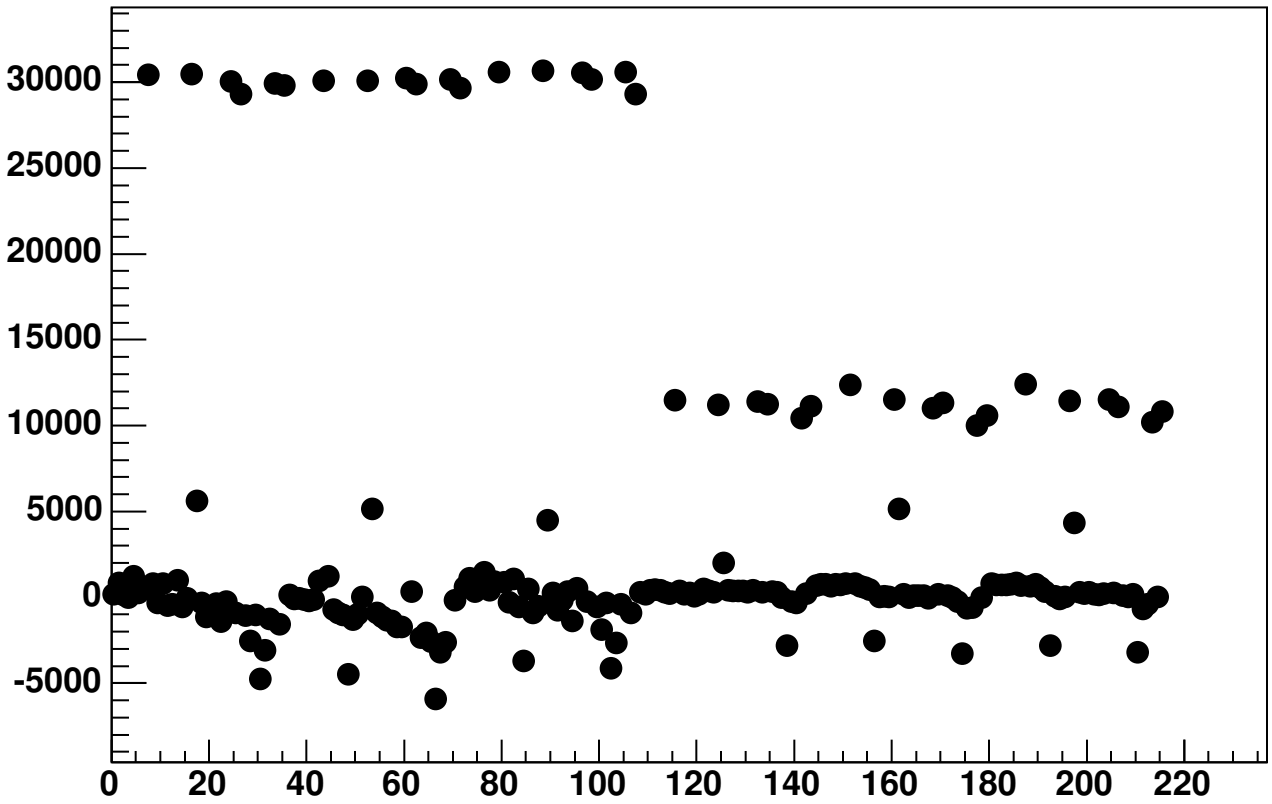
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



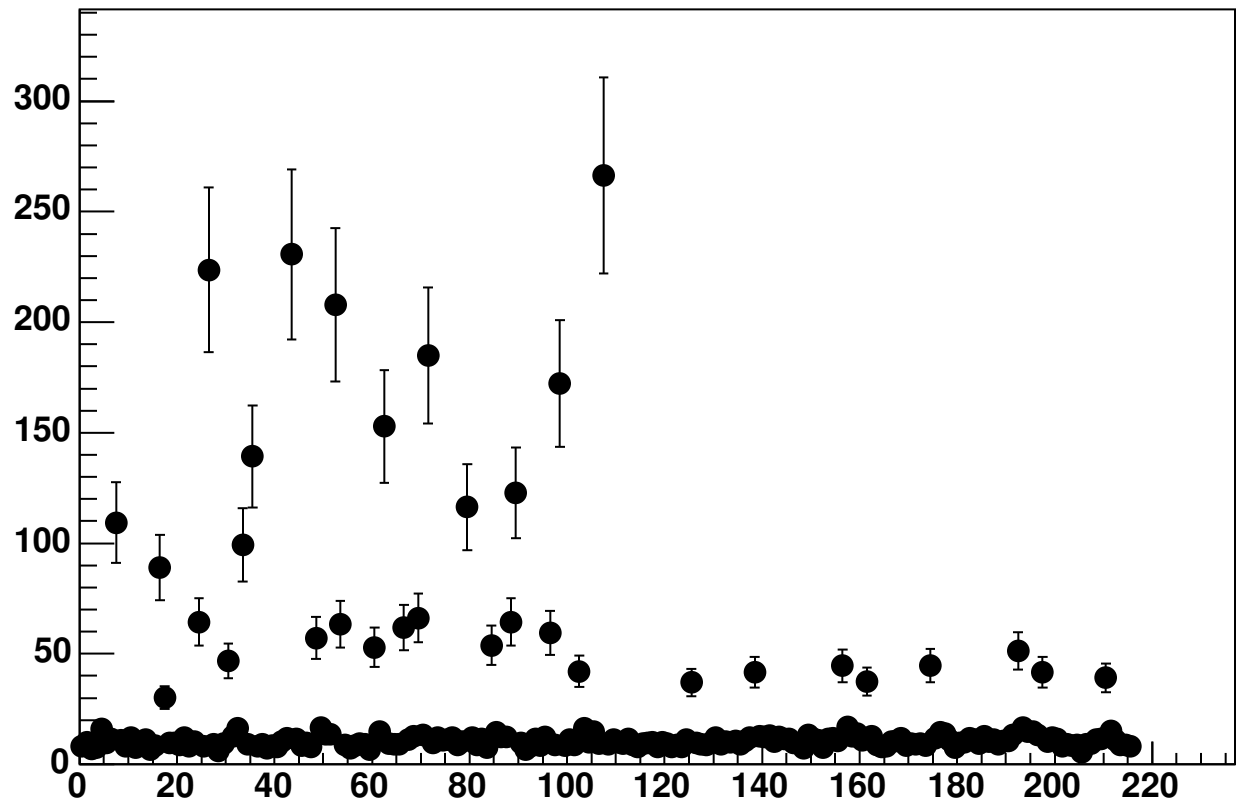
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



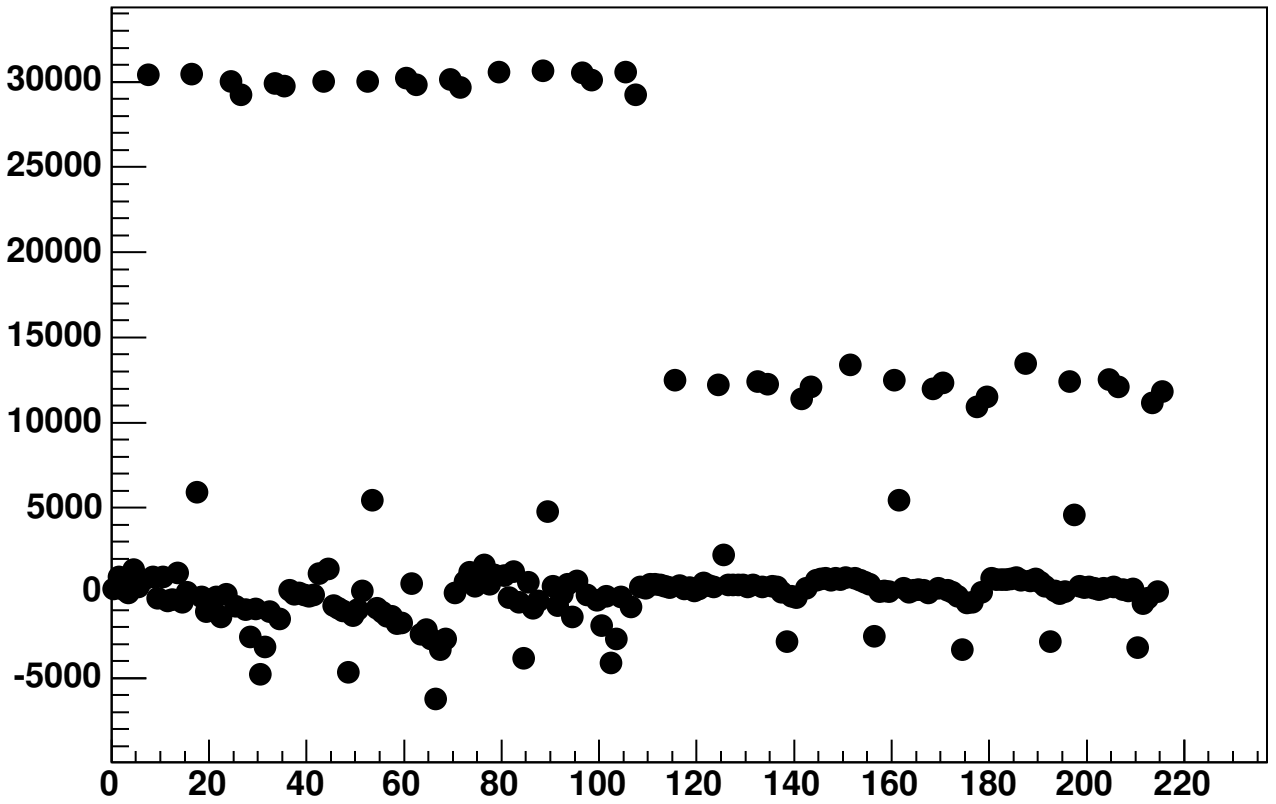
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



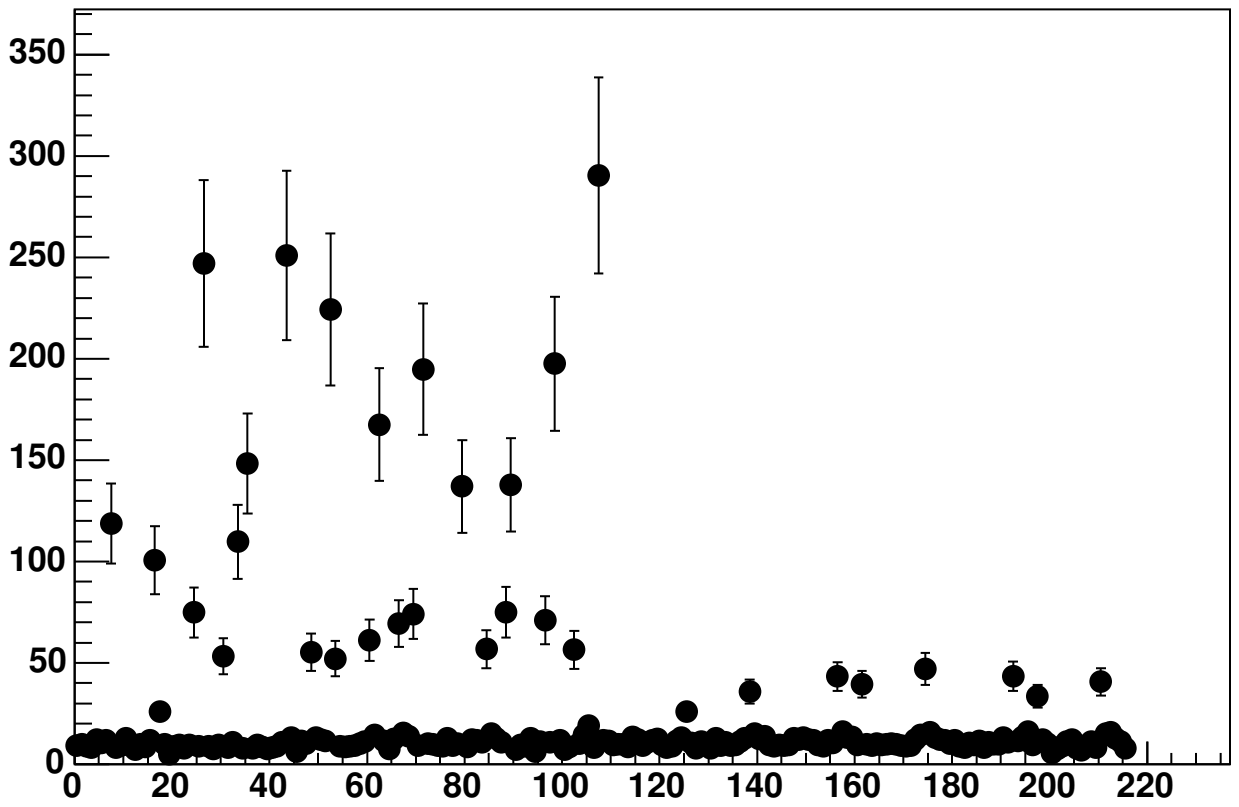
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



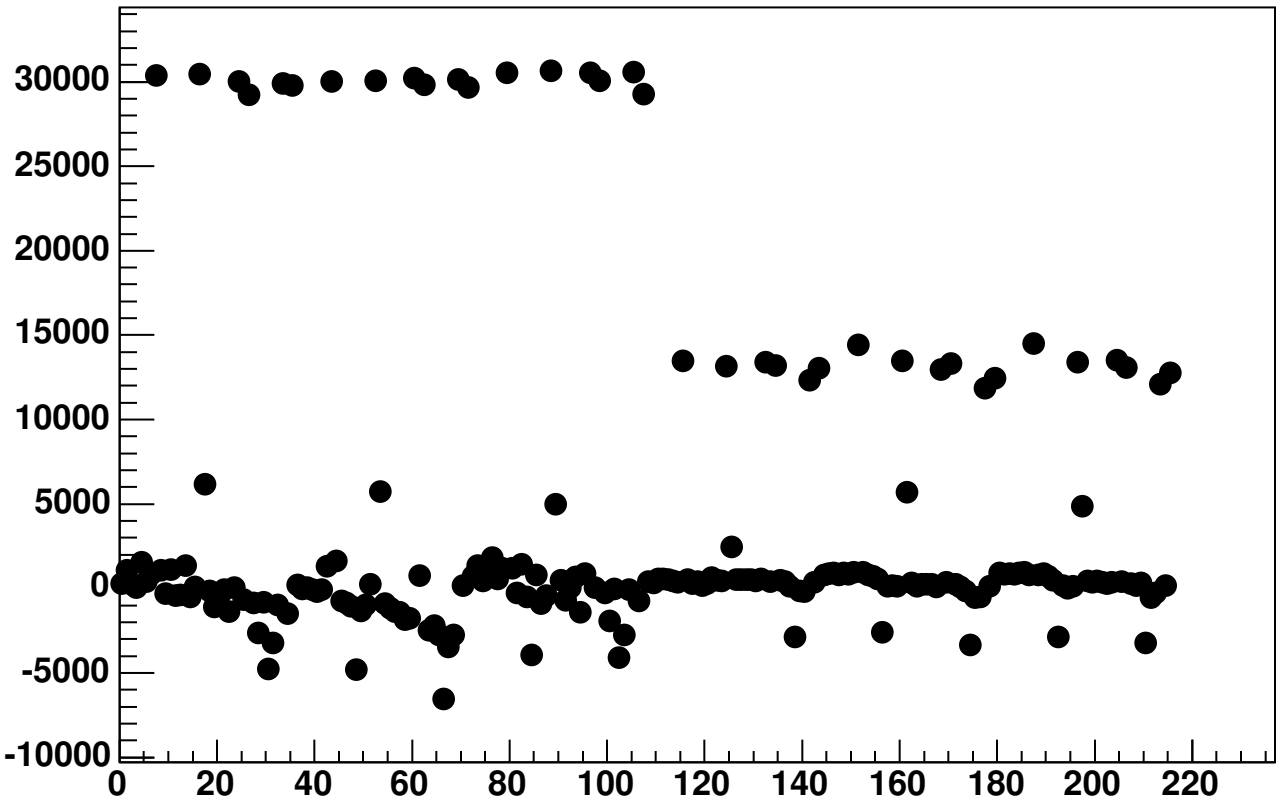
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



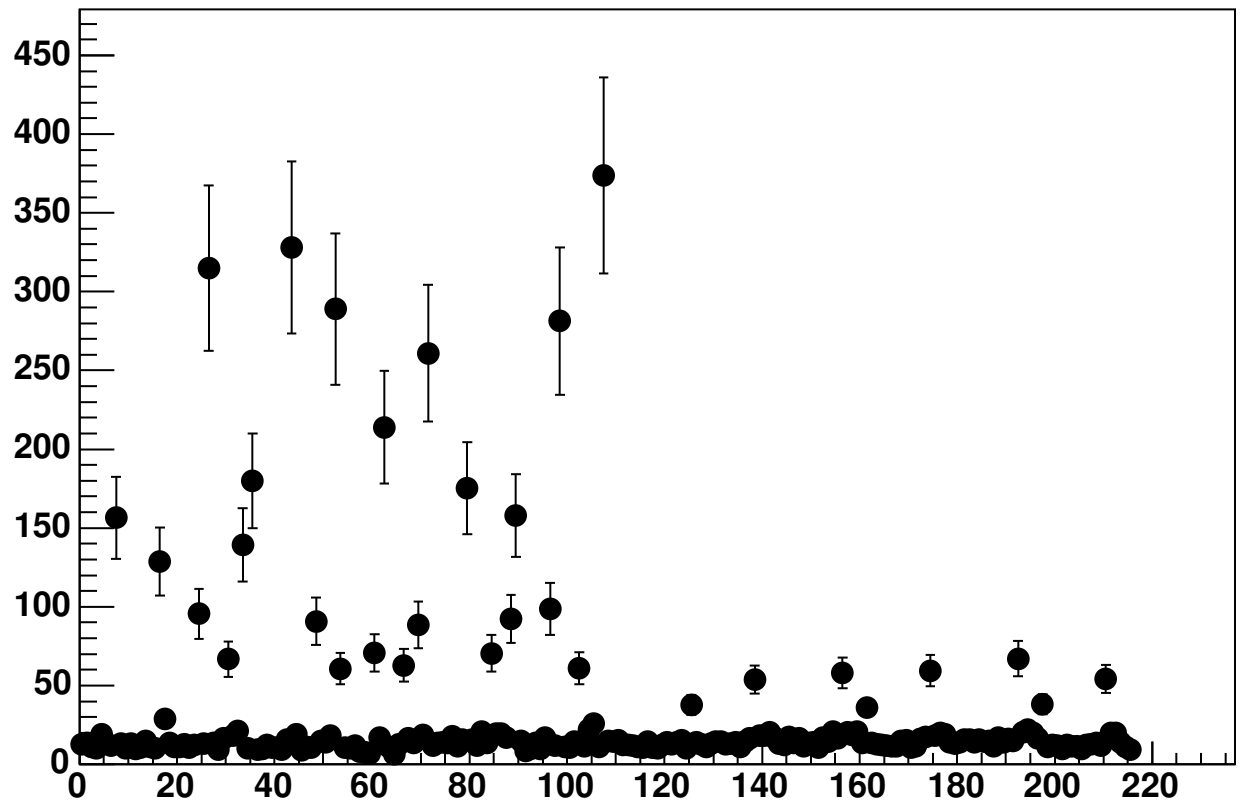
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



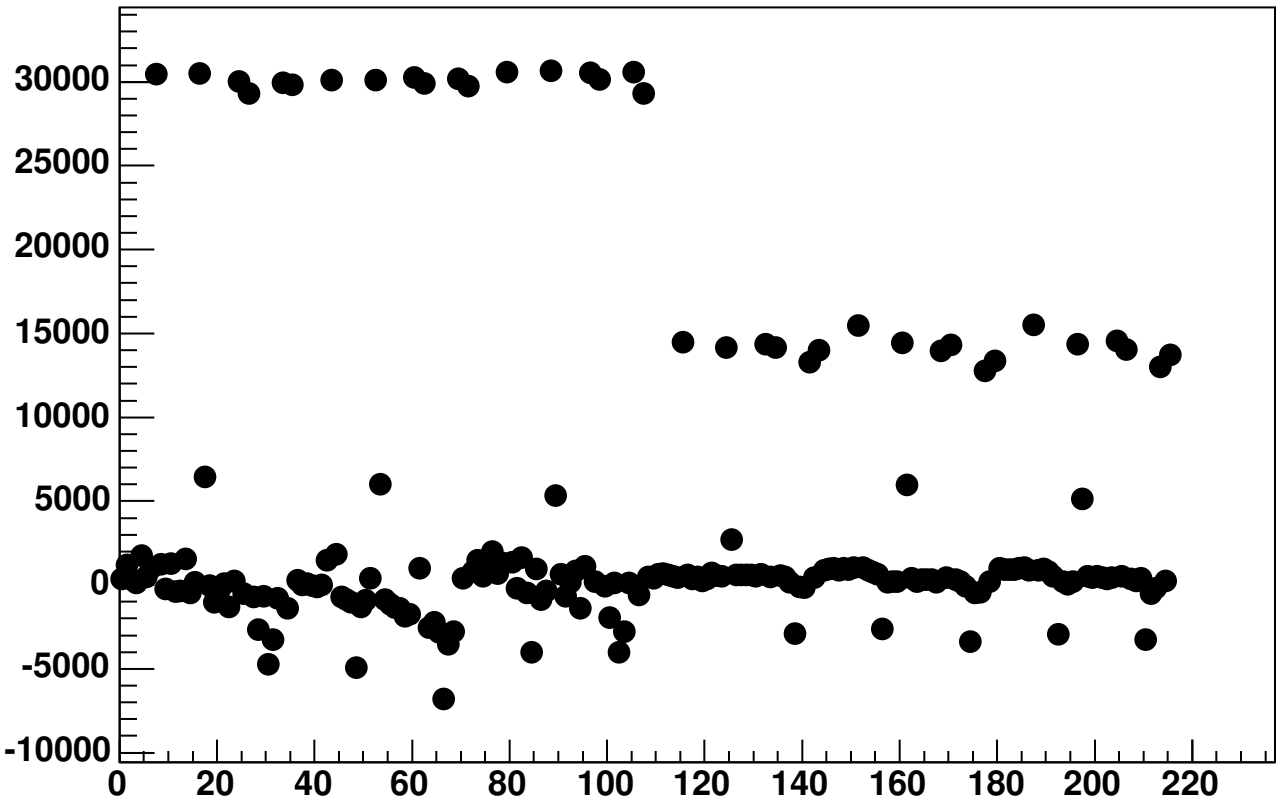
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



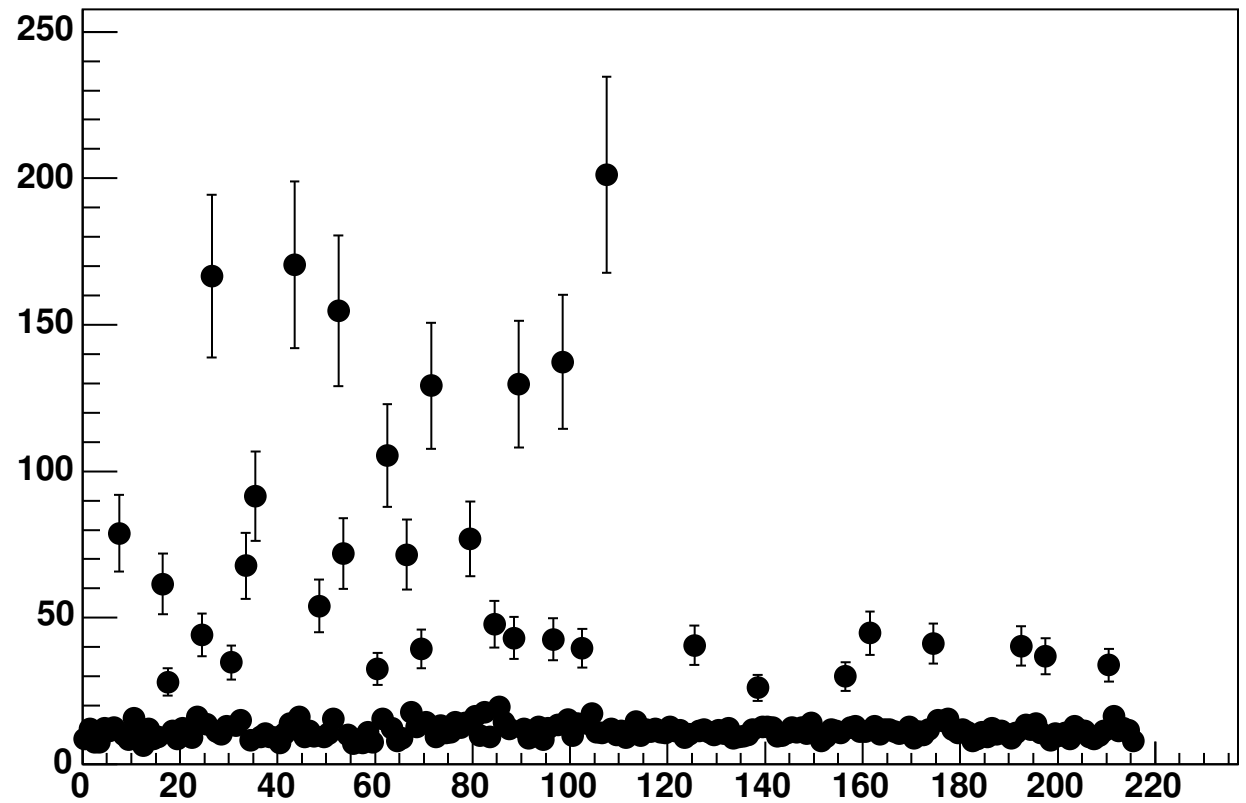
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



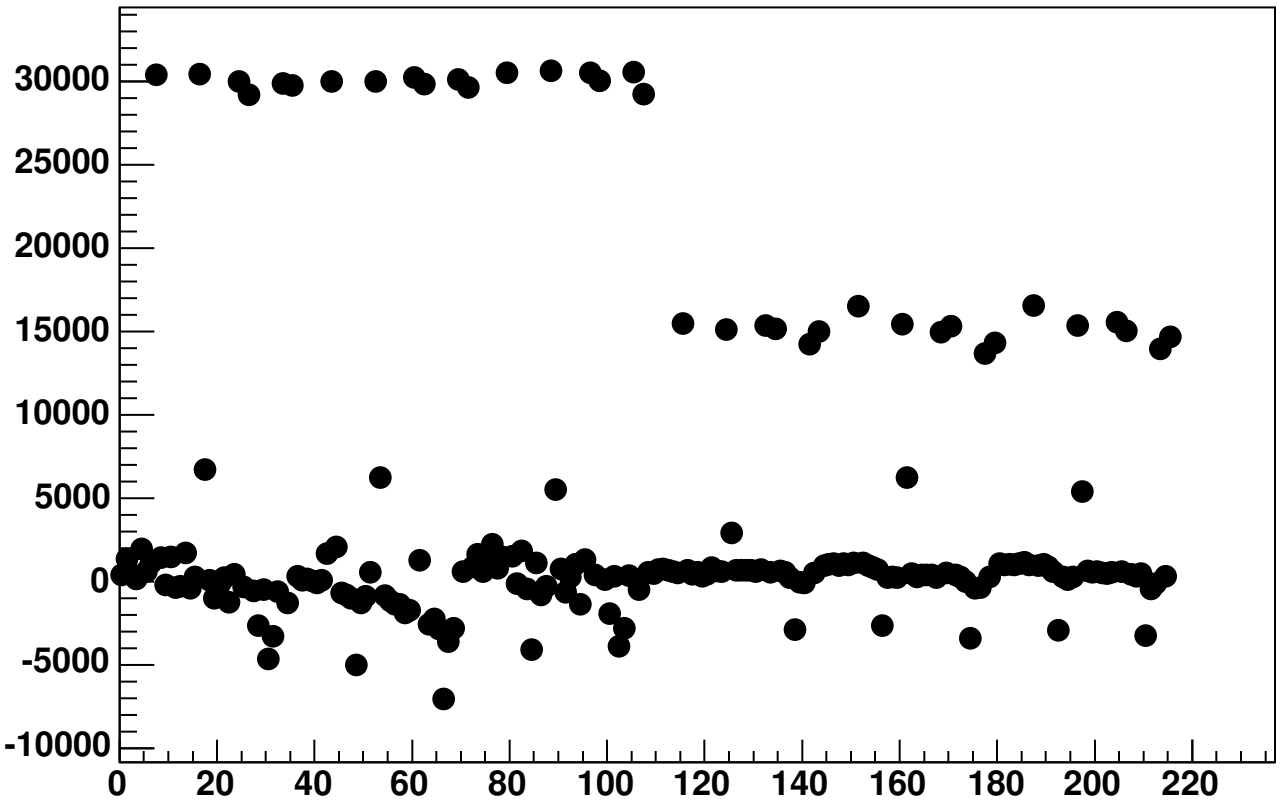
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



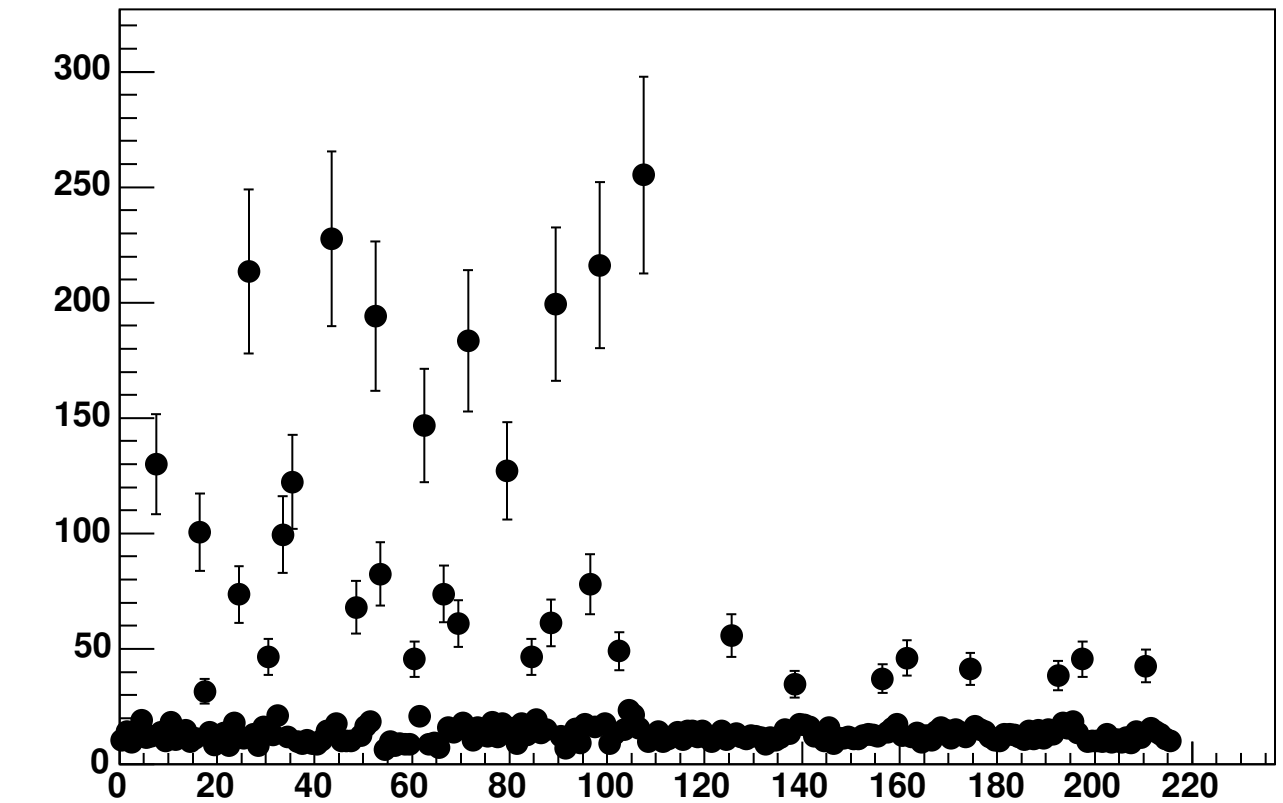
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



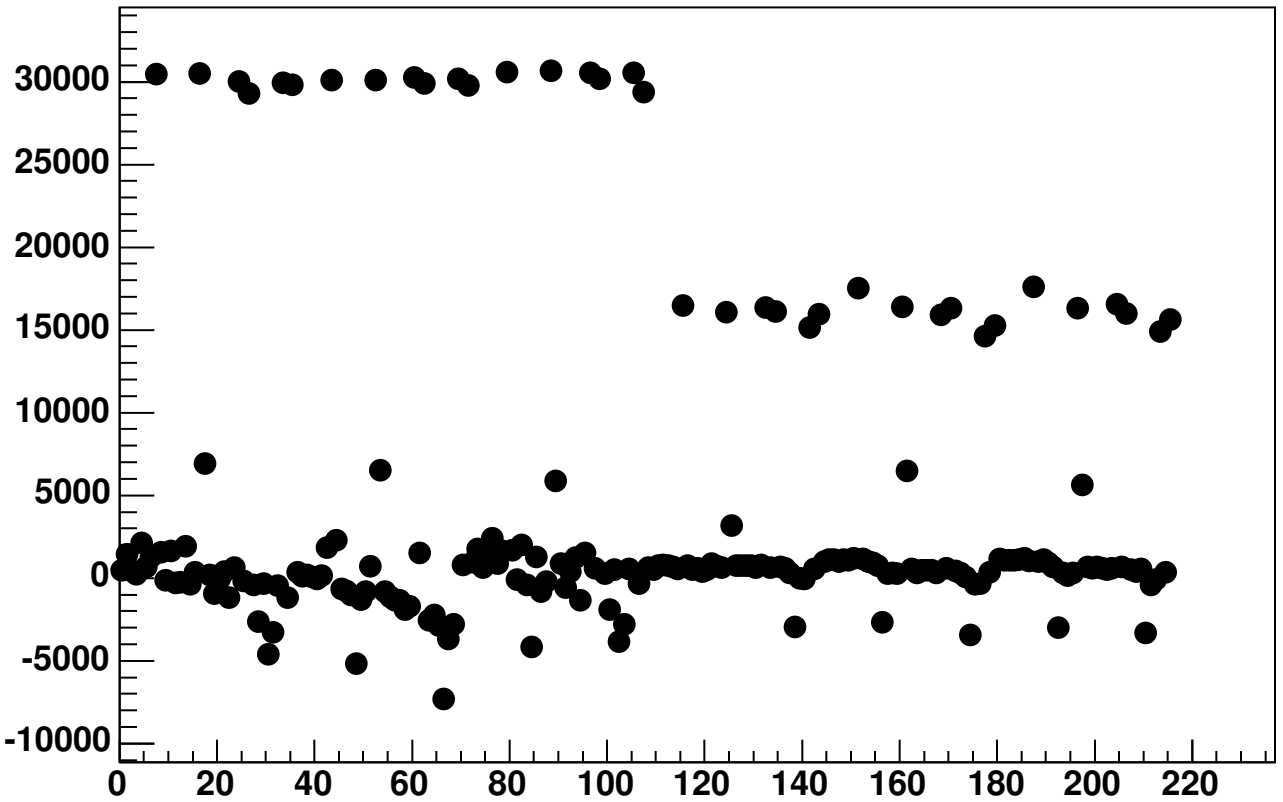
Enable 5, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



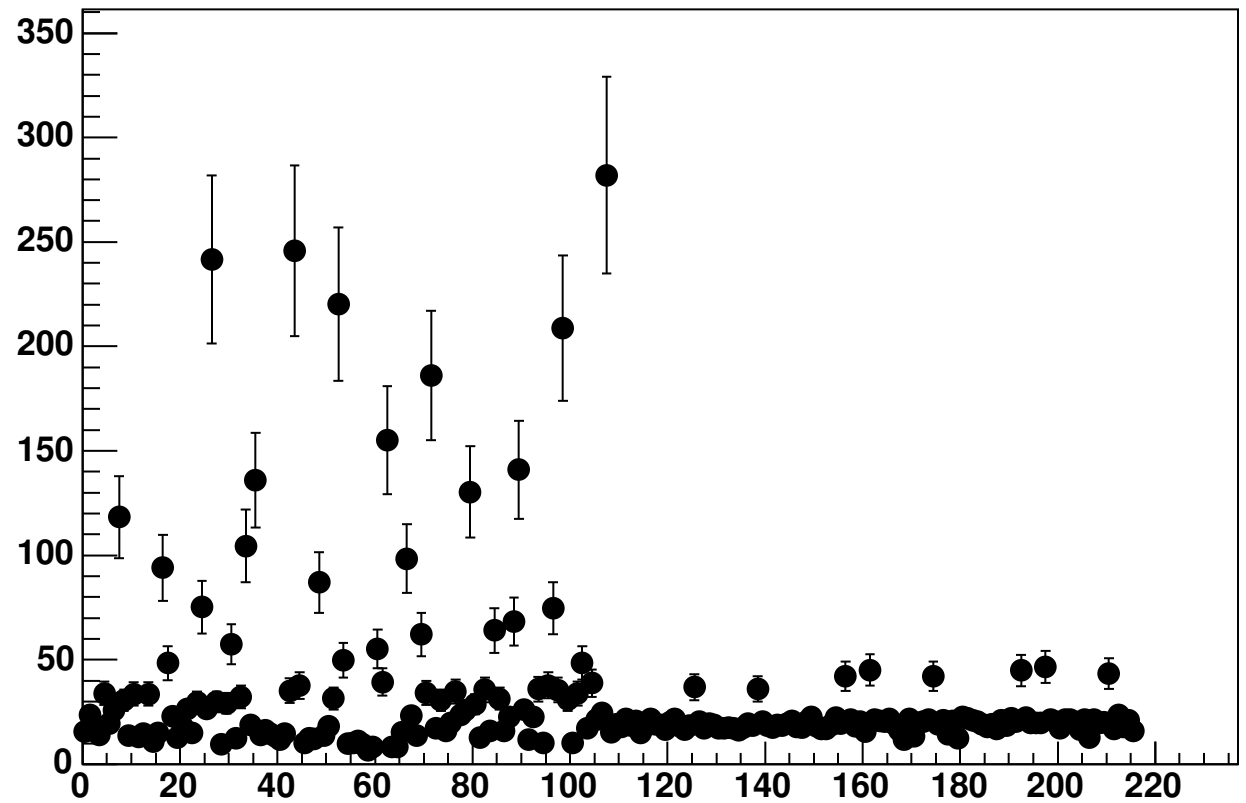
Enable 5, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



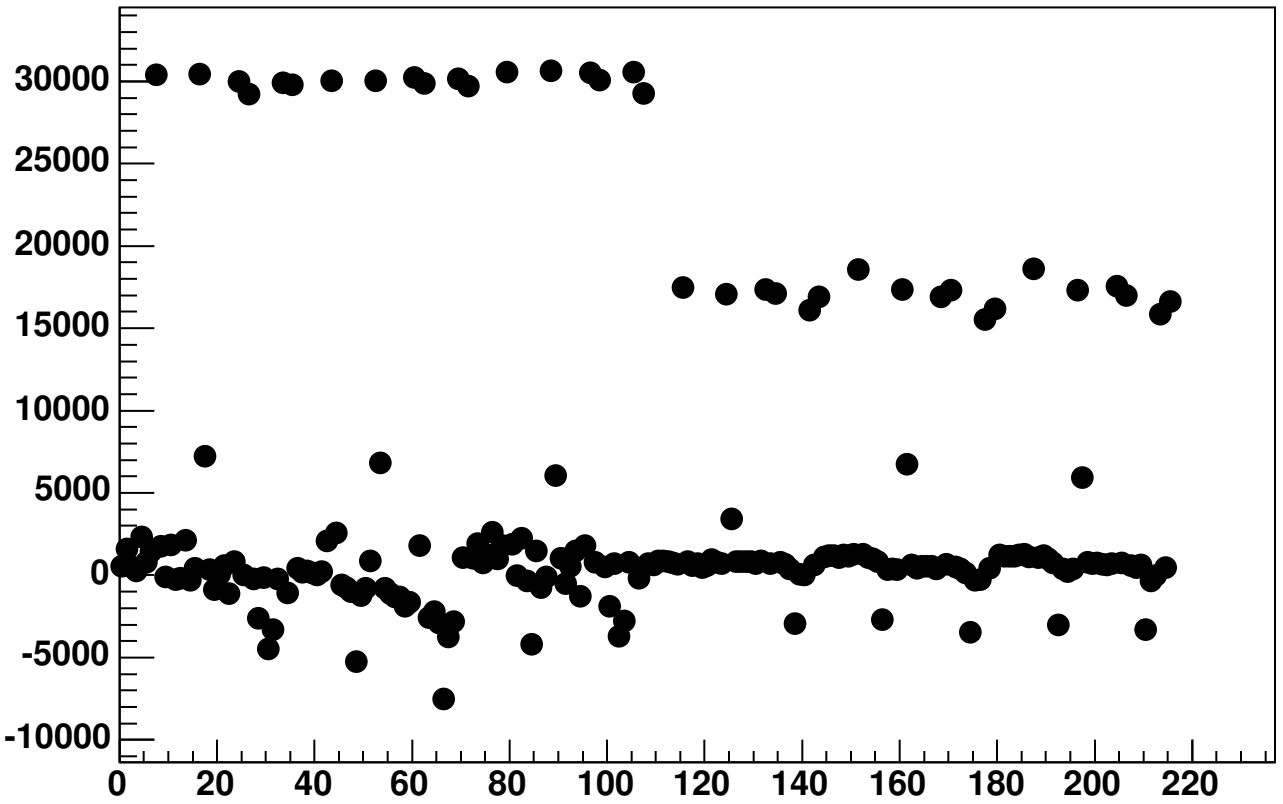
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



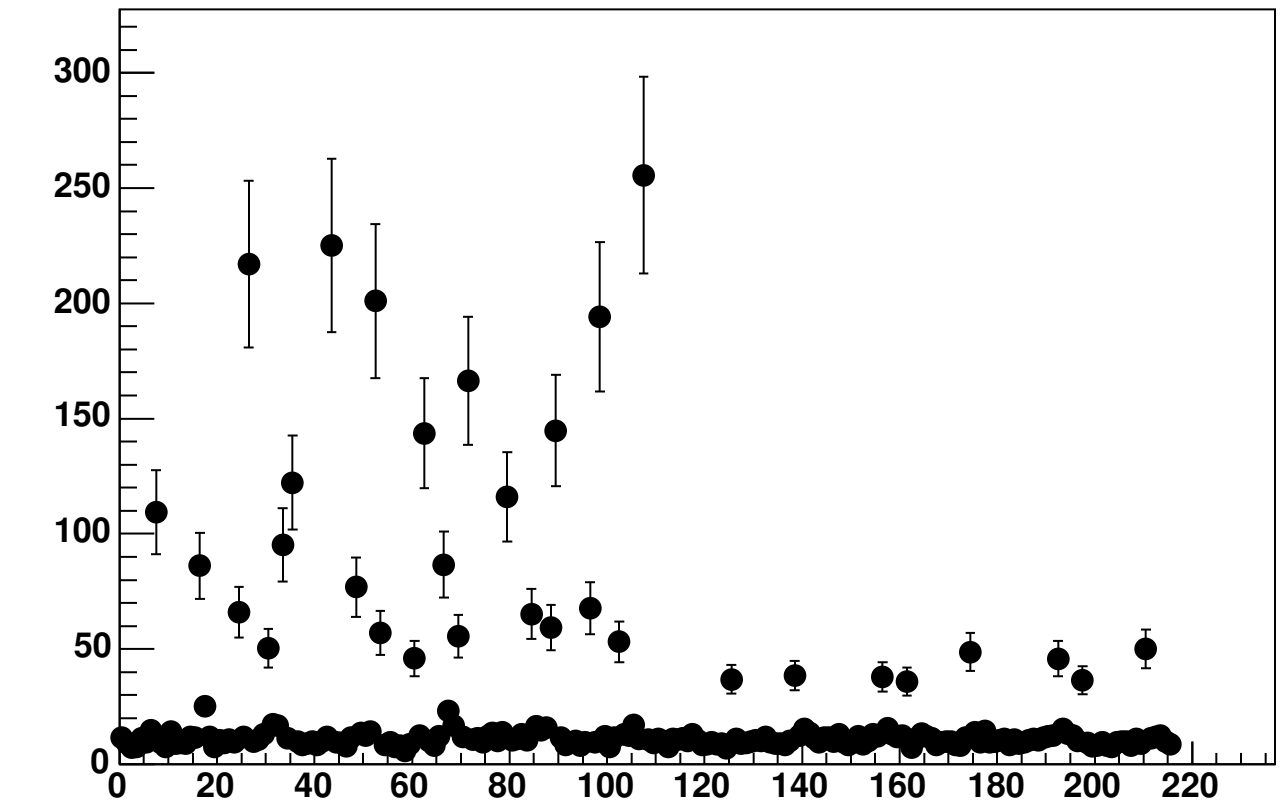
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



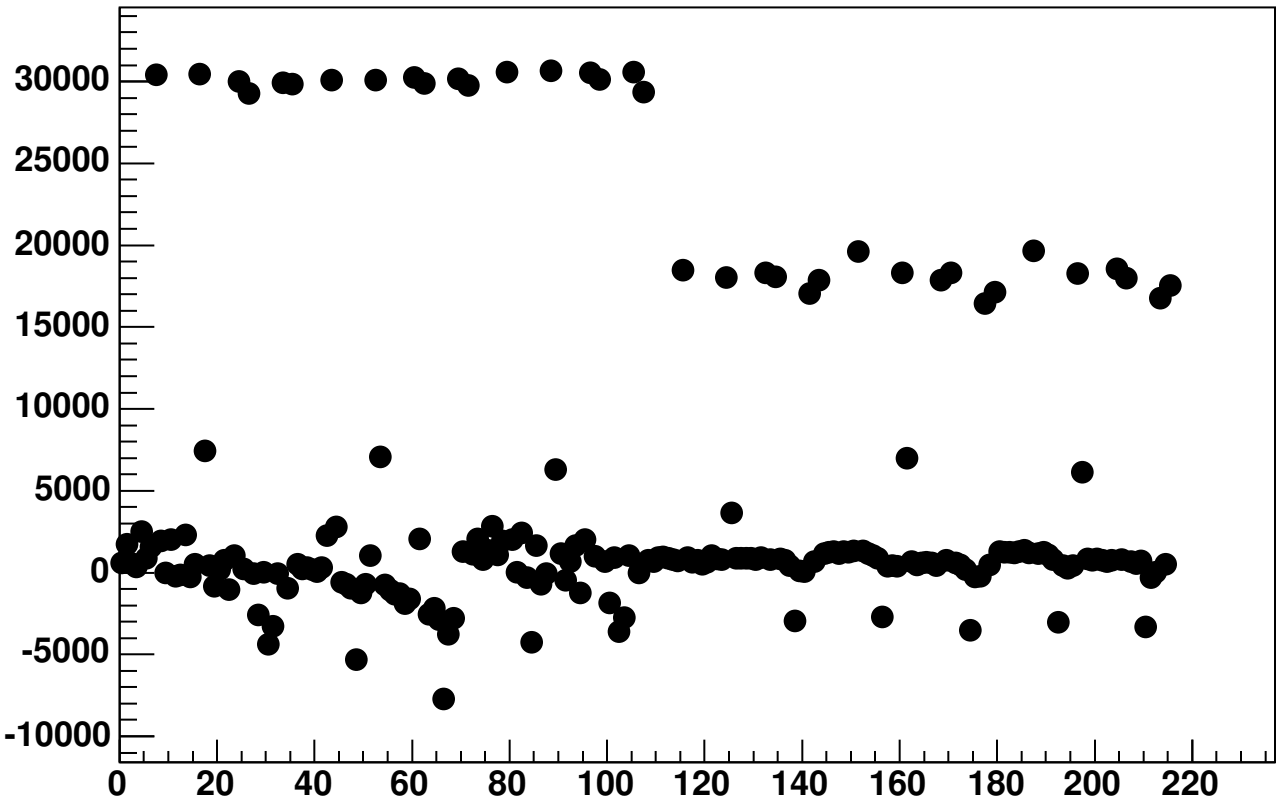
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



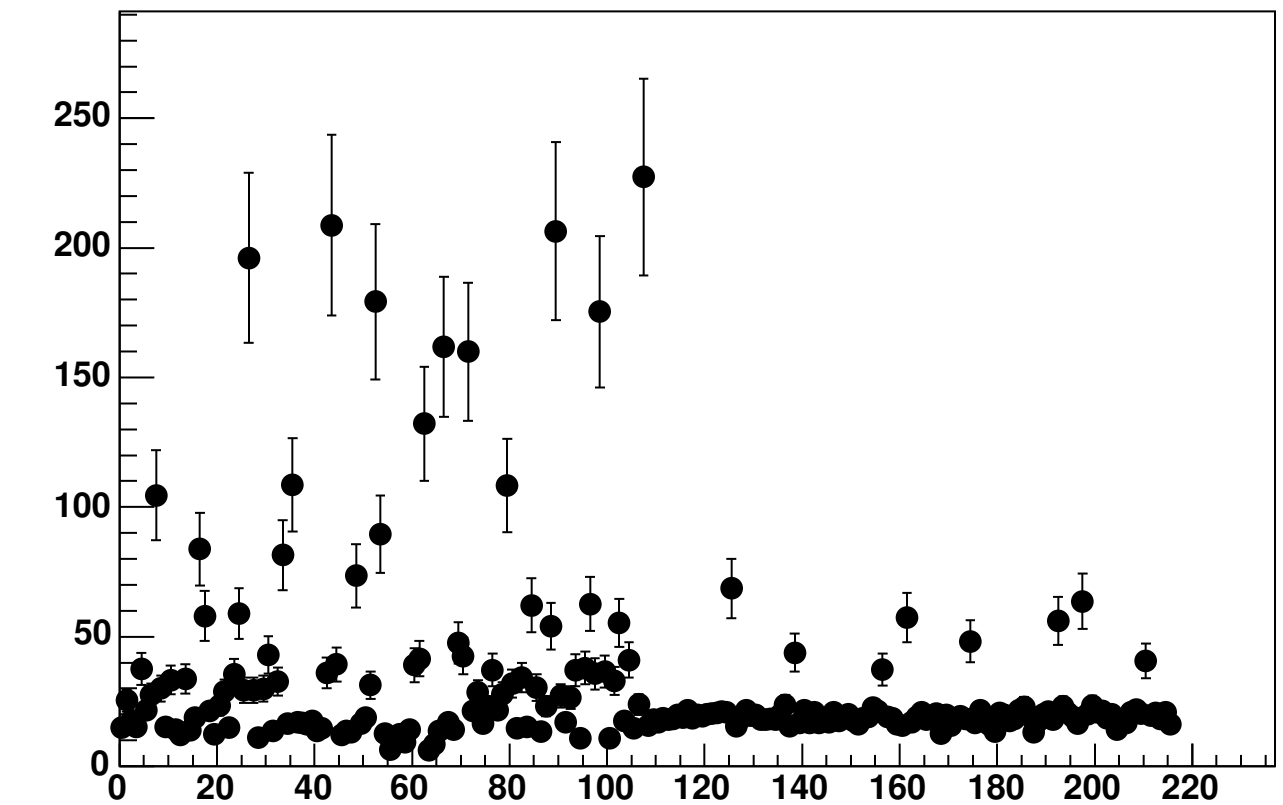
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



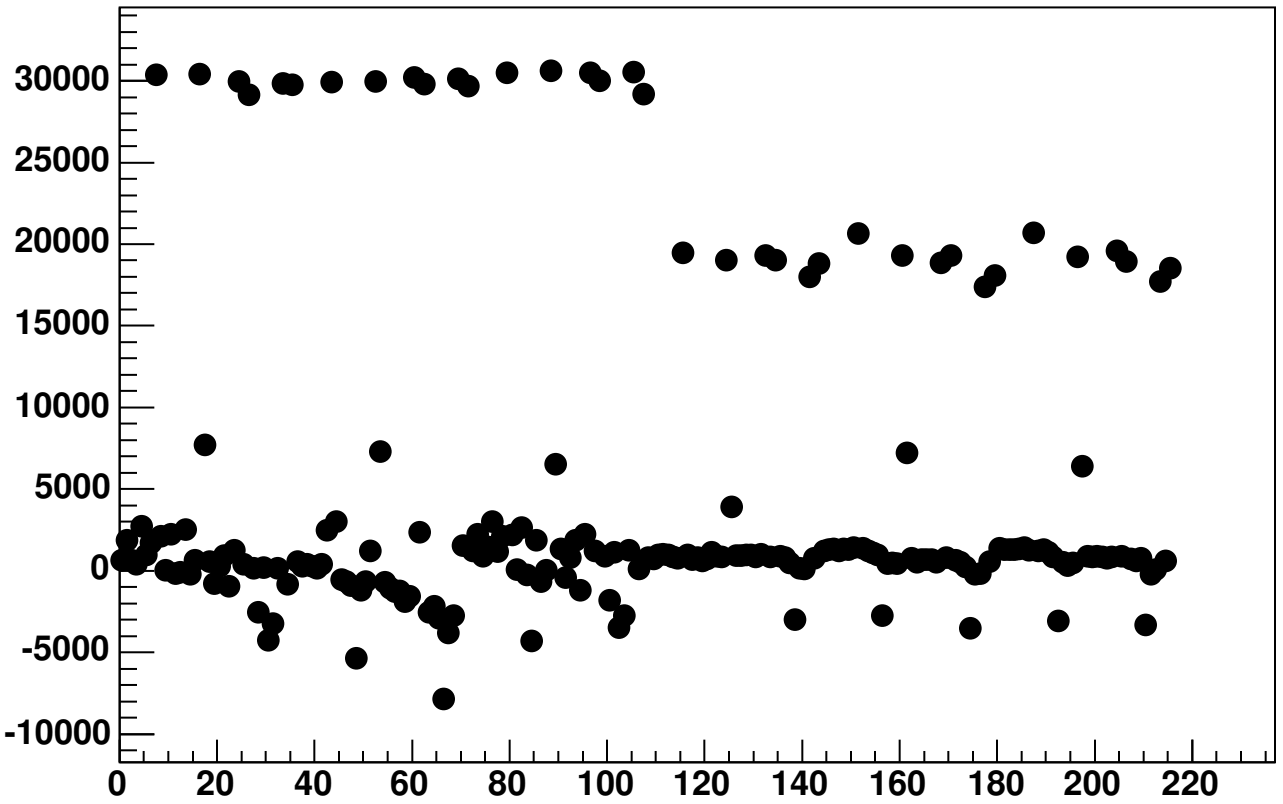
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



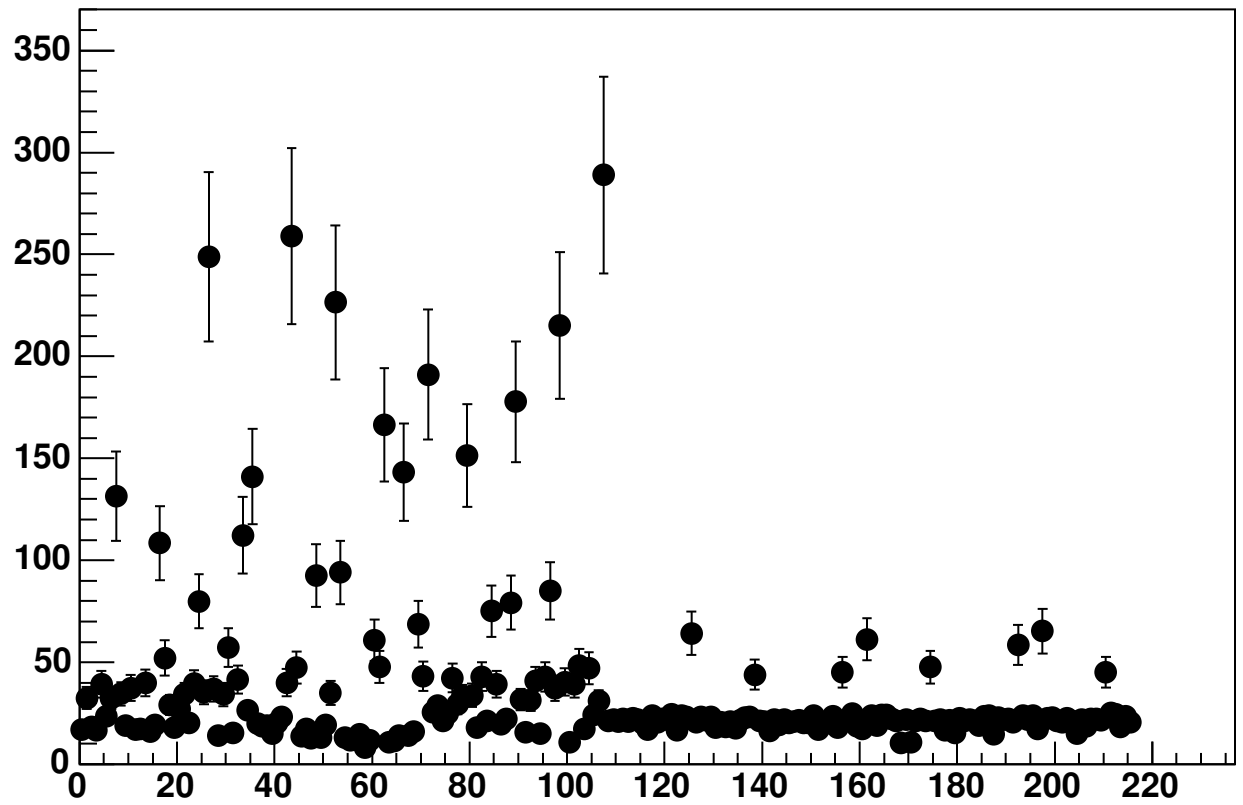
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



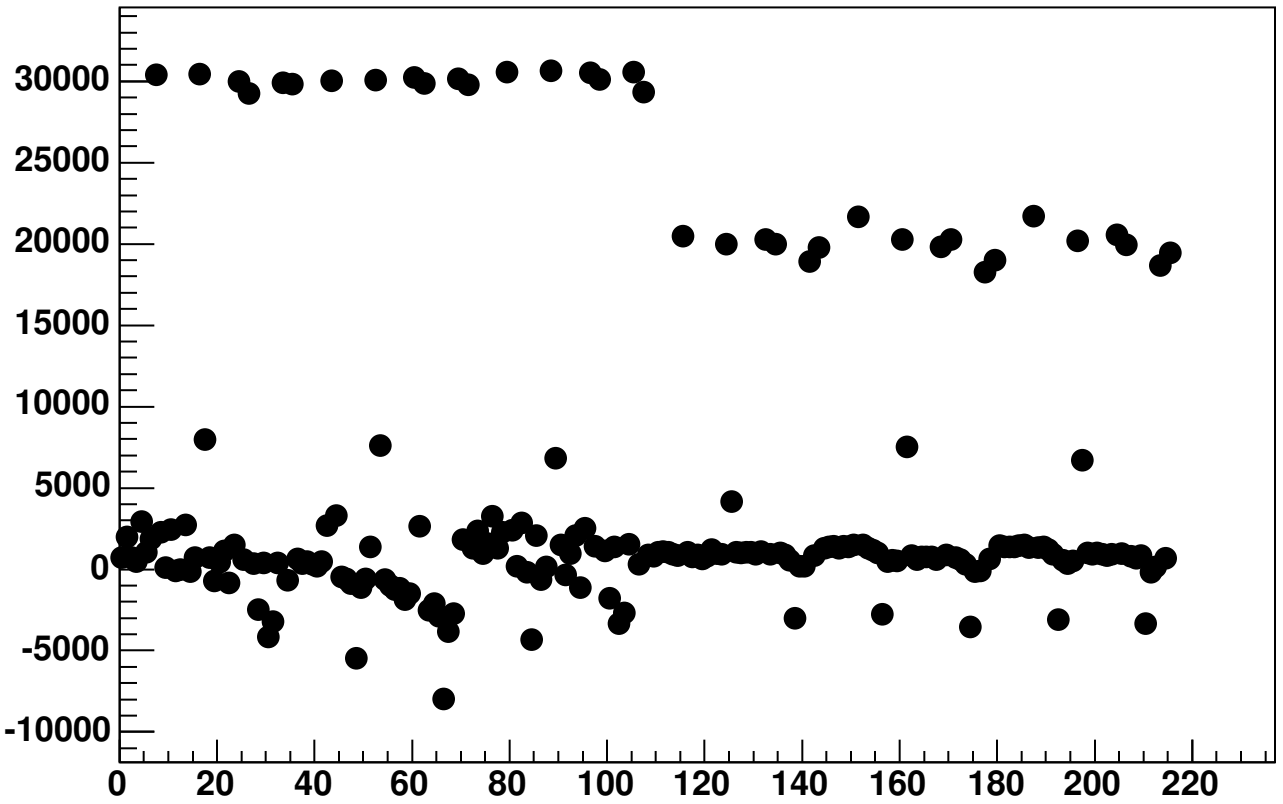
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



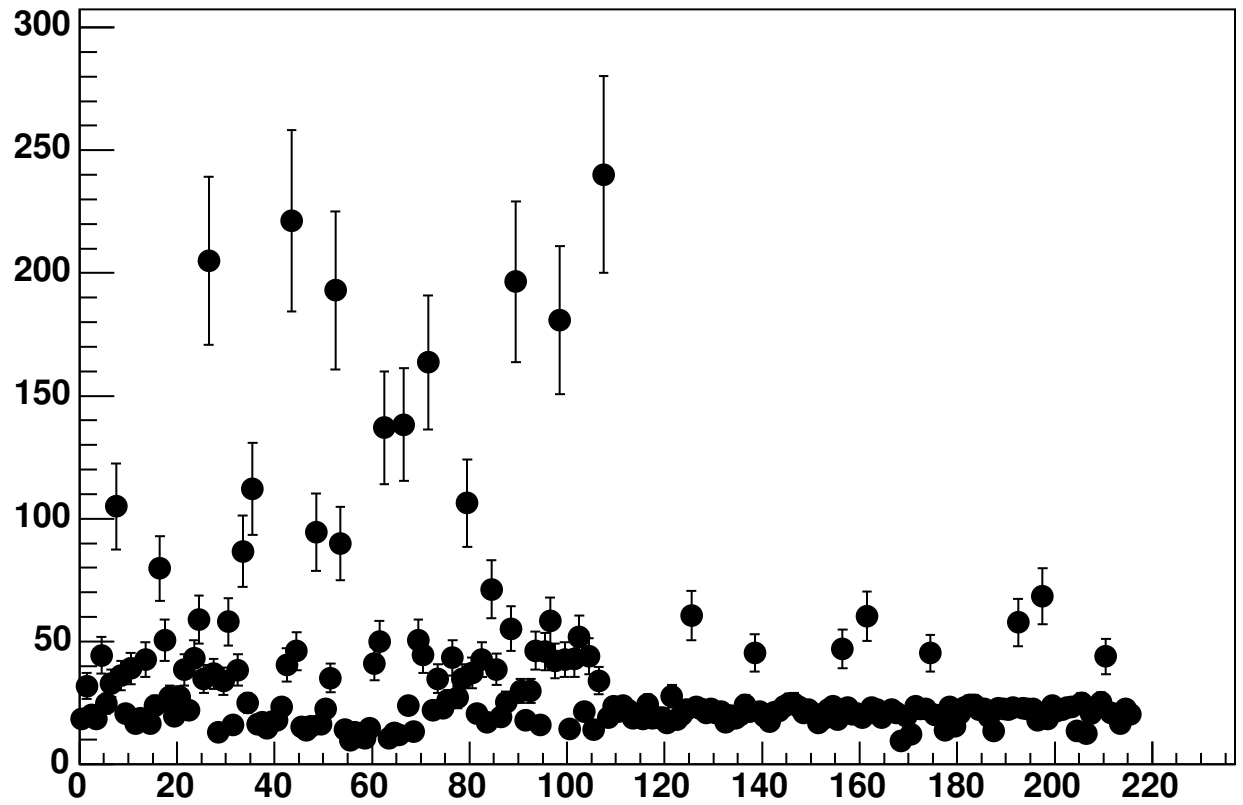
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



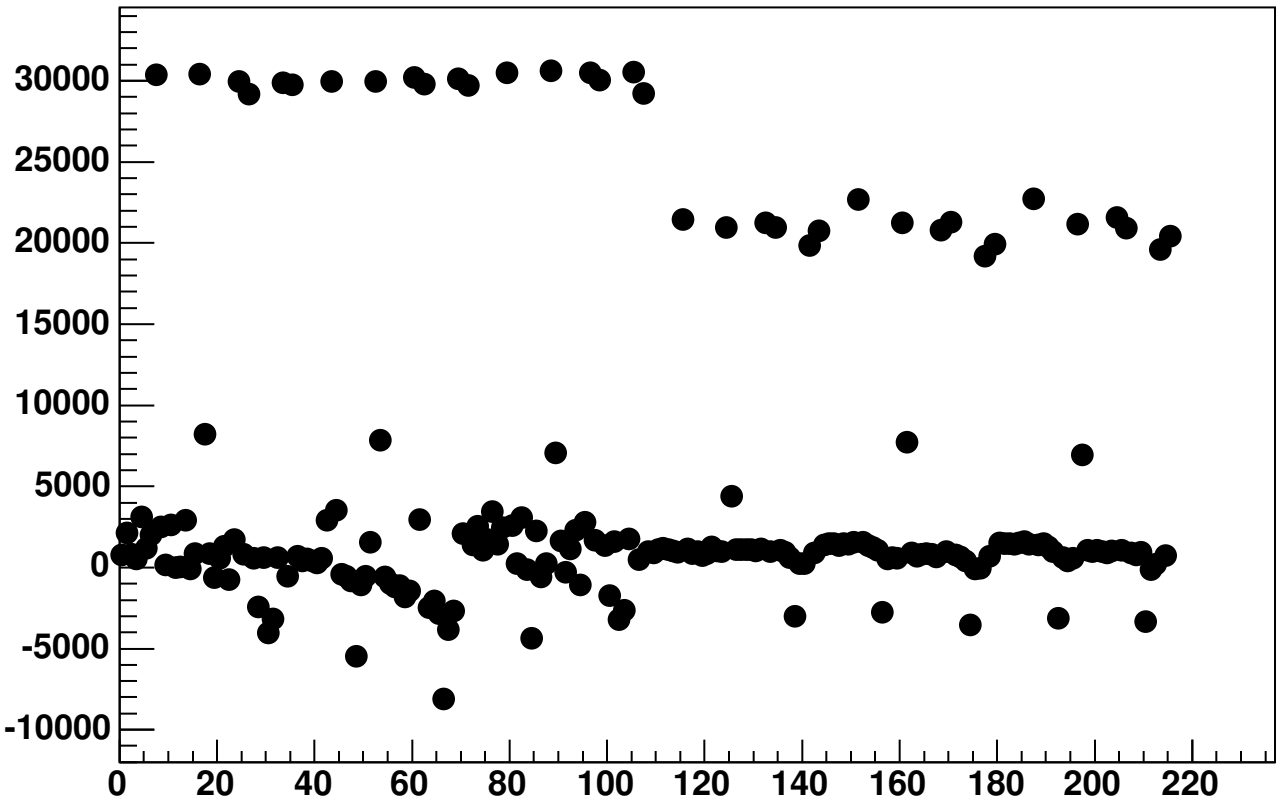
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



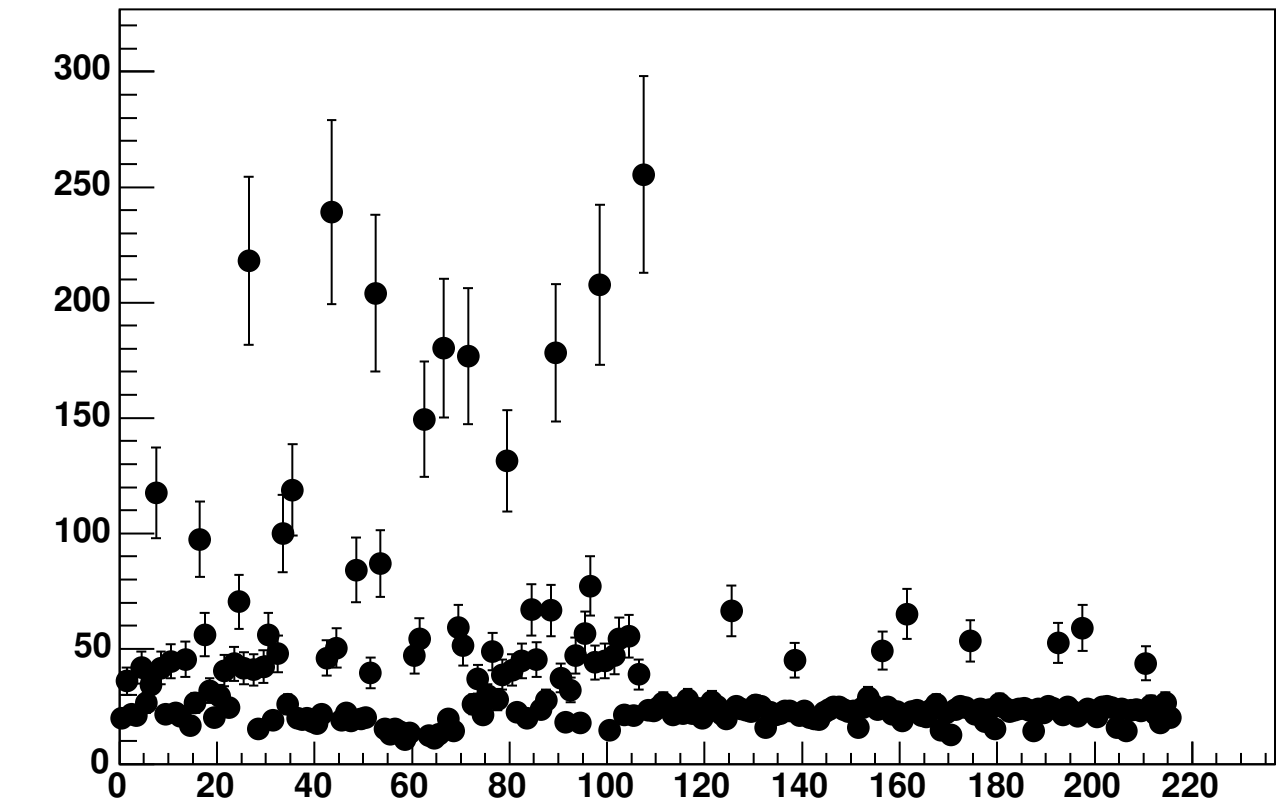
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



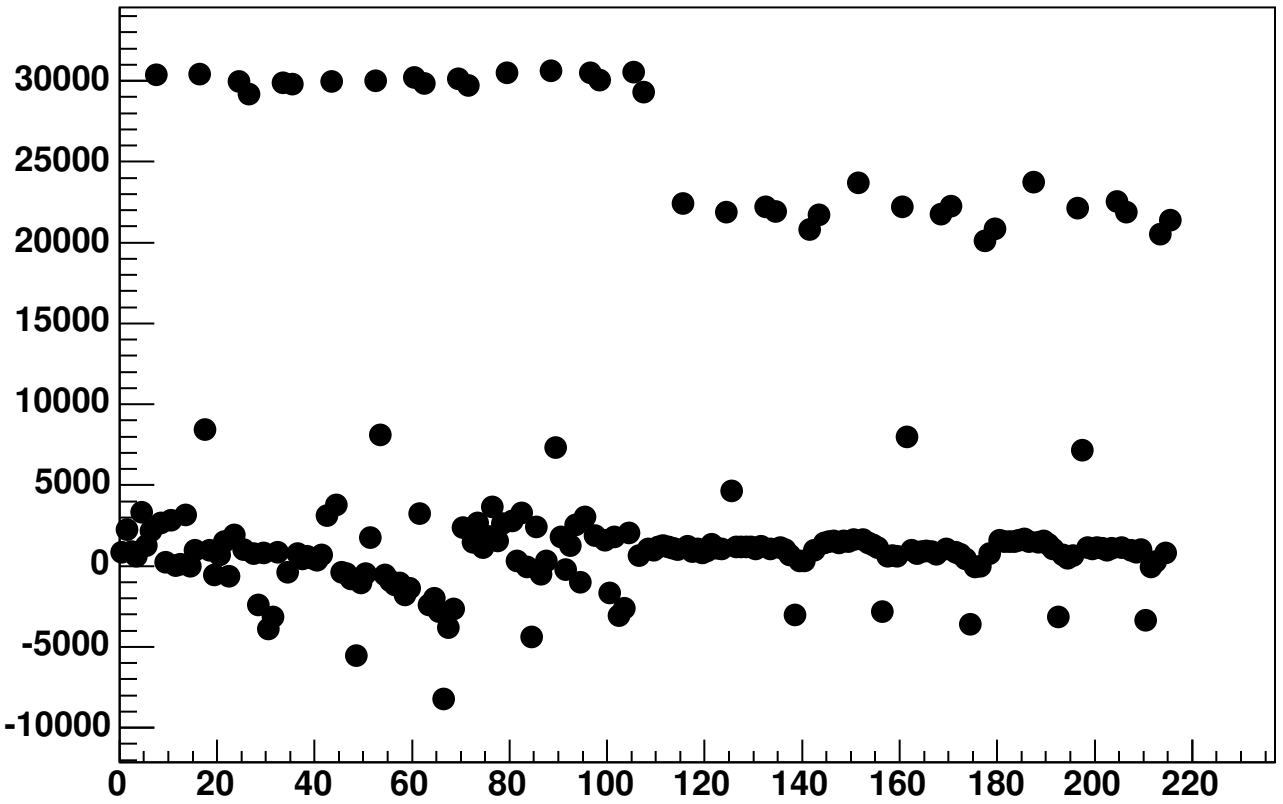
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



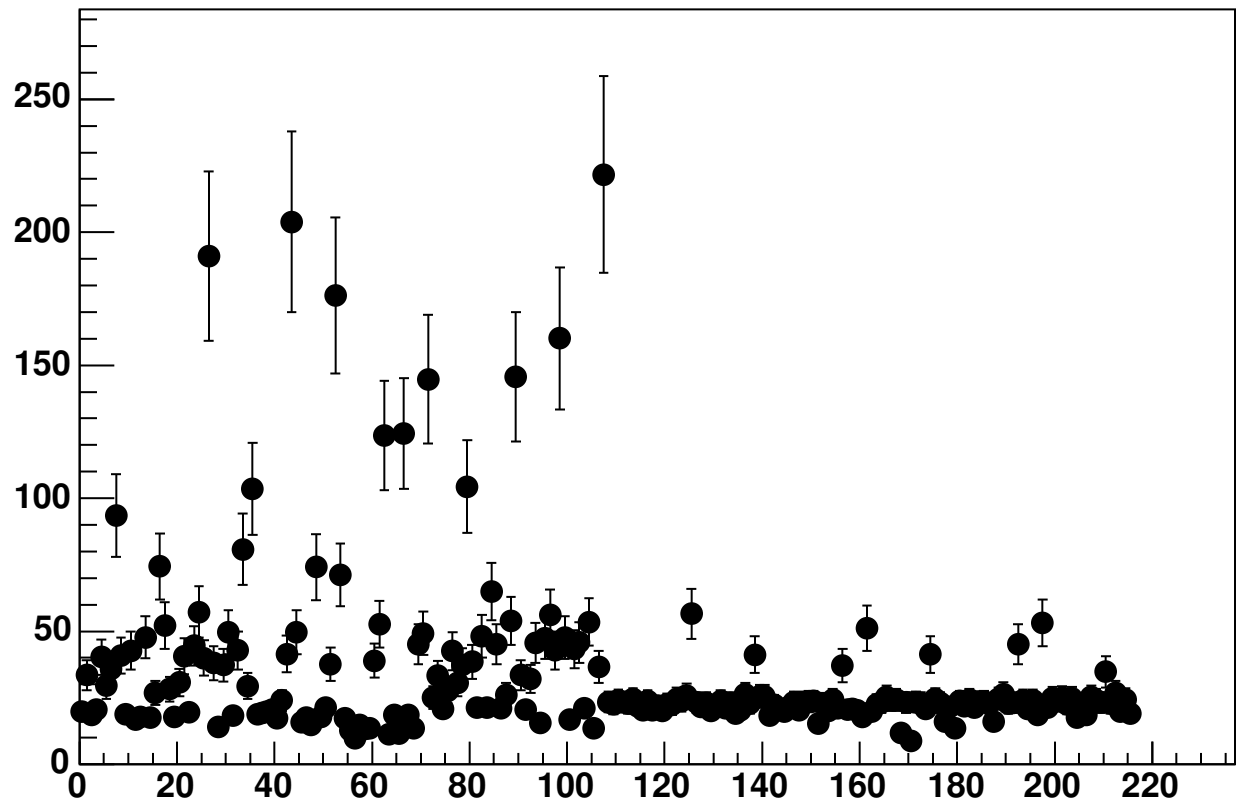
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



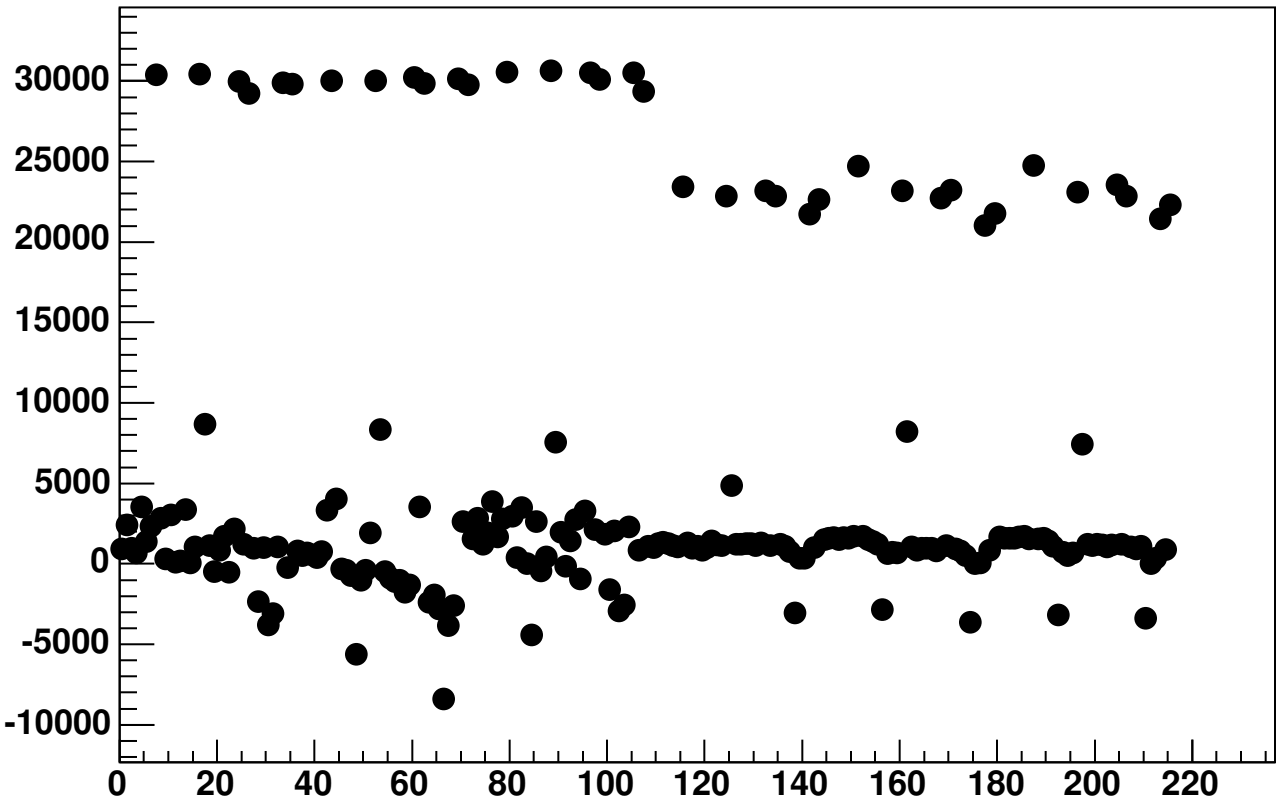
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



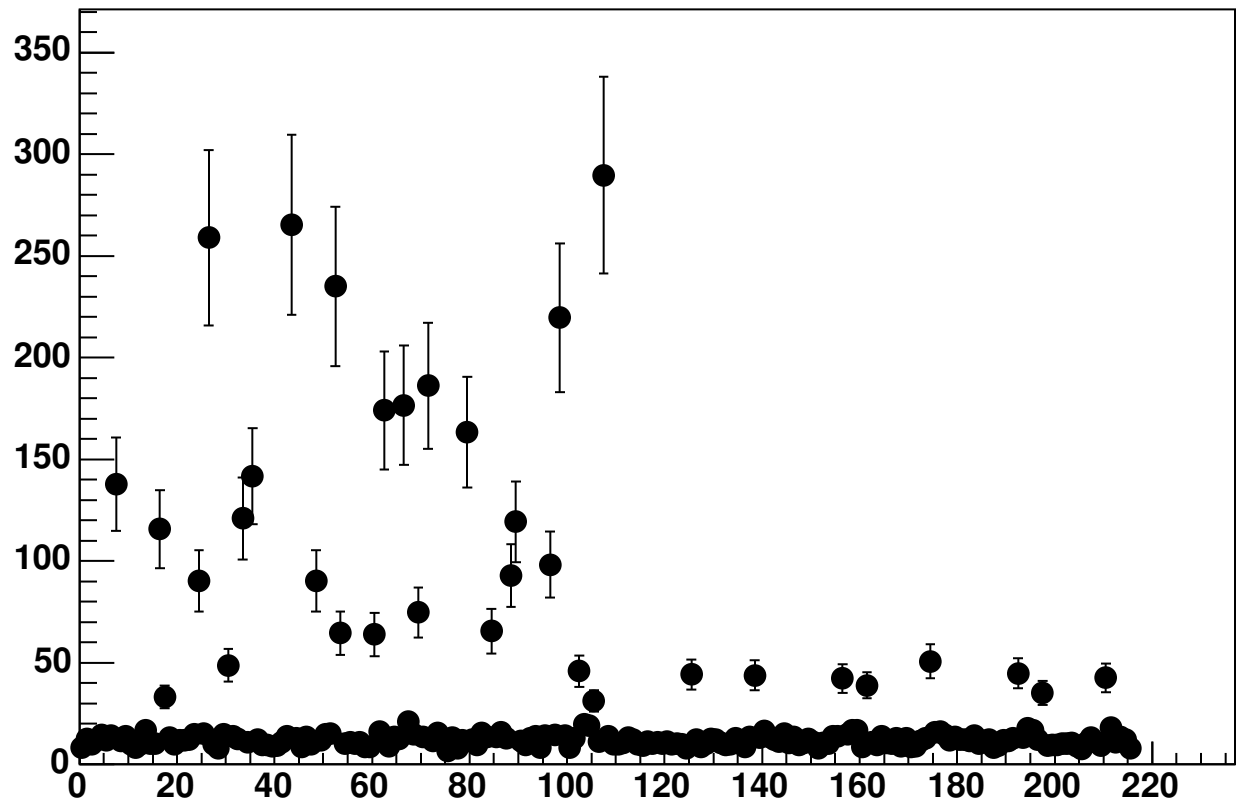
Enable 5, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



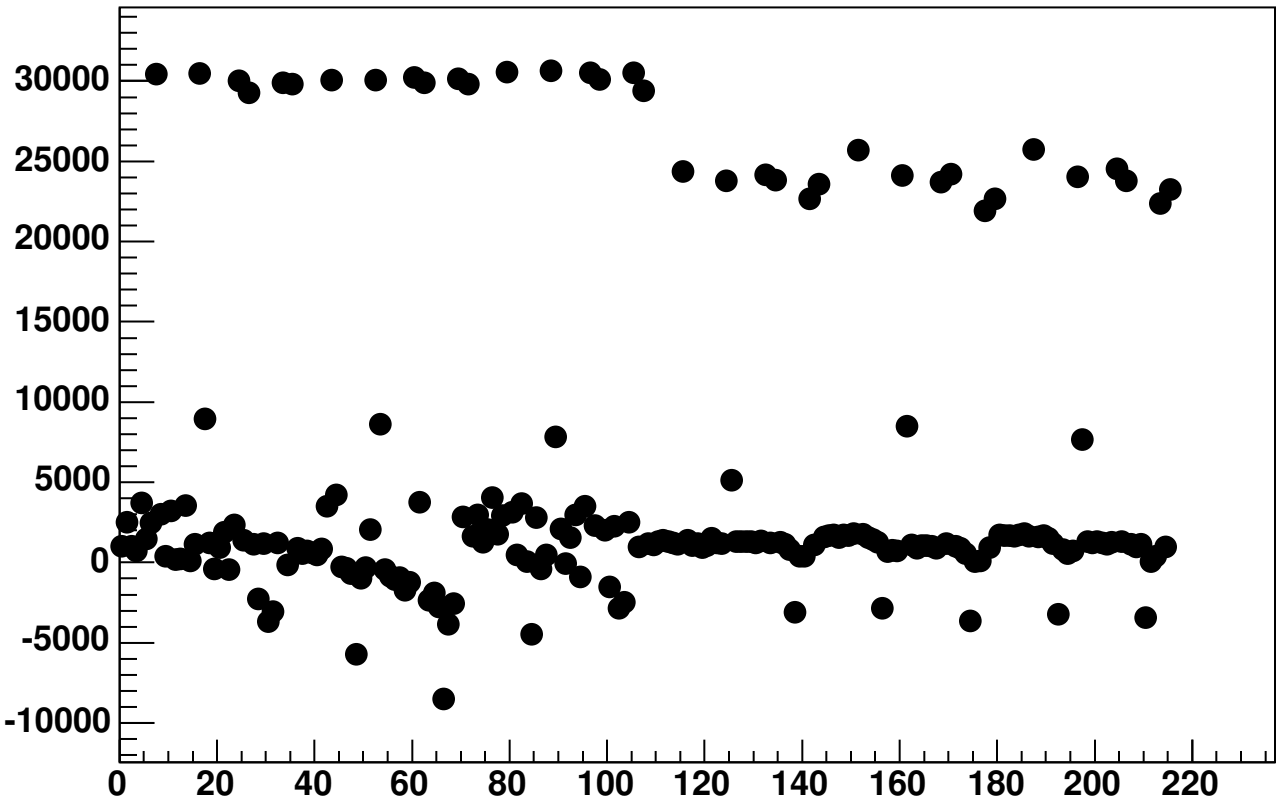
Enable 5, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



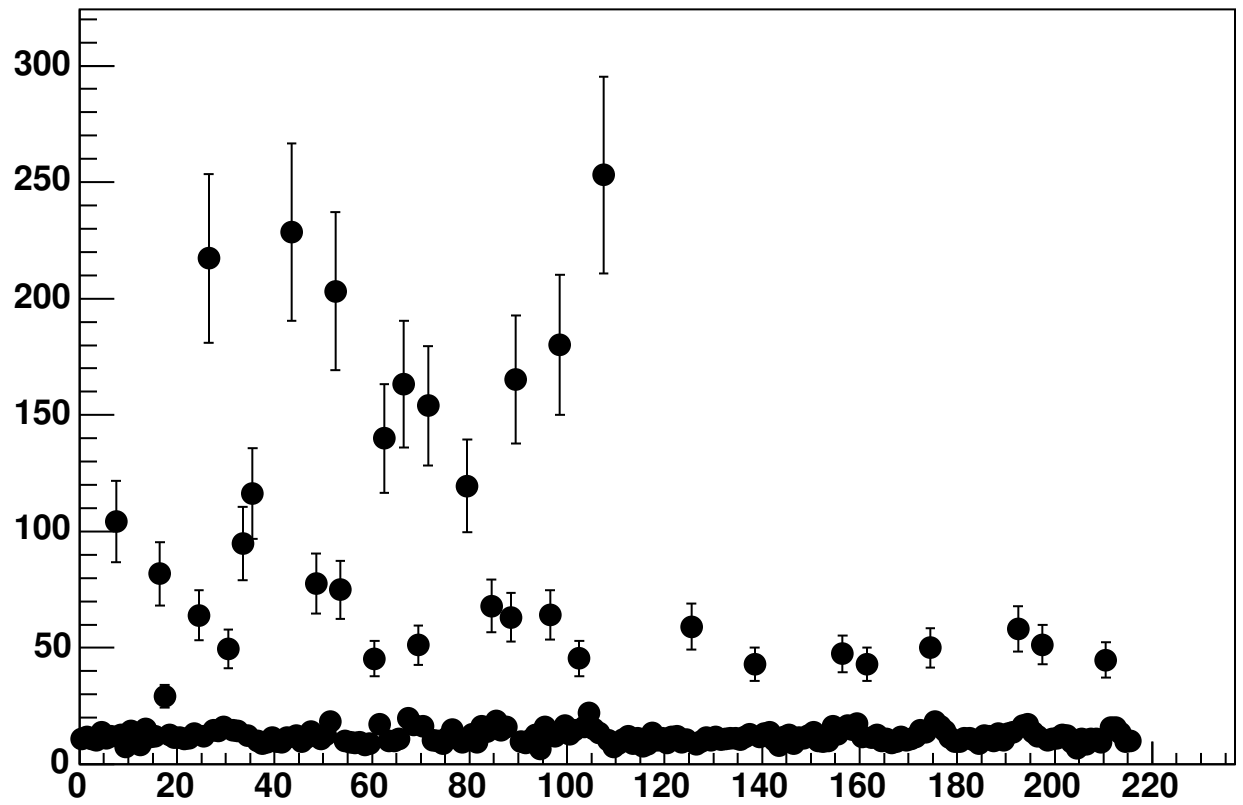
Enable 5, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



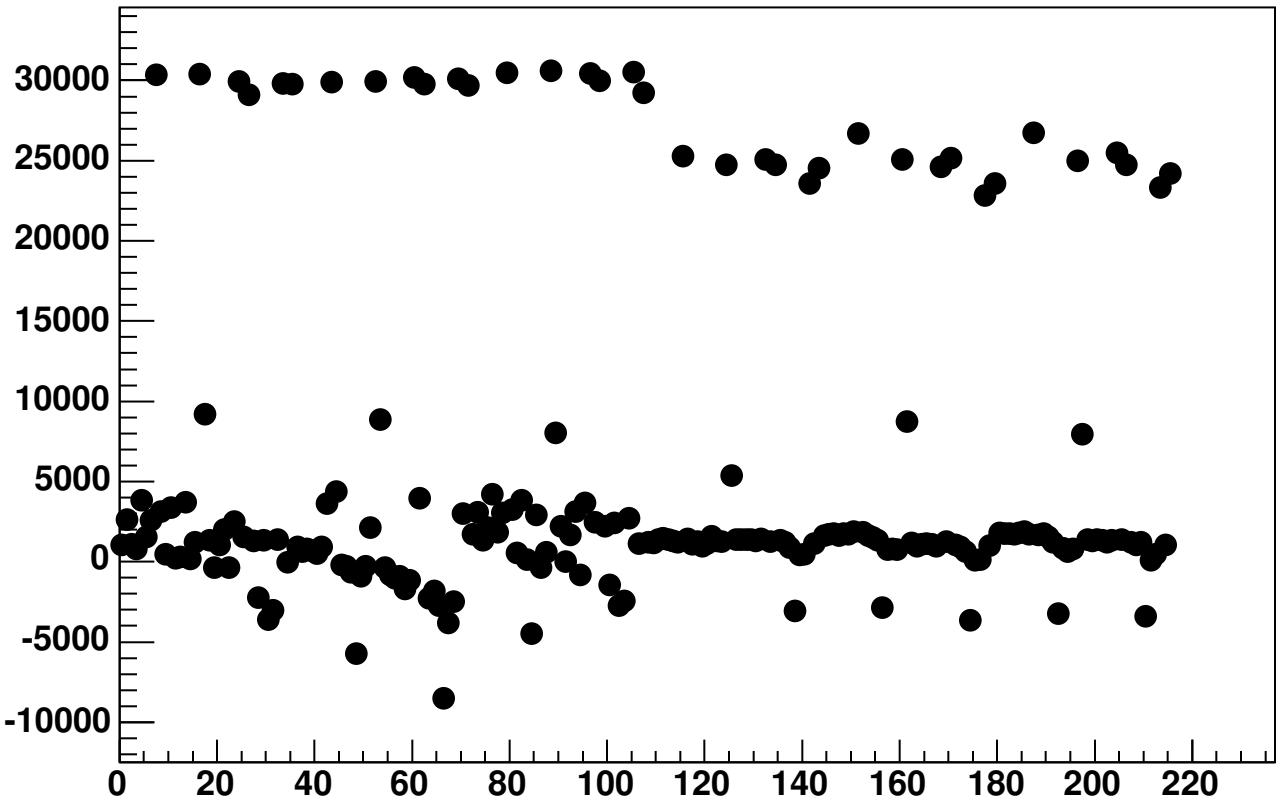
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



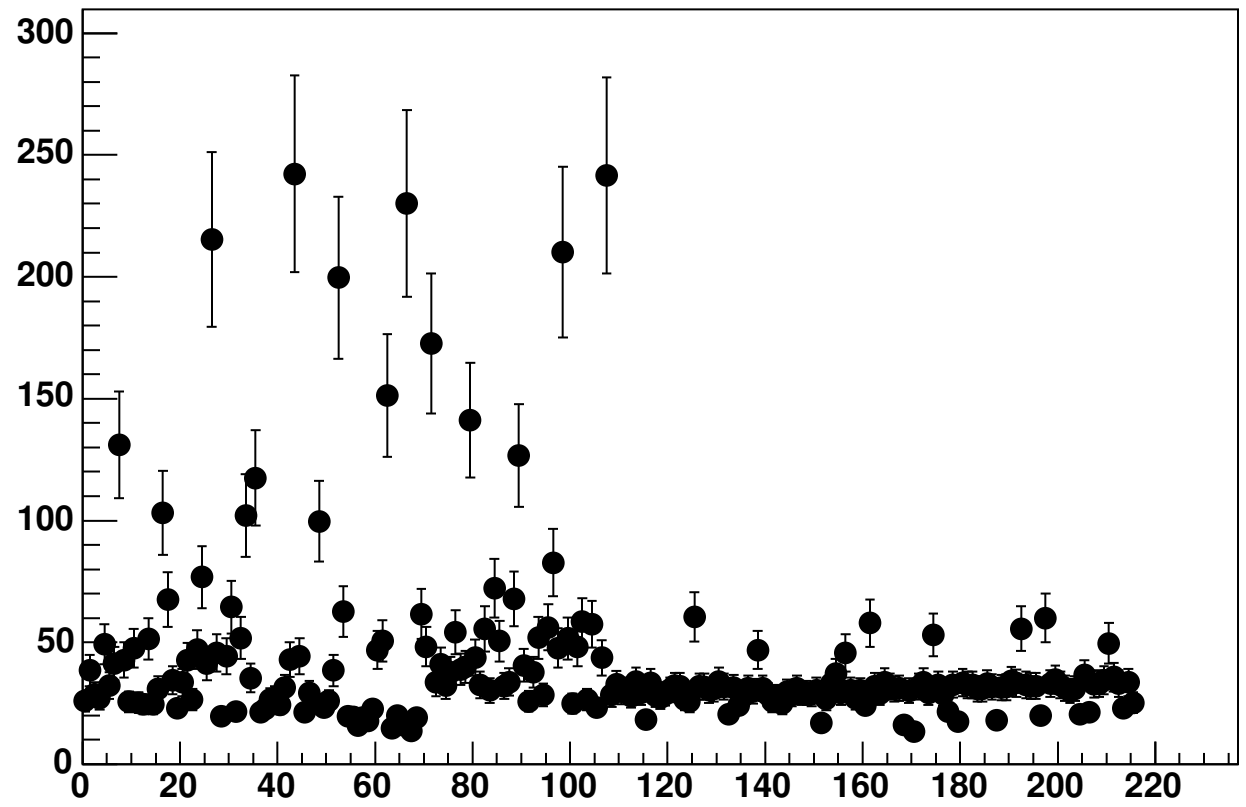
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



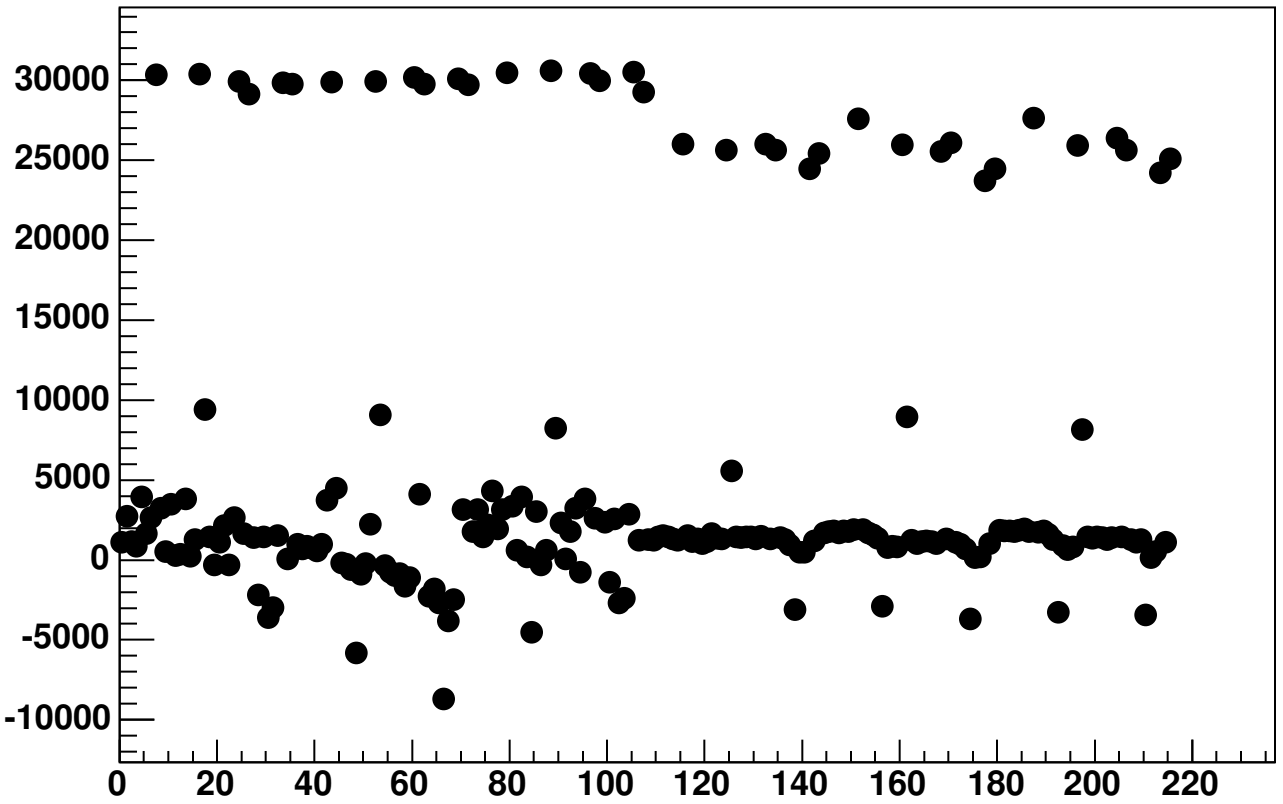
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



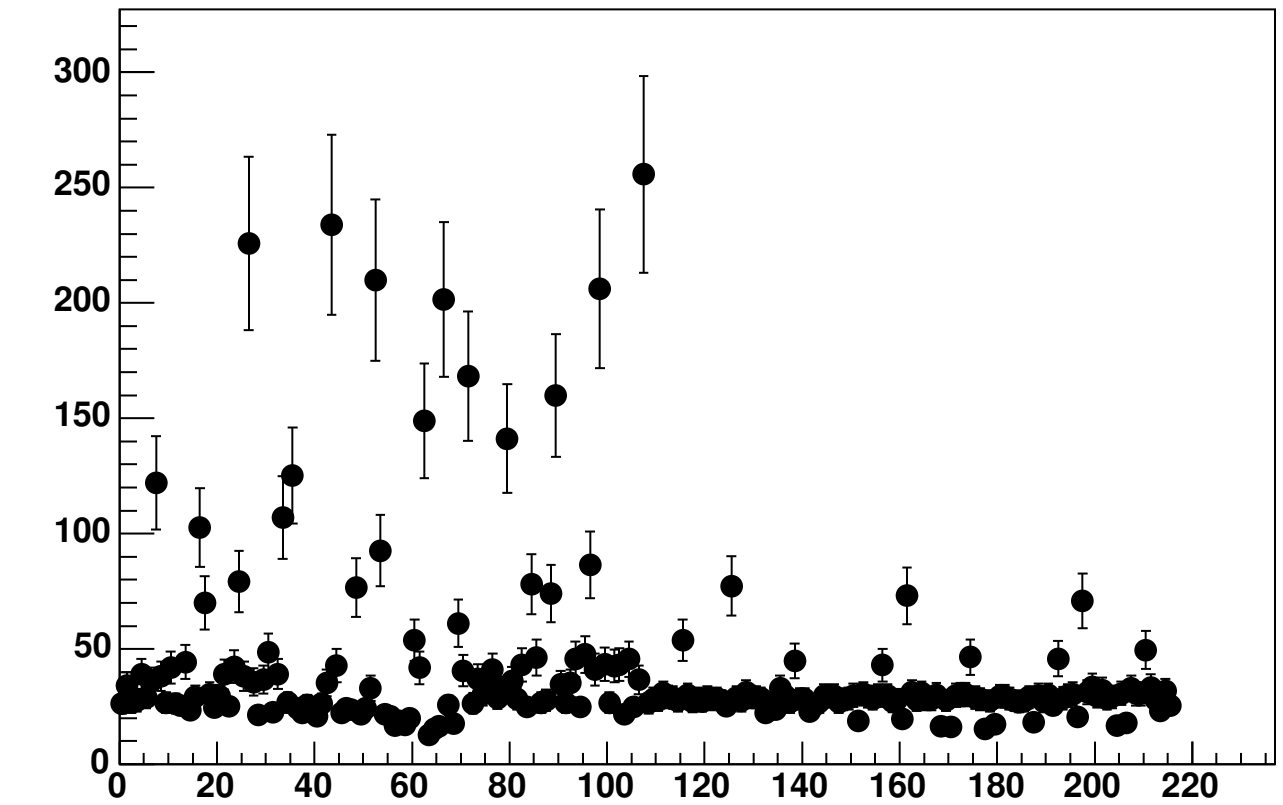
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



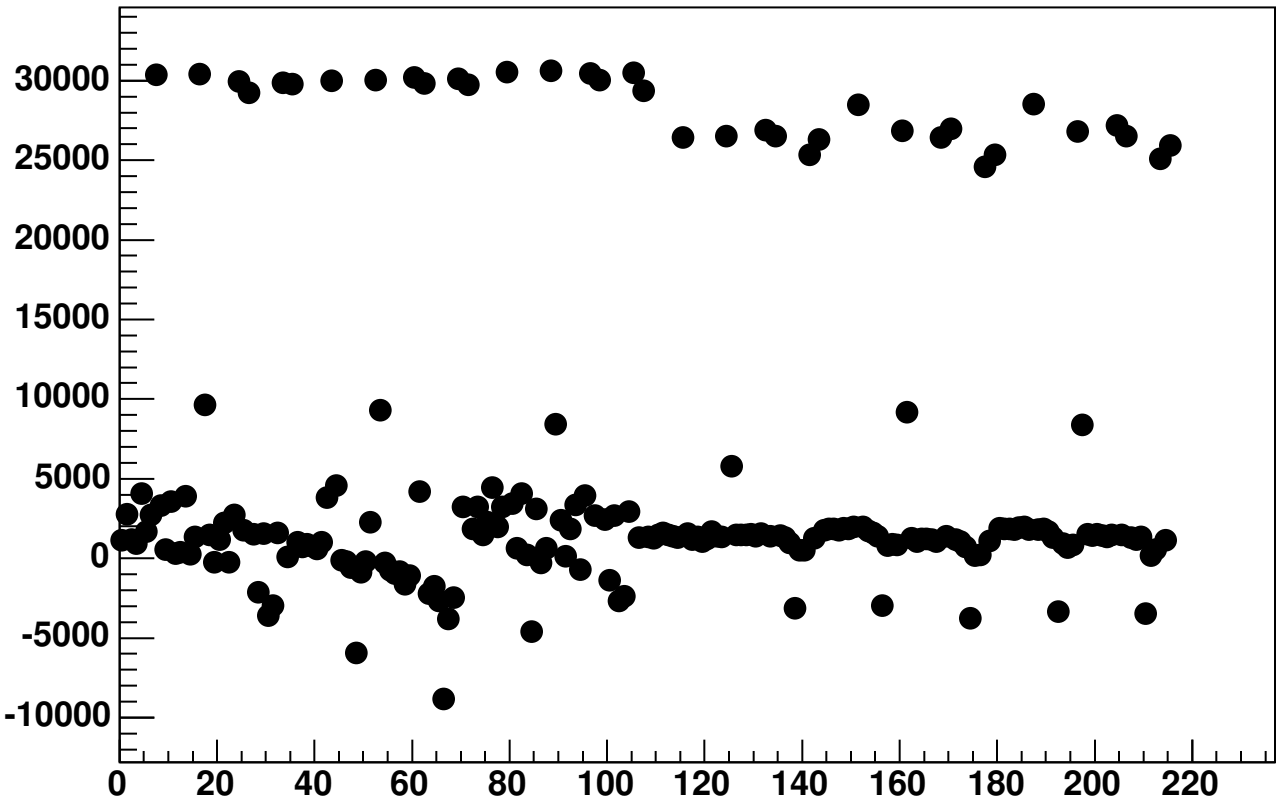
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



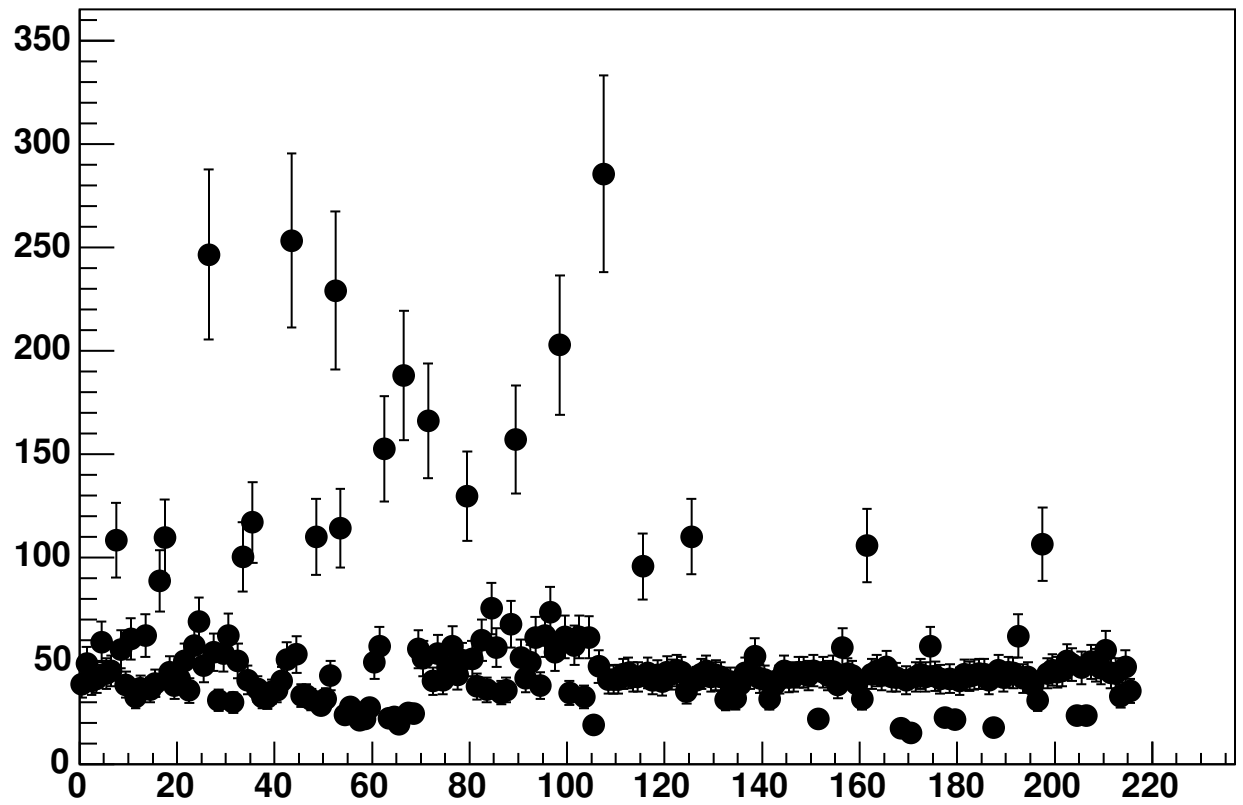
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



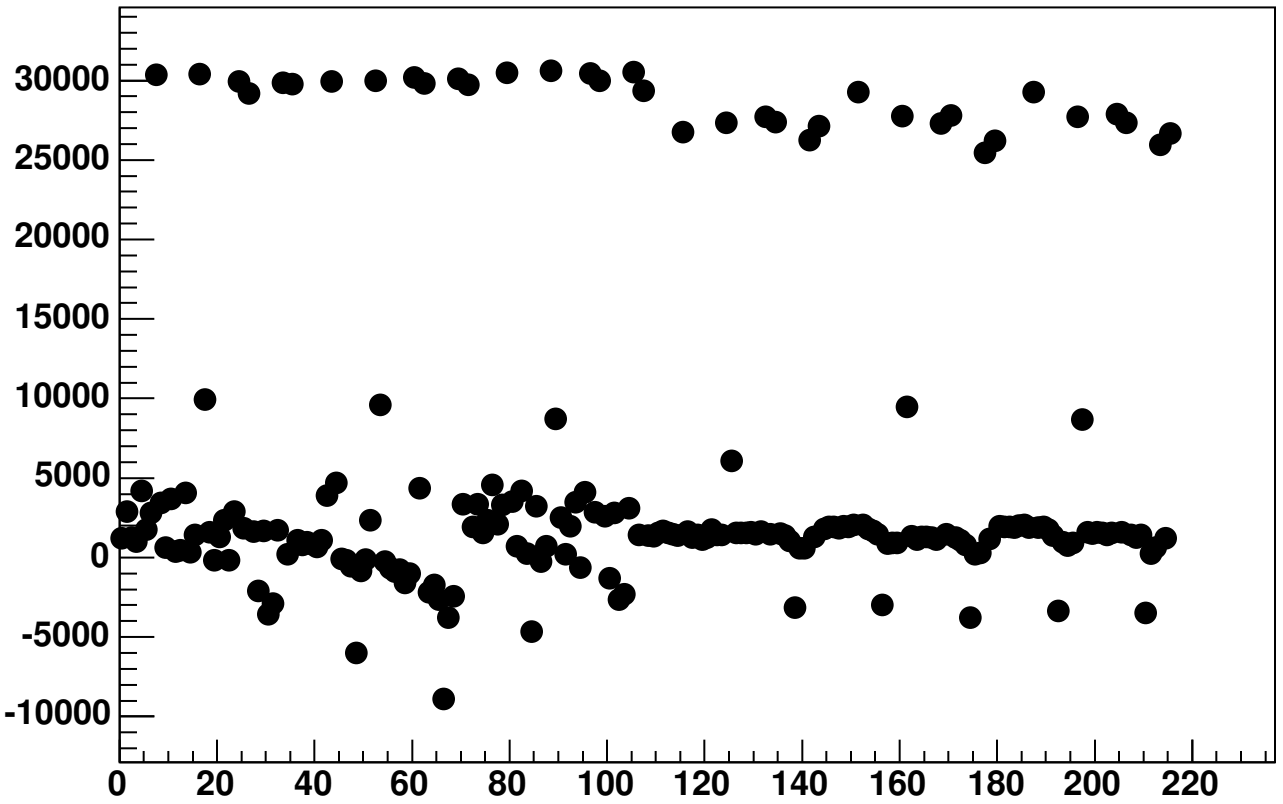
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



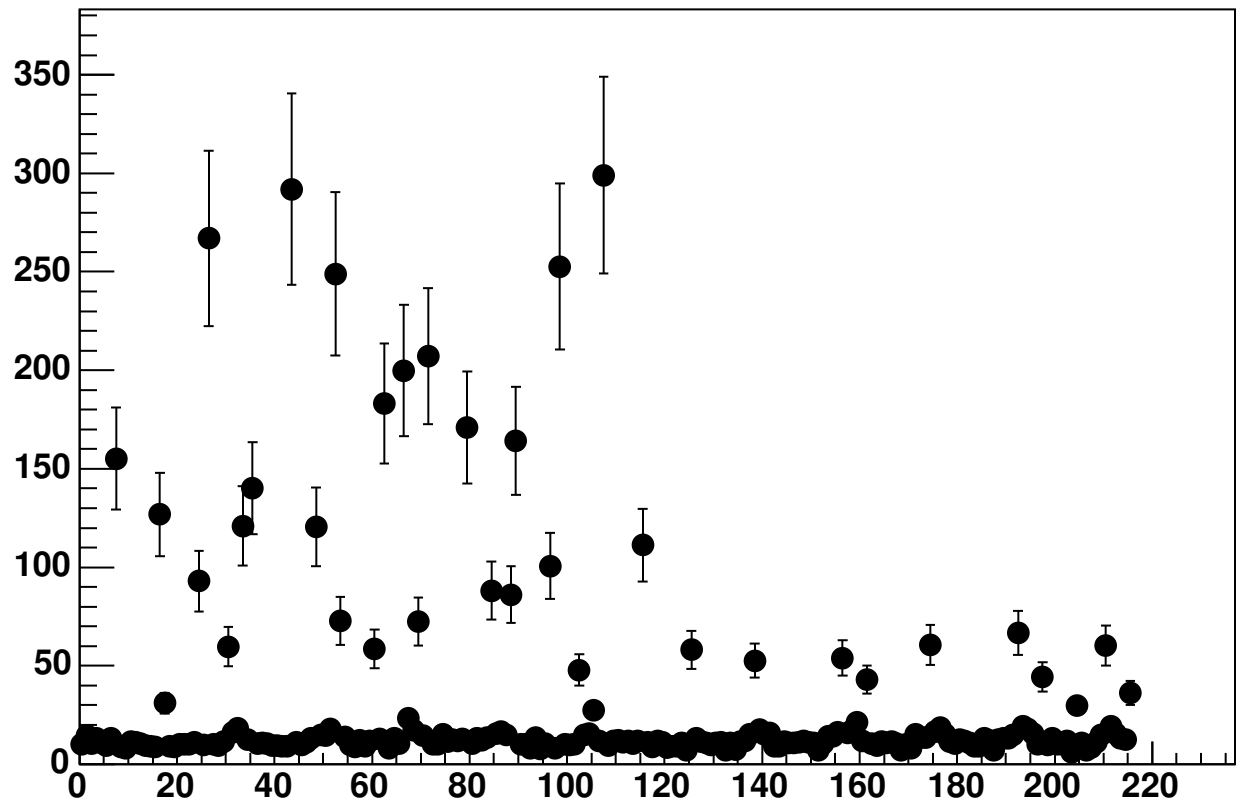
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



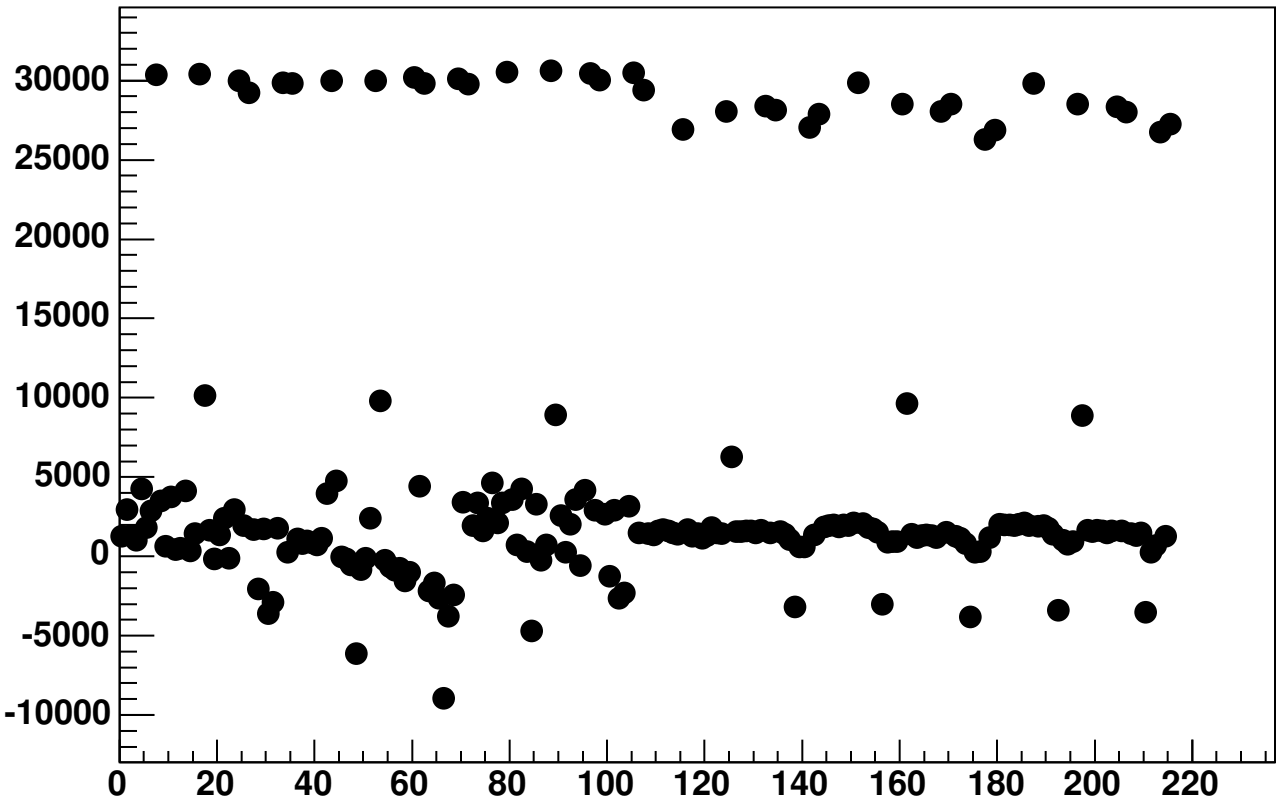
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



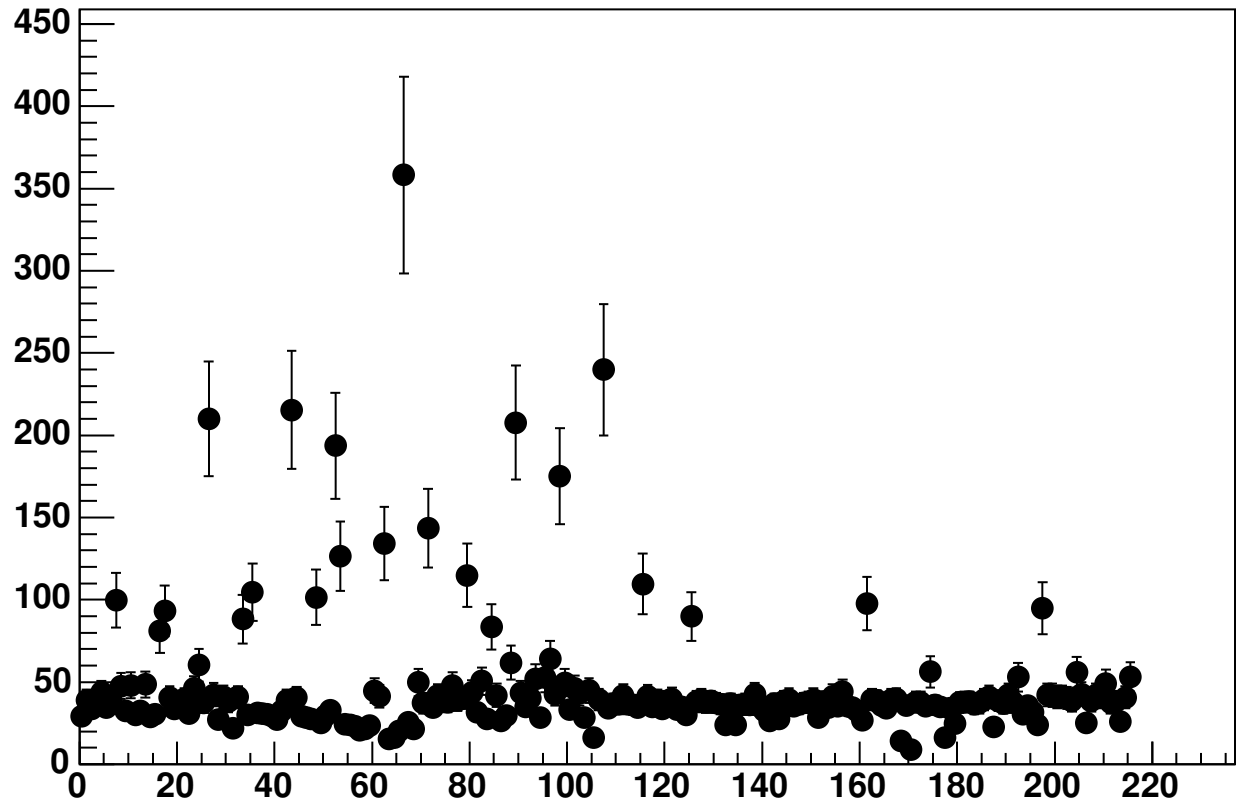
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



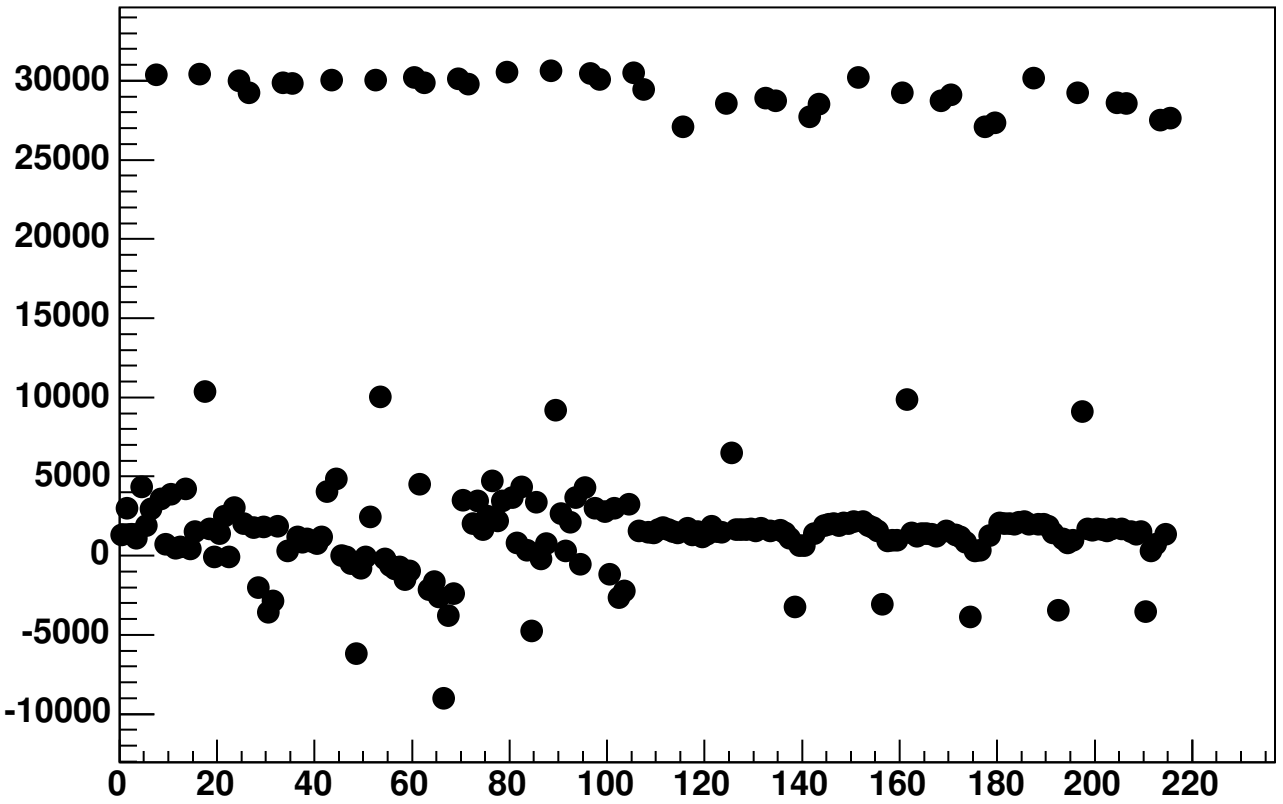
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



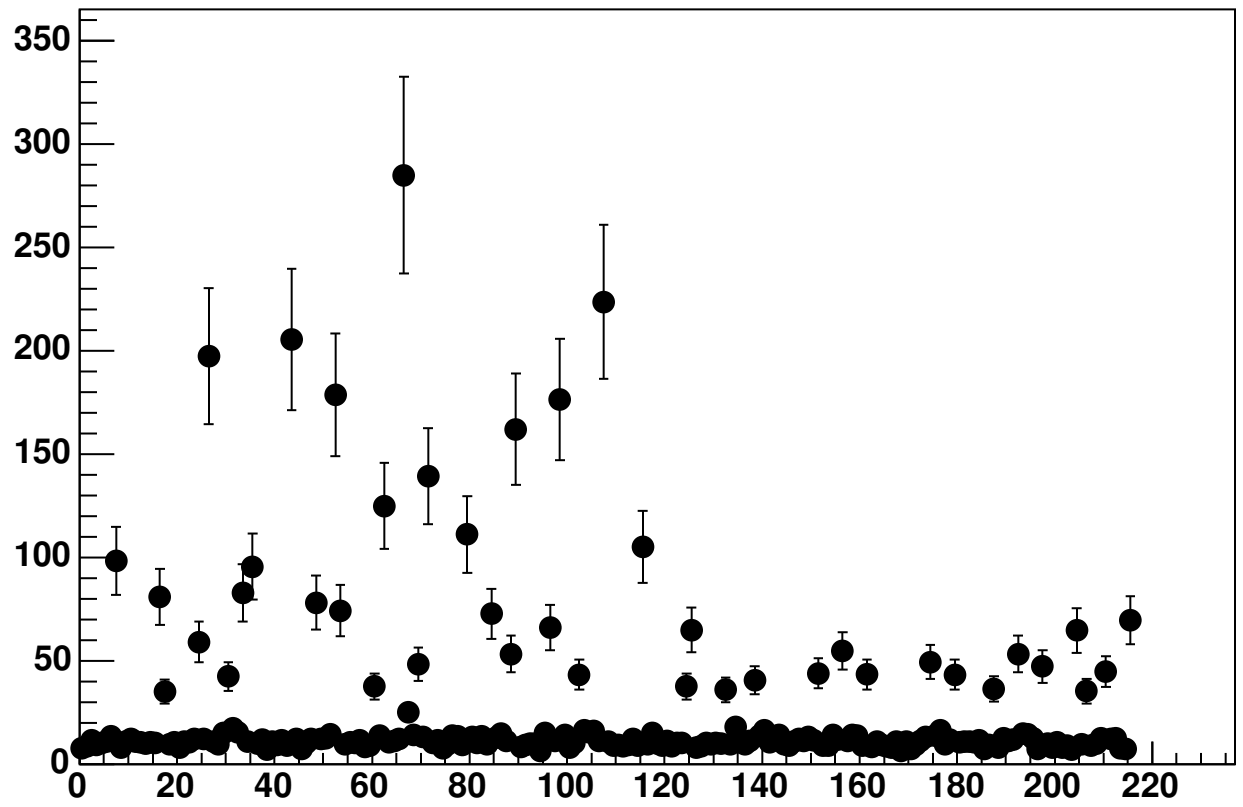
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



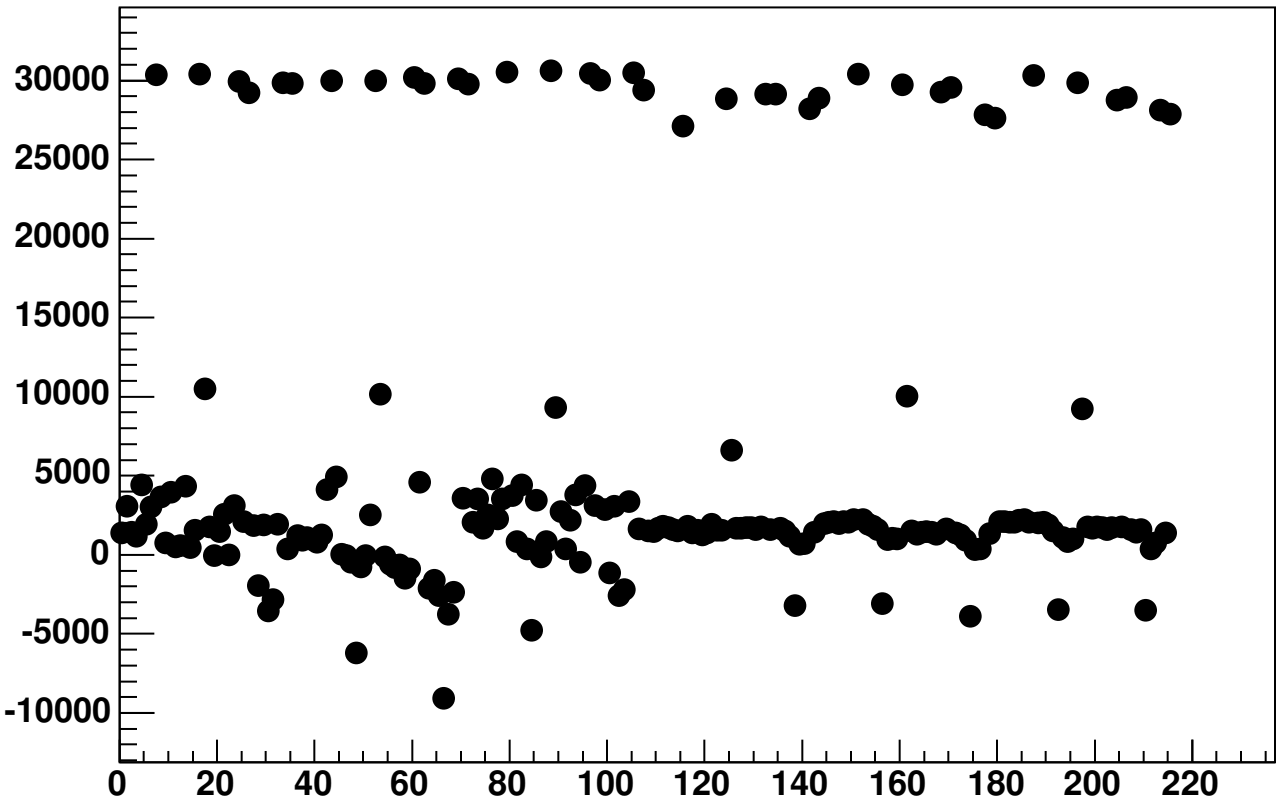
Enable 5, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



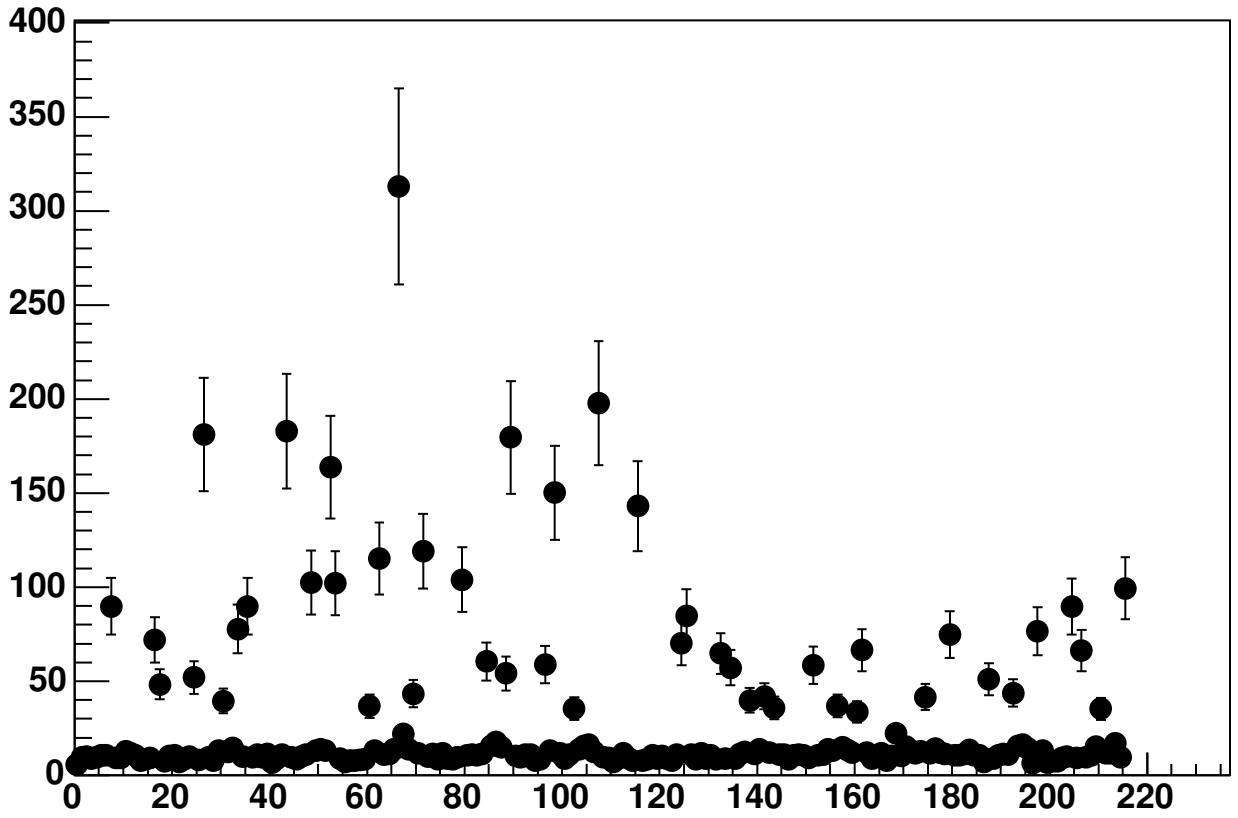
Enable 5, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



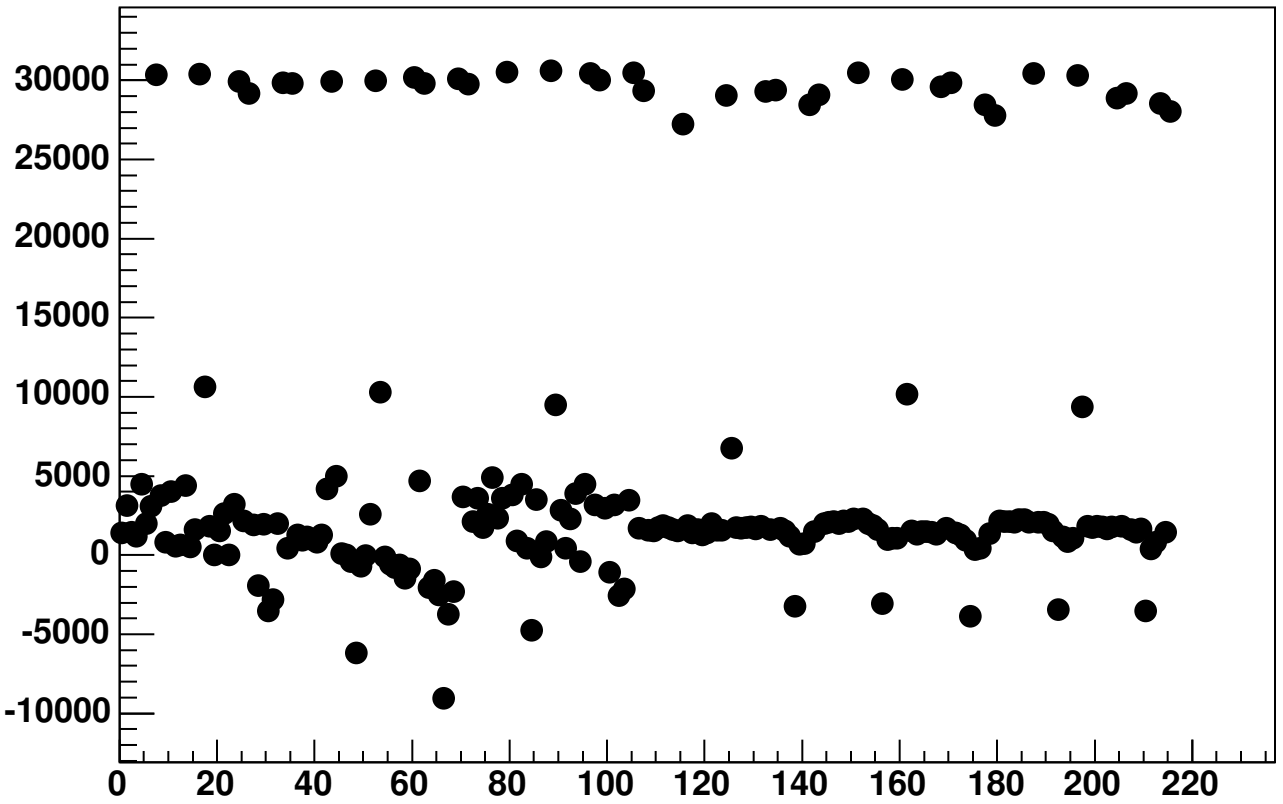
Enable 5, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



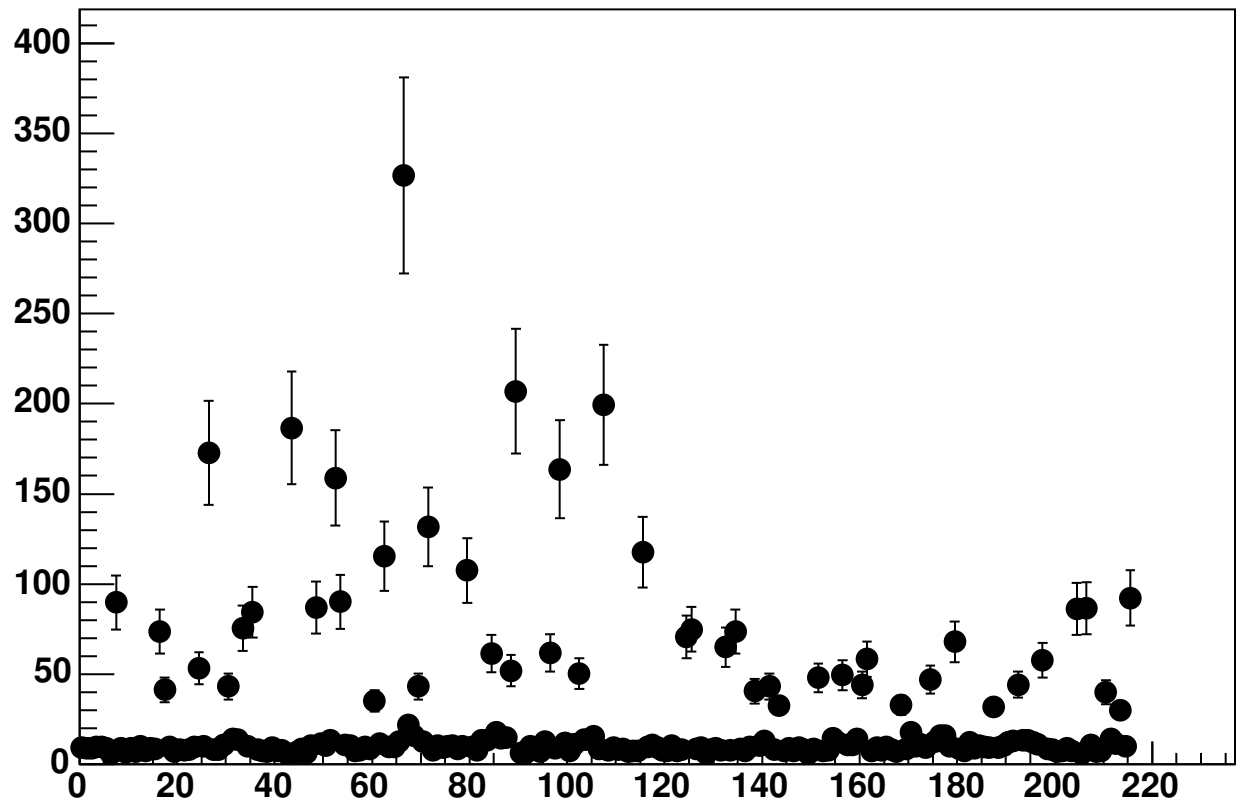
Enable 5, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan

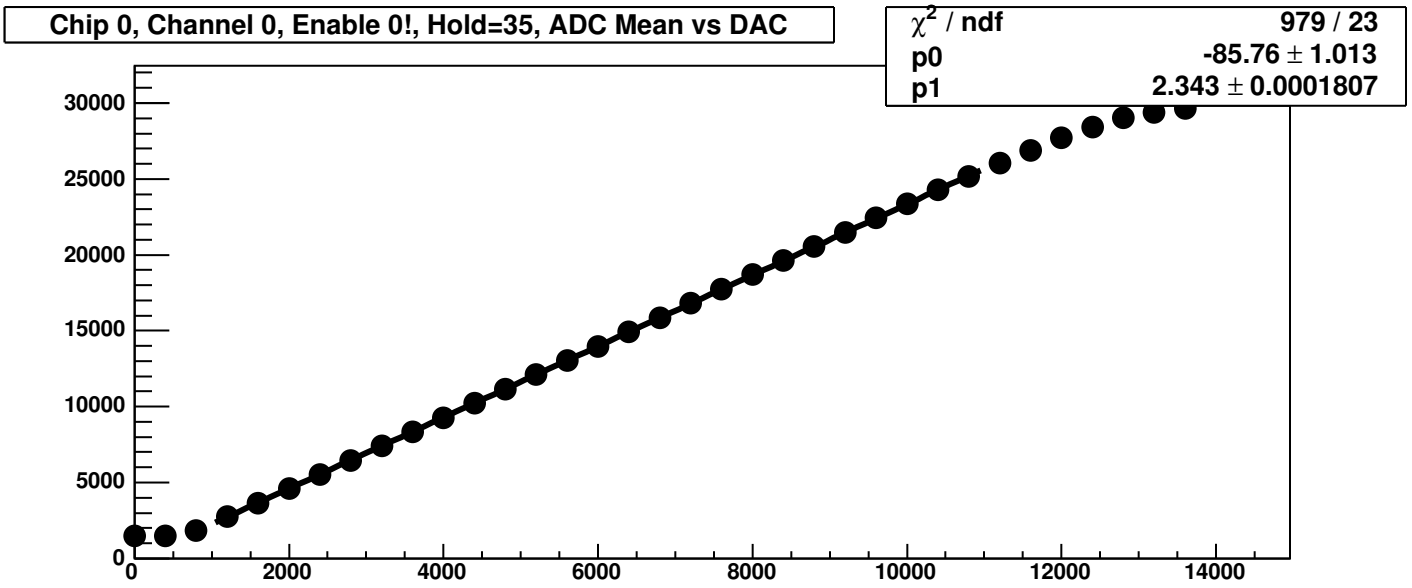


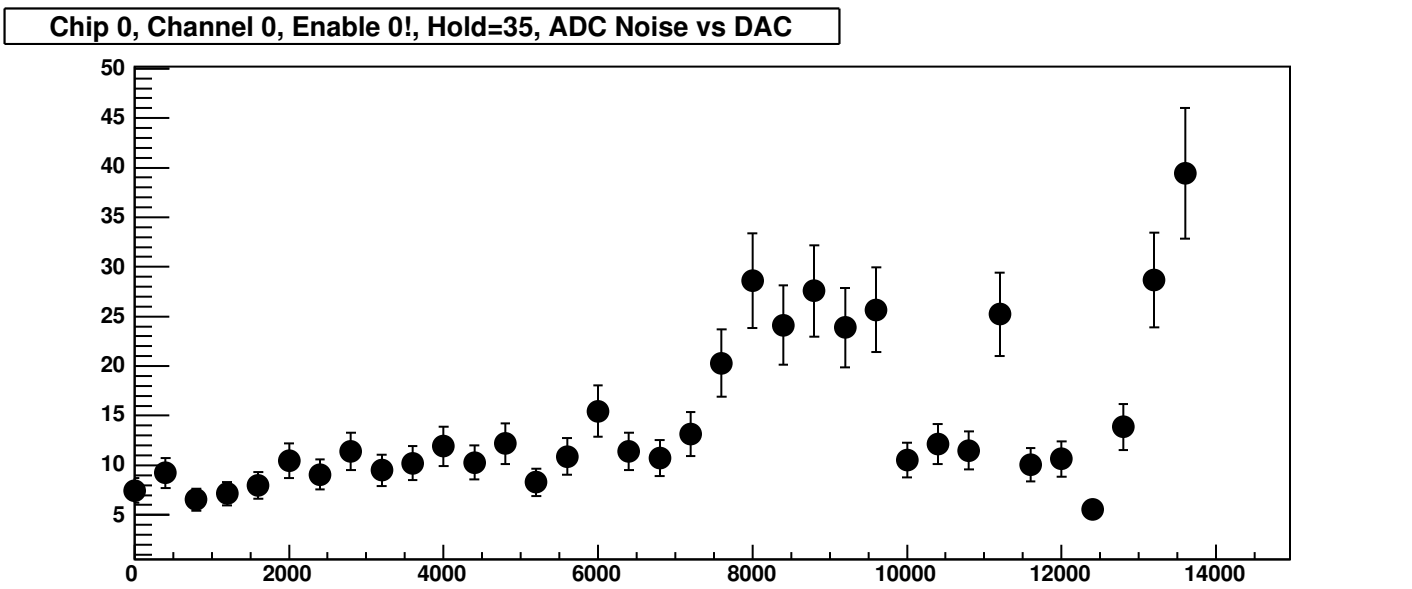
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan

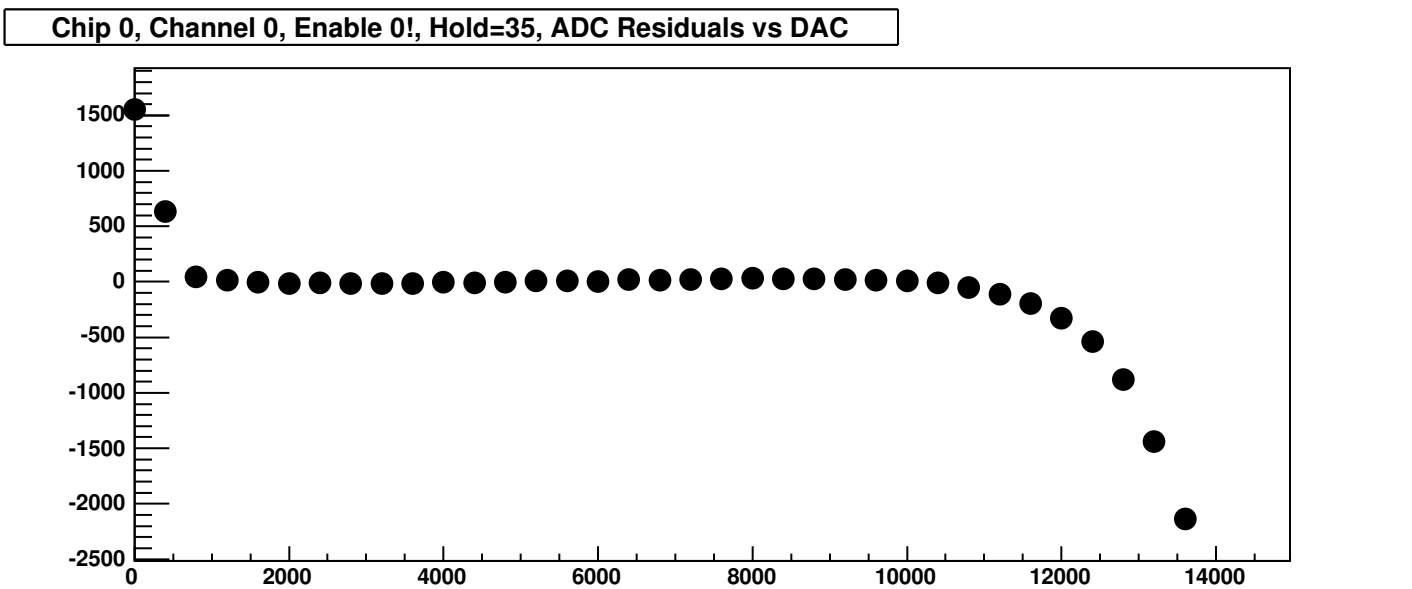


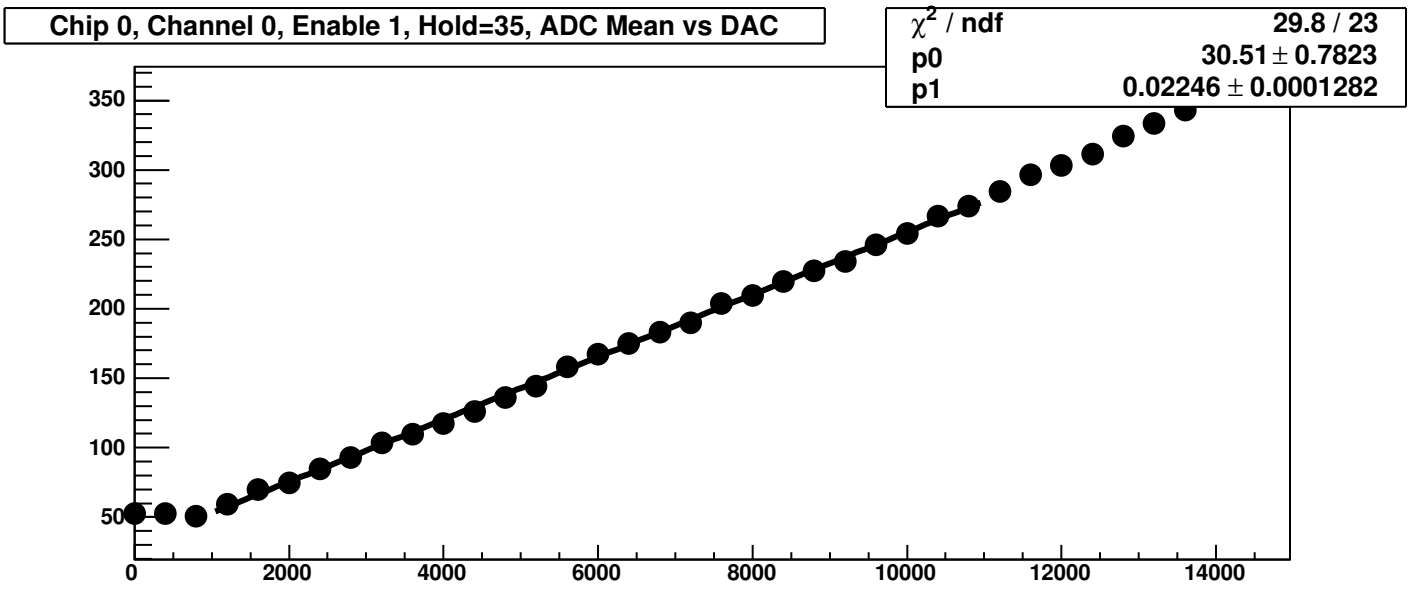
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan

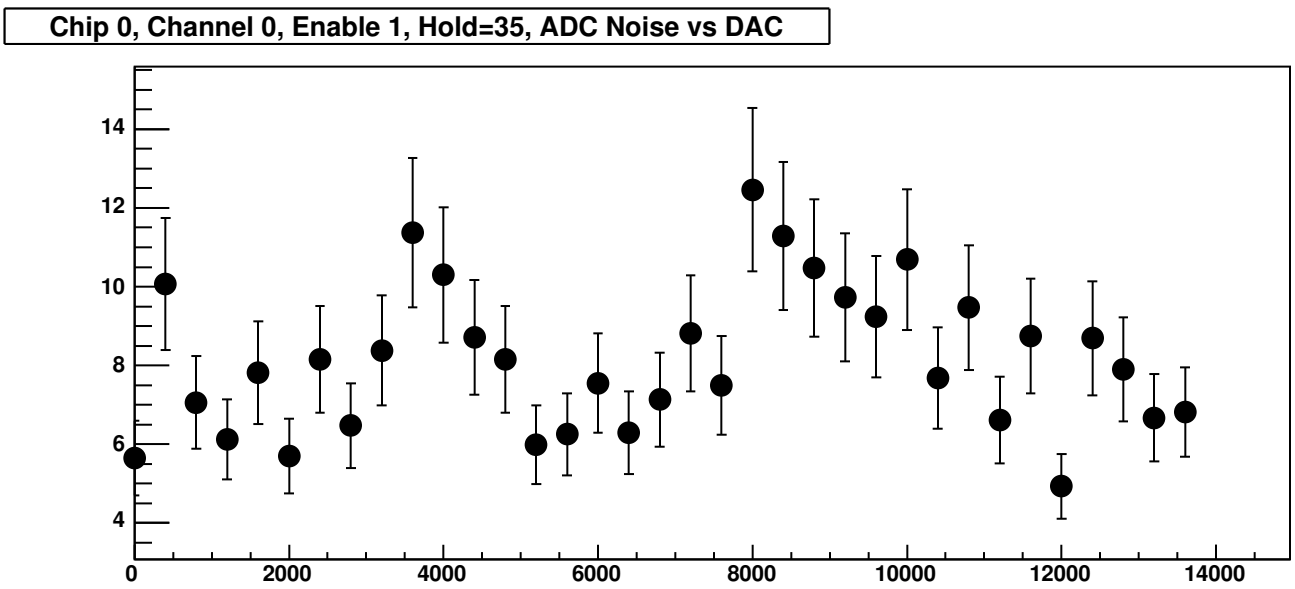


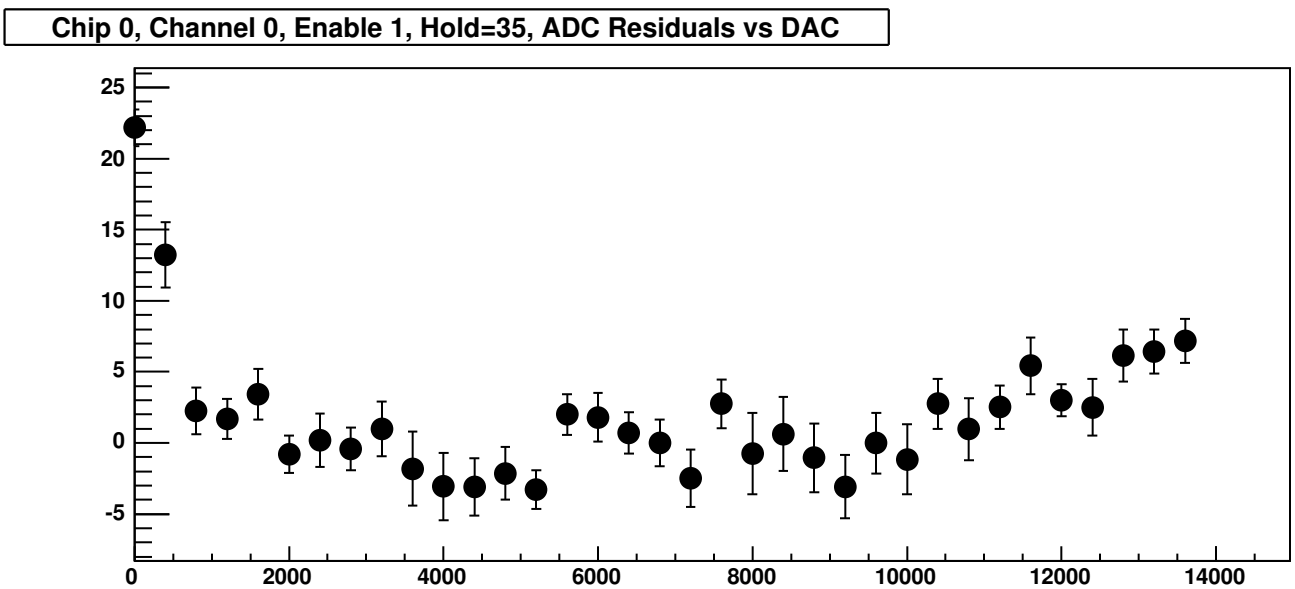

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 979 / 23
p0 -85.76 ± 1.013
p1 2.343 ± 0.0001807
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

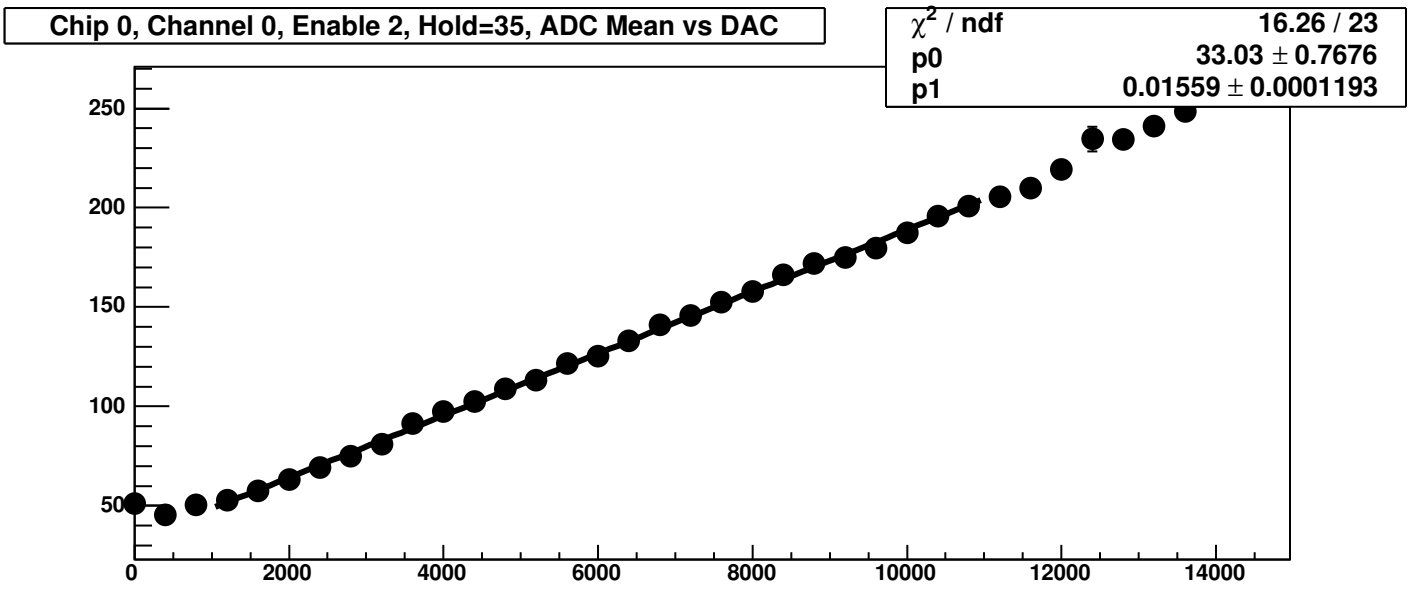

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

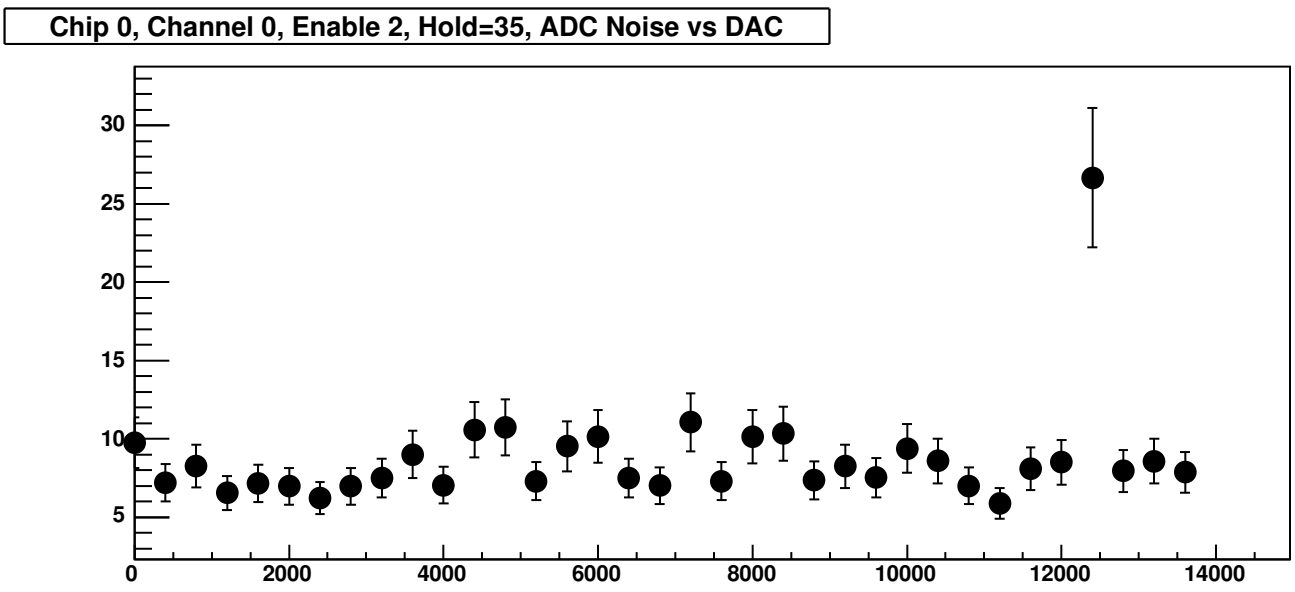

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

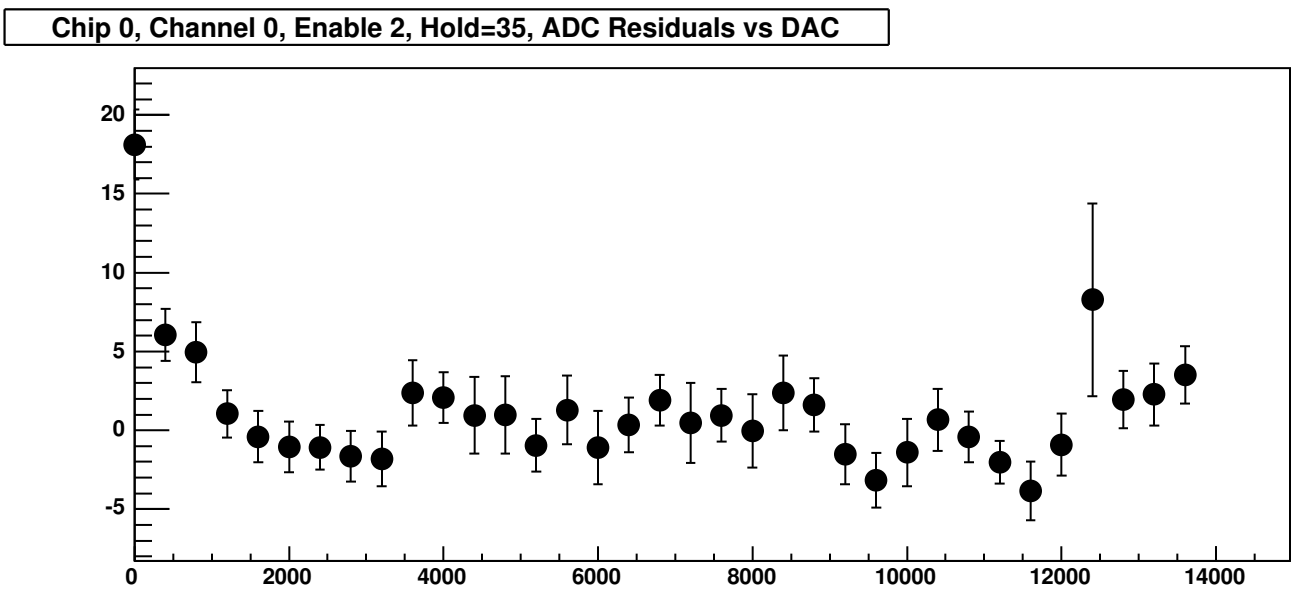

Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.8 / 23
p0 30.51 ± 0.7823
p1 0.02246 ± 0.0001282
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

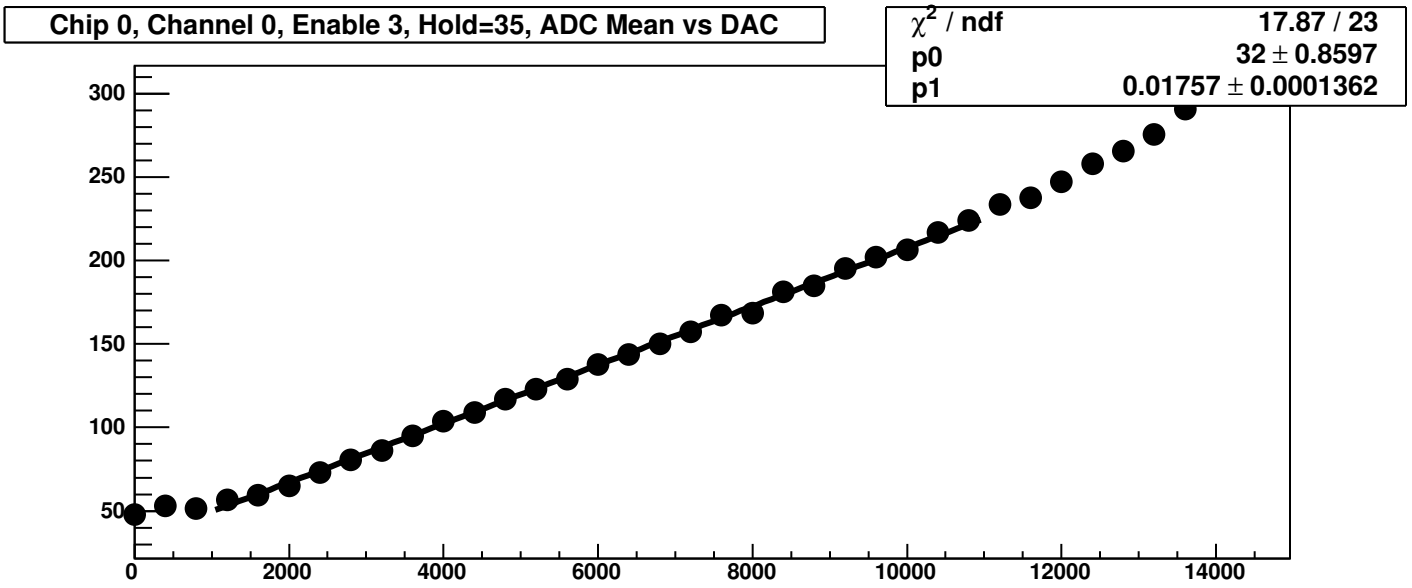

Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

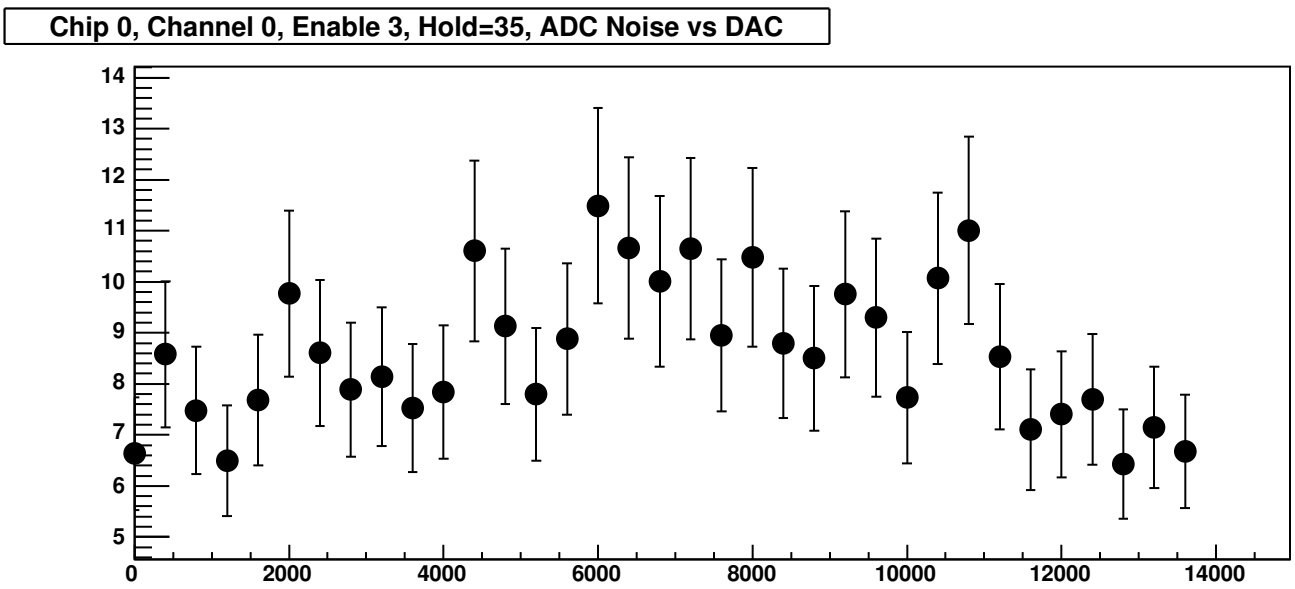

Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

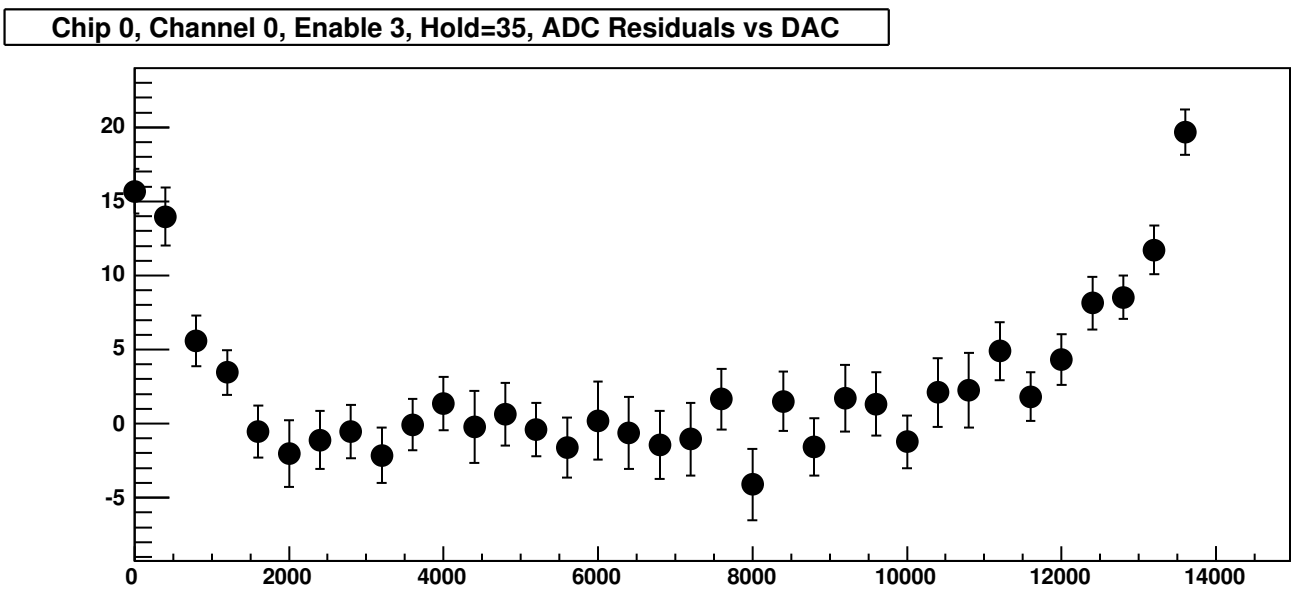

Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 16.26 / 23
p0 33.03 ± 0.7676
p1 0.01559 ± 0.0001193
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

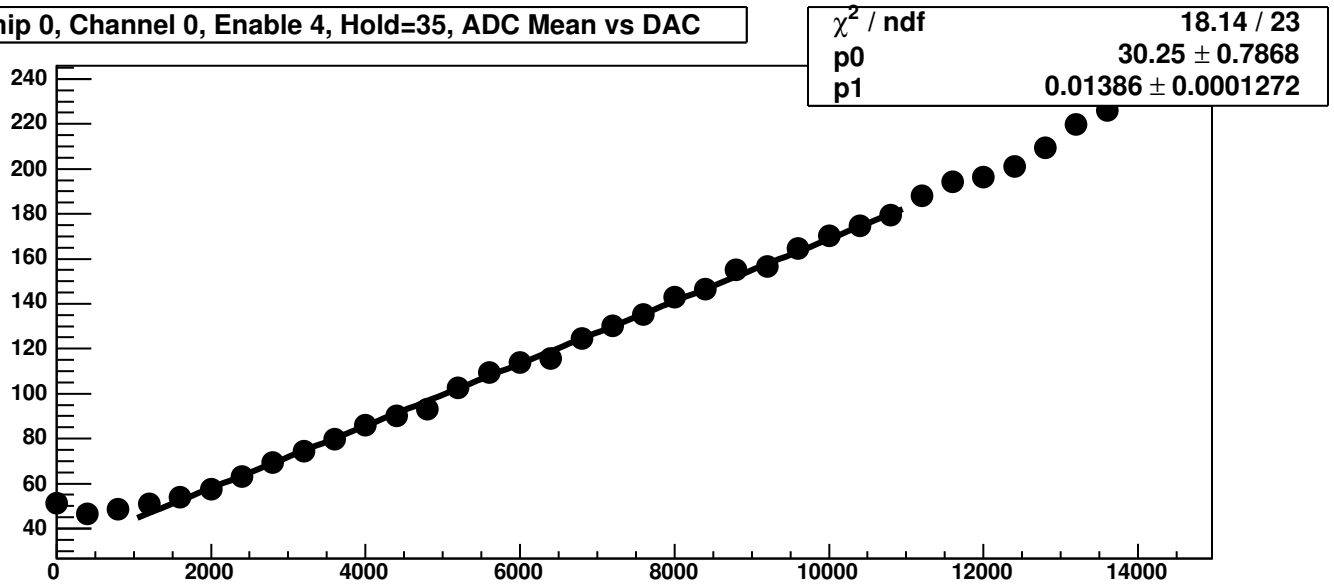

Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.87 / 23
p0 32 ± 0.8597
p1 0.01757 ± 0.0001362
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

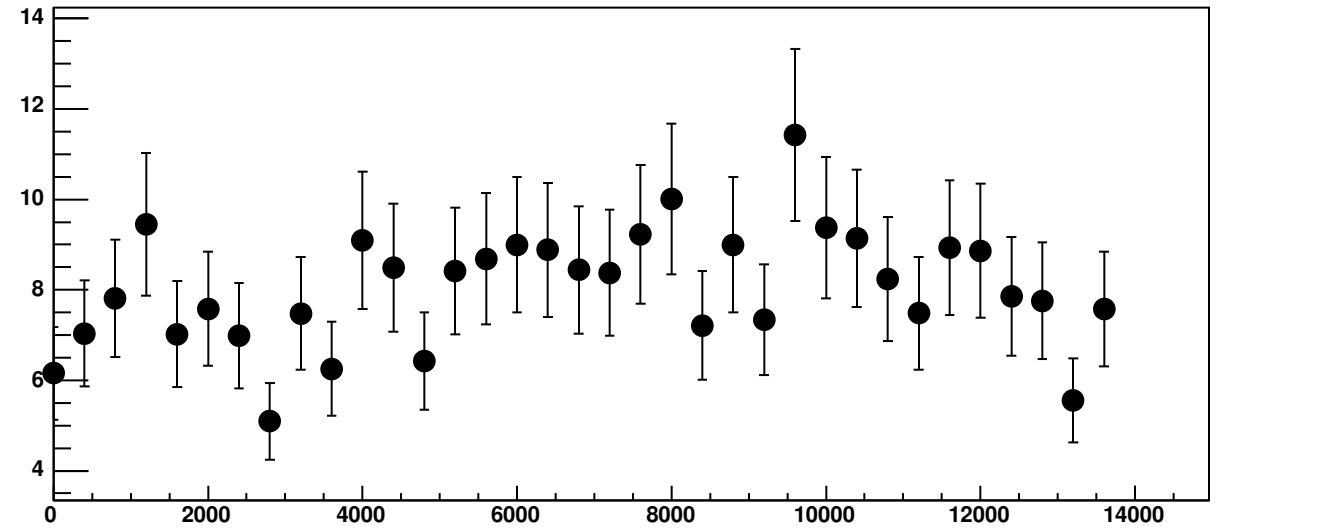

Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

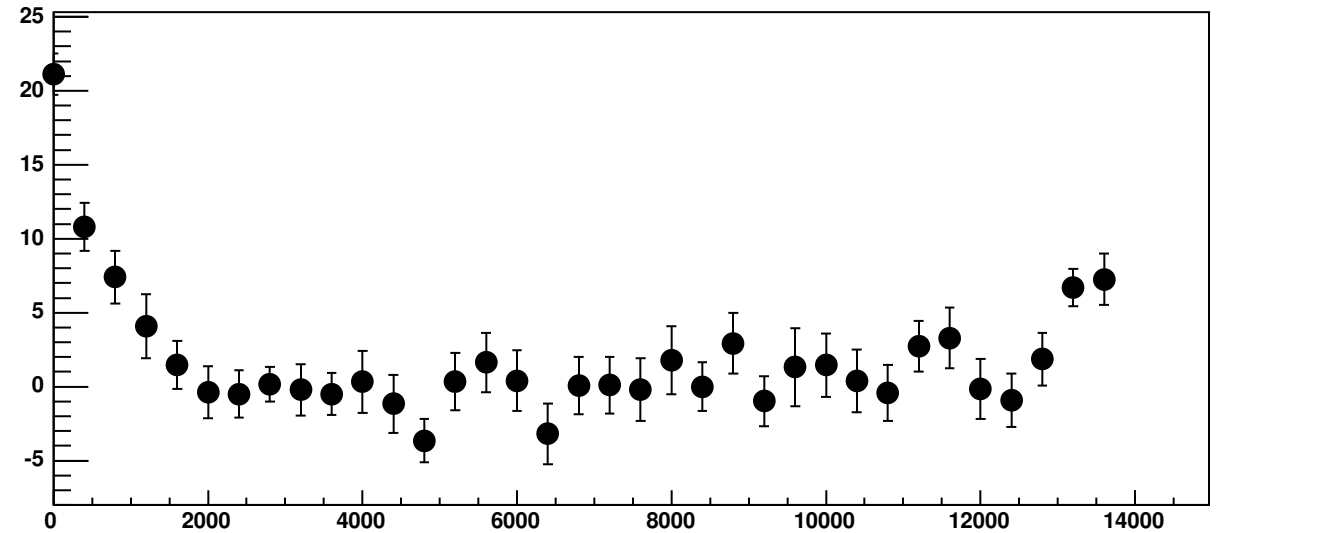
Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

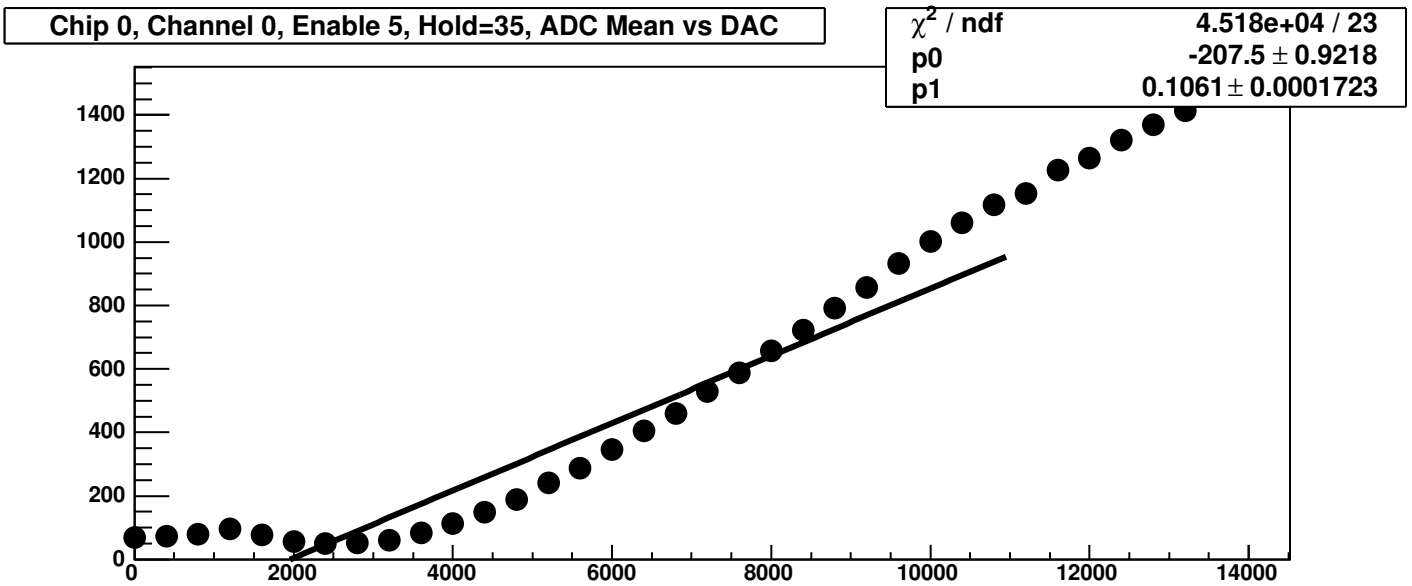


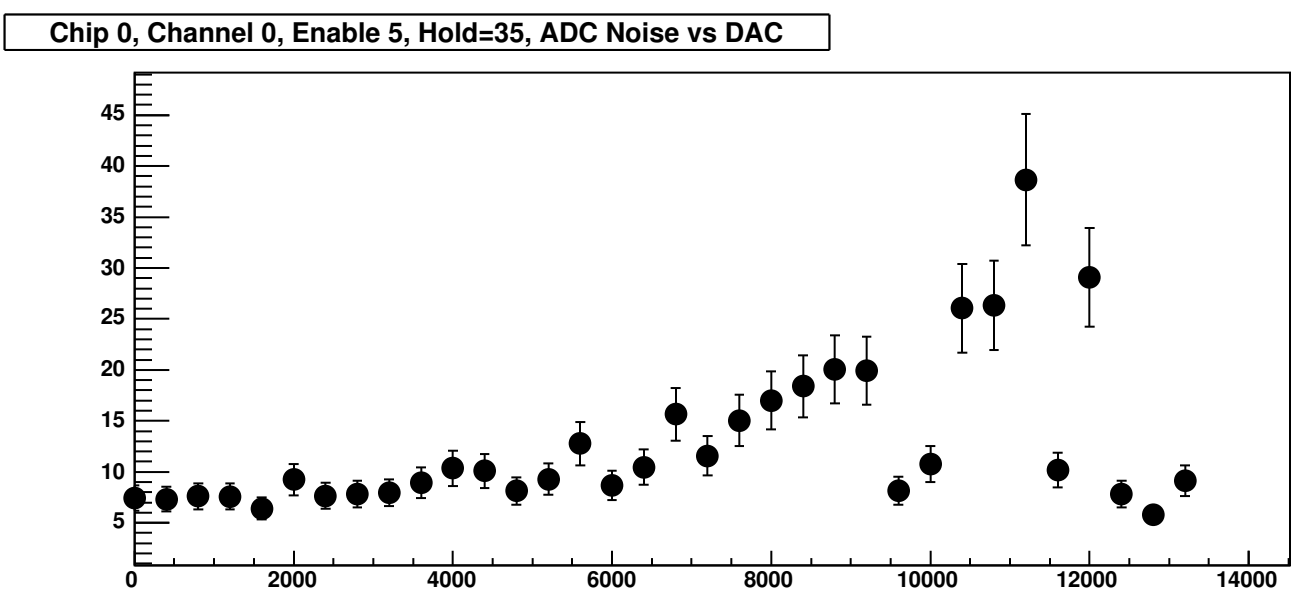
Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

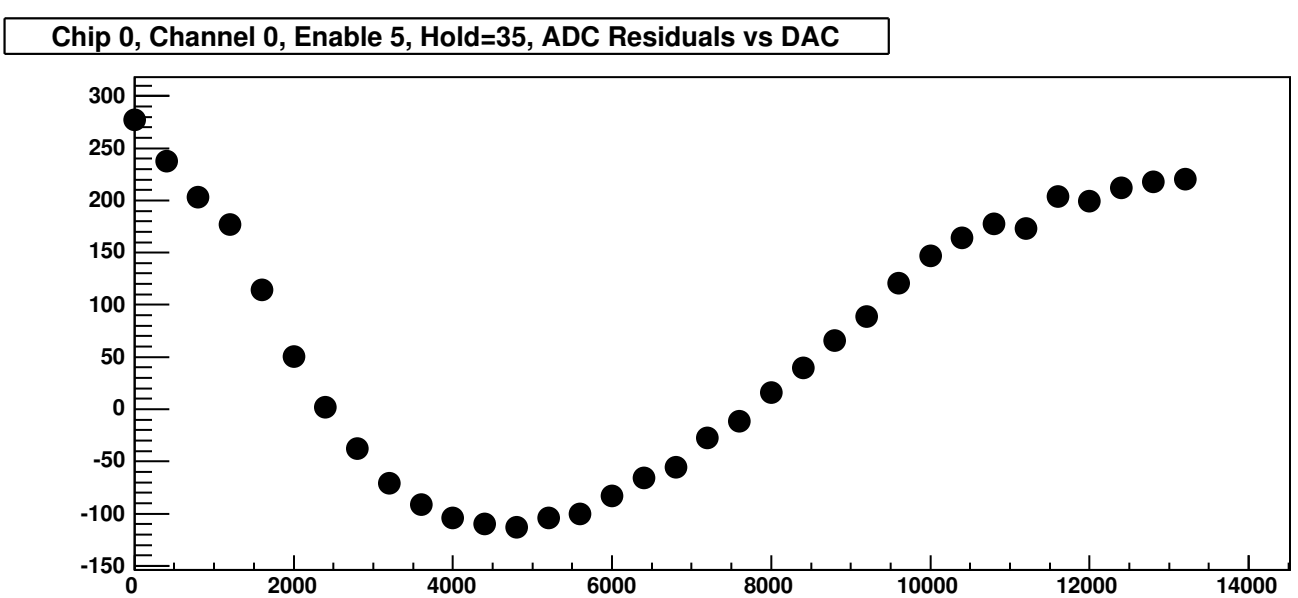


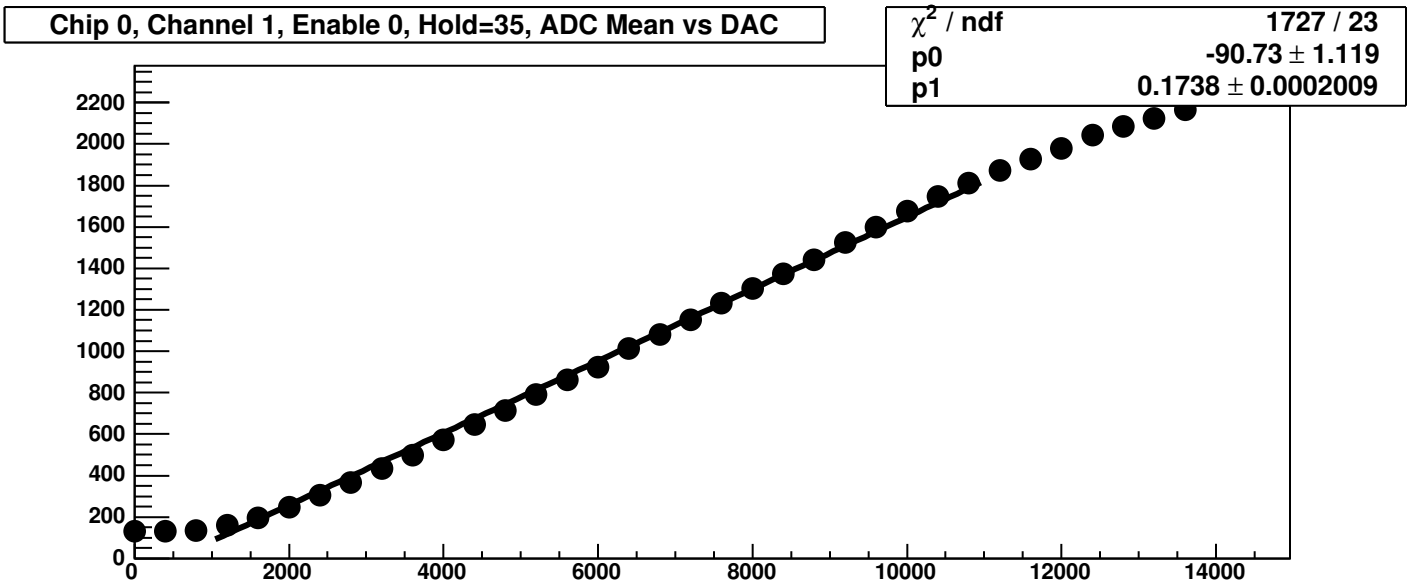
Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

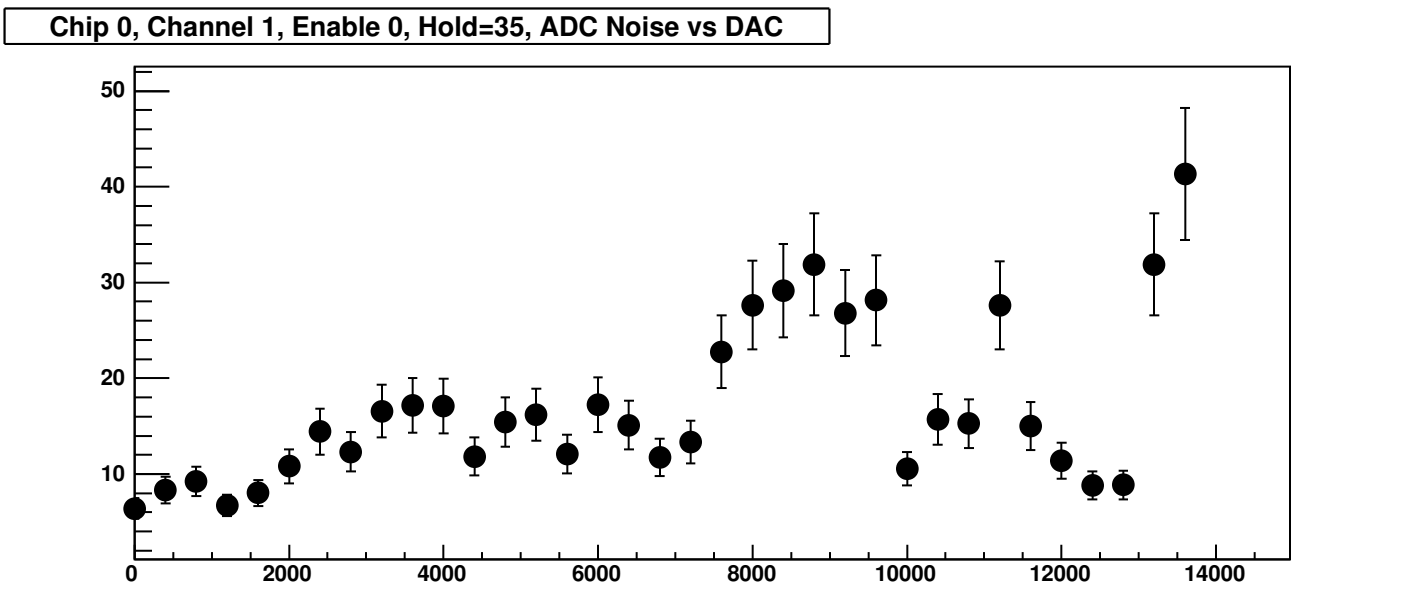


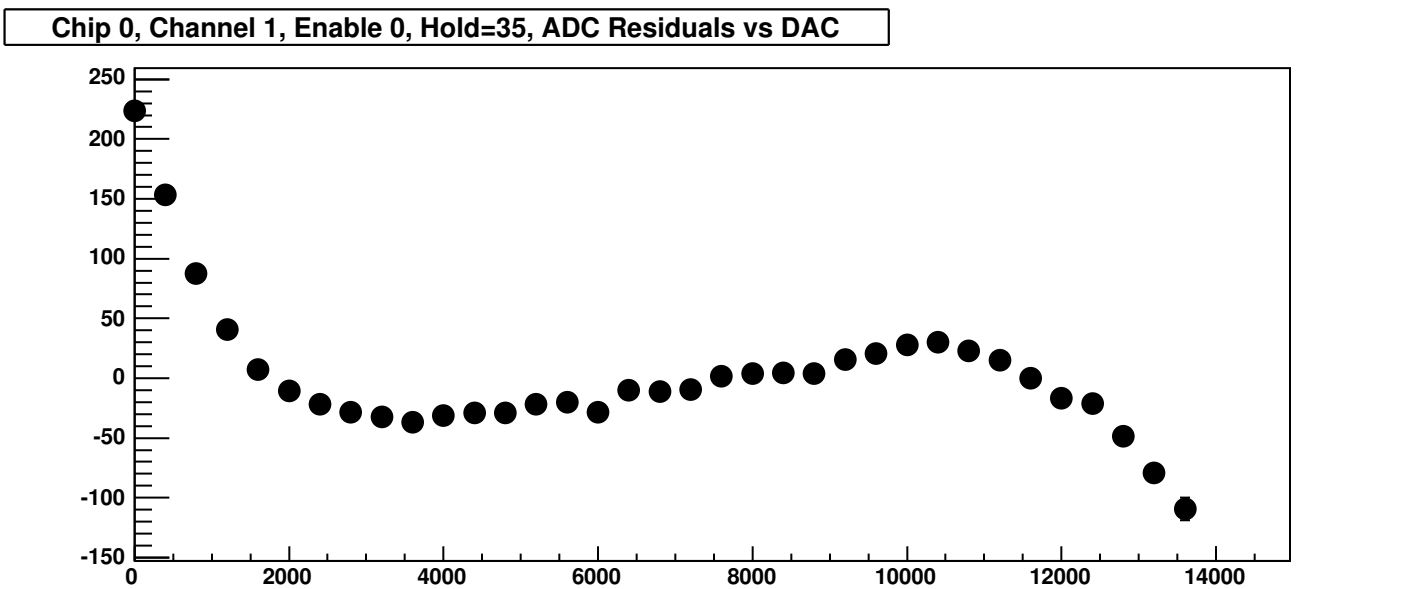

Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.518e+04 / 23
p0 -207.5 ± 0.9218
p1 0.1061 ± 0.0001723
0 200 400 600 800 1000 1200 1400
0 2000 4000 6000 8000 10000 12000 14000

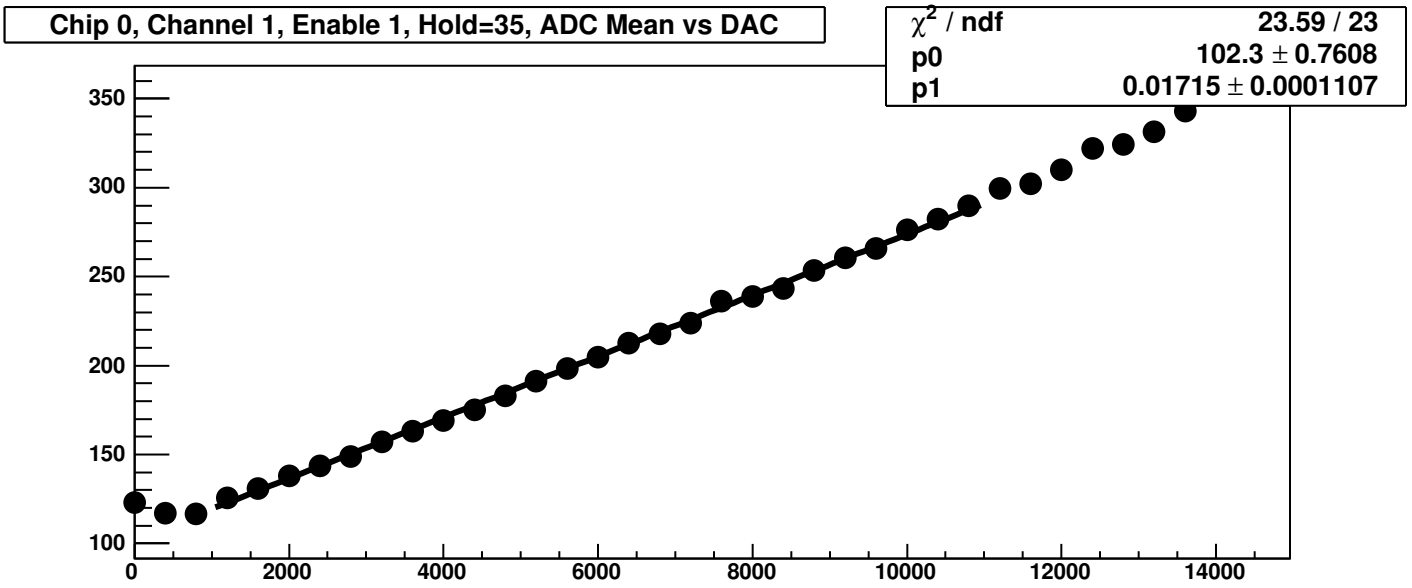

Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
5 10 15 20 25 30 35 40 45
0 2000 4000 6000 8000 10000 12000 14000

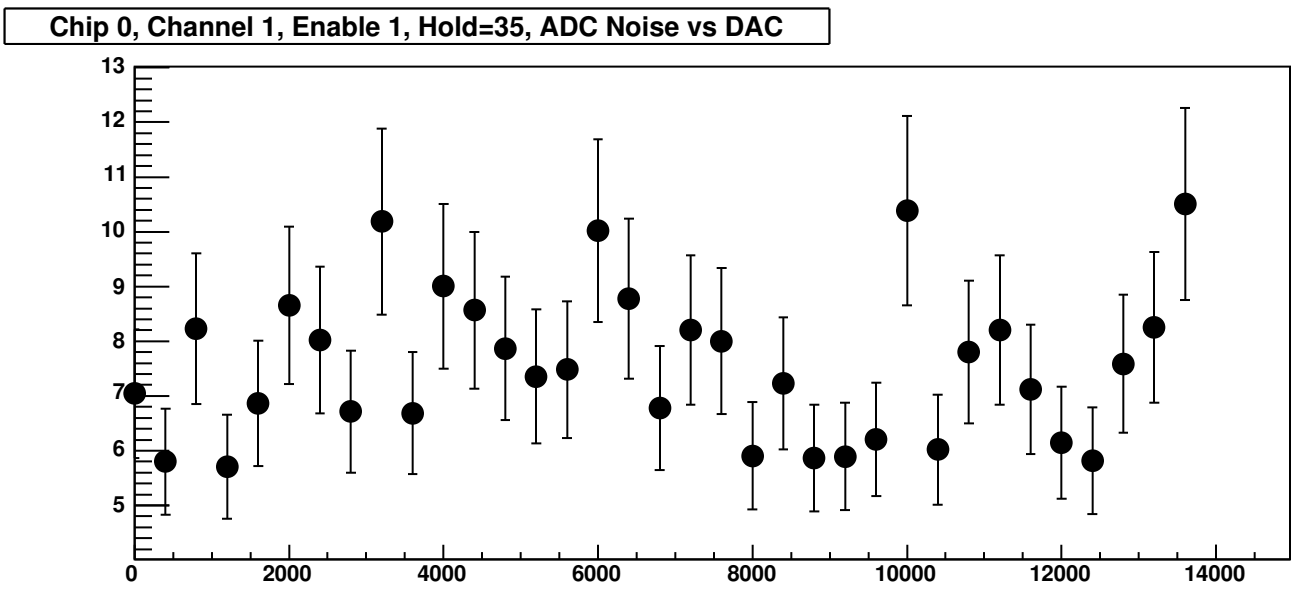

Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
-150 -100 -50 0 50 100 150 200 250 300
0 2000 4000 6000 8000 10000 12000 14000

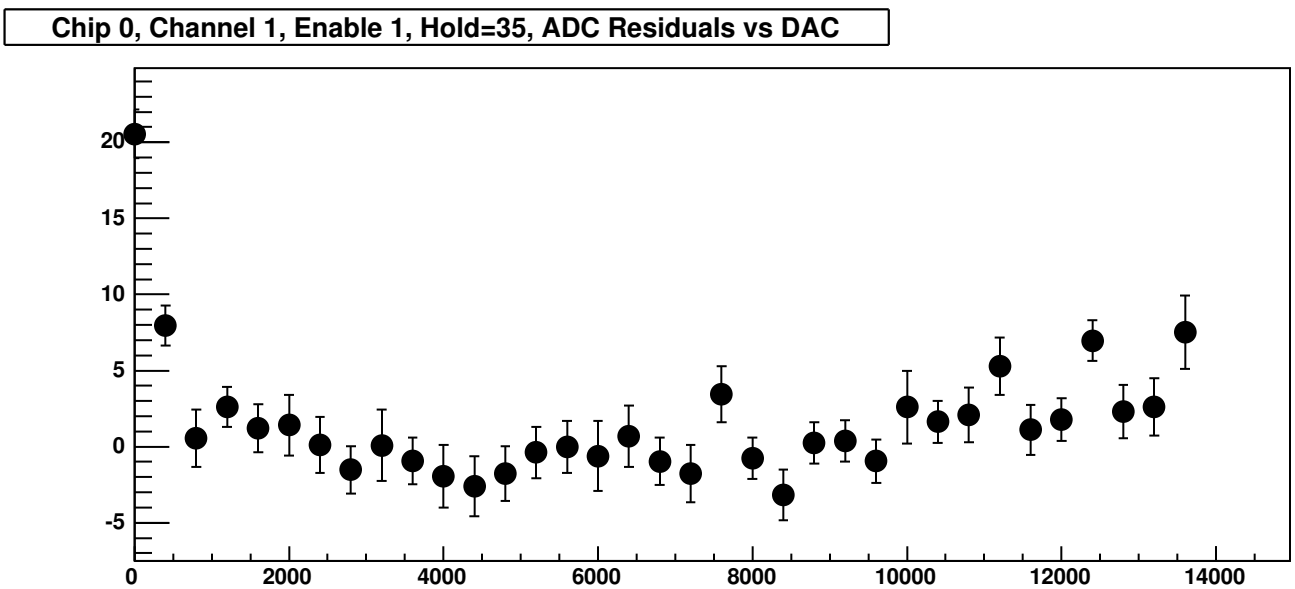

Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1727 / 23
p0 -90.73 ± 1.119
p1 0.1738 ± 0.0002009
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

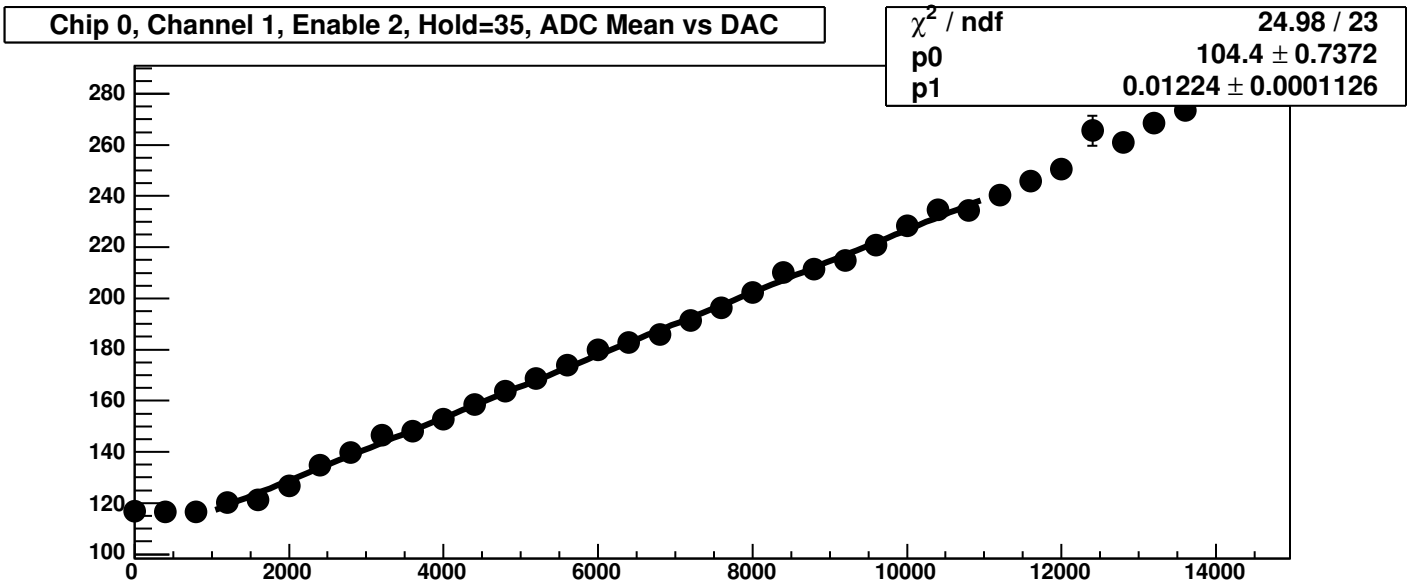

Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

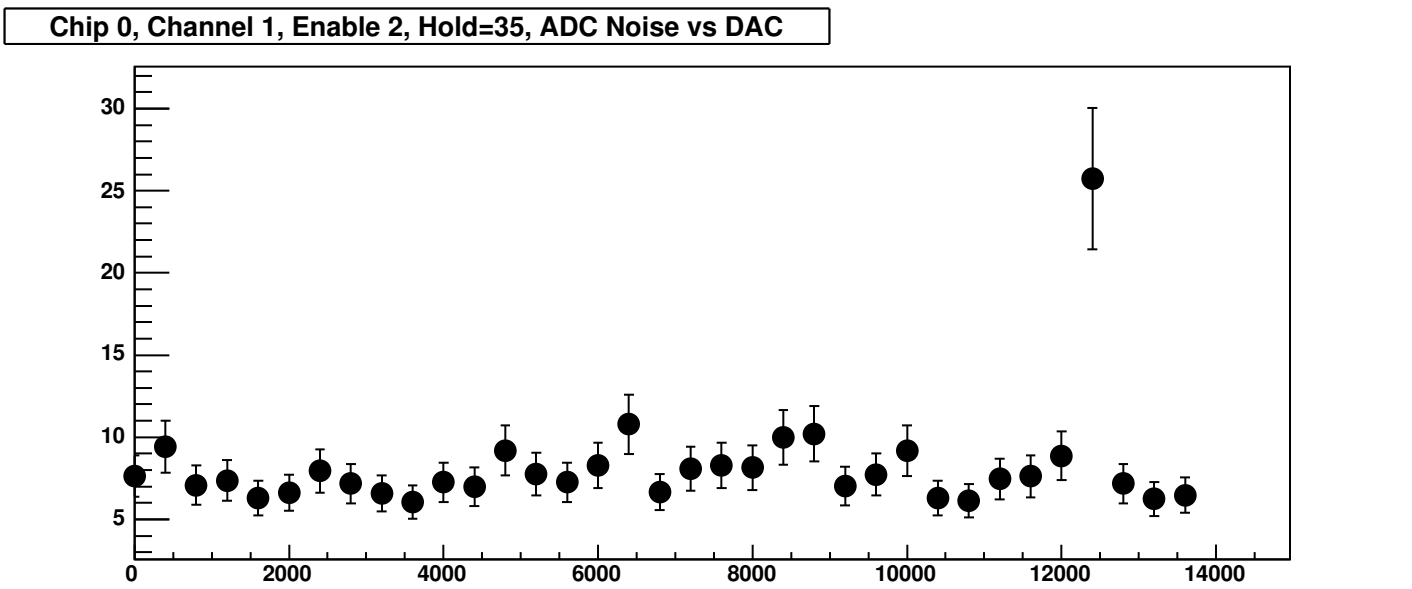

Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

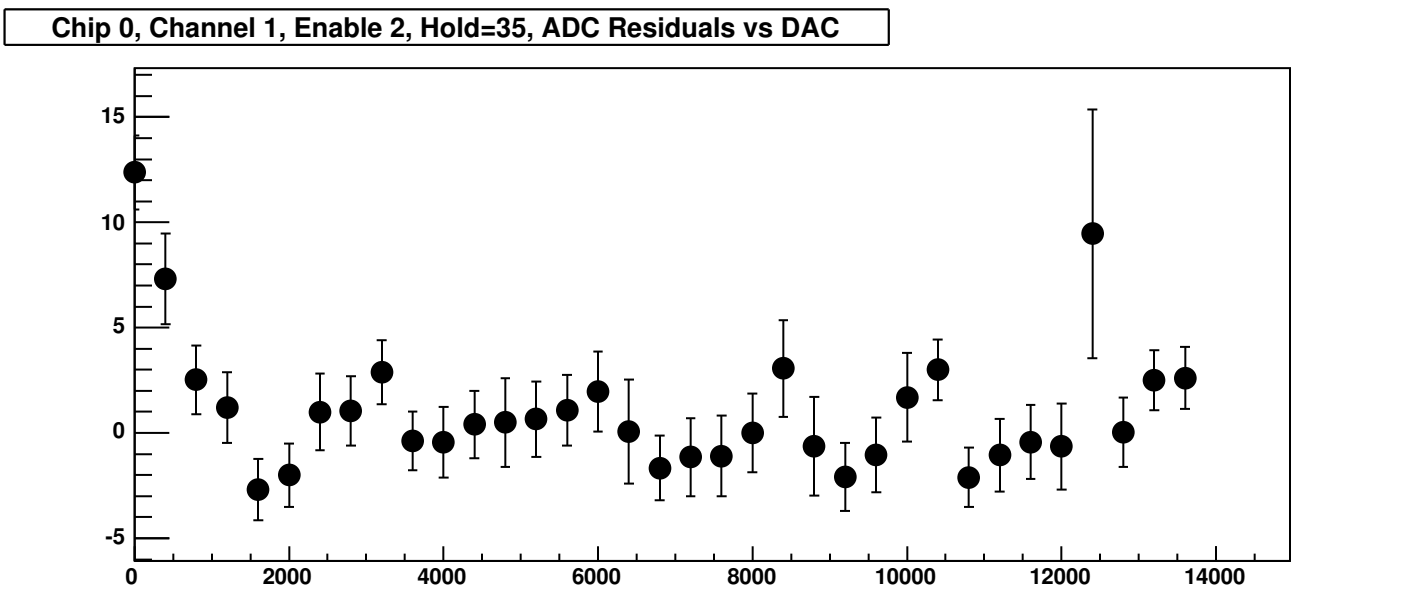

Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.59 / 23
p0 102.3 ± 0.7608
p1 0.01715 ± 0.0001107
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

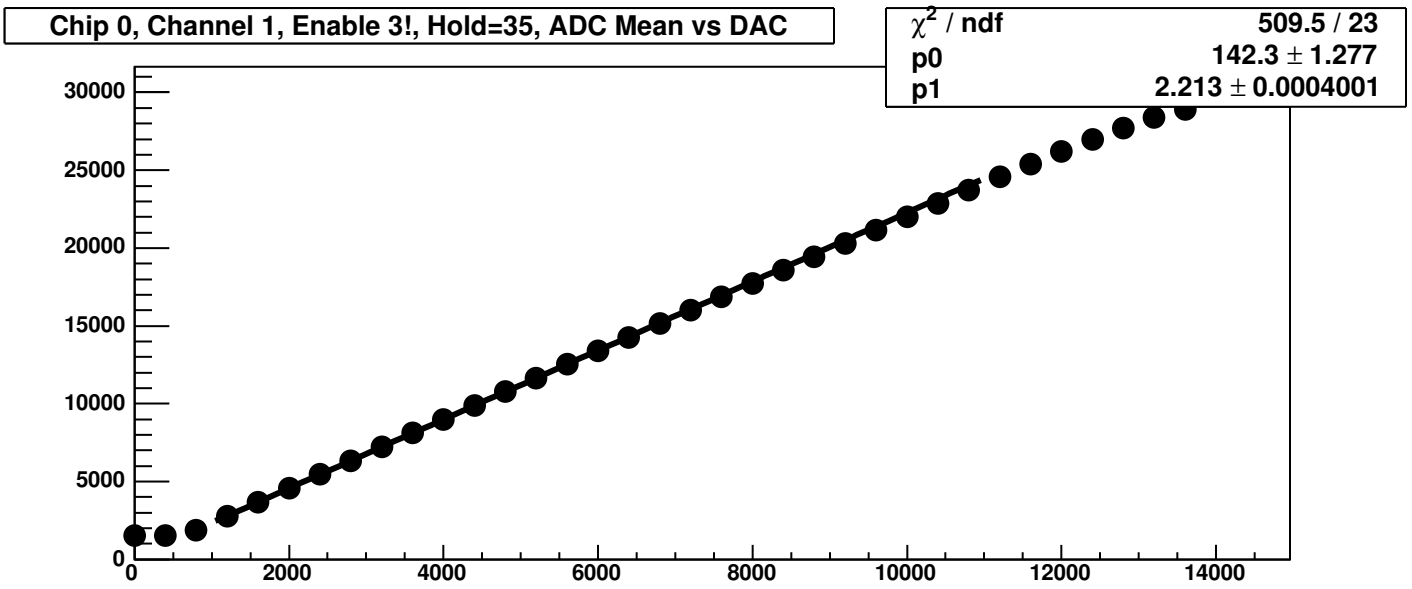

Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

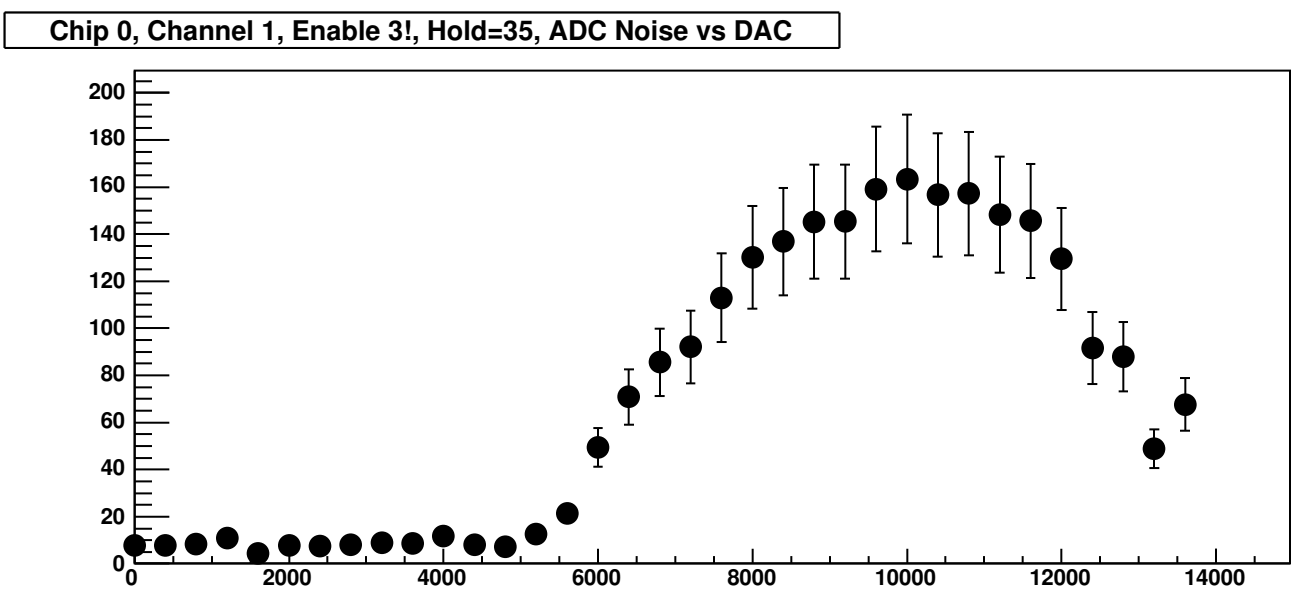

Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

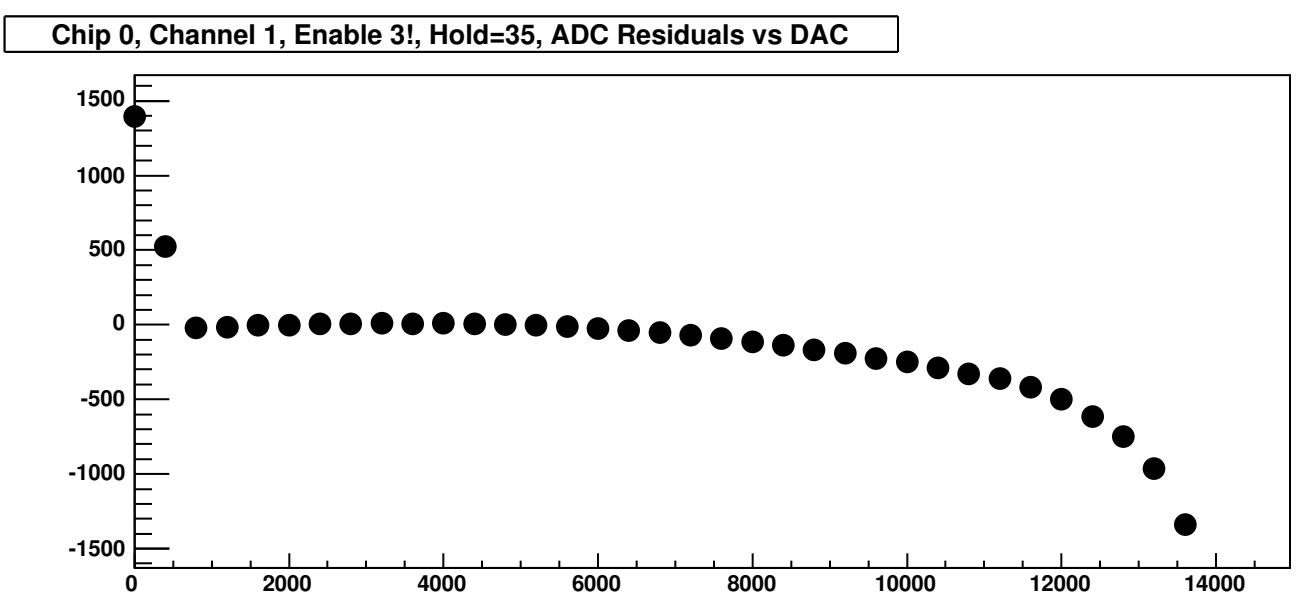

Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.98 / 23
p0 104.4 ± 0.7372
p1 0.01224 ± 0.0001126
280
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

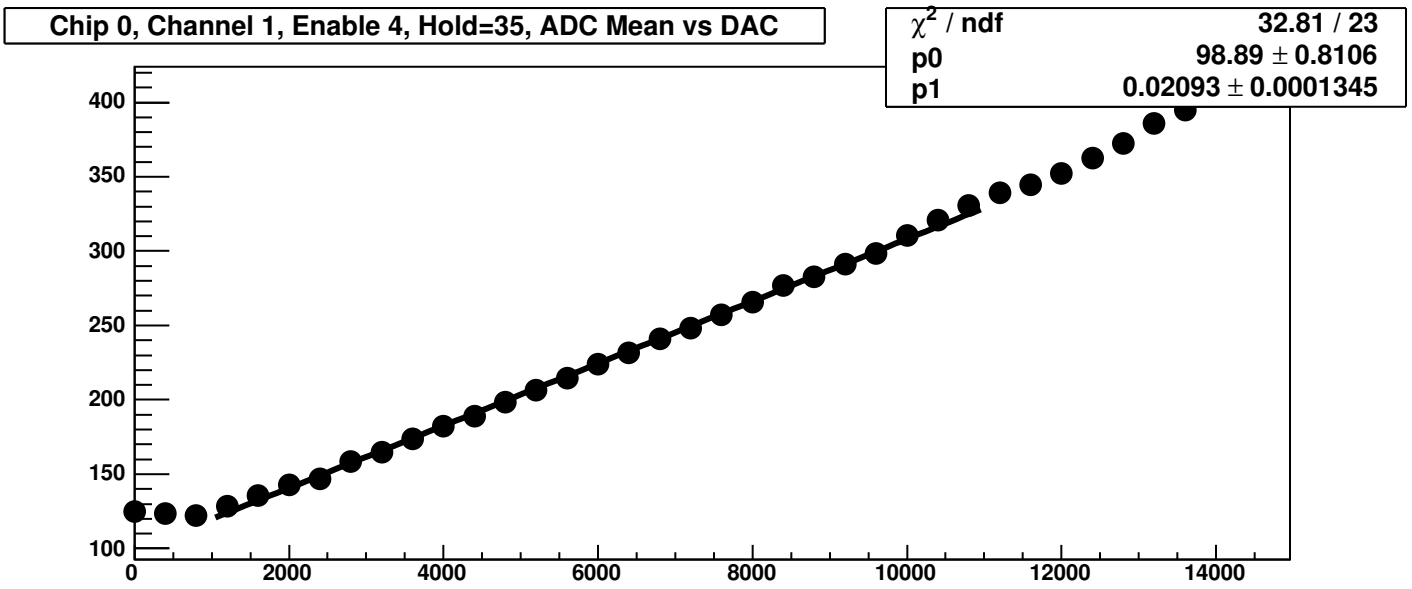

Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

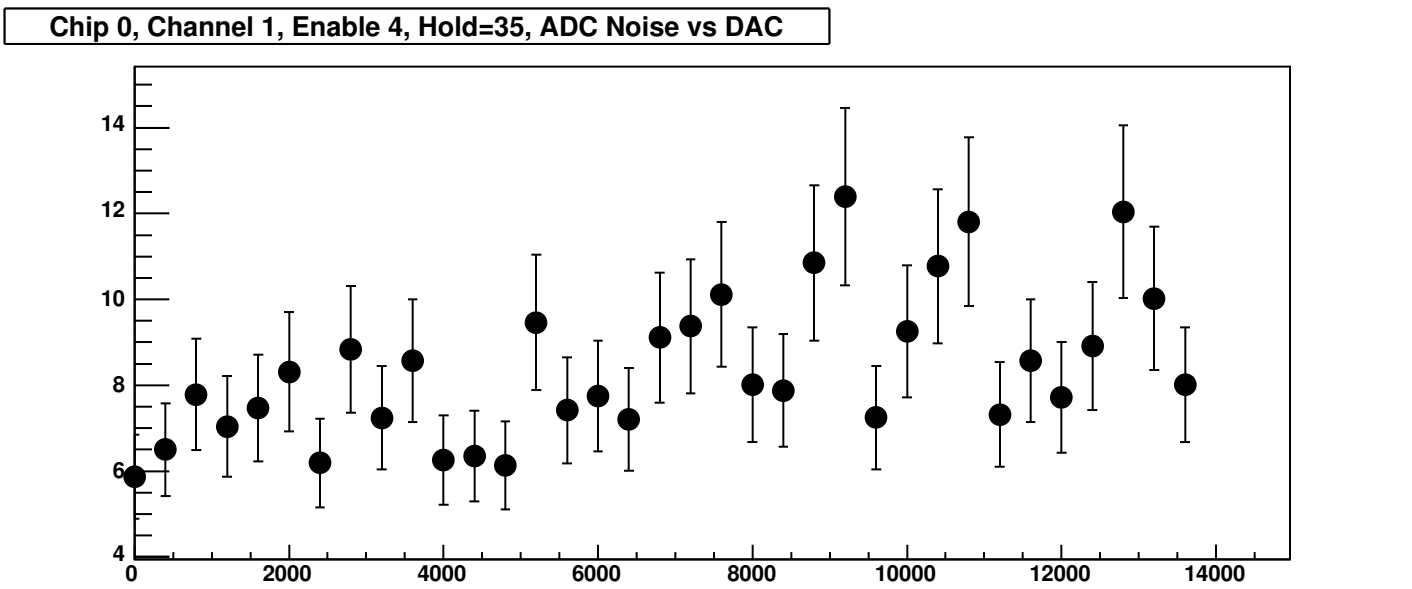

Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

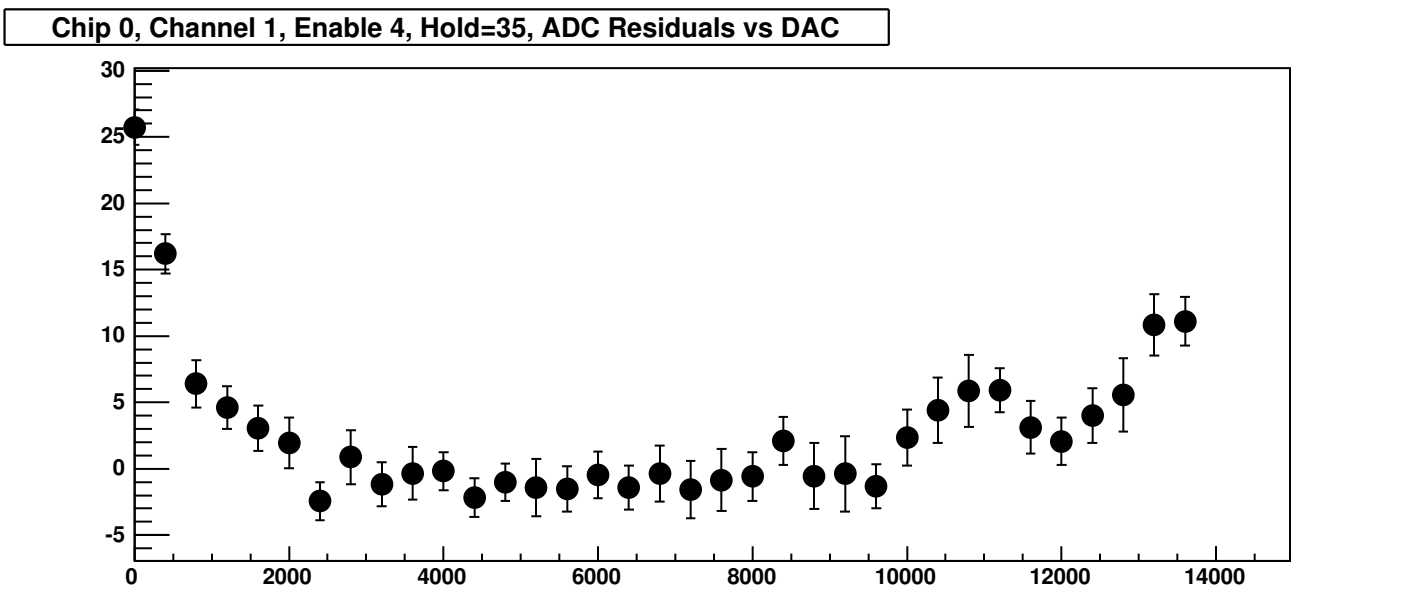

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 509.5 / 23
p0 142.3 ± 1.277
p1 2.213 ± 0.0004001
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

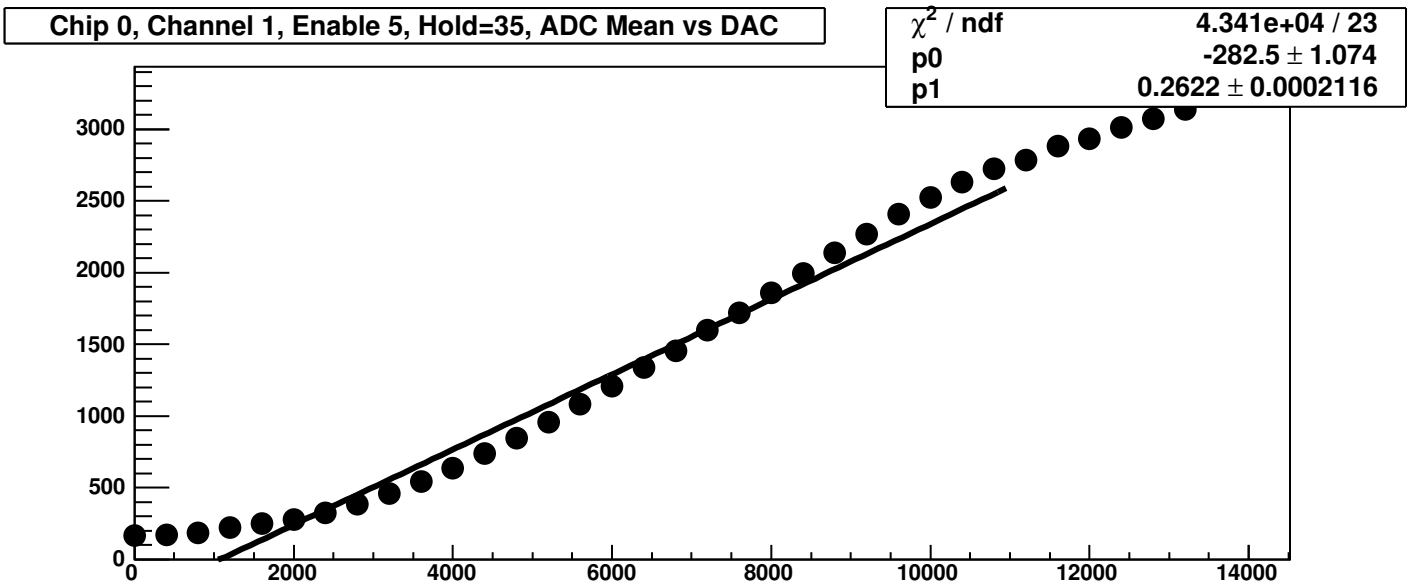

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

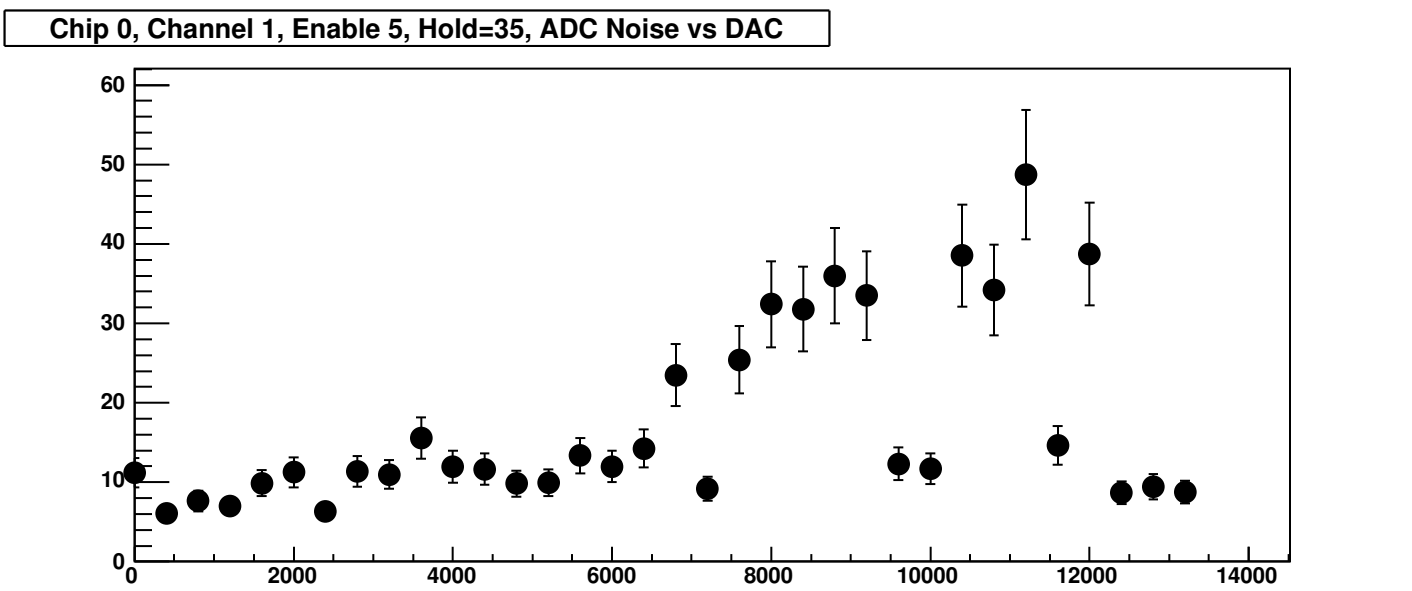

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

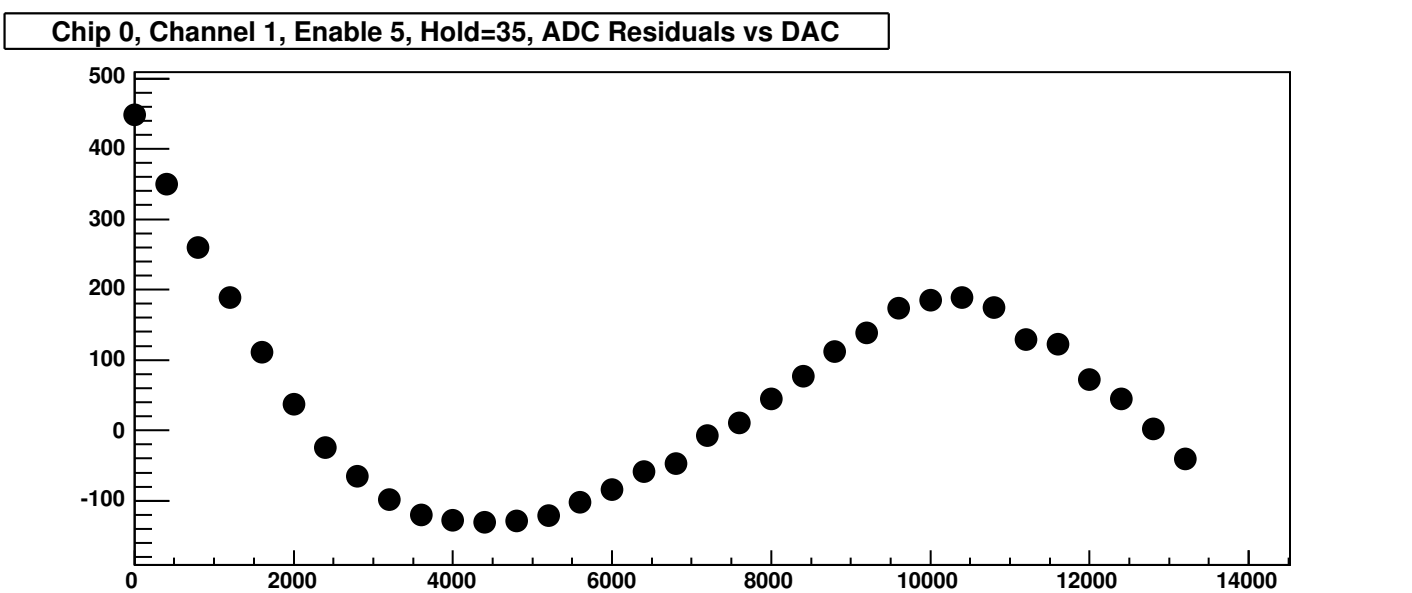

Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.81 / 23
p0 98.89 ± 0.8106
p1 0.02093 ± 0.0001345
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

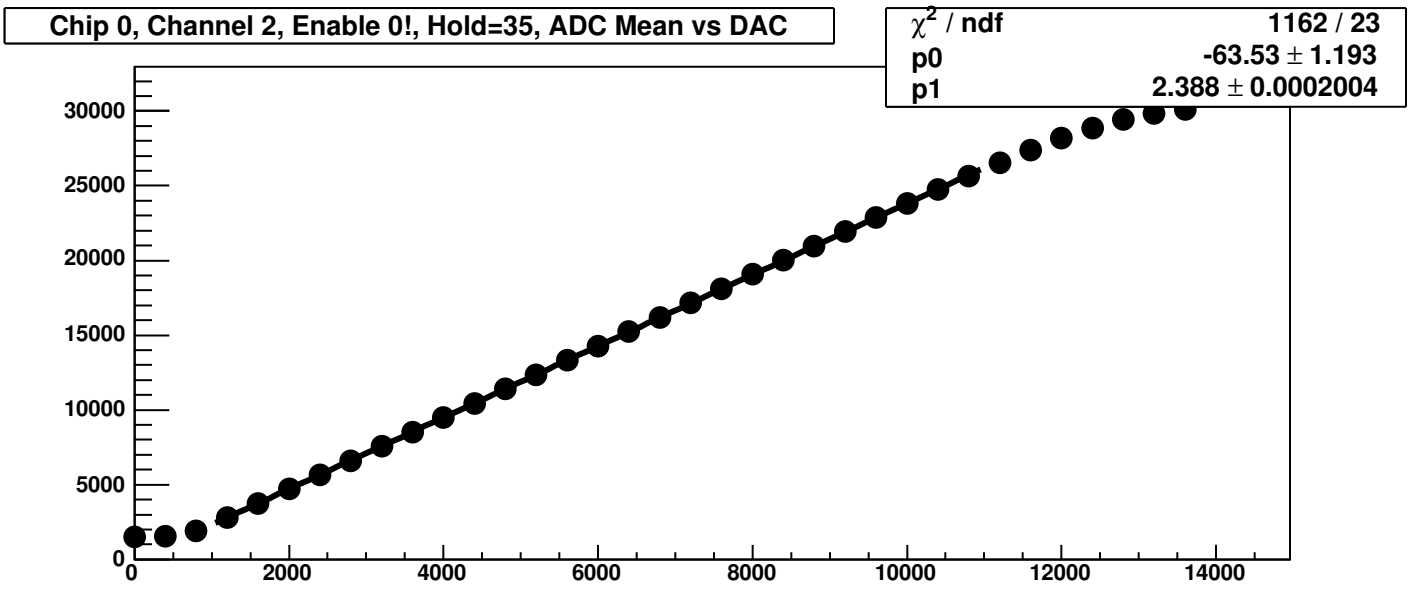

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

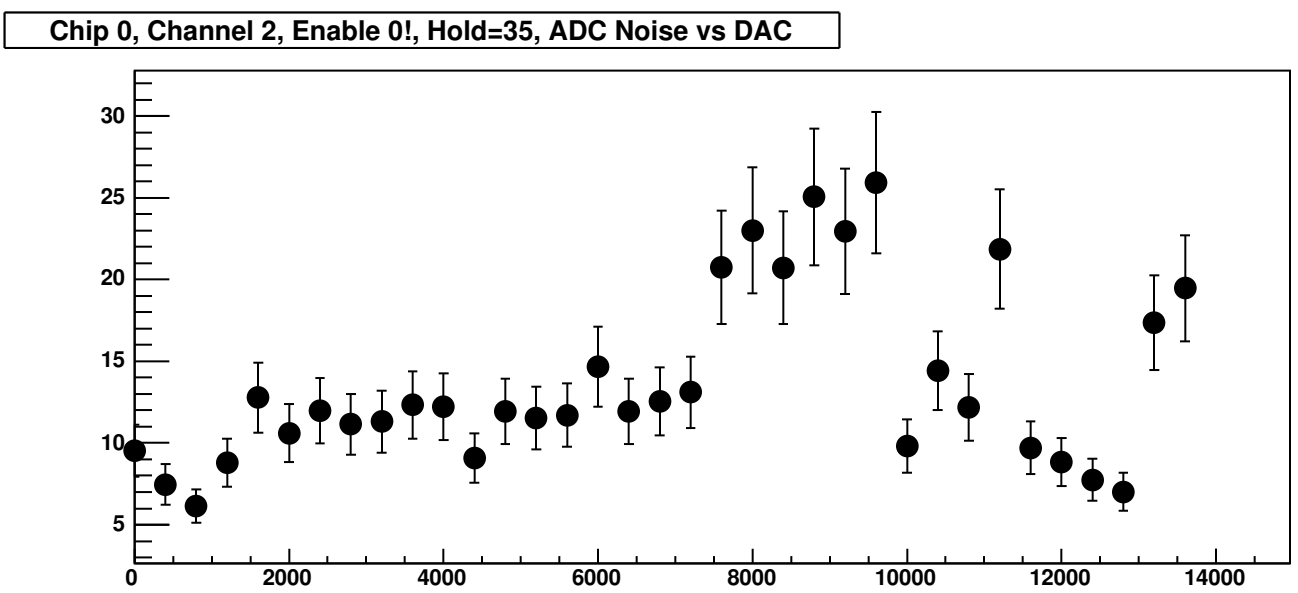

Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

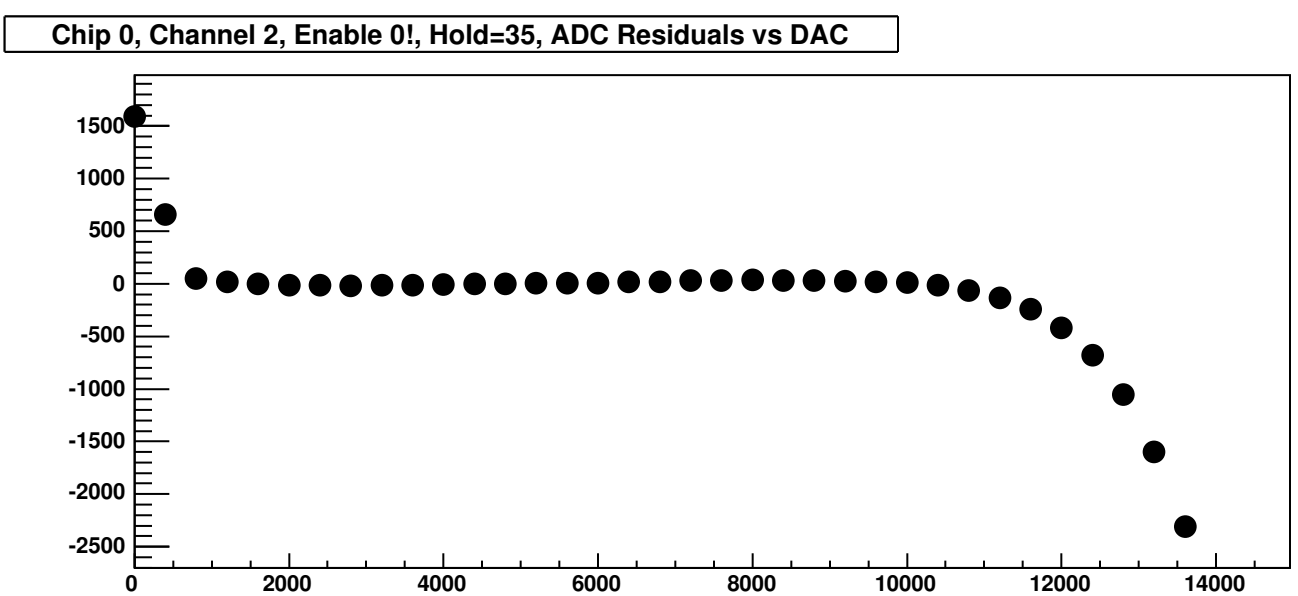

Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.341e+04 / 23
p0 -282.5 ± 1.074
p1 0.2622 ± 0.0002116
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

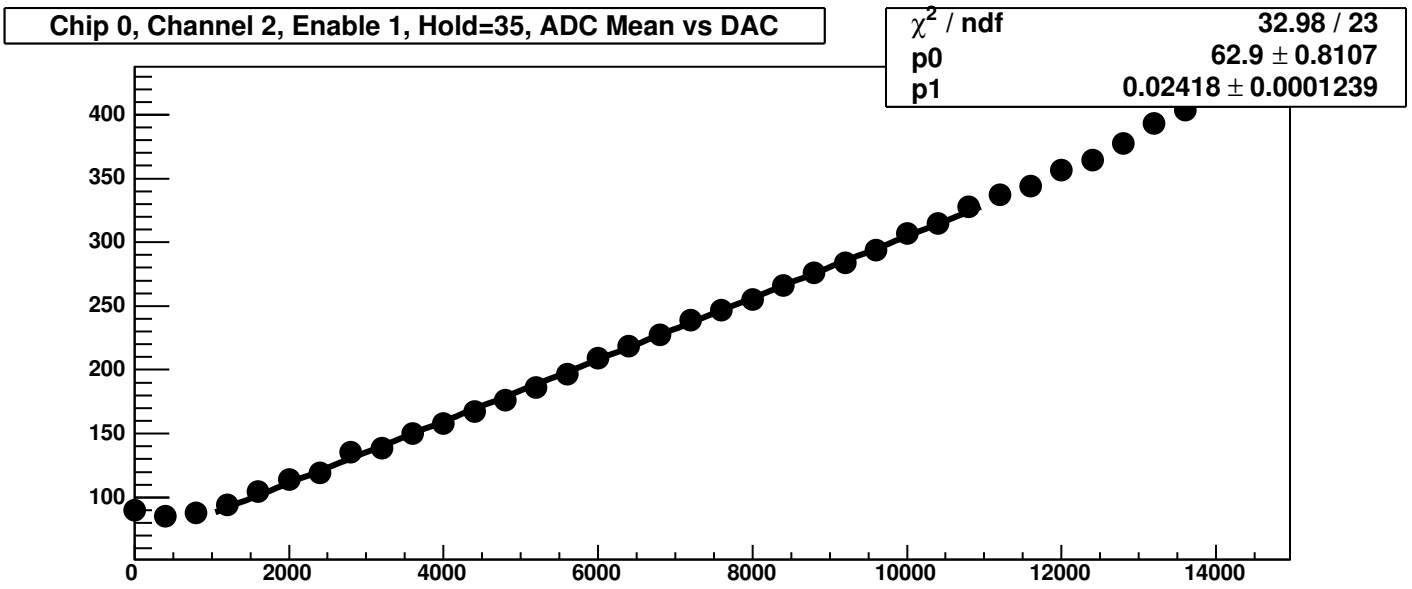

Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

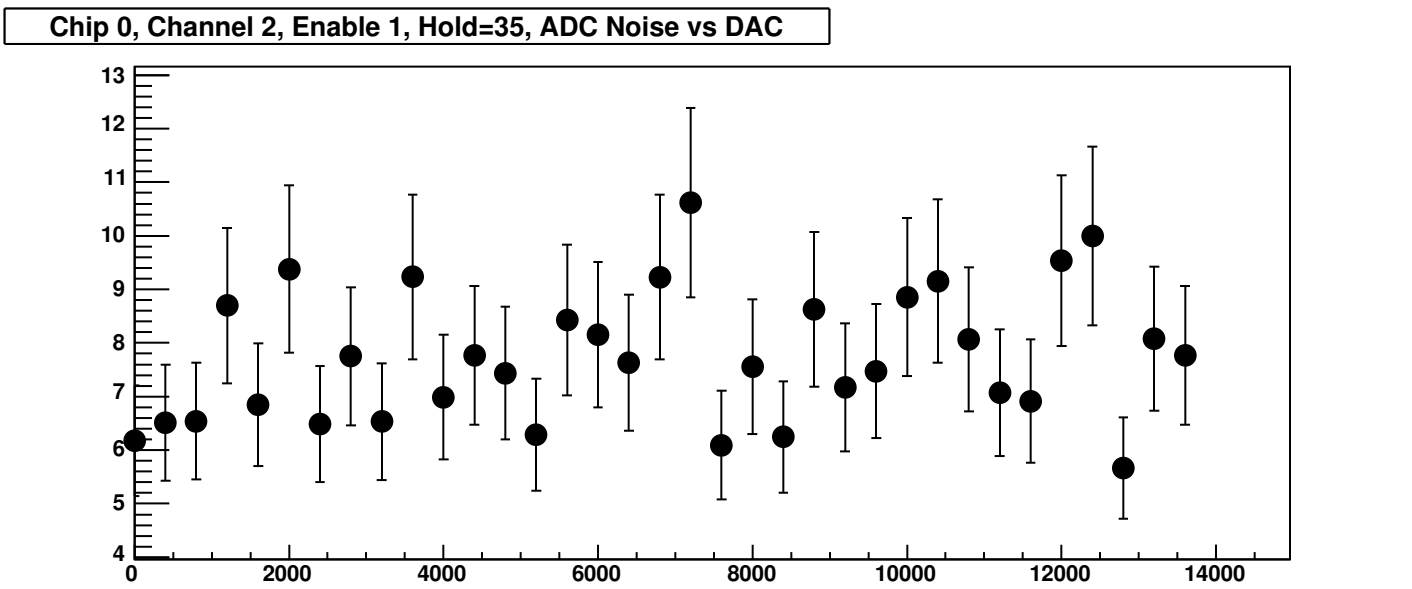

Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

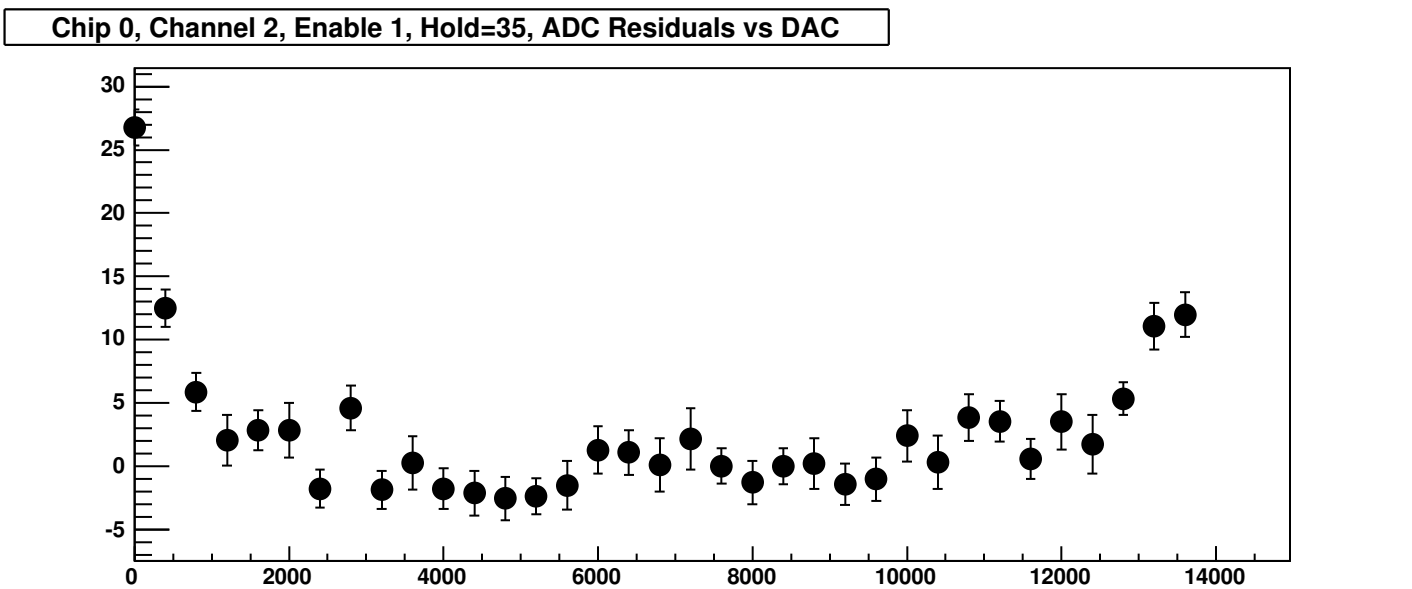

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1162 / 23
p0 -63.53 ± 1.193
p1 2.388 ± 0.0002004
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

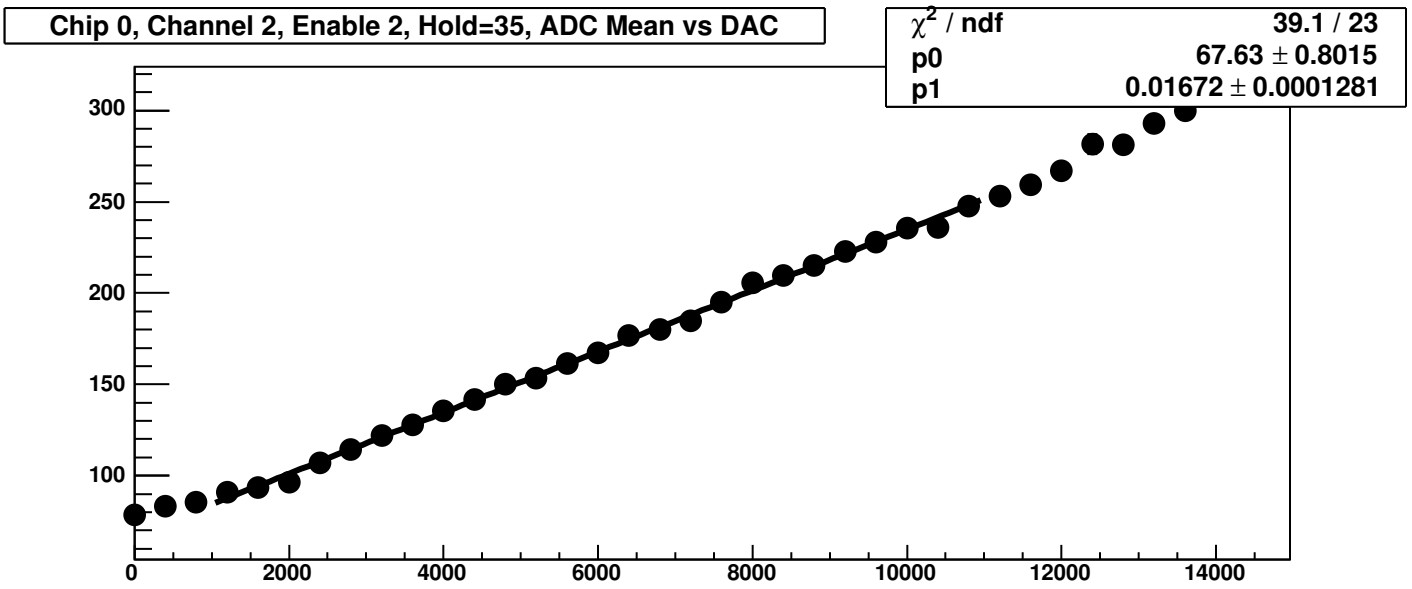

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

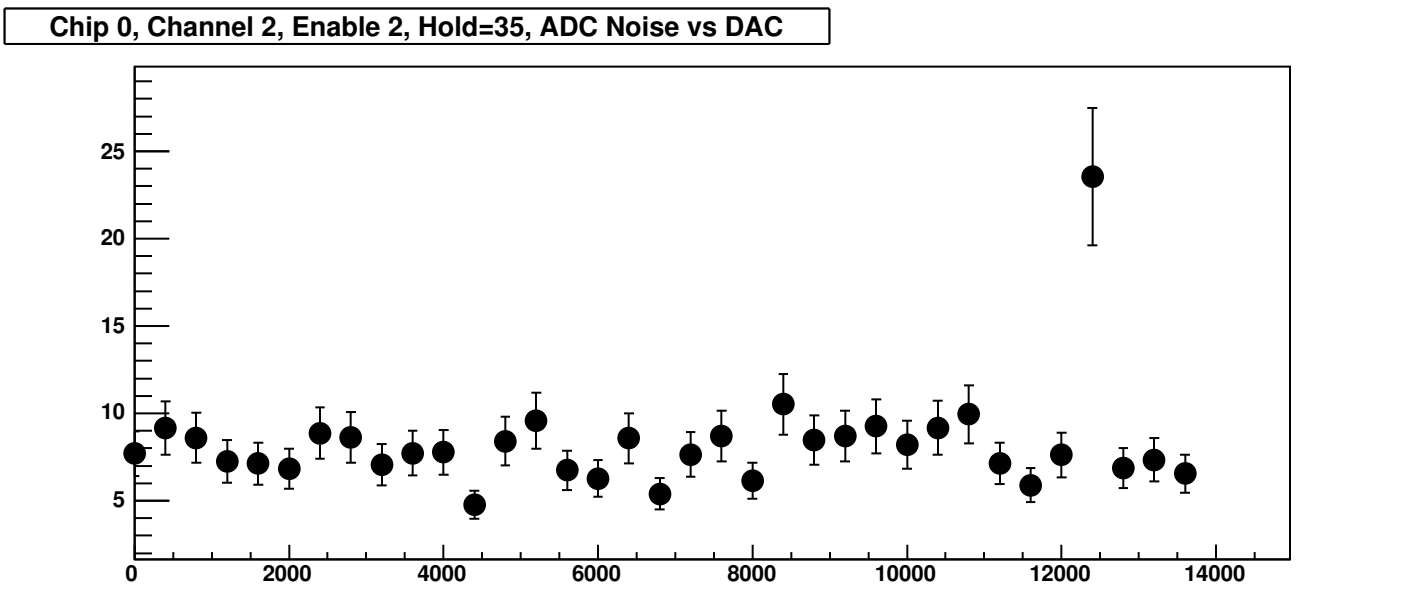

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

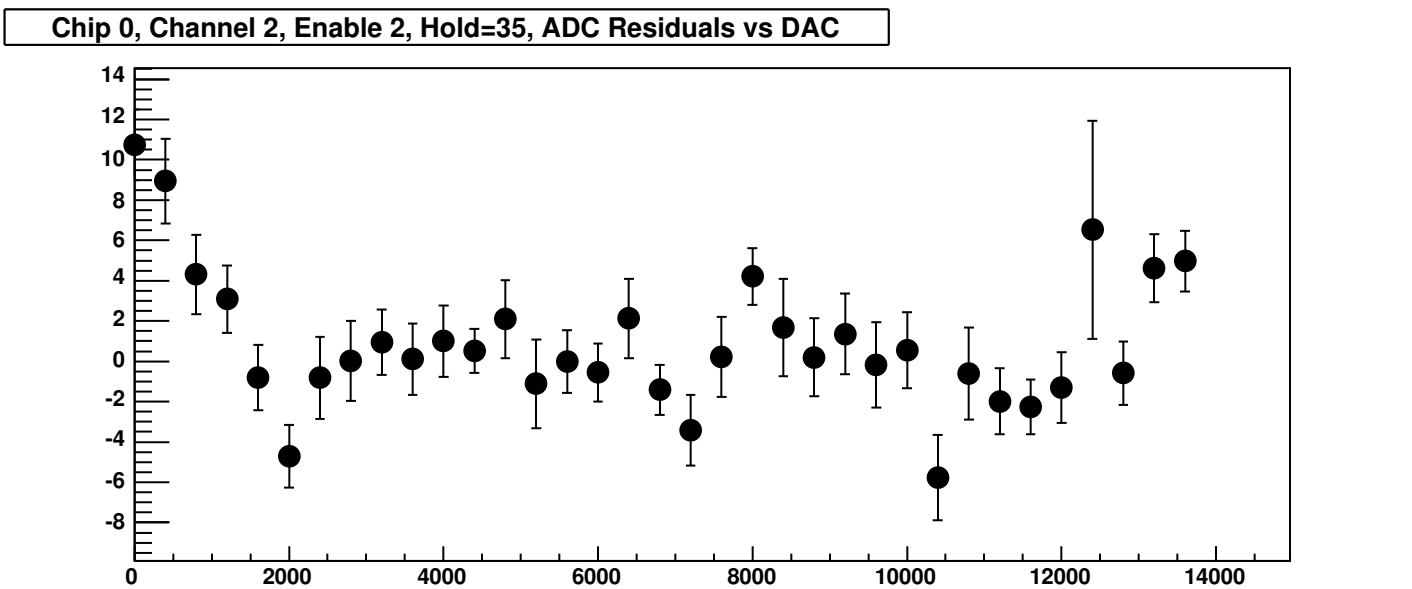

Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.98 / 23
p0 62.9 ± 0.8107
p1 0.02418 ± 0.0001239
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

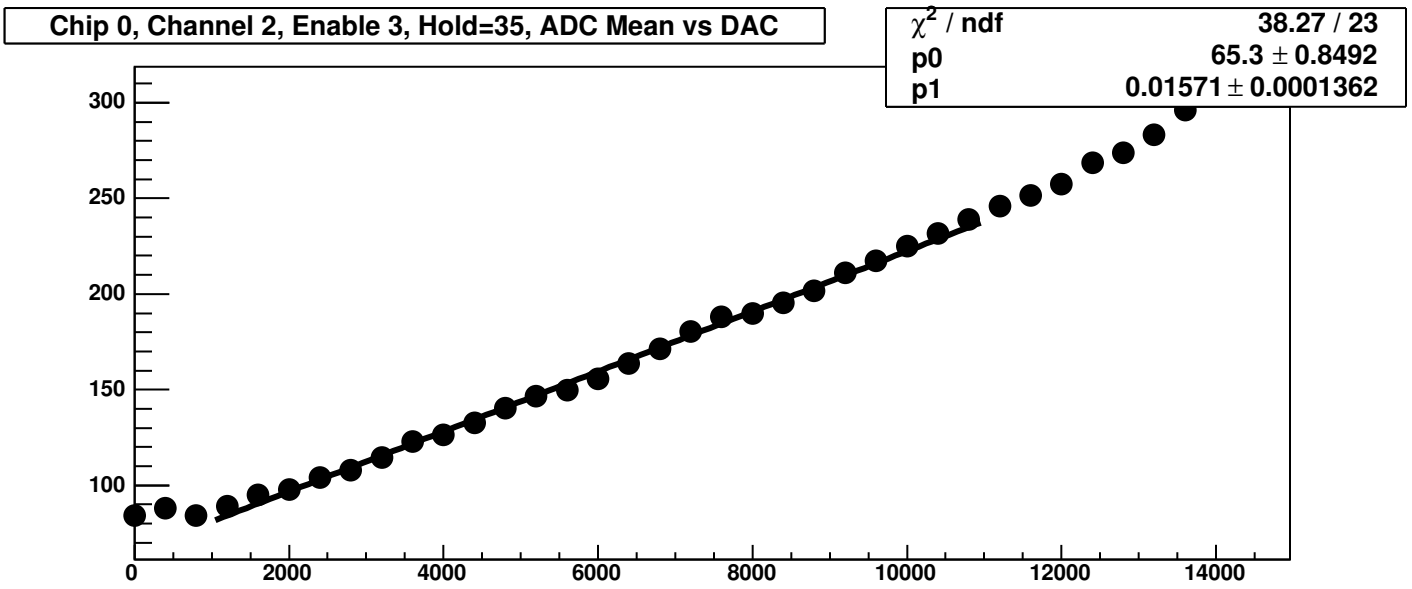

Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

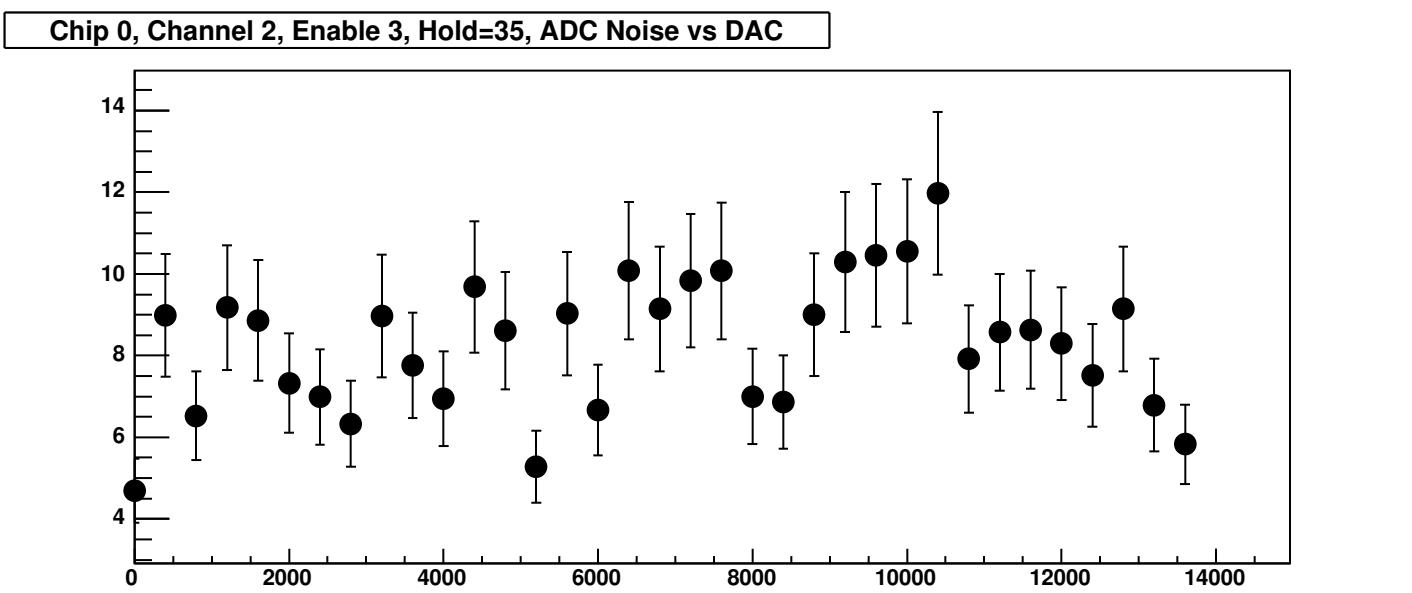

Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

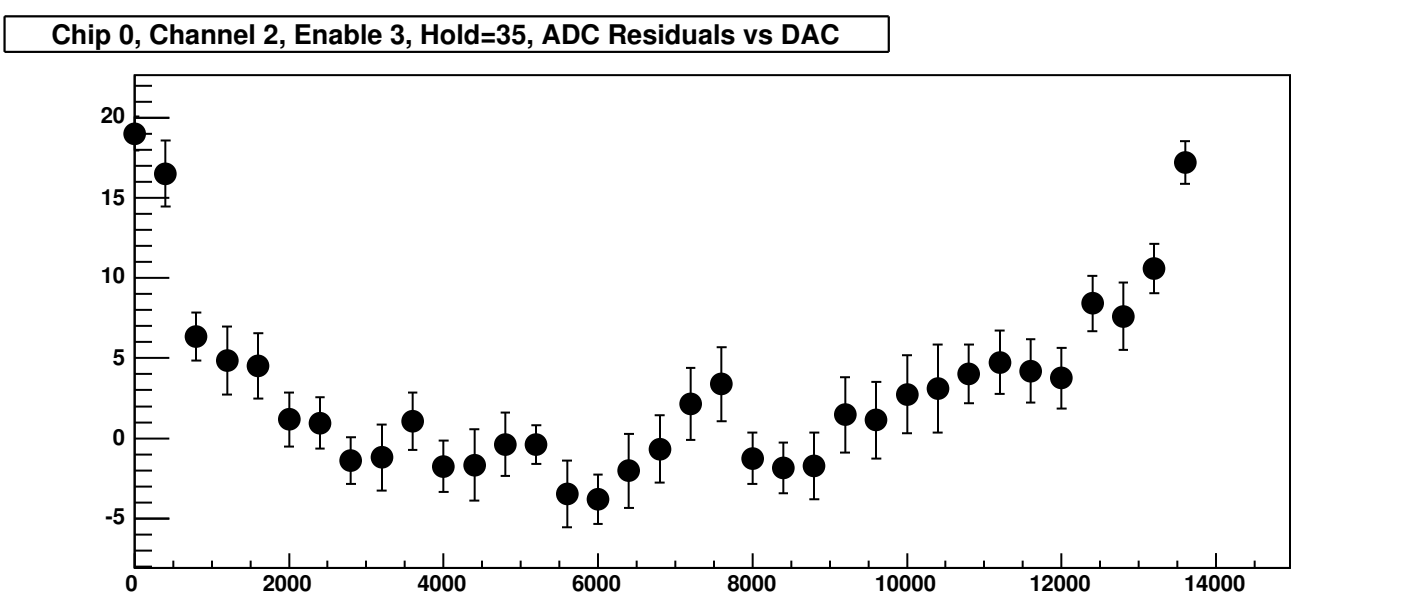

Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.1 / 23
p0 67.63 ± 0.8015
p1 0.01672 ± 0.0001281
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

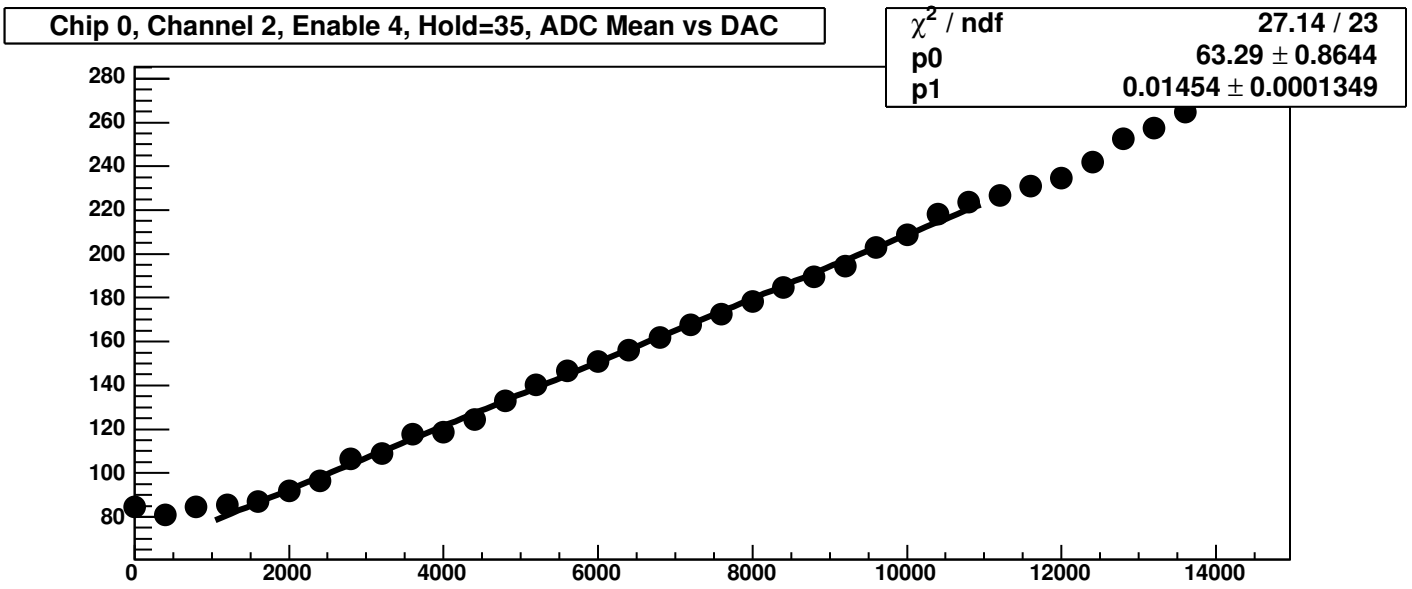

Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

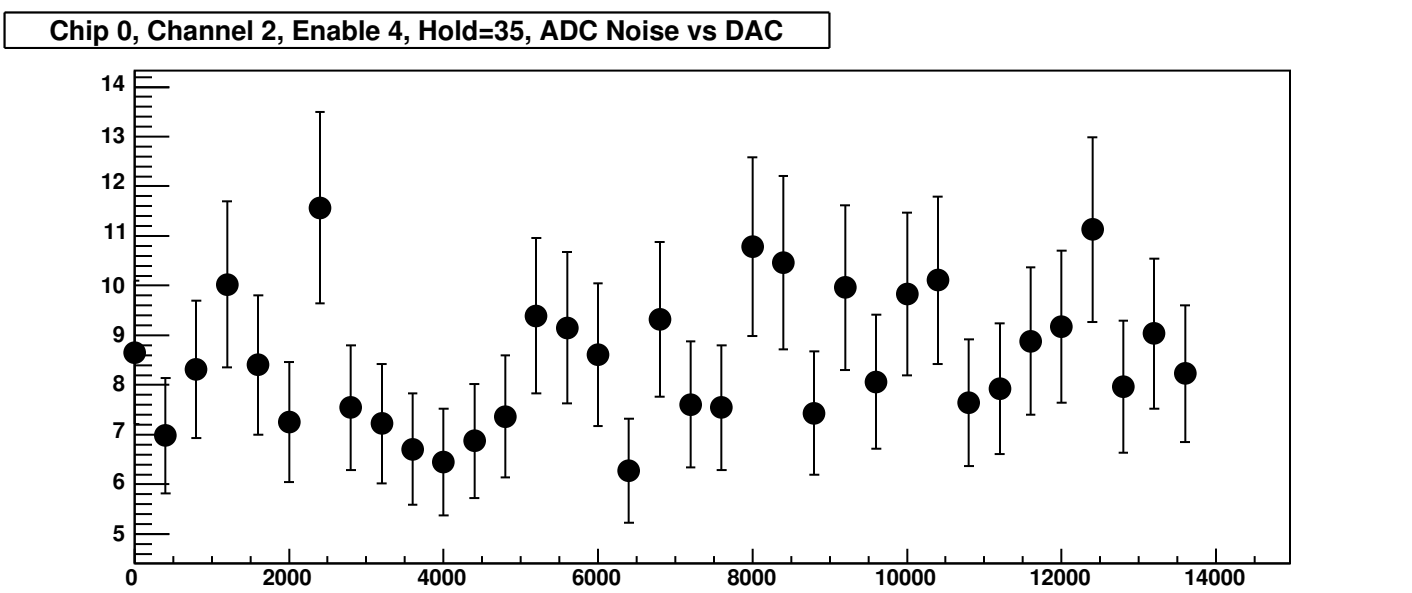

Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

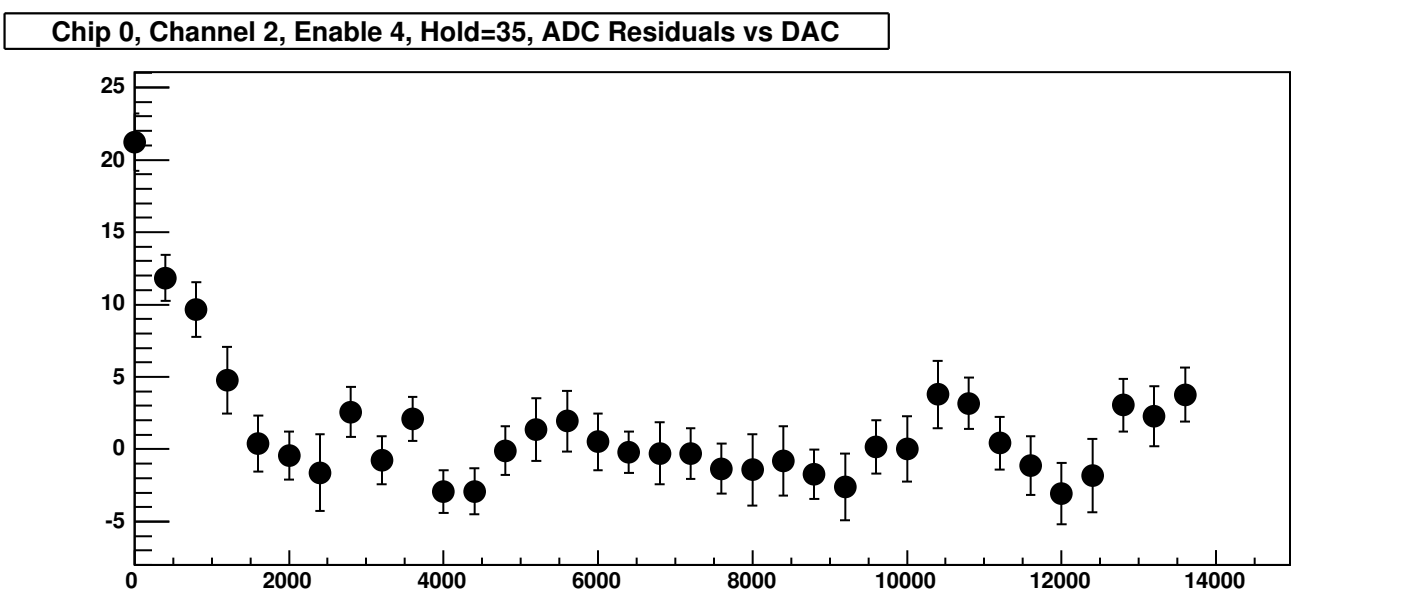

Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.27 / 23
p0 65.3 ± 0.8492
p1 0.01571 ± 0.0001362
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

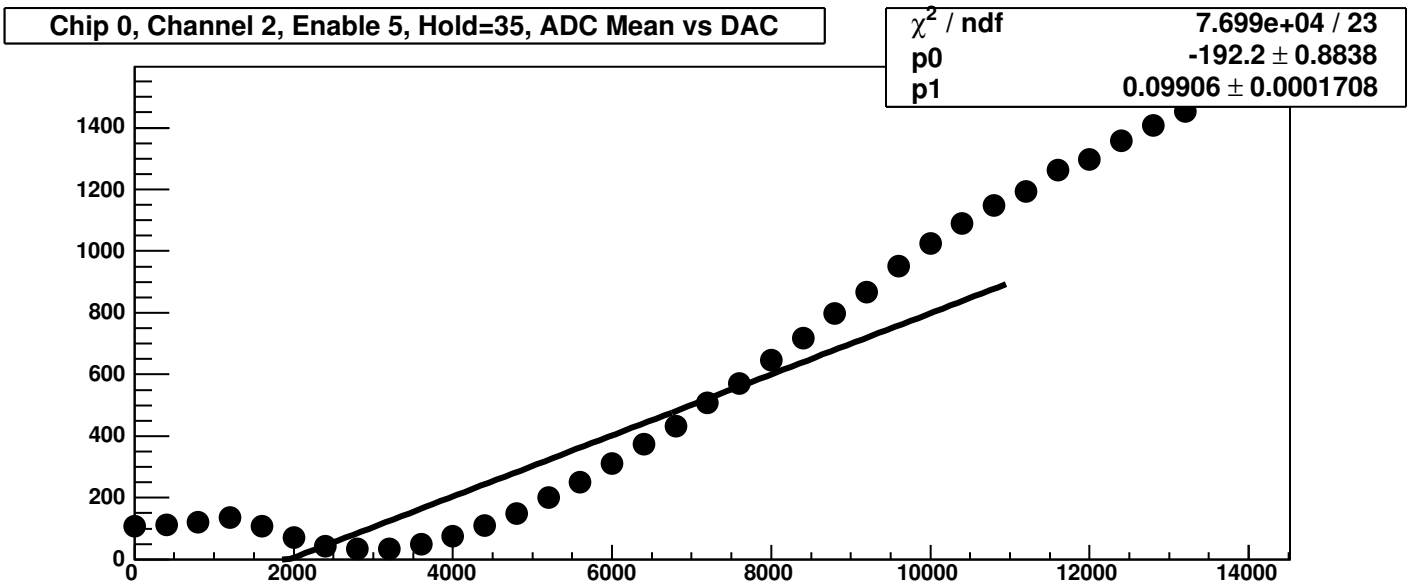

Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

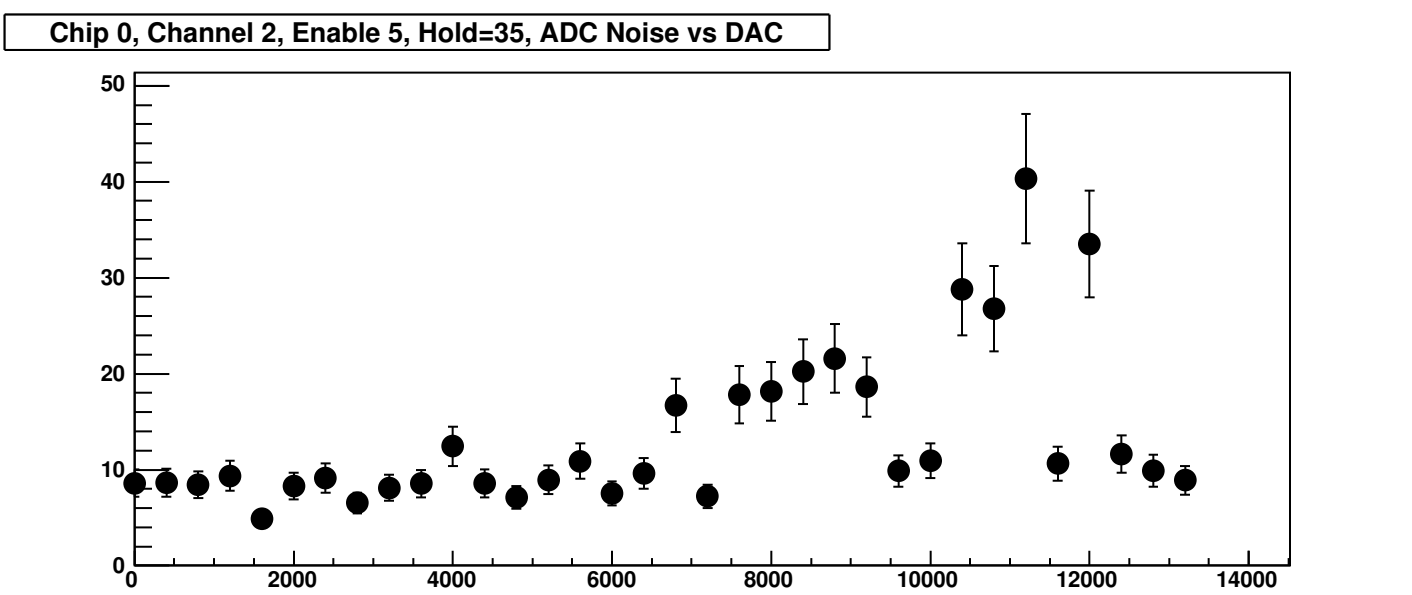

Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

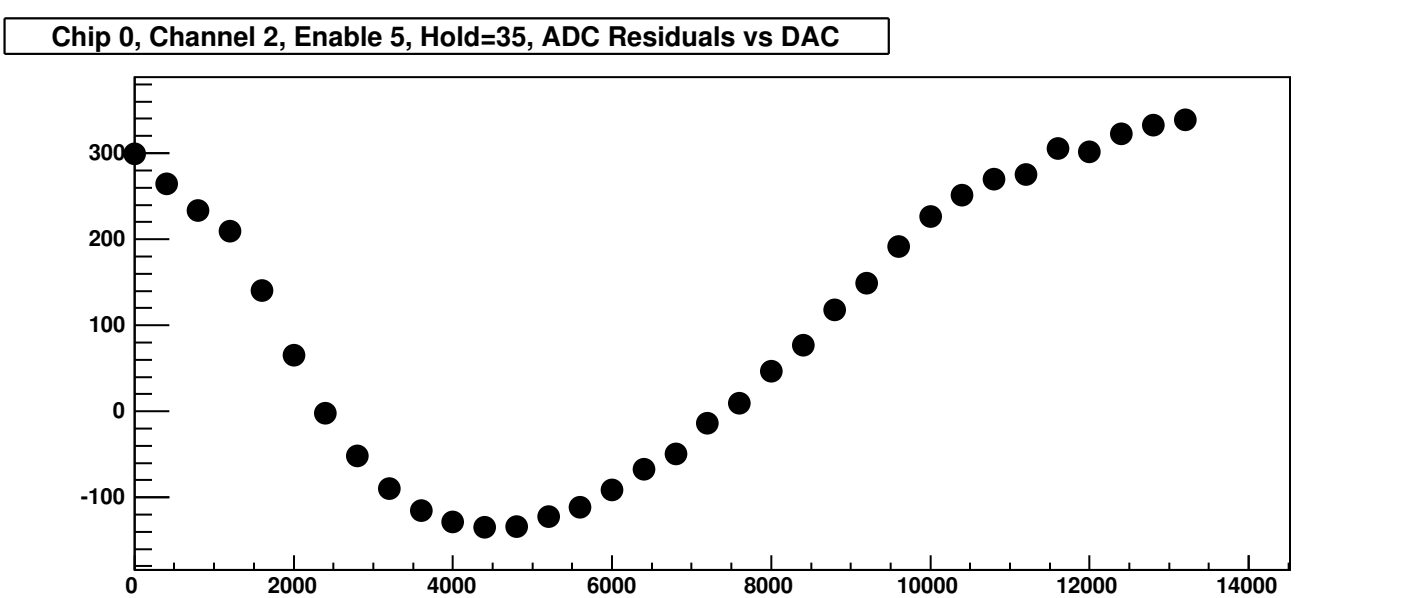

Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.14 / 23
p0 63.29 ± 0.8644
p1 0.01454 ± 0.0001349
280
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000

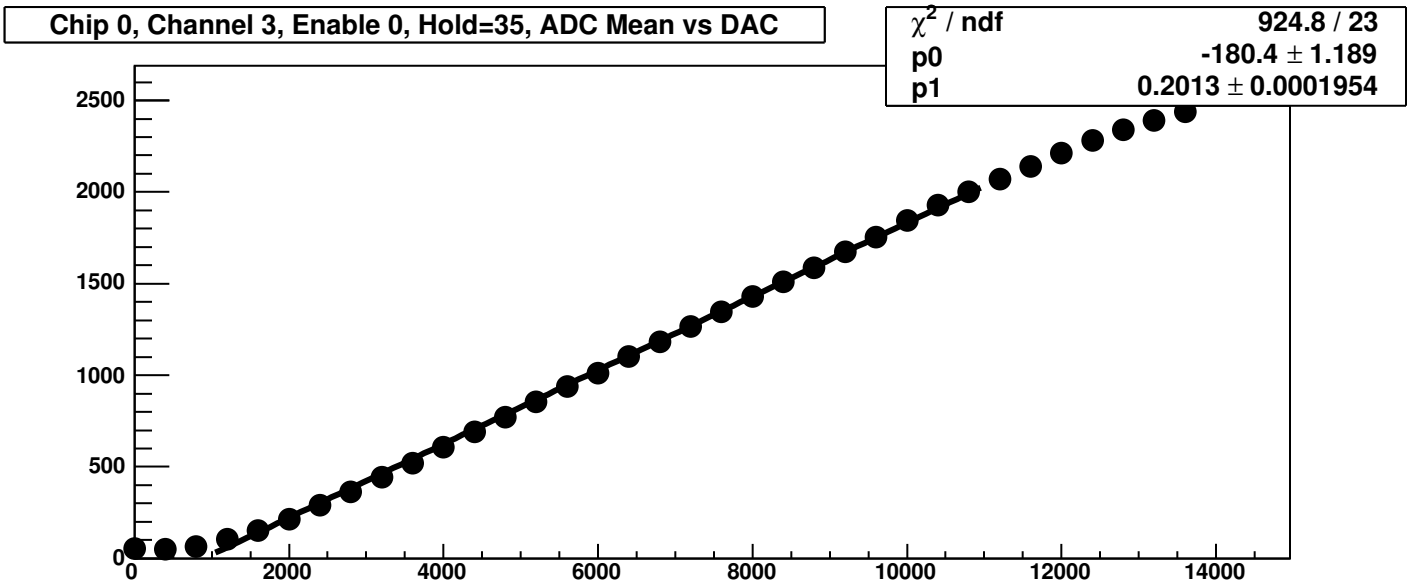

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

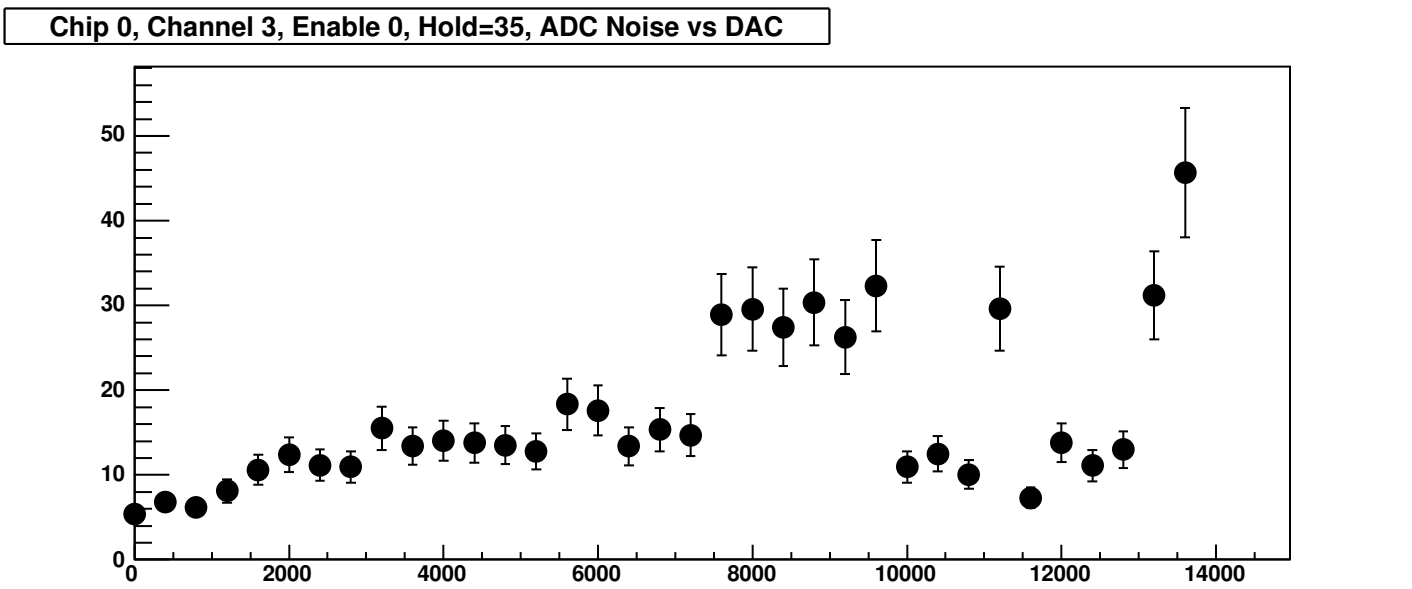

Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

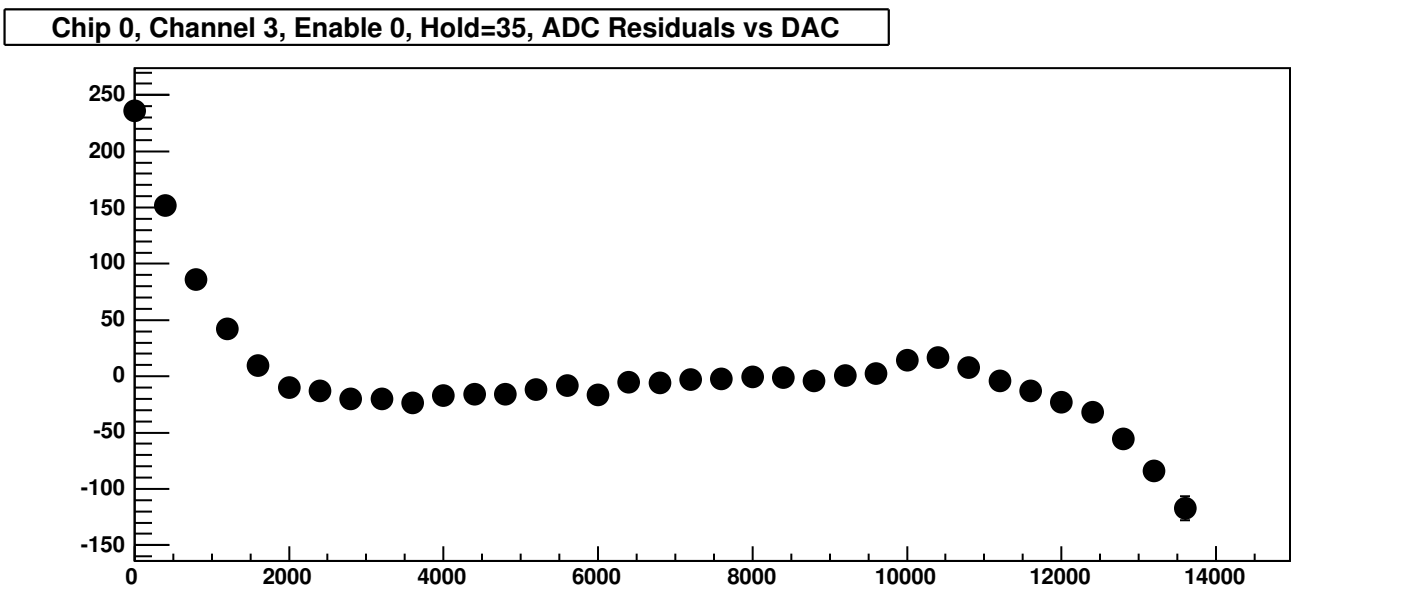

Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.699e+04 / 23
p0 -192.2 ± 0.8838
p1 0.09906 ± 0.0001708
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

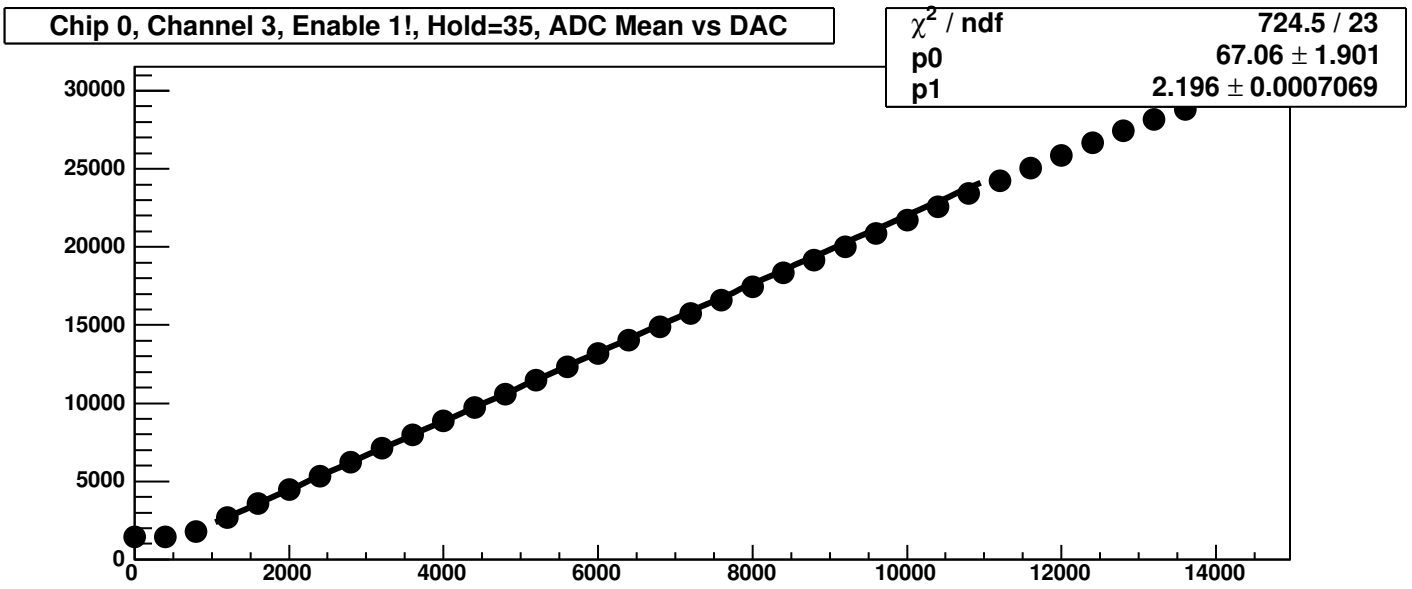

Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

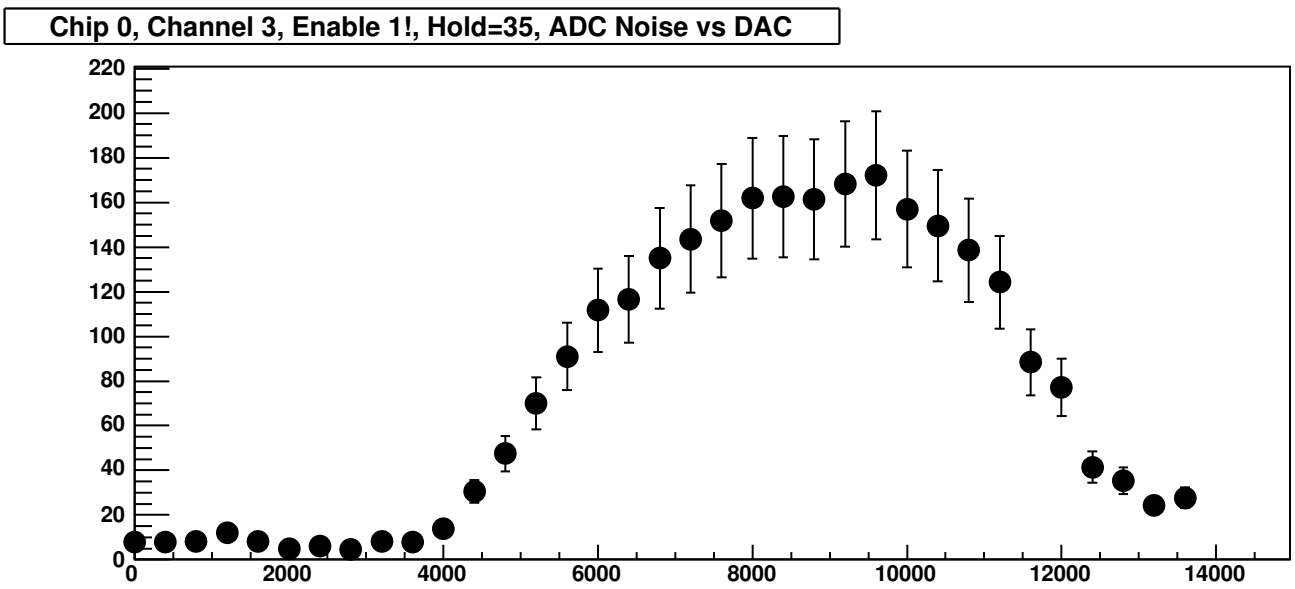

Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

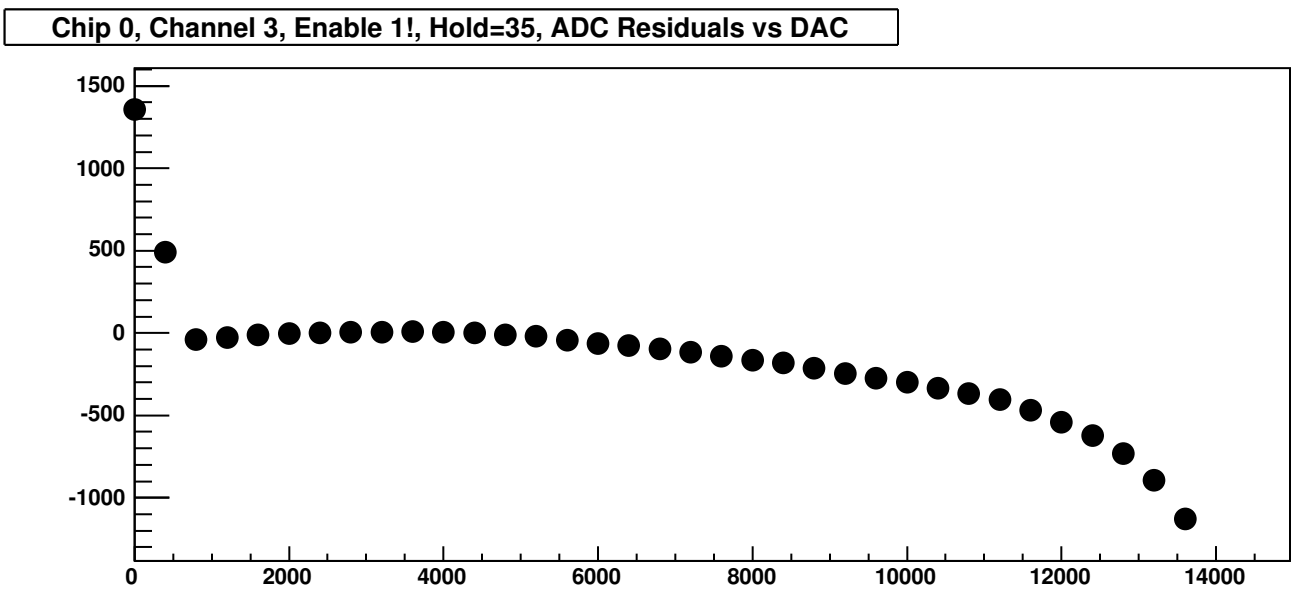

Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 924.8 / 23
p0 -180.4 ± 1.189
p1 0.2013 ± 0.0001954
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

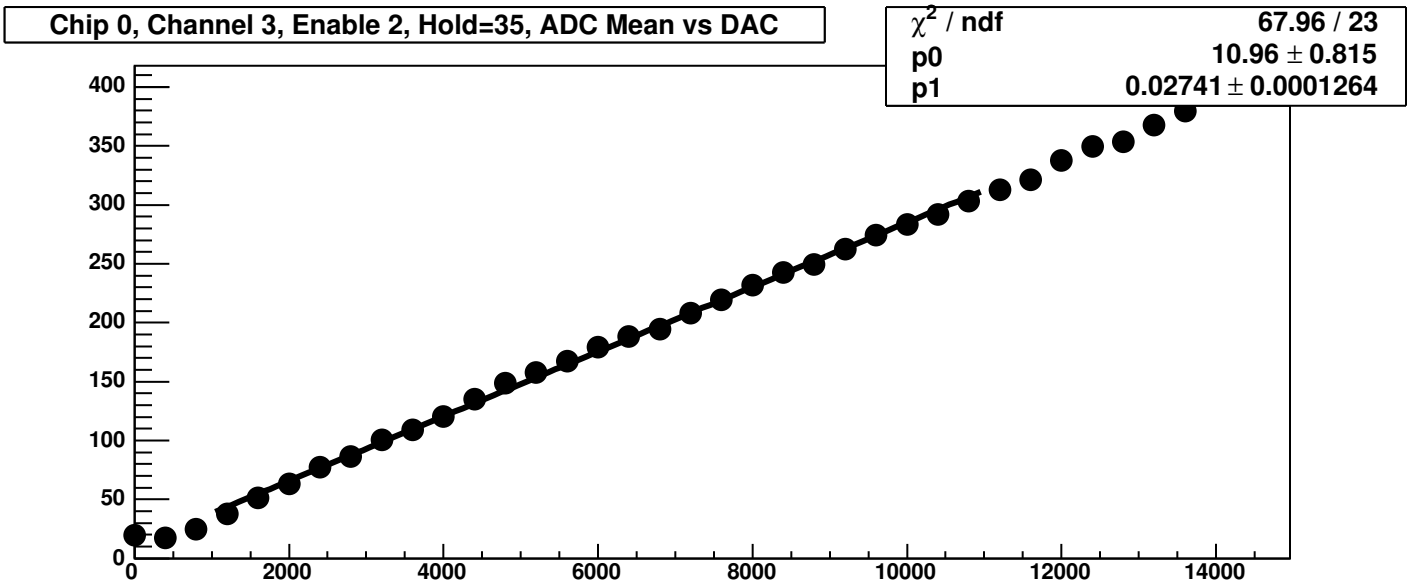

Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

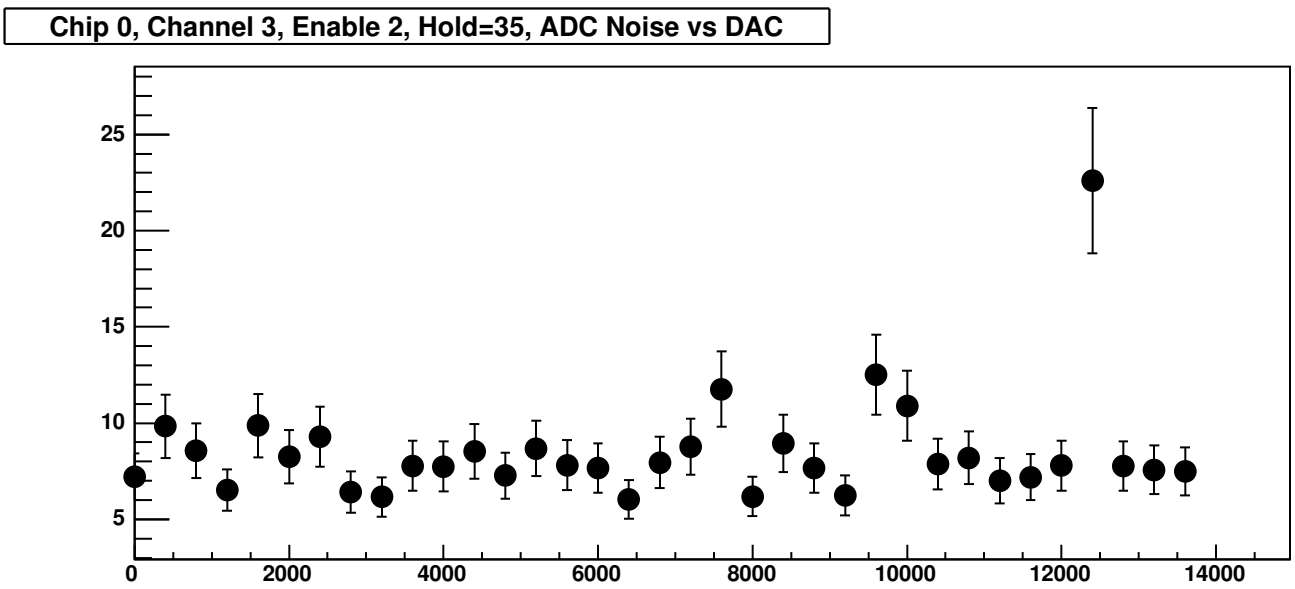

Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

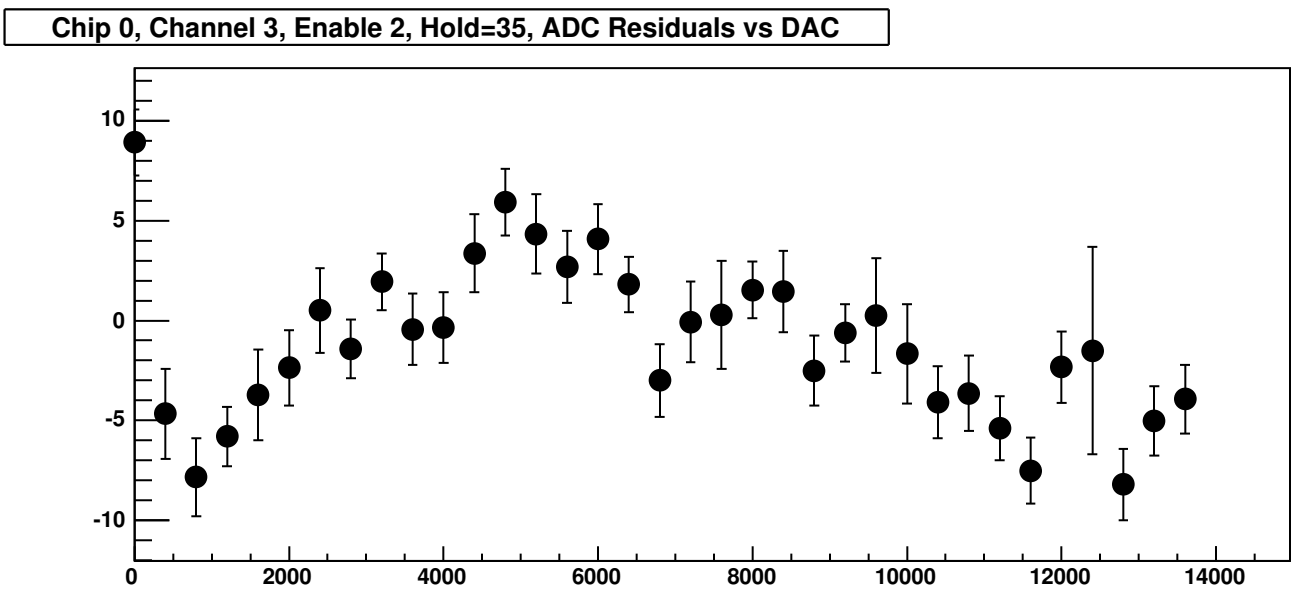

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 724.5 / 23
p0 67.06 ± 1.901
p1 2.196 ± 0.0007069
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

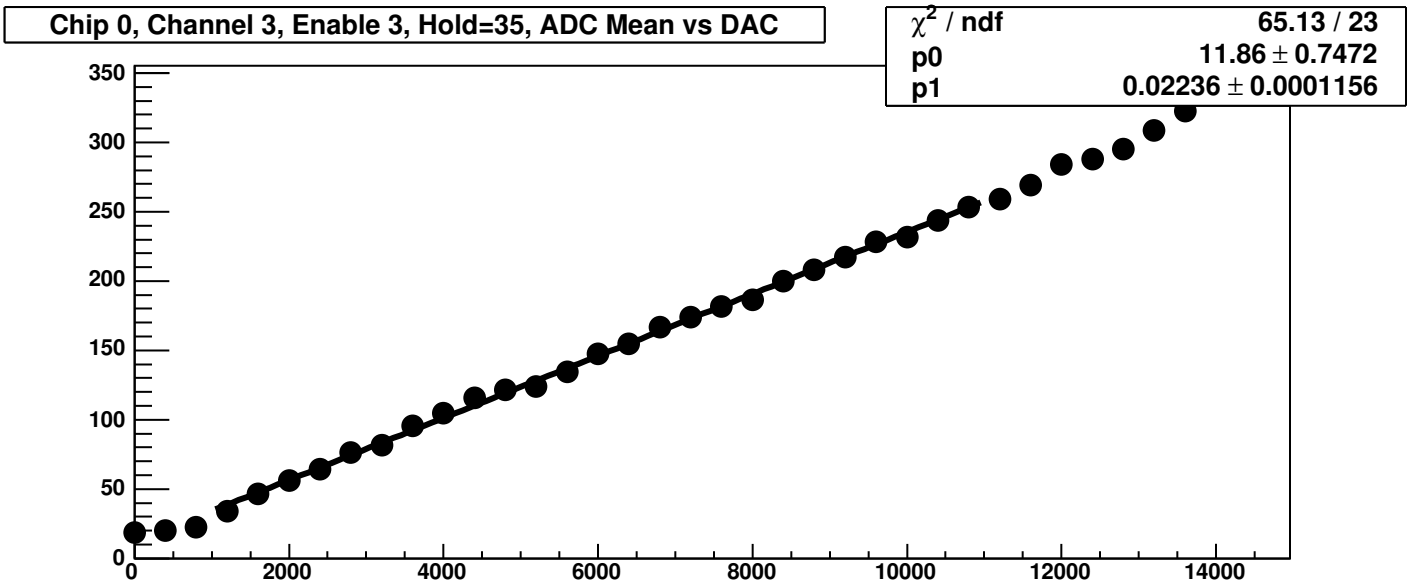

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

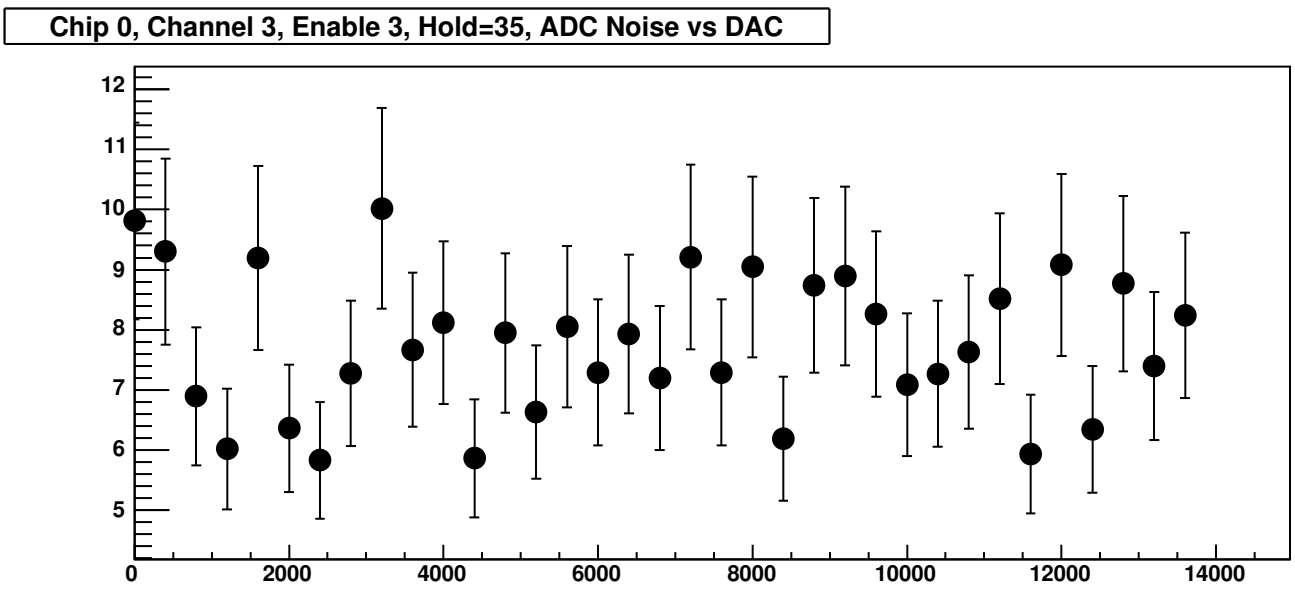

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

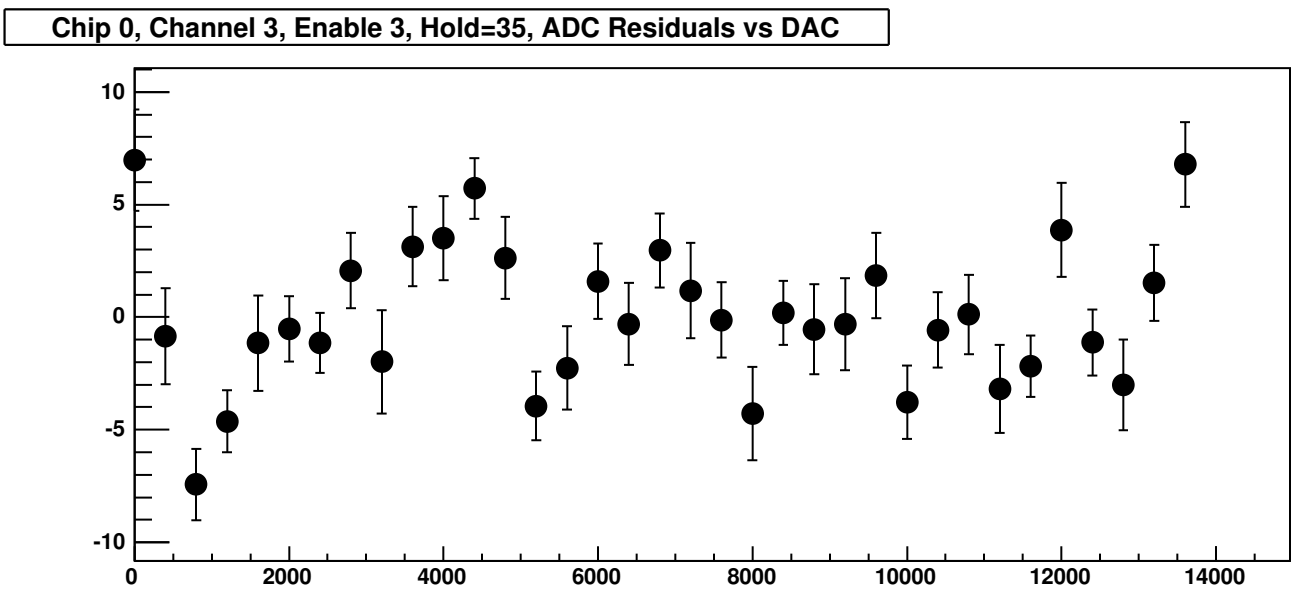

Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 67.96 / 23
p0 10.96 ± 0.815
p1 0.02741 ± 0.0001264
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

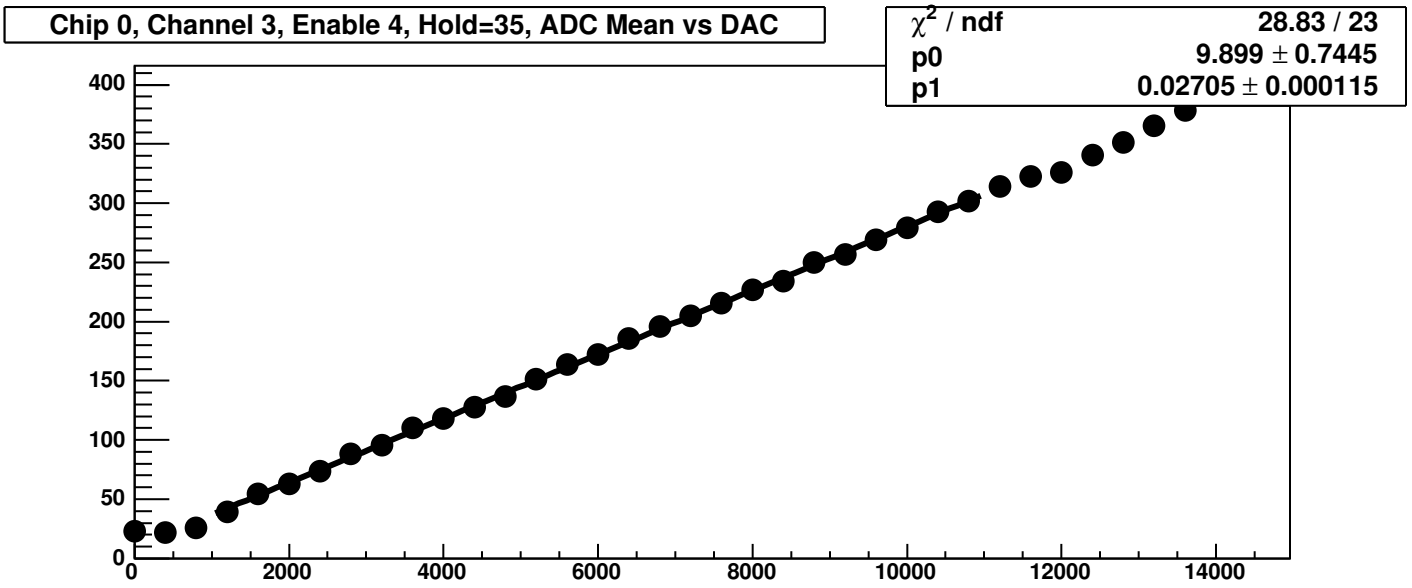

Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

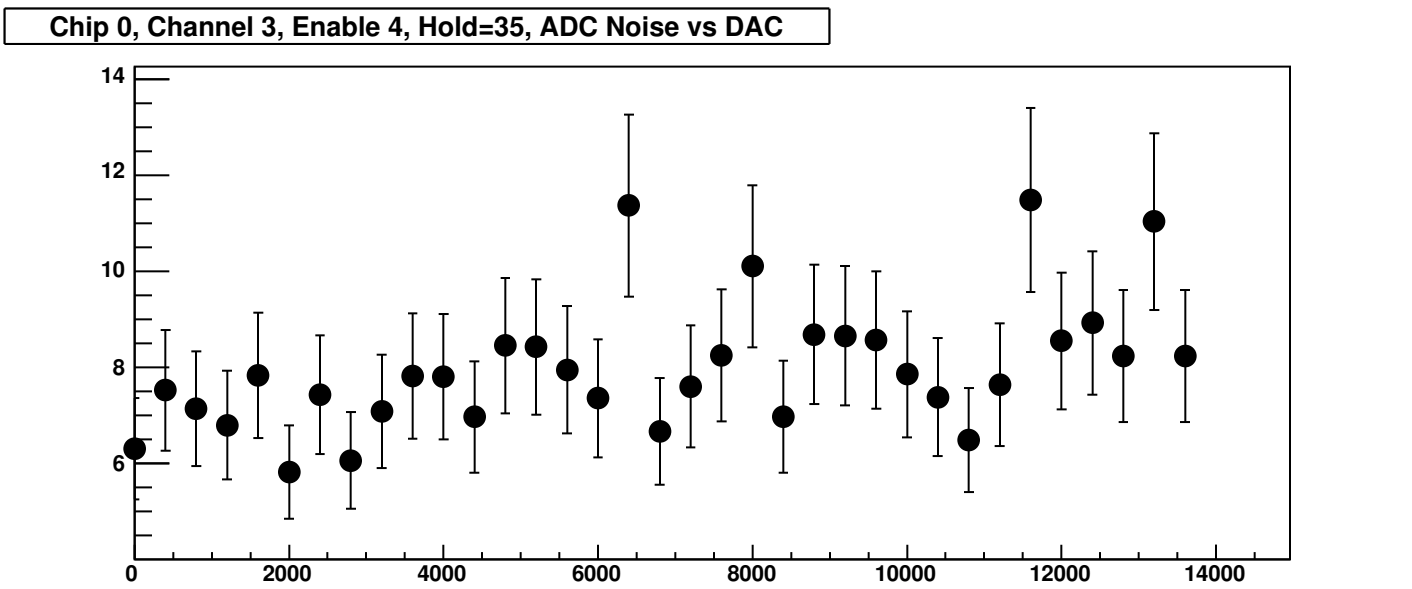

Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

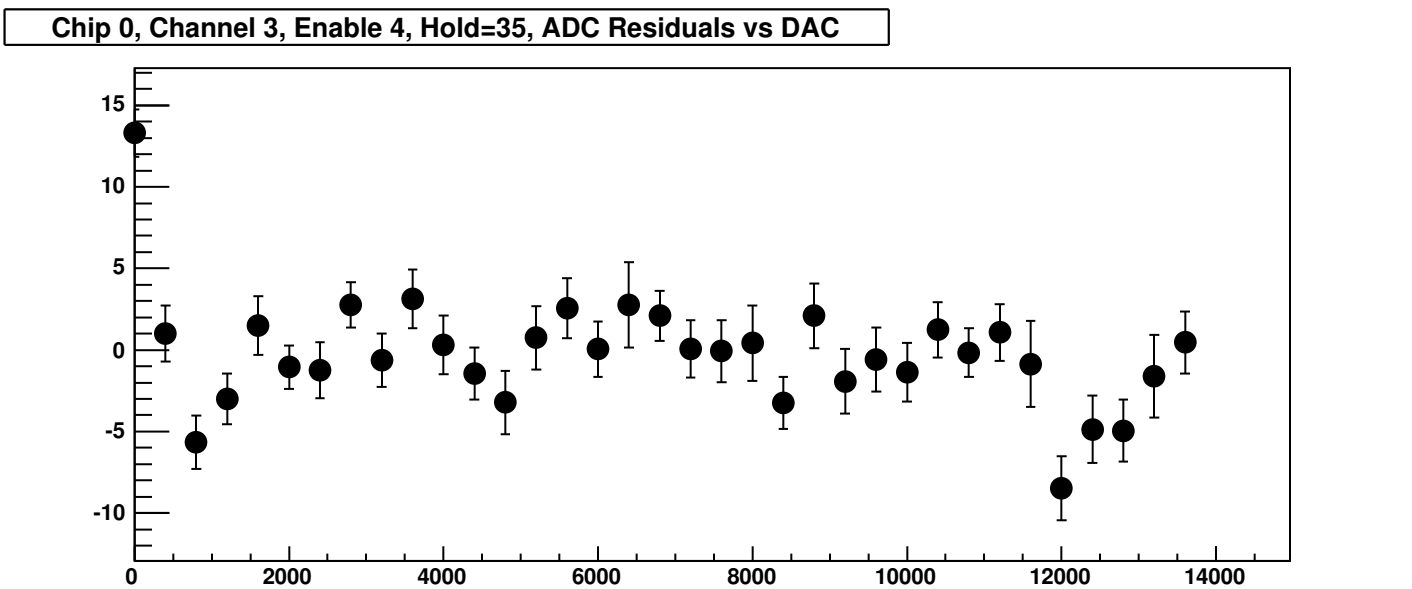

Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 65.13 / 23
p0 11.86 ± 0.7472
p1 0.02236 ± 0.0001156
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

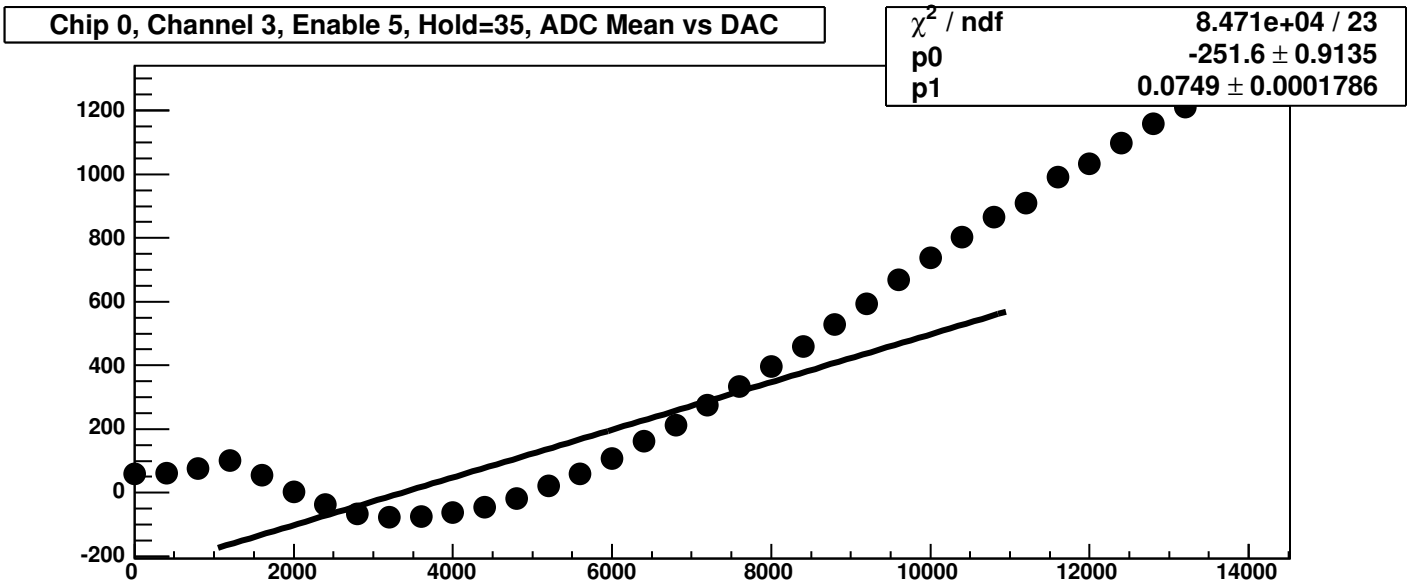

Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

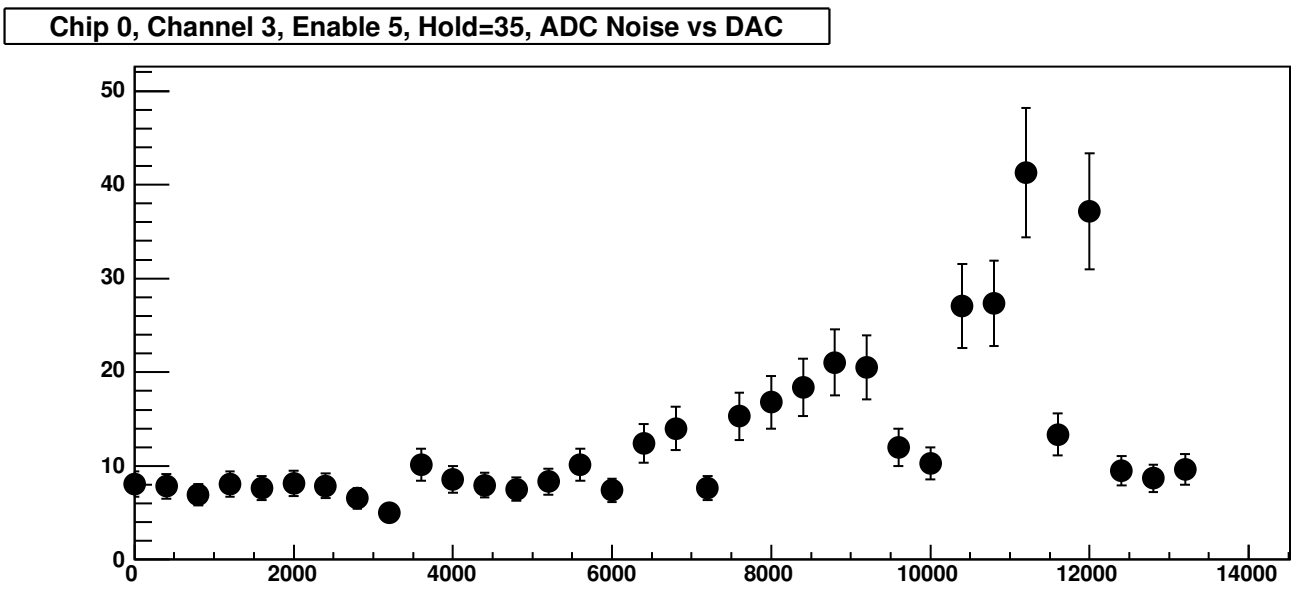

Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

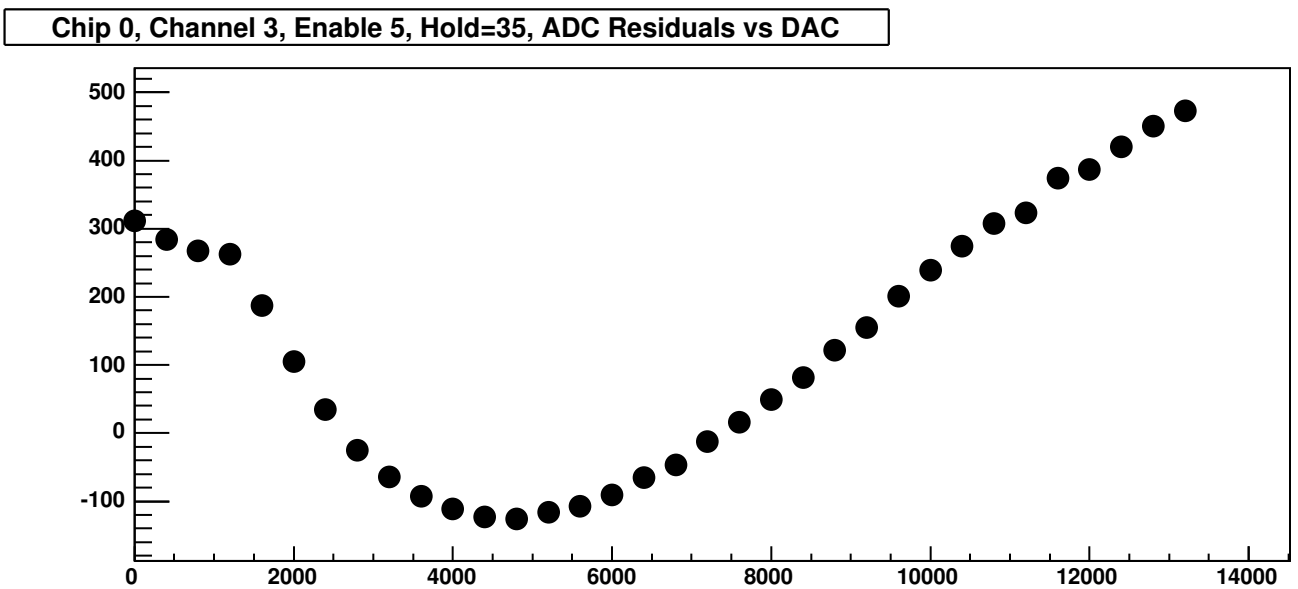

Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.83 / 23
p0 9.899 ± 0.7445
p1 0.02705 ± 0.000115
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

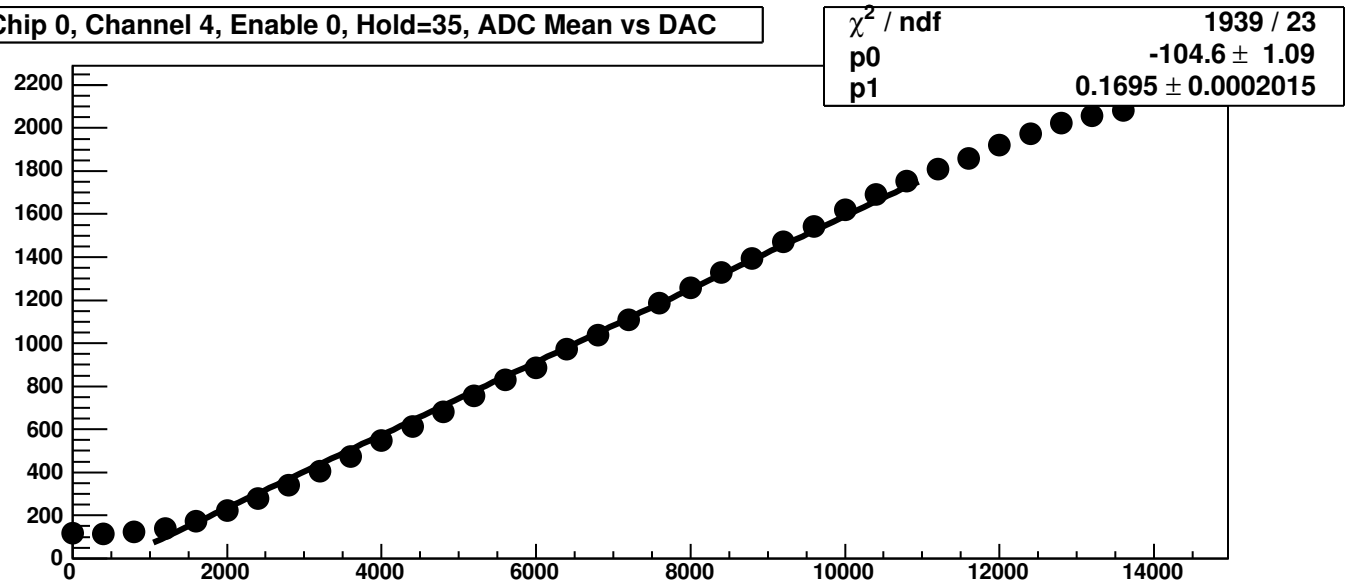

Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8.471e+04 / 23
p0 -251.6 ± 0.9135
p1 0.0749 ± 0.0001786
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

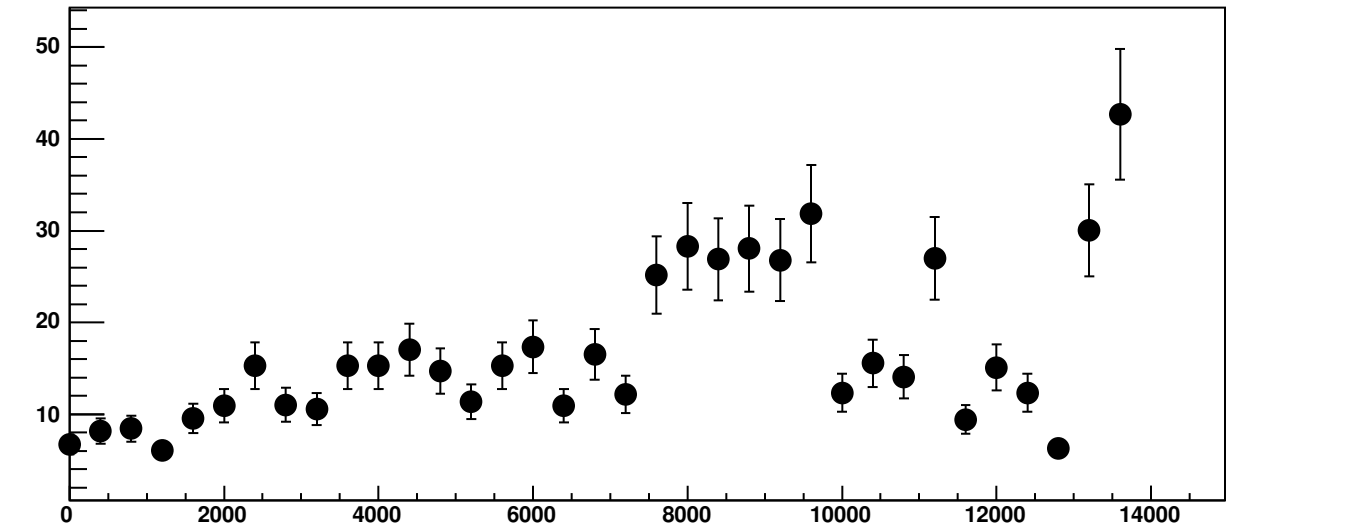

Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

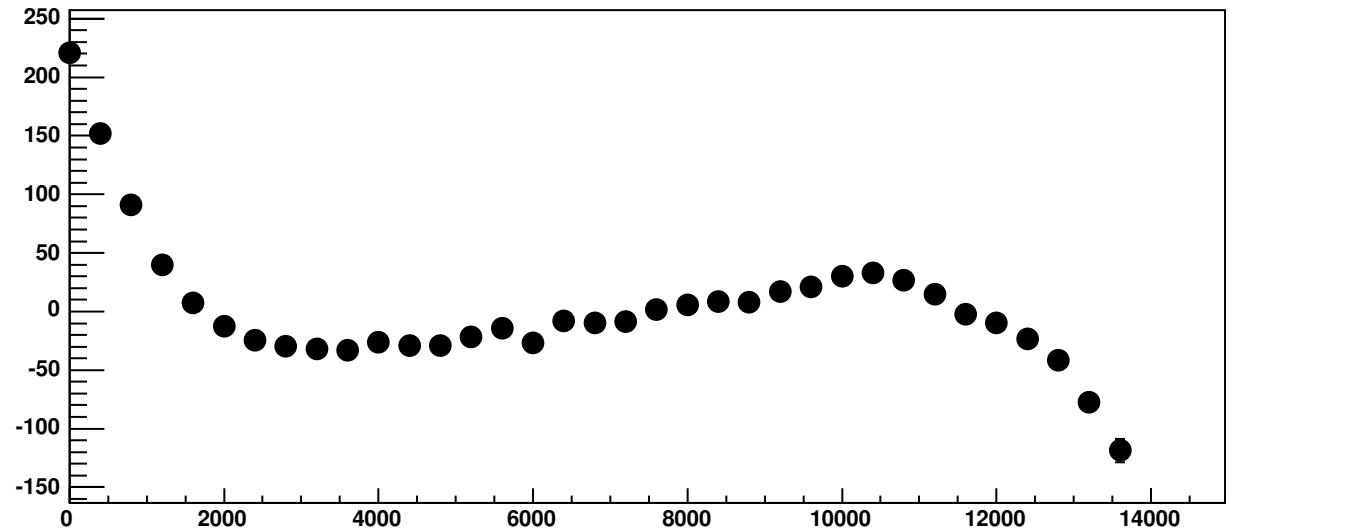
Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

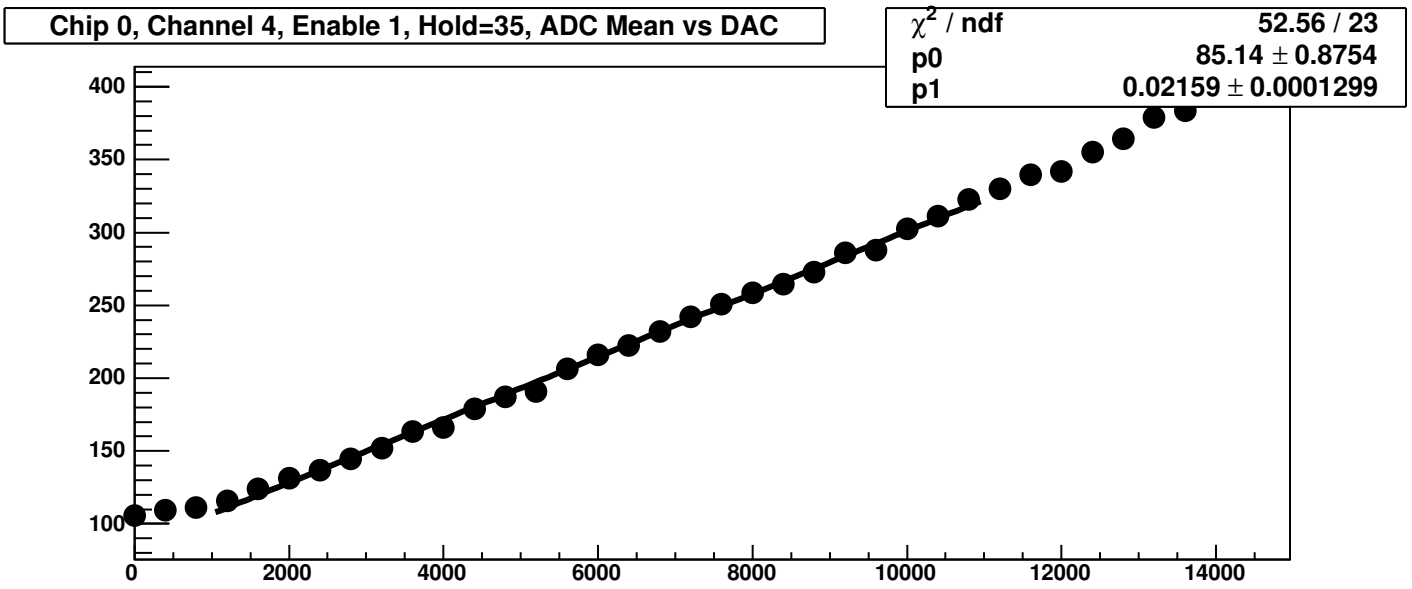


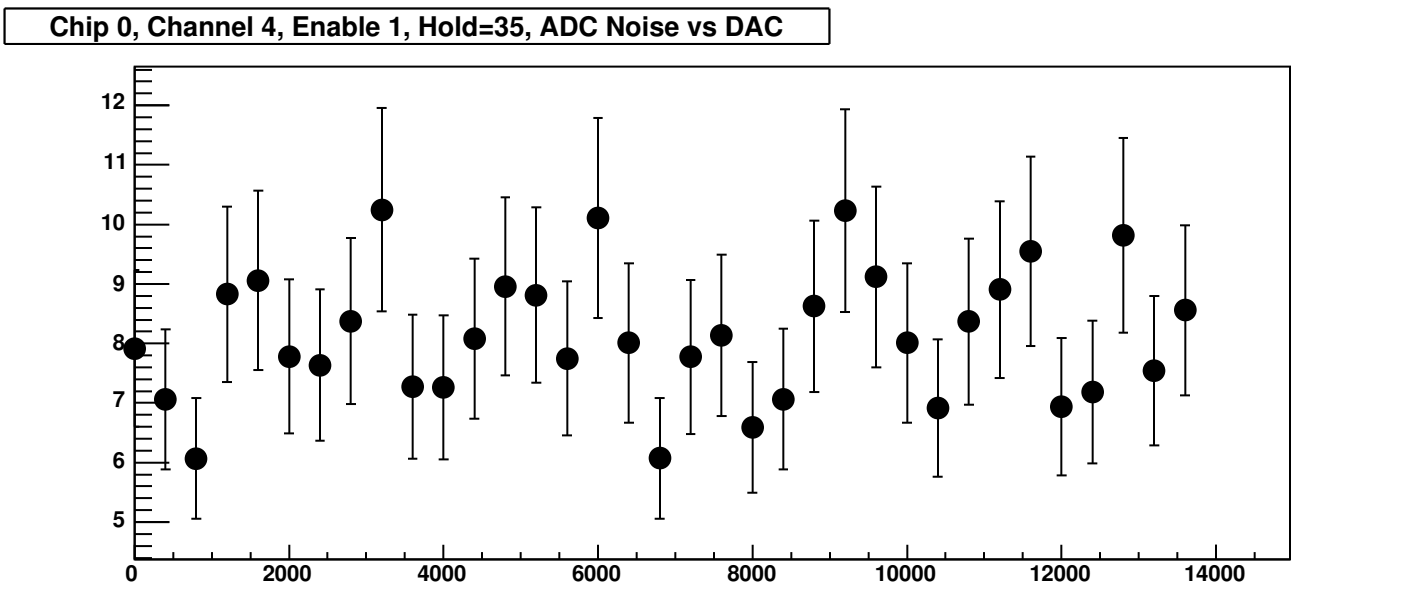
Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

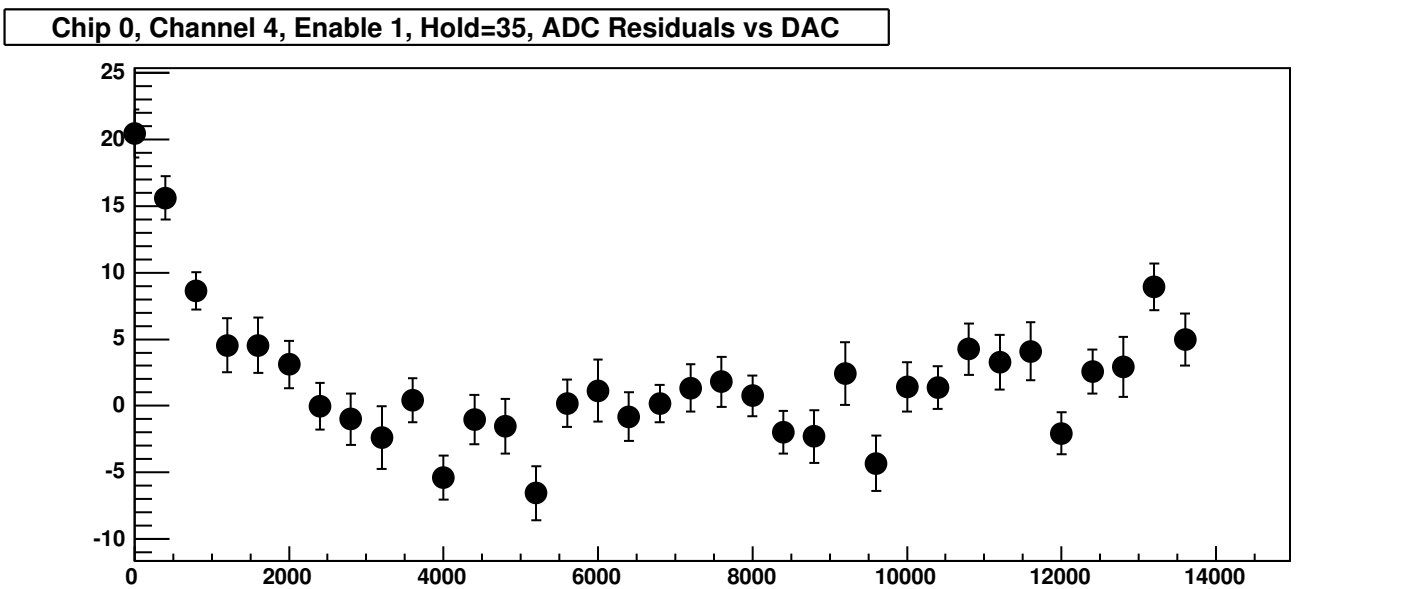


Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

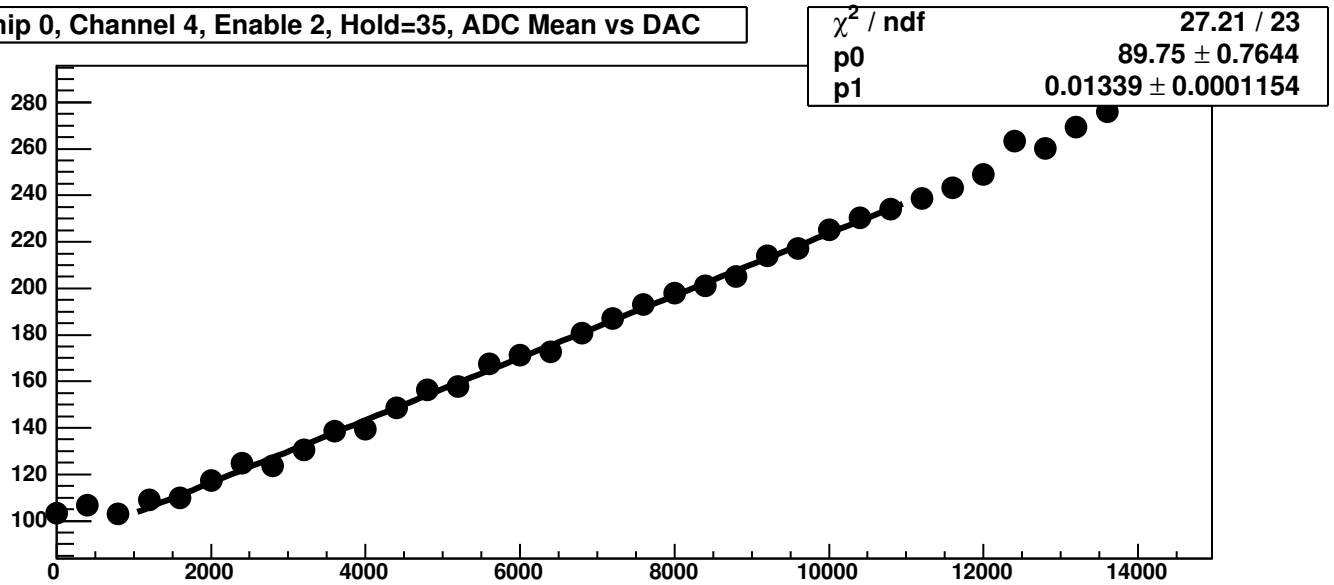



Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.56 / 23
p0 85.14 ± 0.8754
p1 0.02159 ± 0.0001299
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

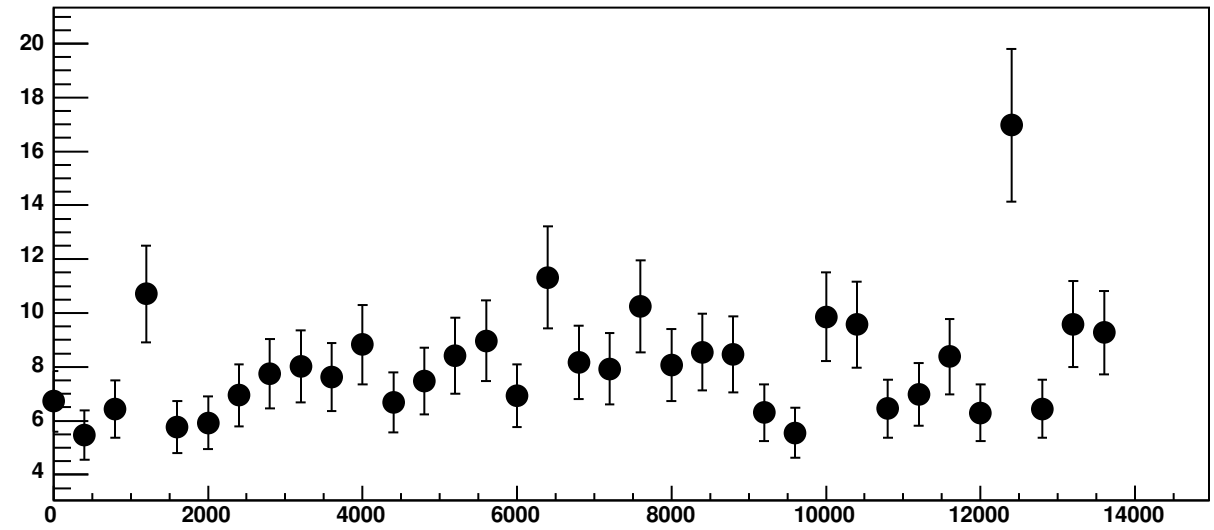

Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

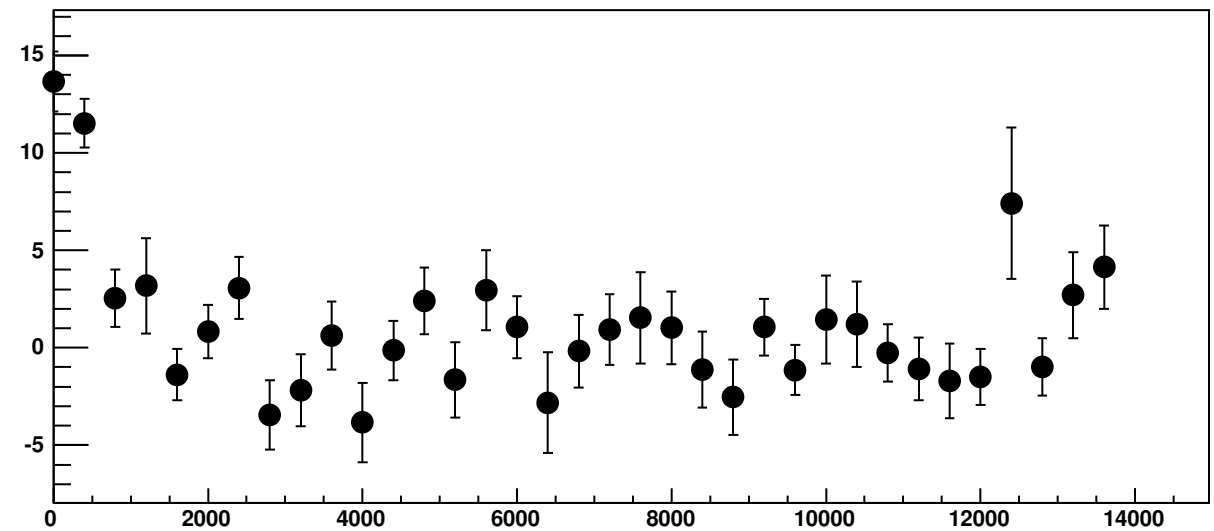
Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

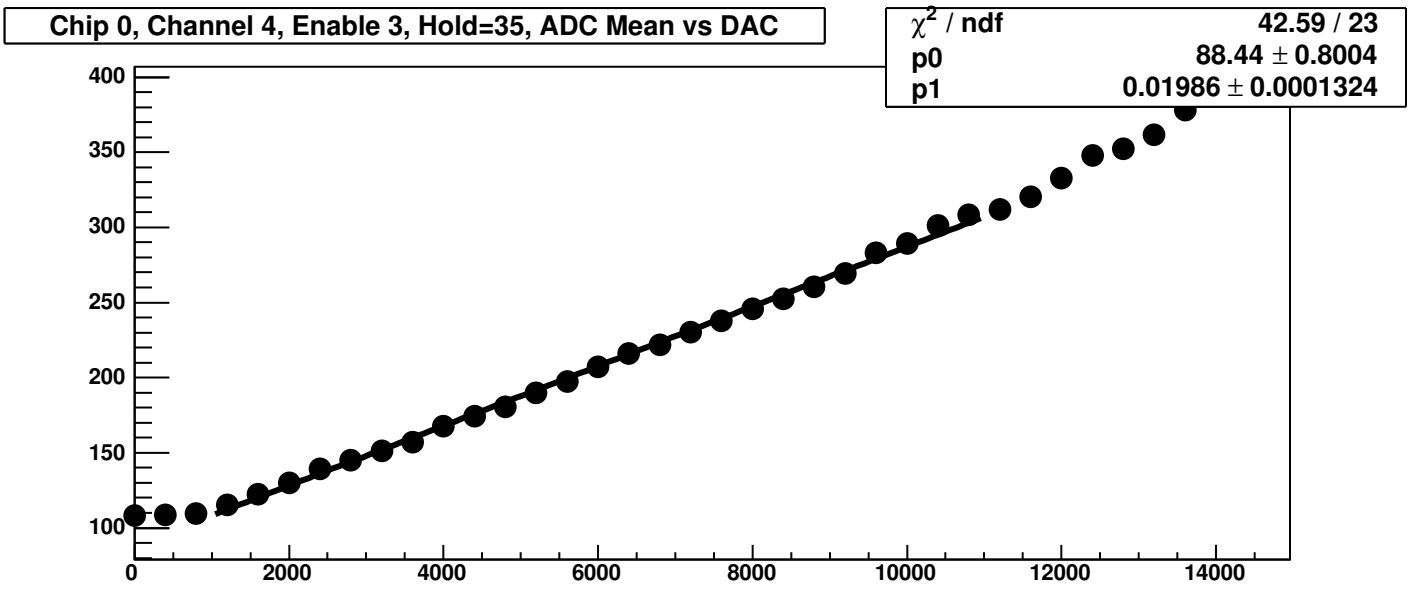


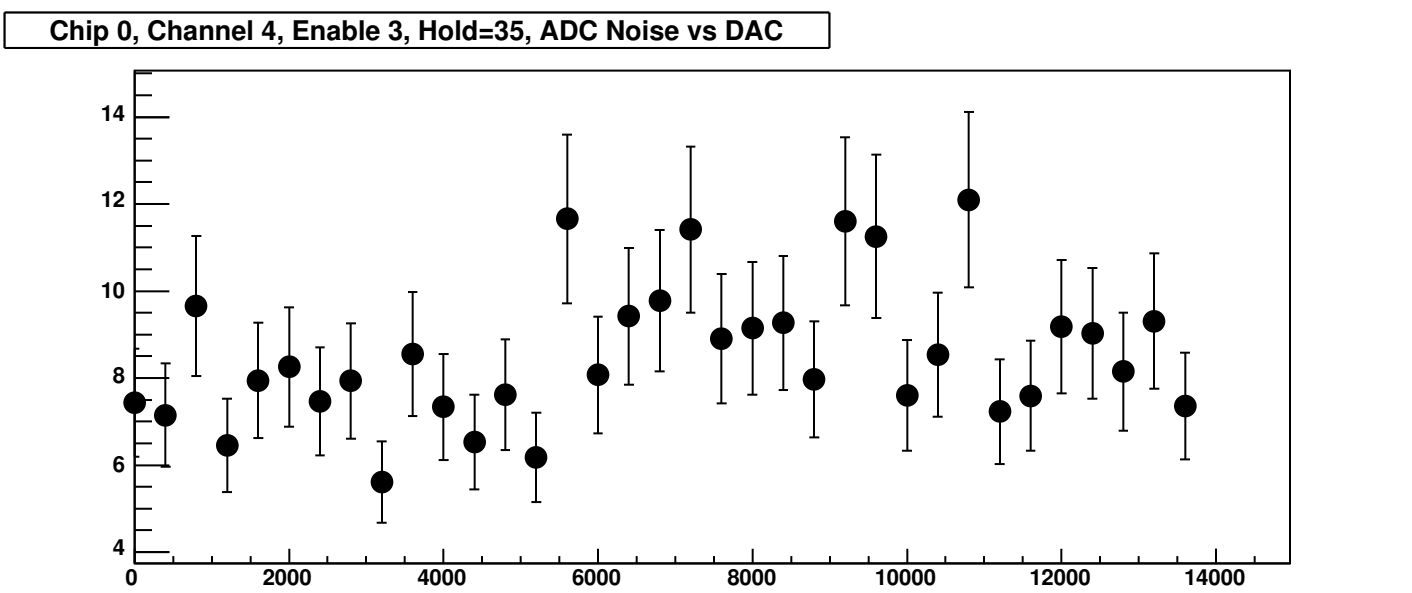
Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

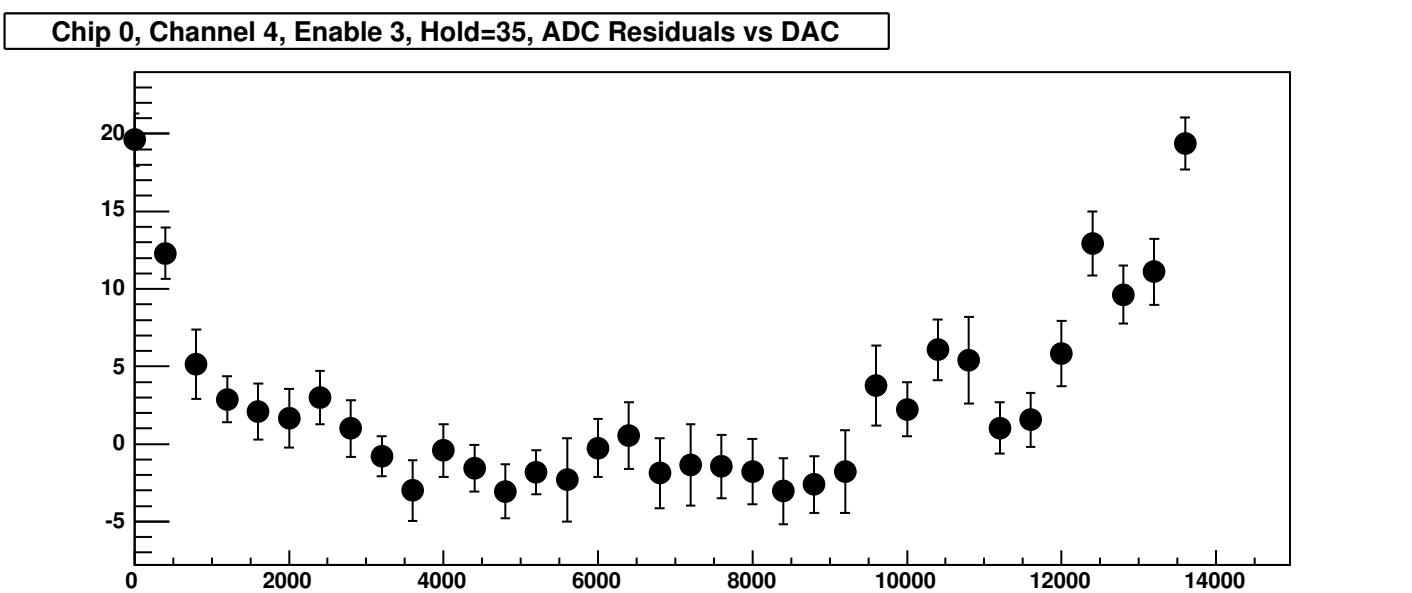


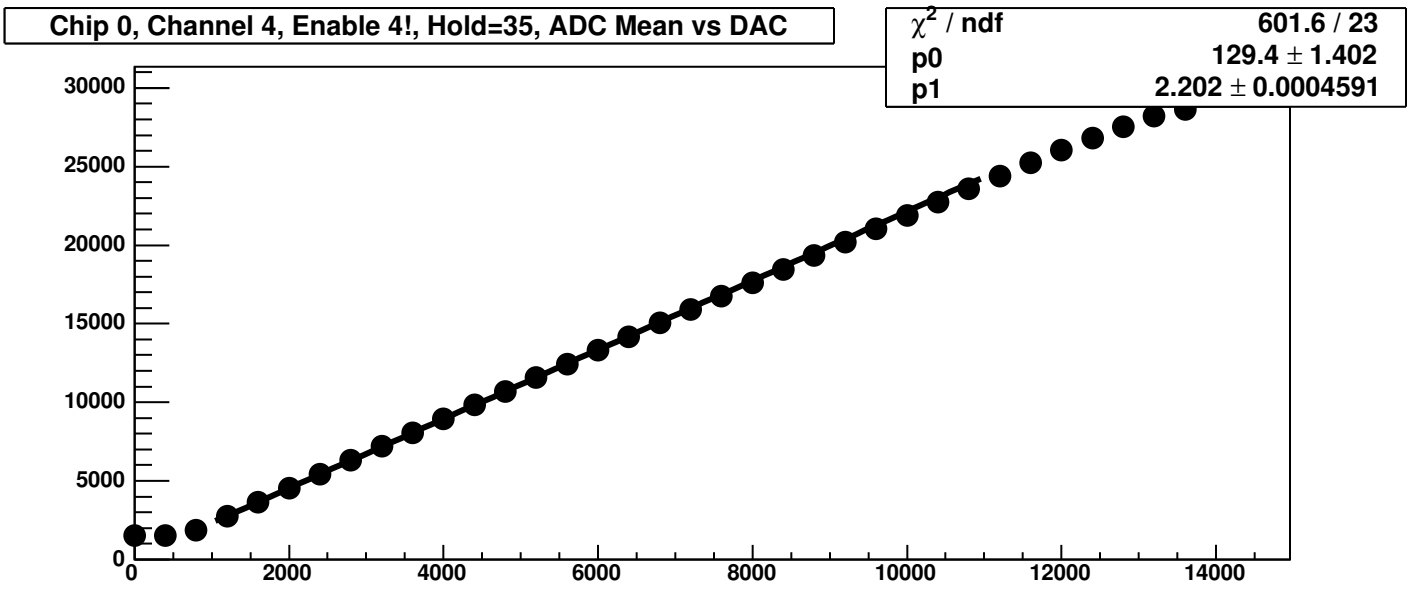
Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

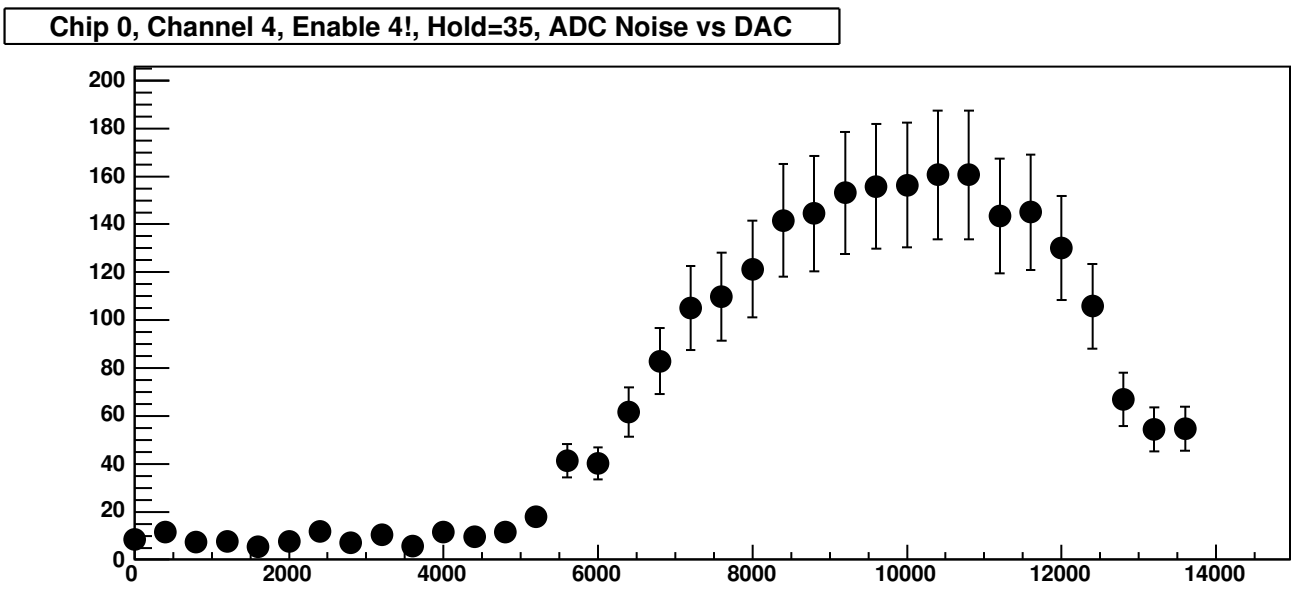


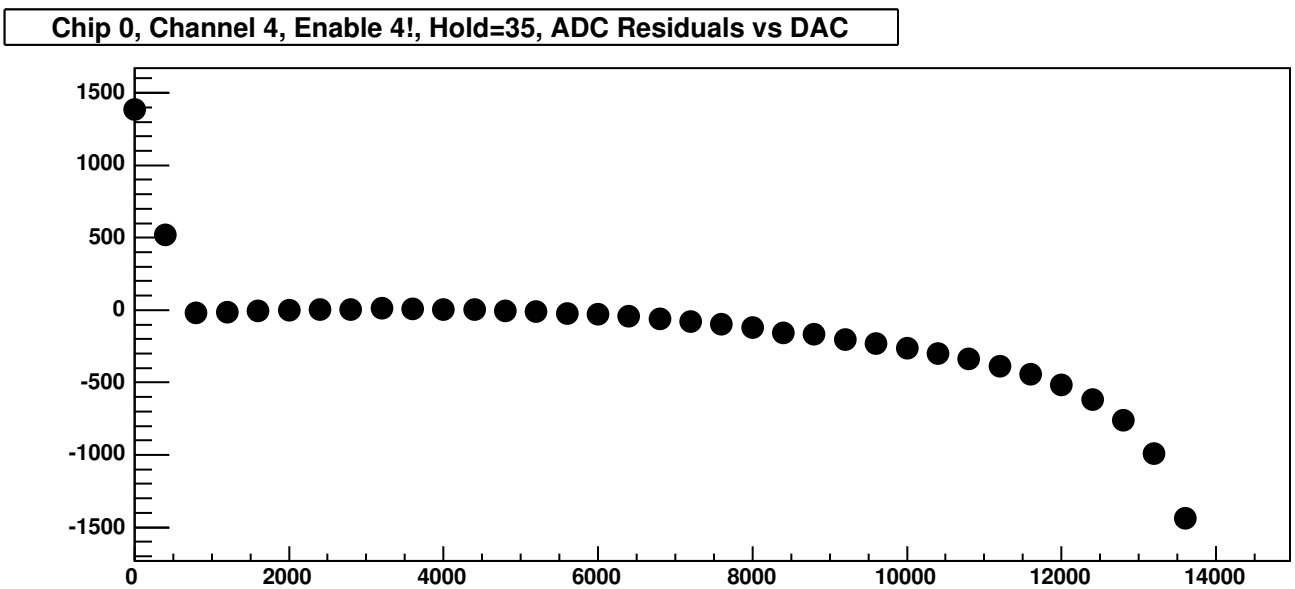

Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.59 / 23
p0 88.44 ± 0.8004
p1 0.01986 ± 0.0001324
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

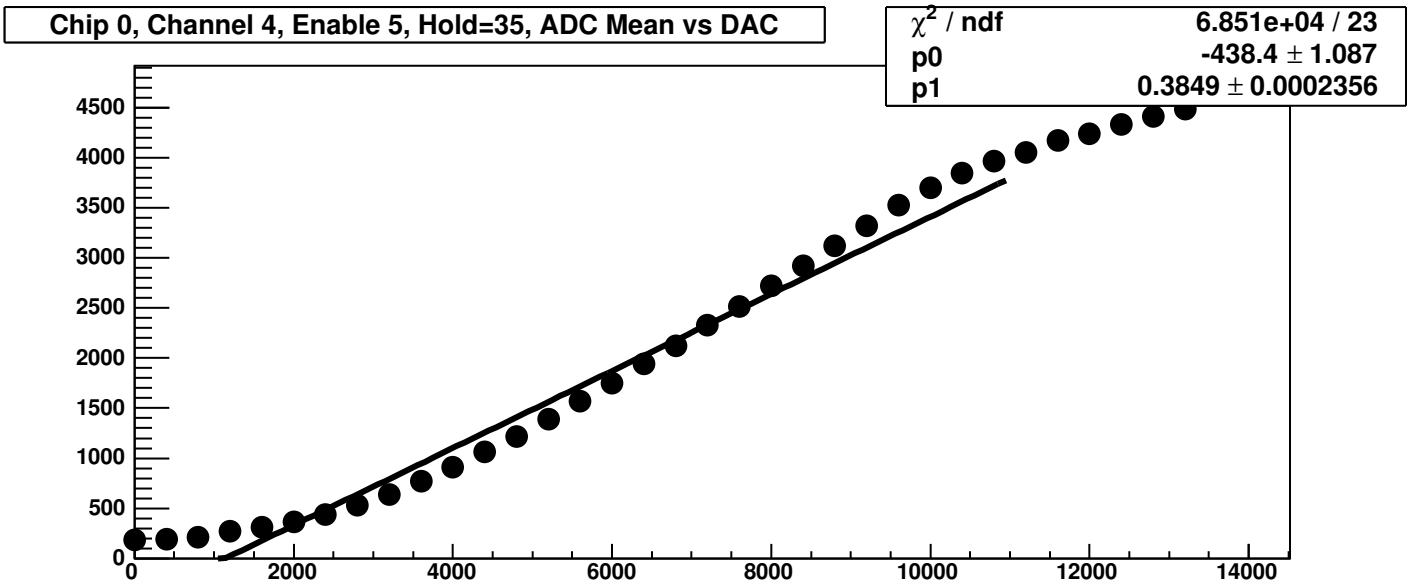

Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

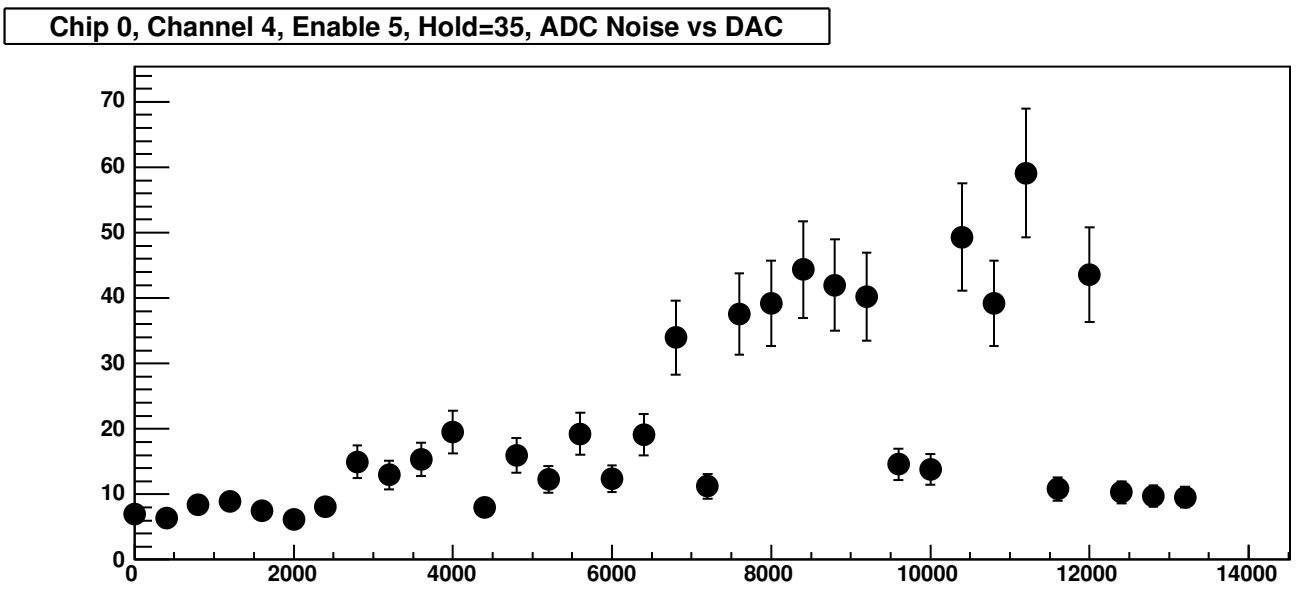

Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

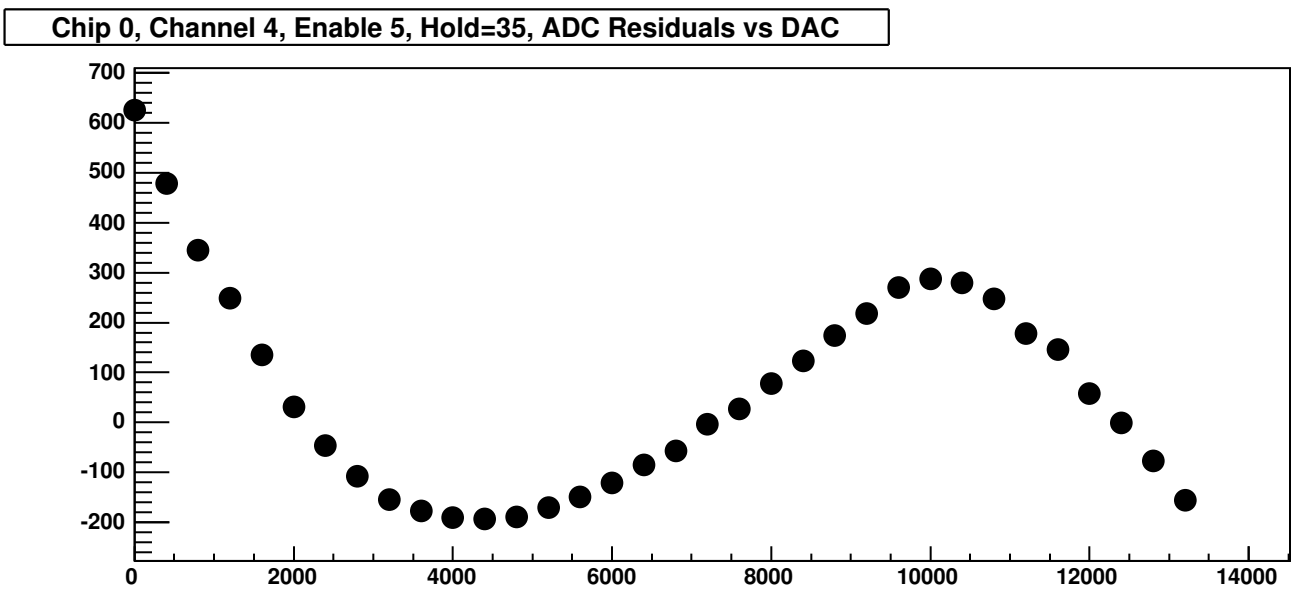

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 601.6 / 23
p0 129.4 ± 1.402
p1 2.202 ± 0.0004591
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

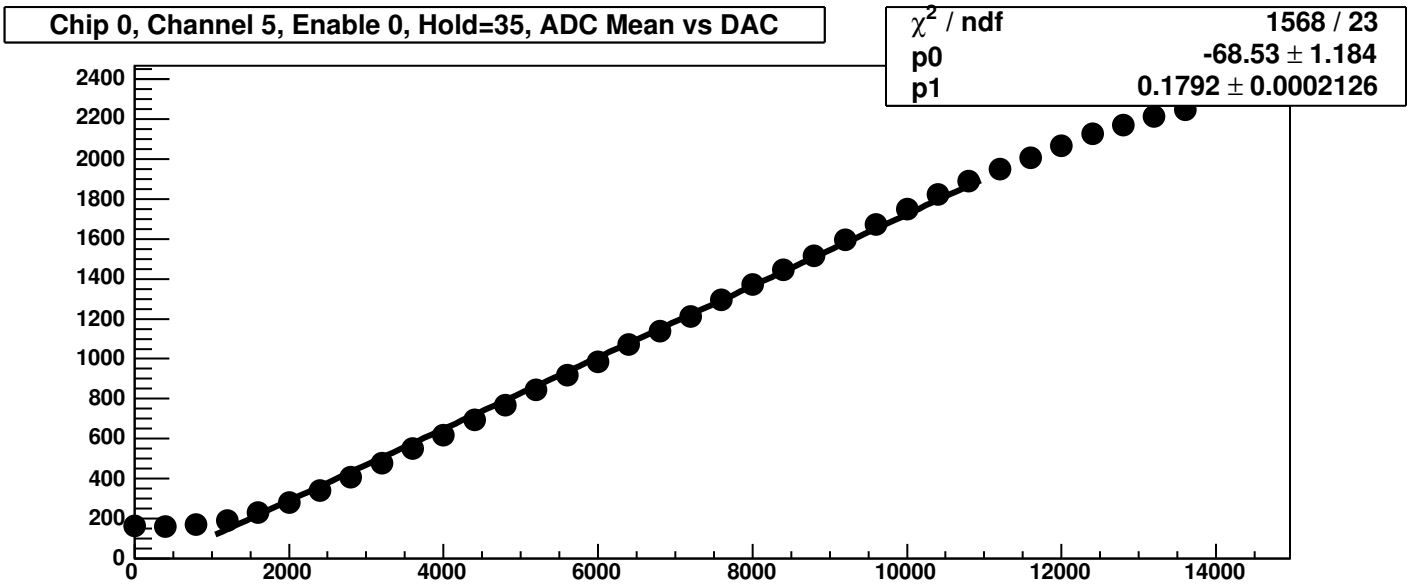

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

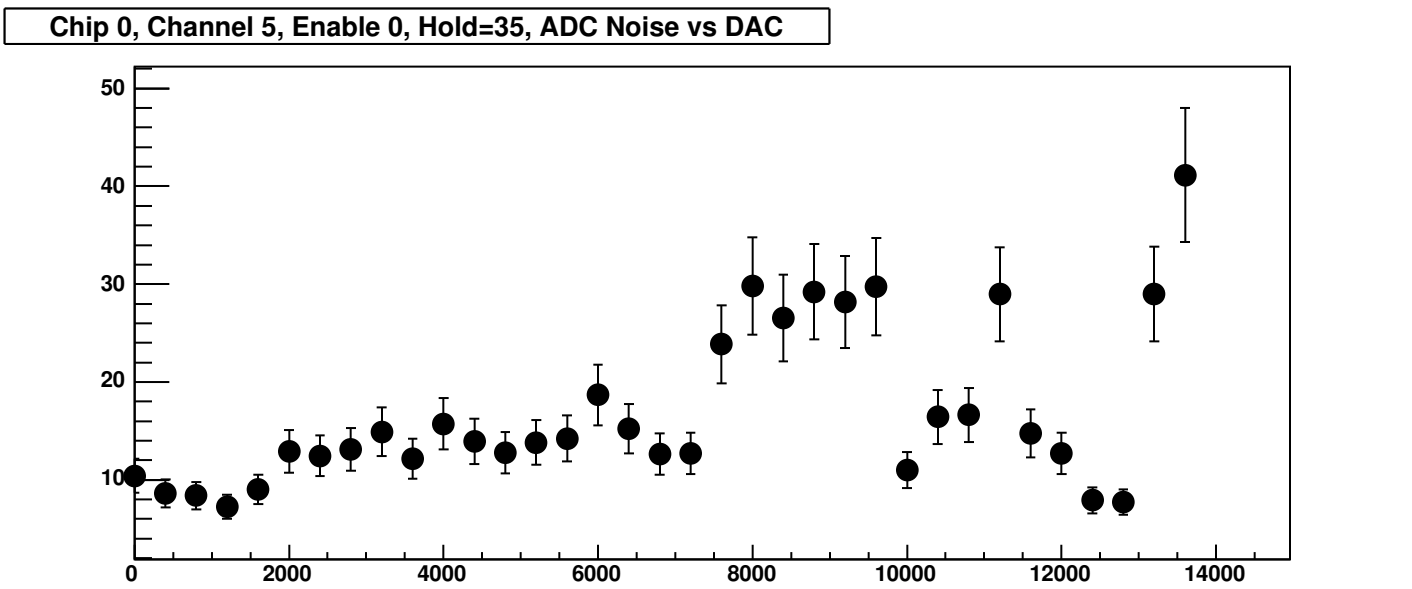

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

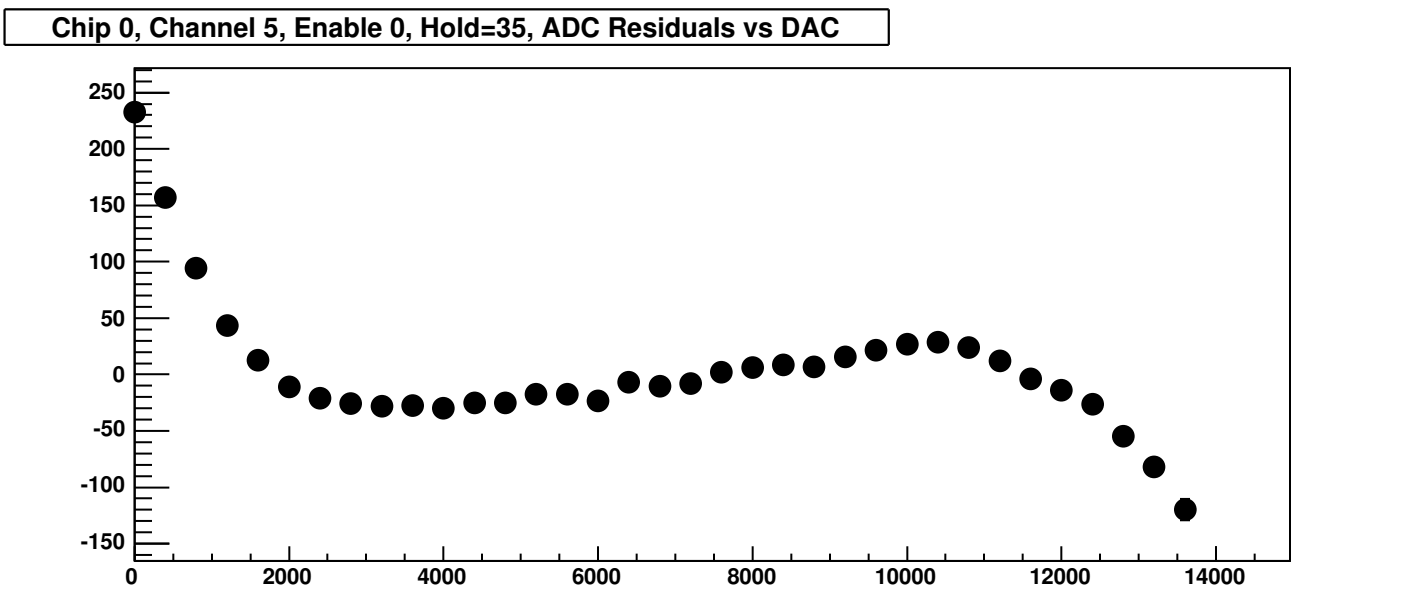

Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.851e+04 / 23
p0 -438.4 ± 1.087
p1 0.3849 ± 0.0002356
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

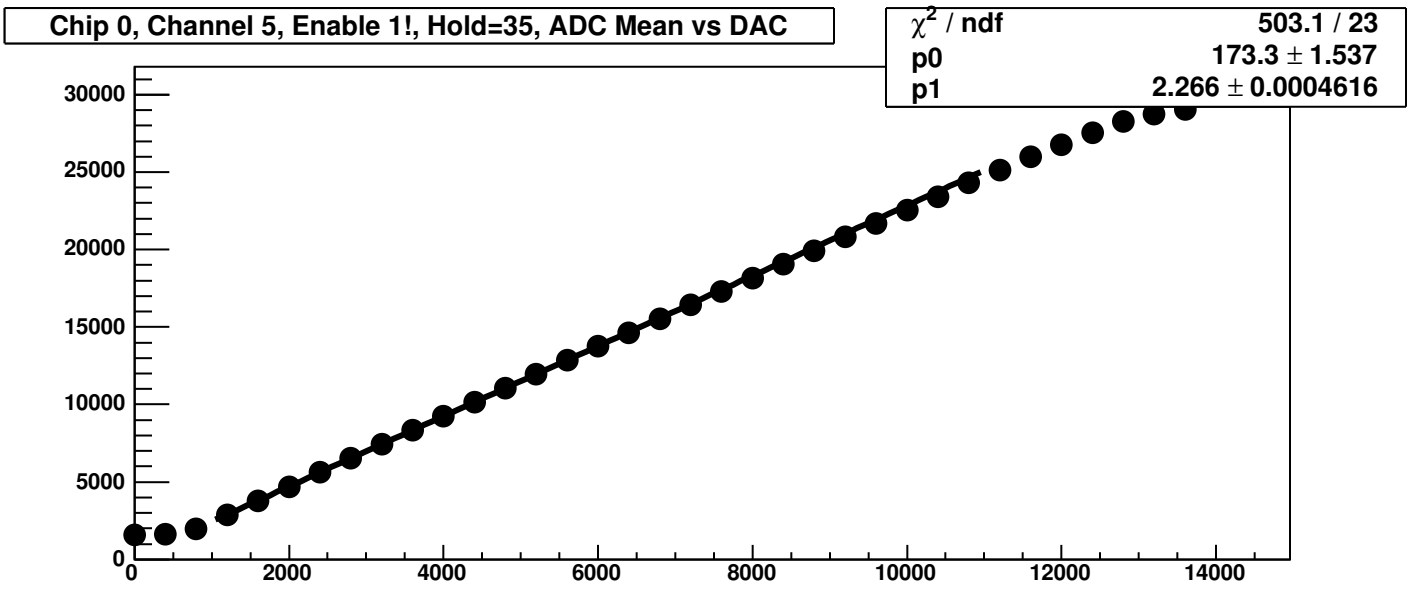

Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

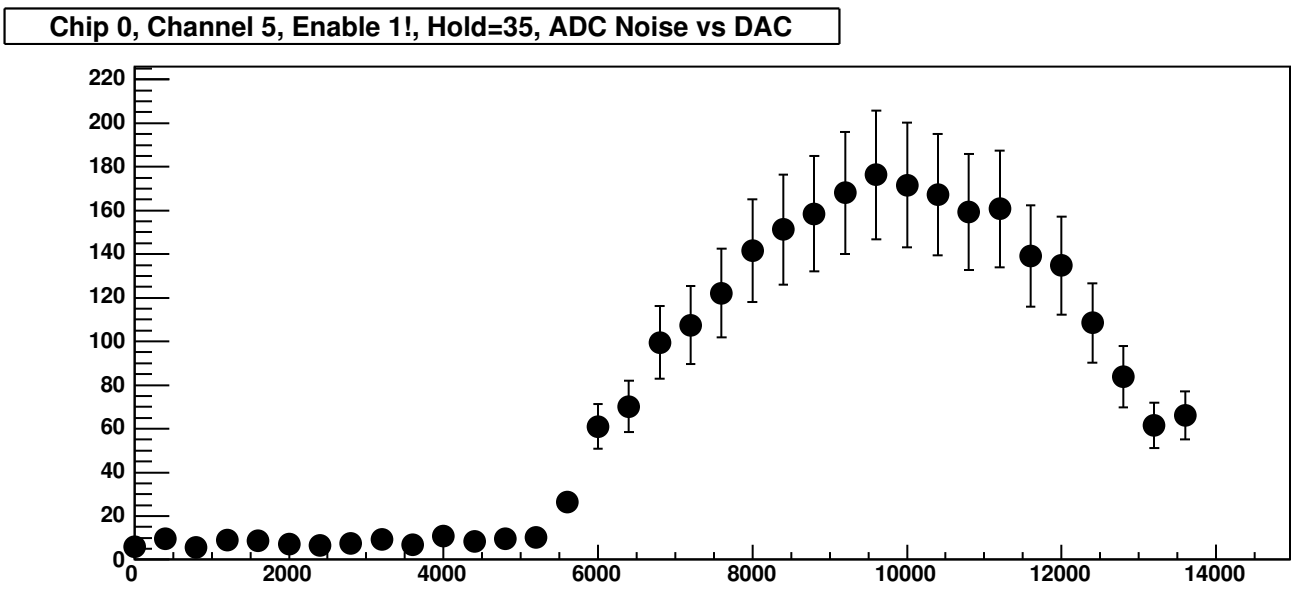

Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
700
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

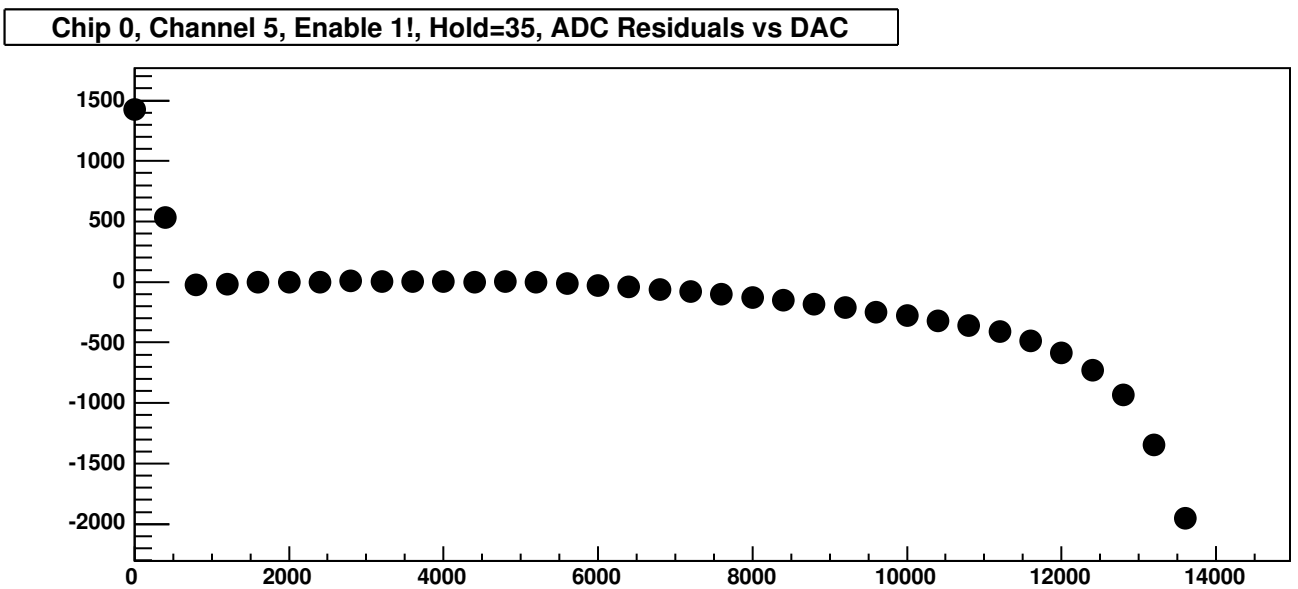

Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1568 / 23
p0 -68.53 ± 1.184
p1 0.1792 ± 0.0002126
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

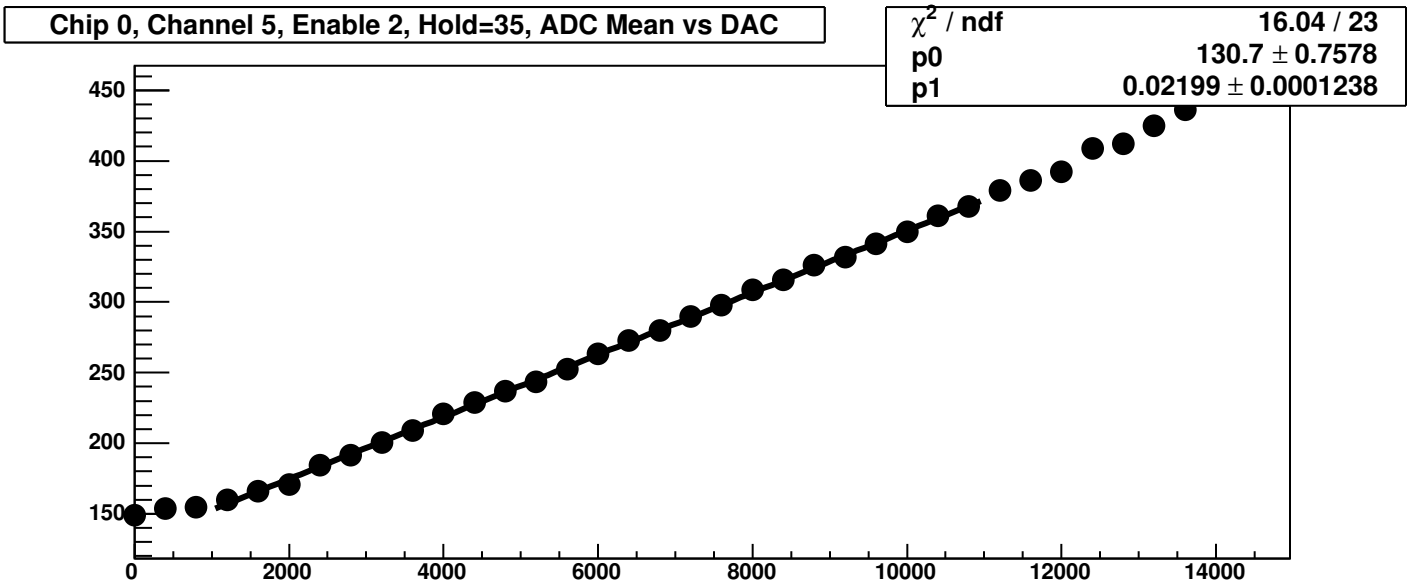

Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

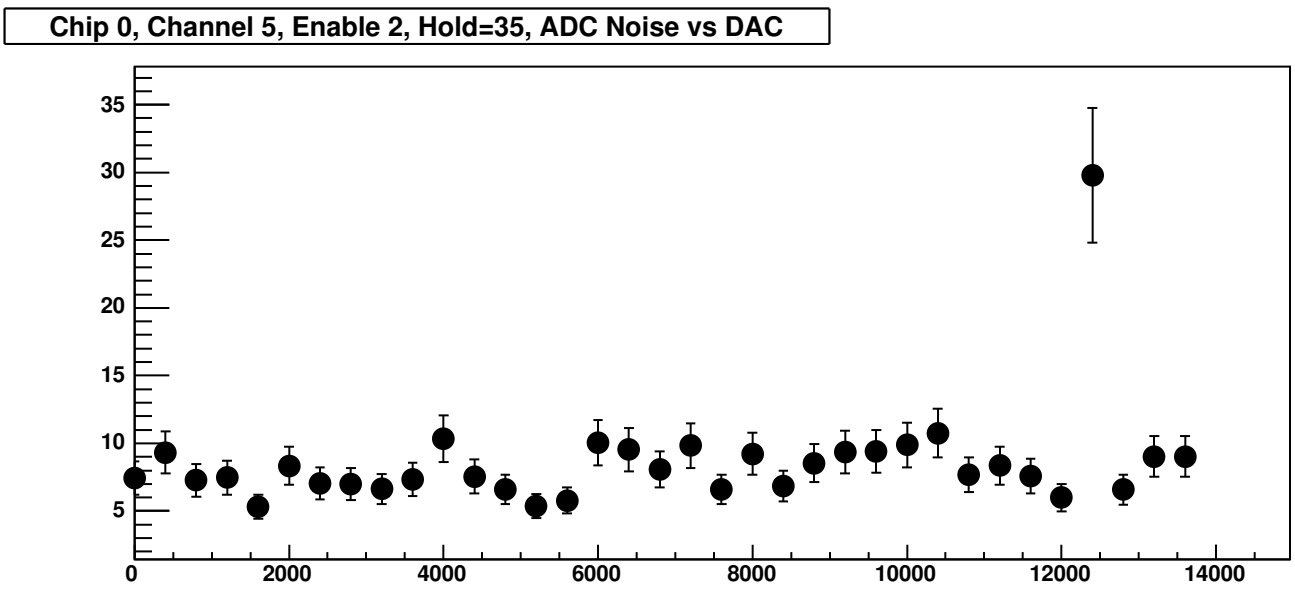

Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

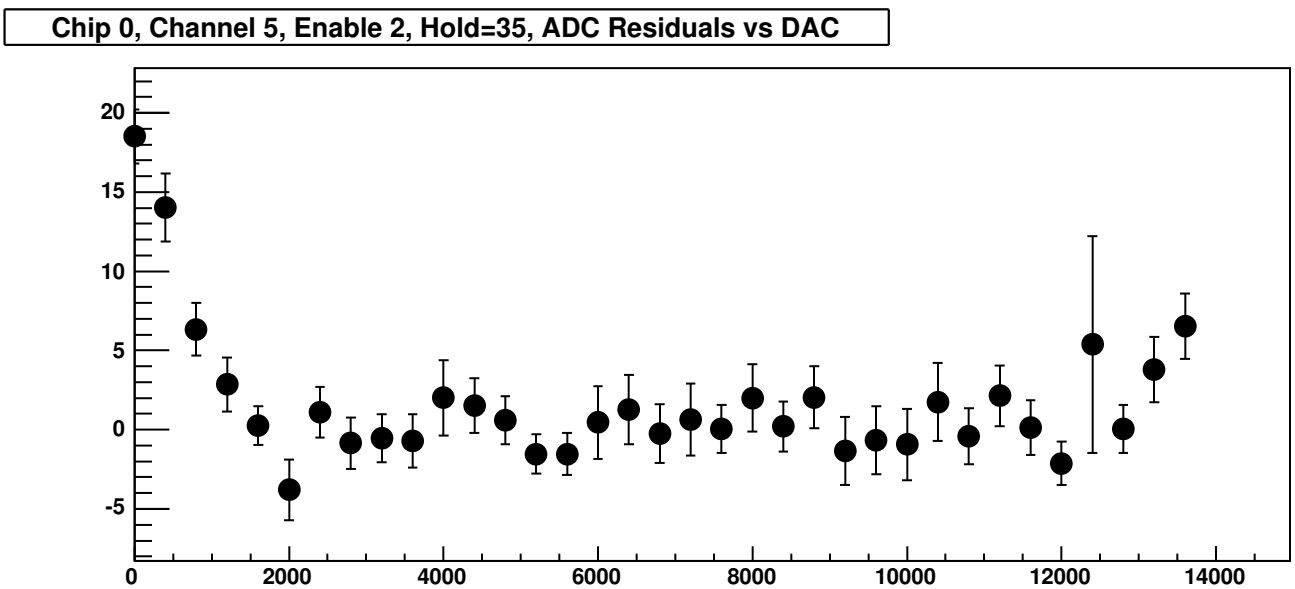

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 503.1 / 23
p0 173.3 ± 1.537
p1 2.266 ± 0.0004616
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

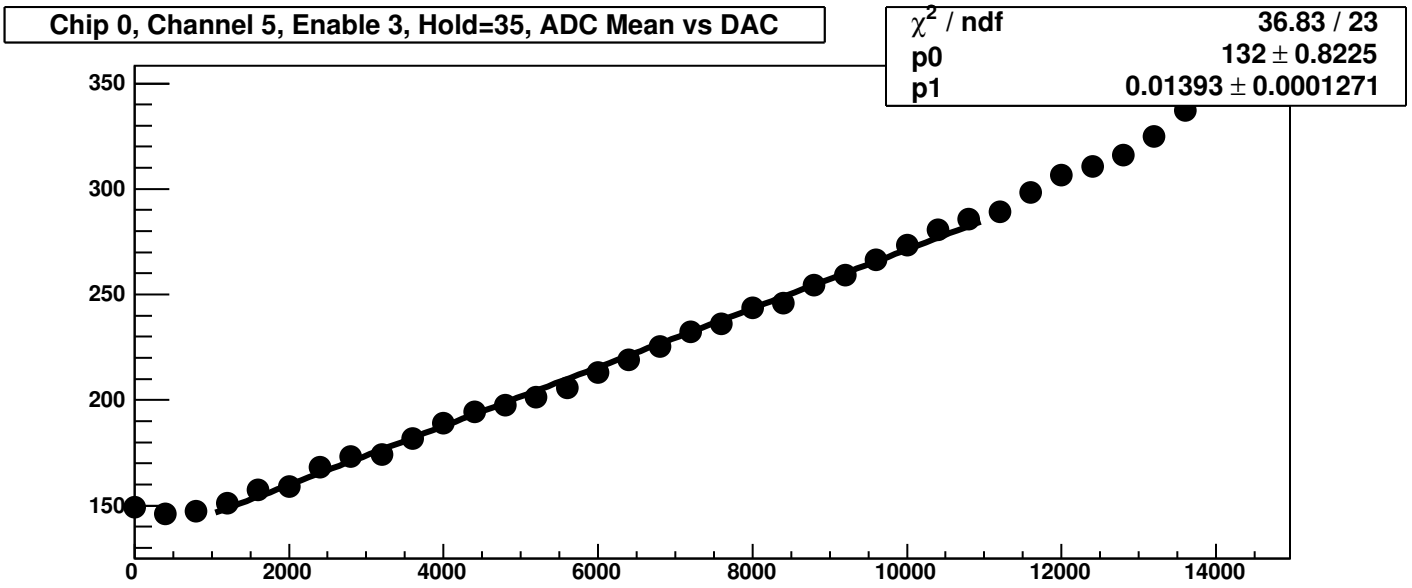

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

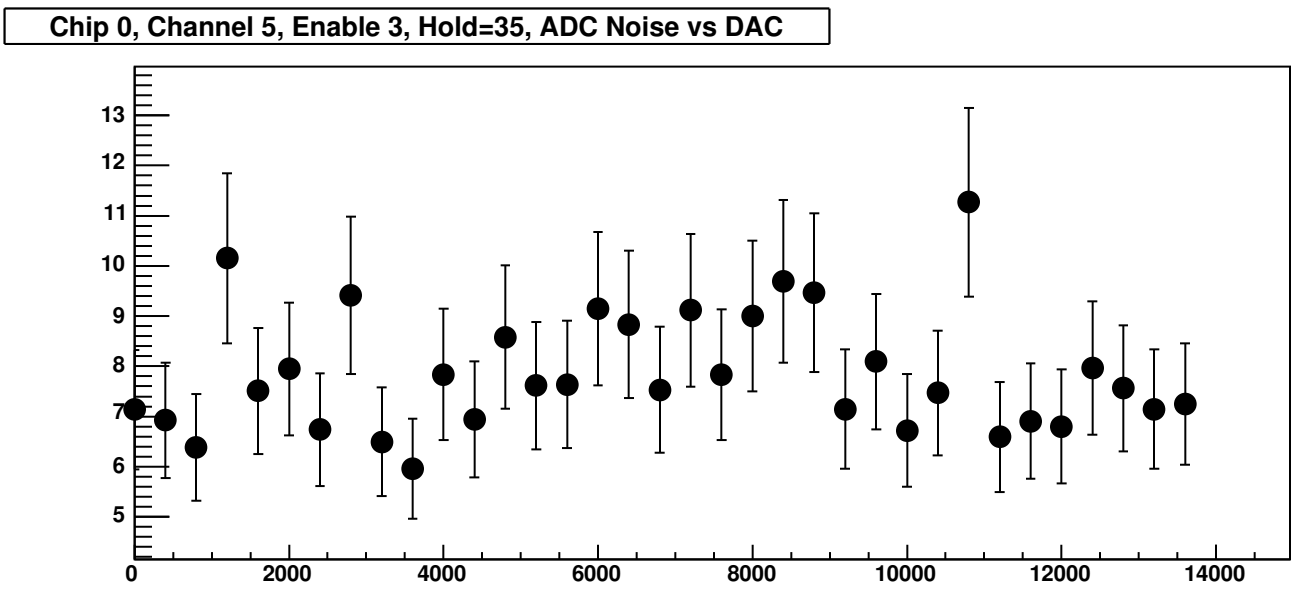

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

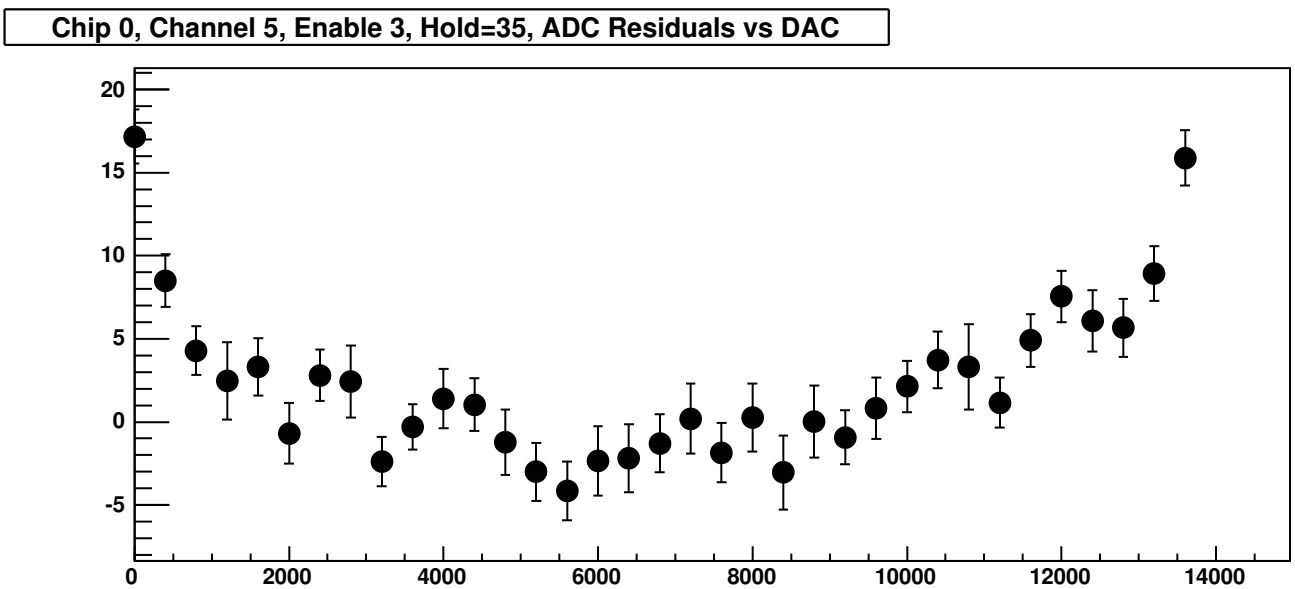

Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 16.04 / 23
p0 130.7 ± 0.7578
p1 0.02199 ± 0.0001238
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

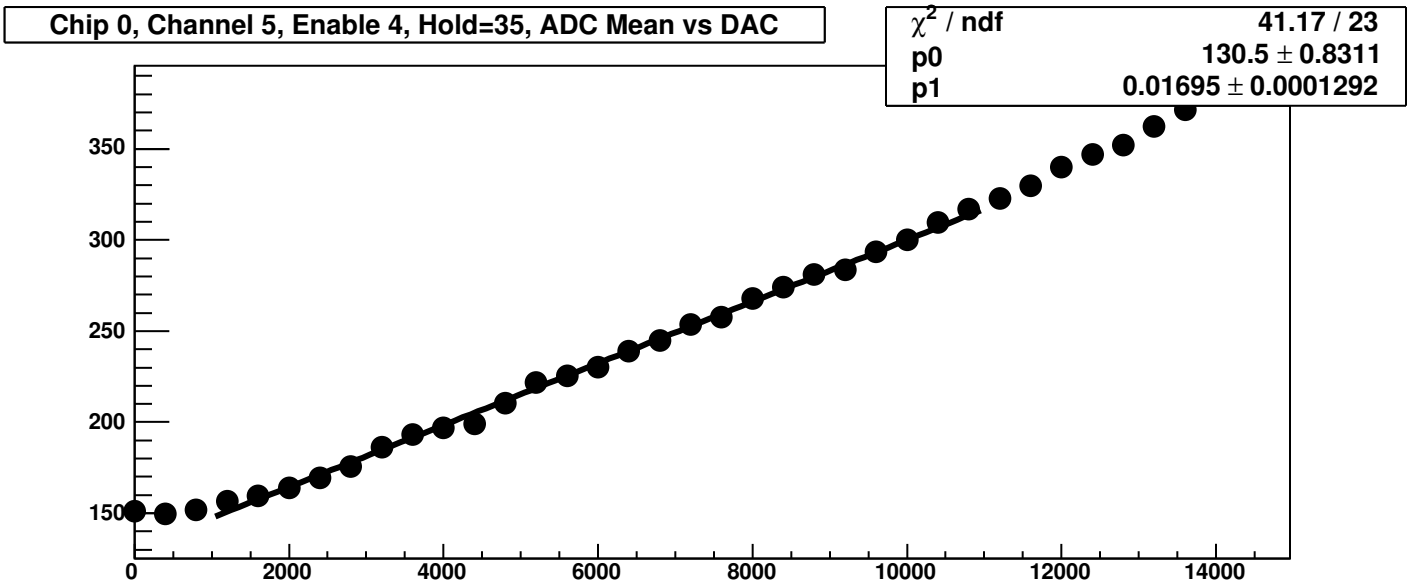

Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

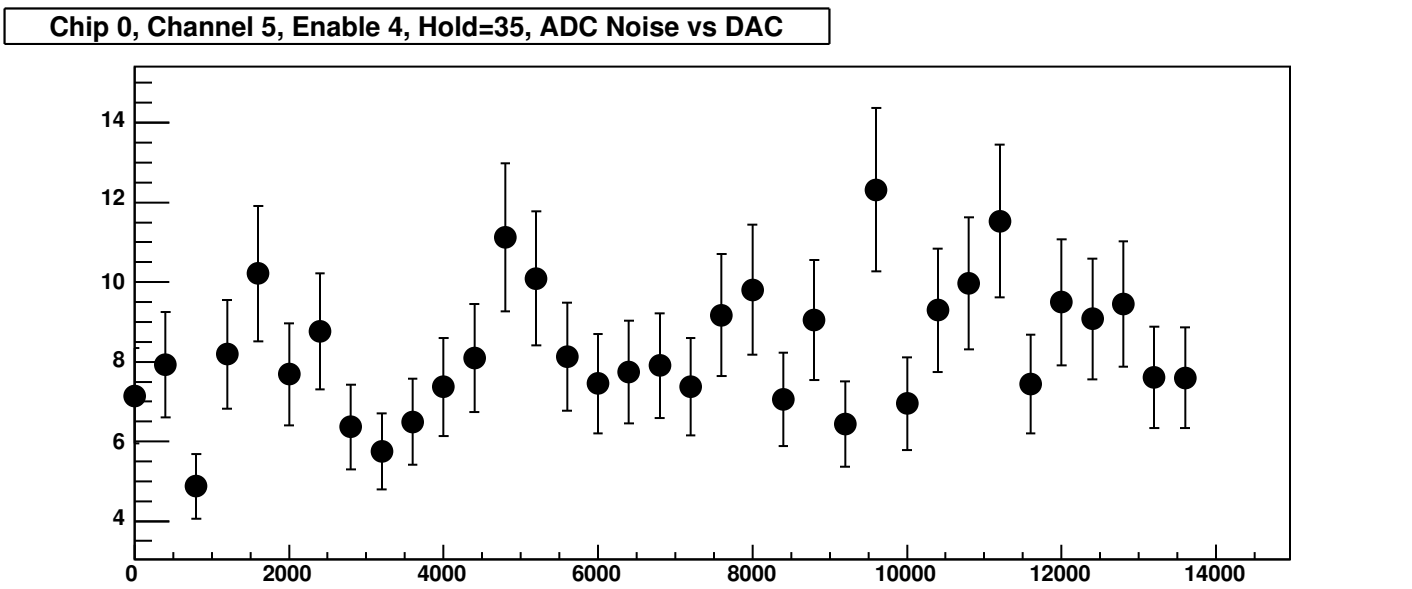

Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

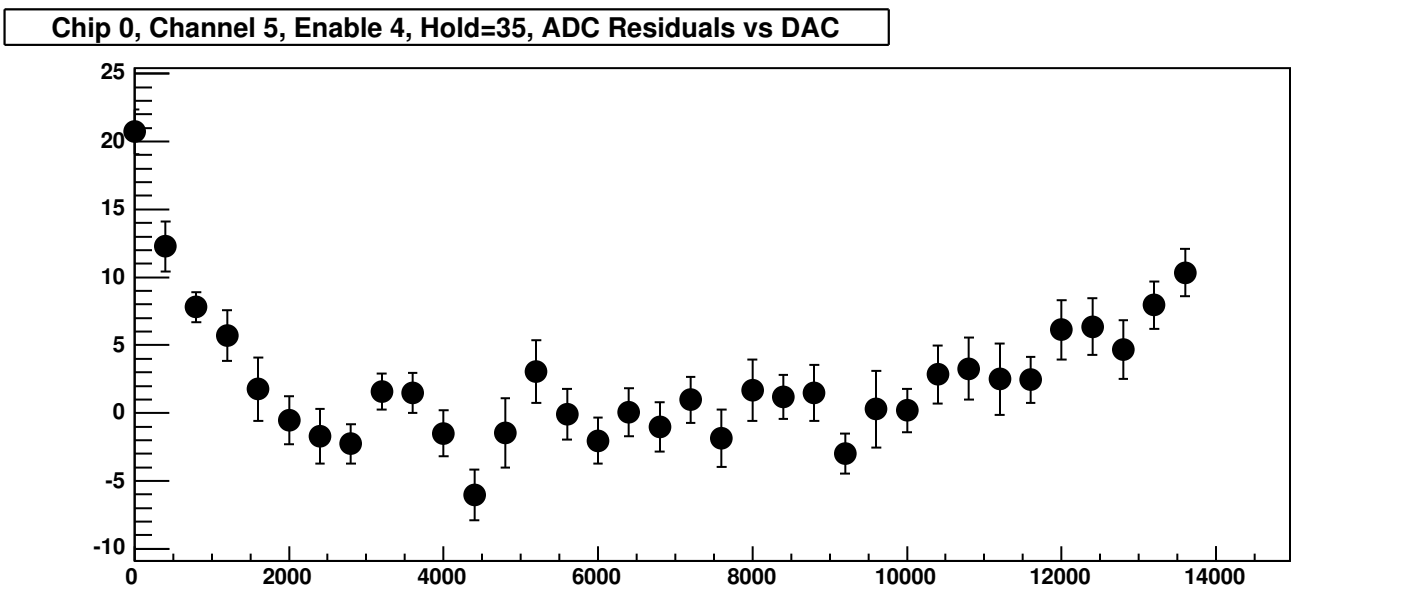

Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.83 / 23
p0 132 ± 0.8225
p1 0.01393 ± 0.0001271
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

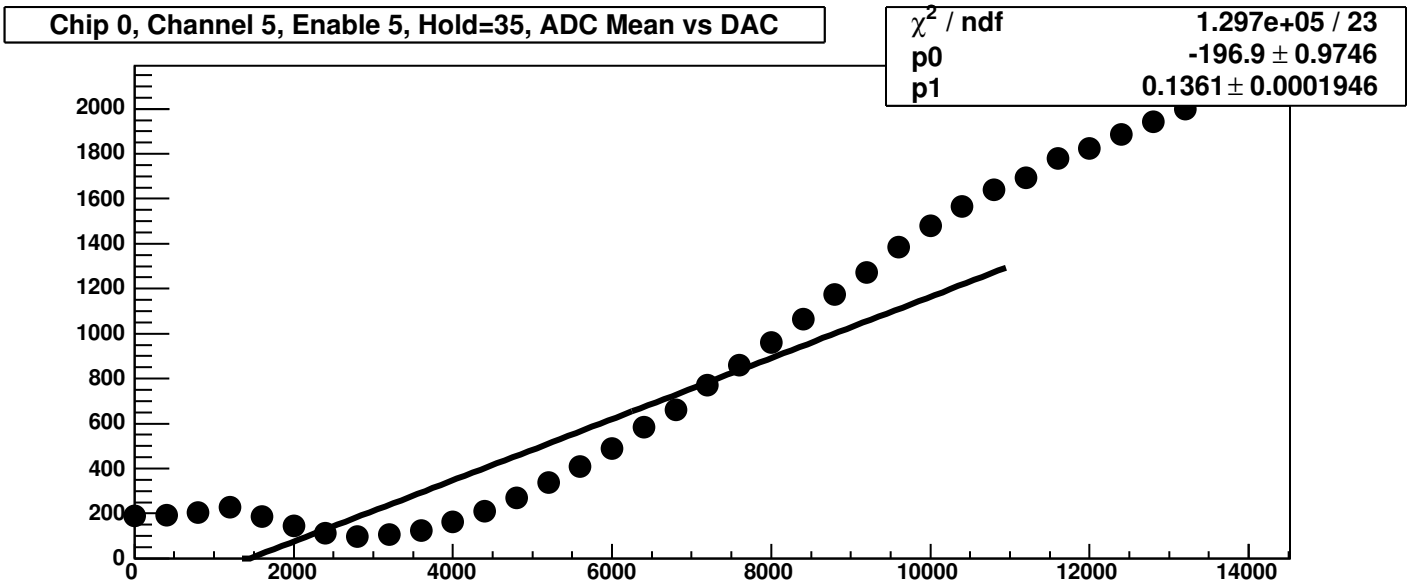

Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

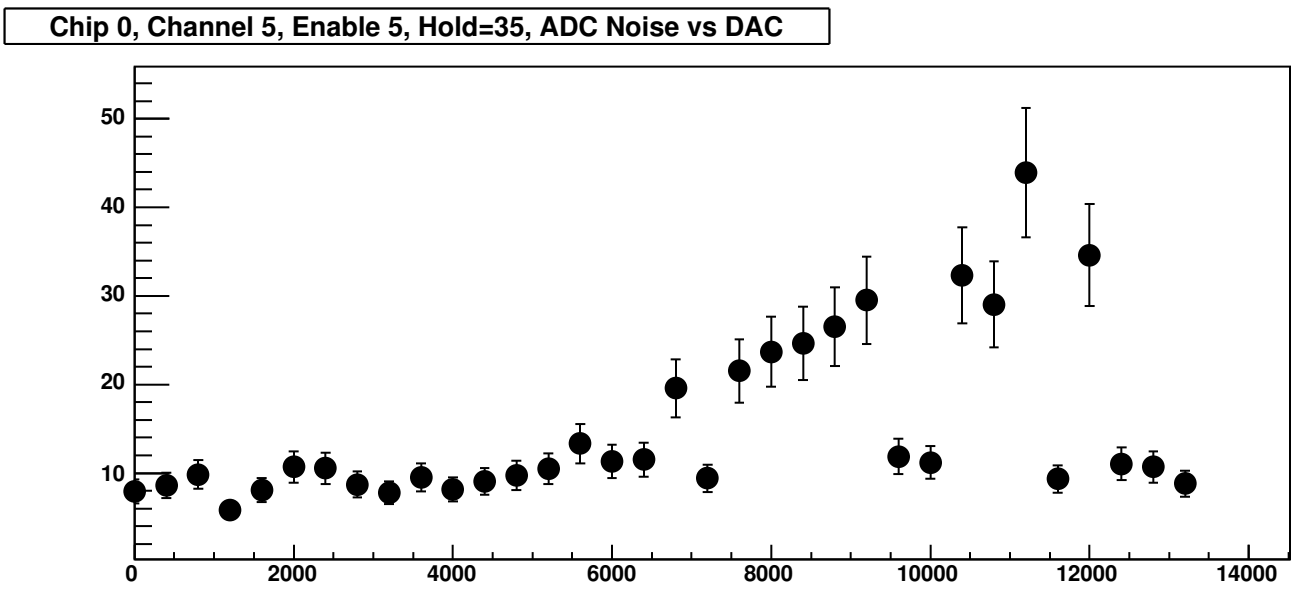

Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

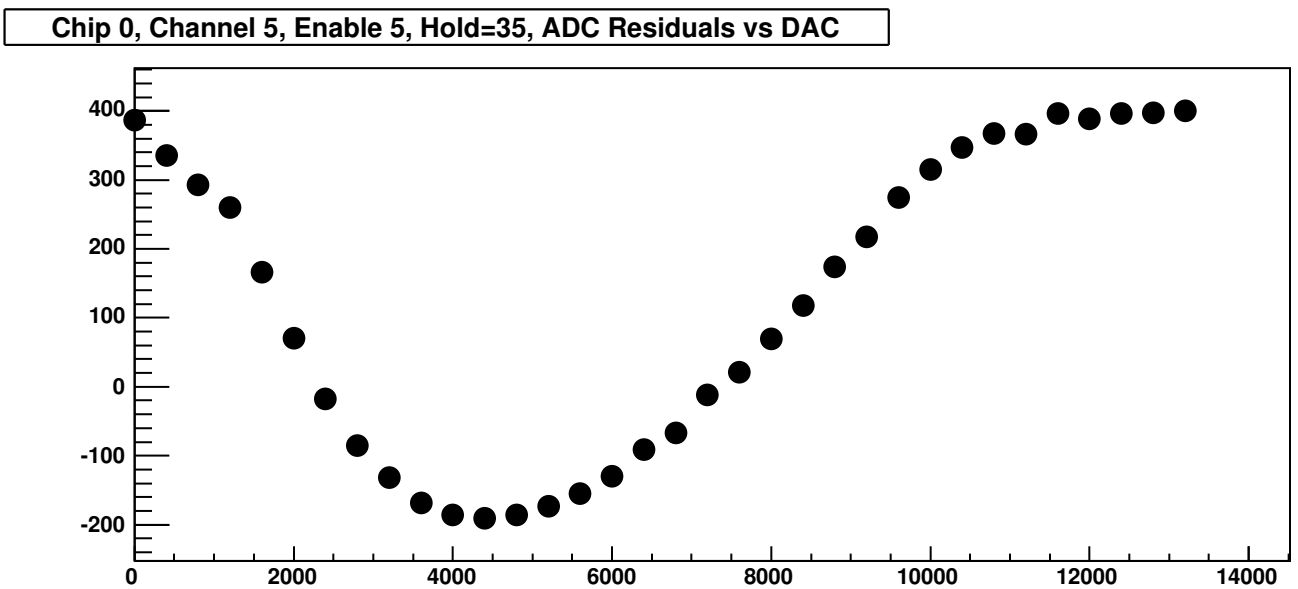

Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 41.17 / 23
p0 130.5 ± 0.8311
p1 0.01695 ± 0.0001292
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

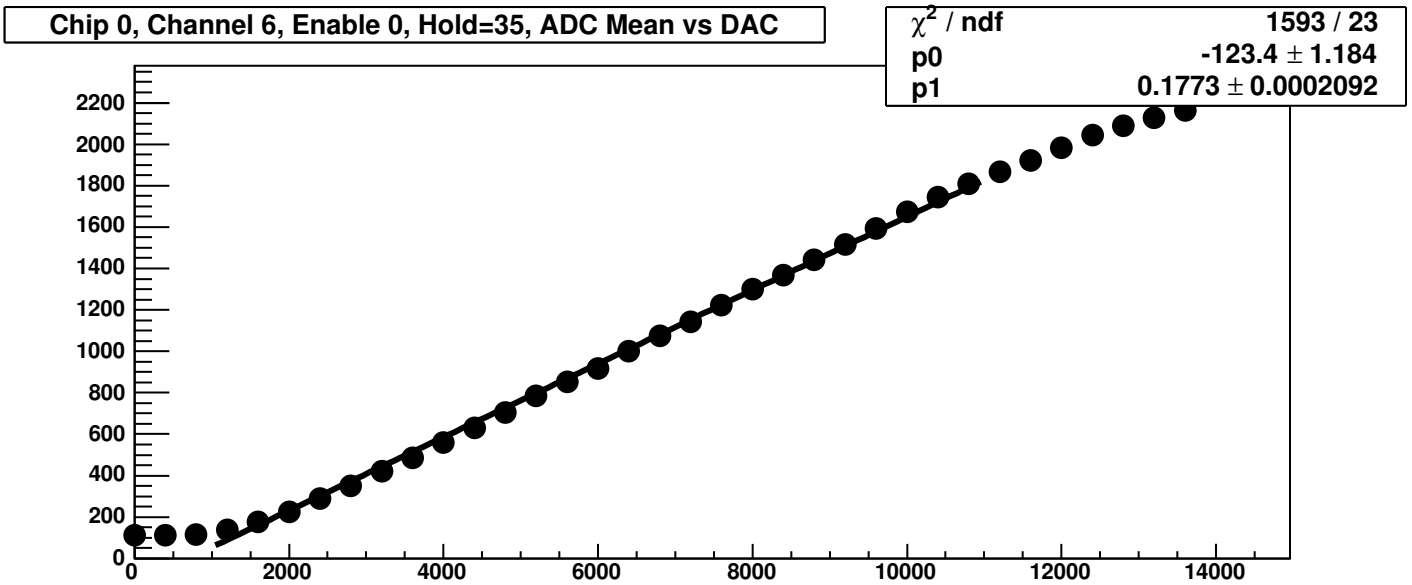

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

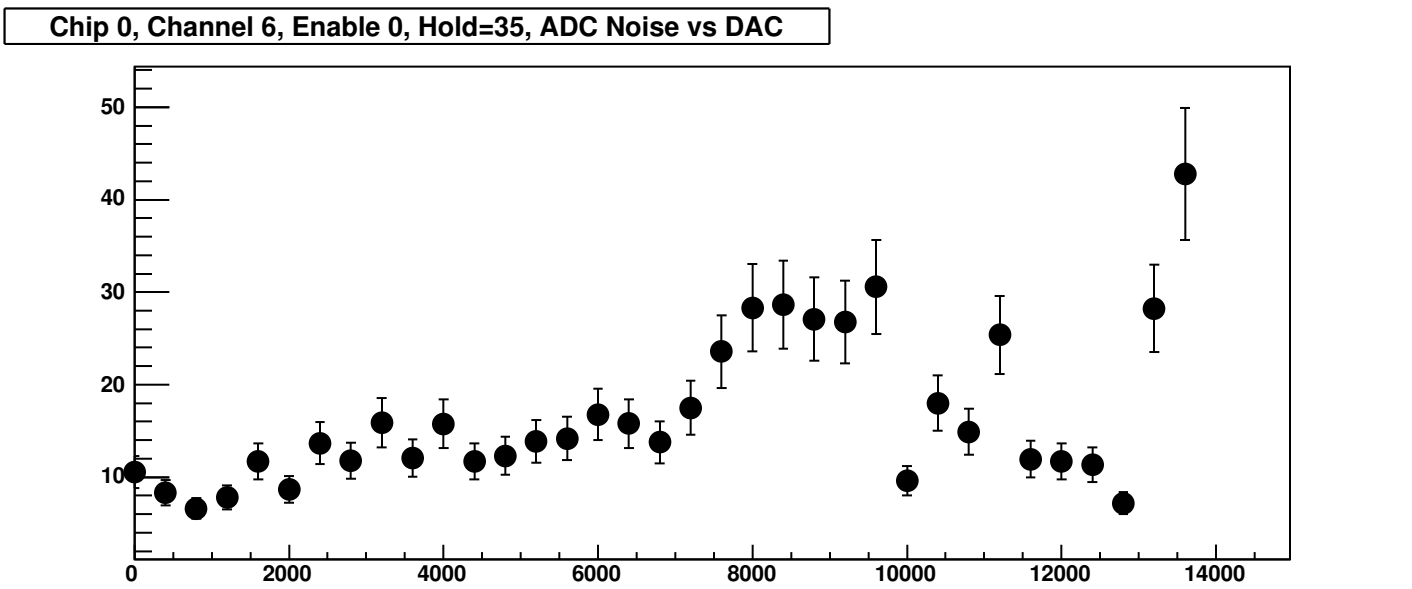

Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

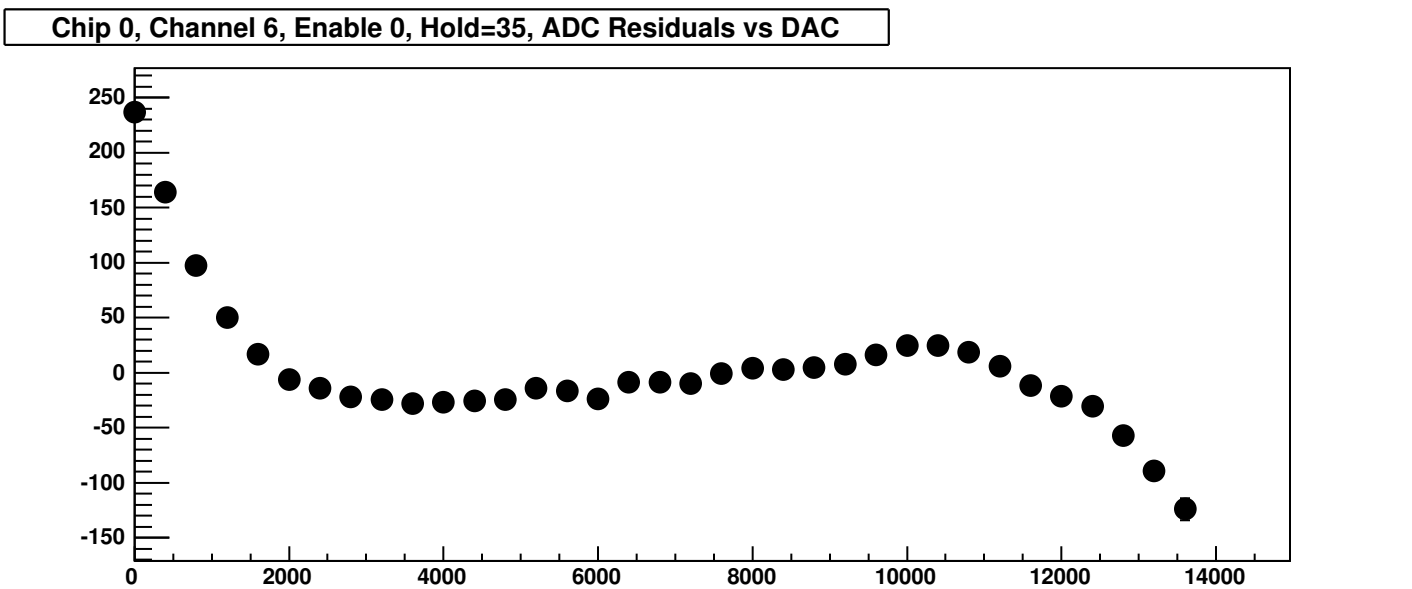

Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.297e+05 / 23
p0 -196.9 ± 0.9746
p1 0.1361 ± 0.0001946
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

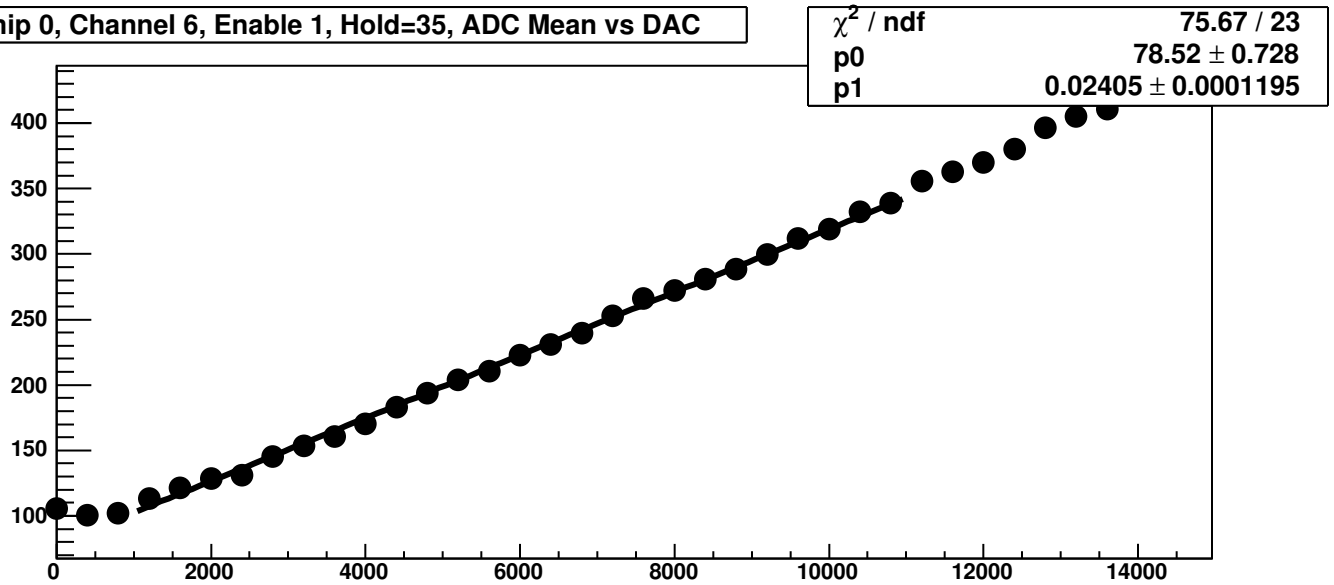

Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1593 / 23
p0 -123.4 ± 1.184
p1 0.1773 ± 0.0002092
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

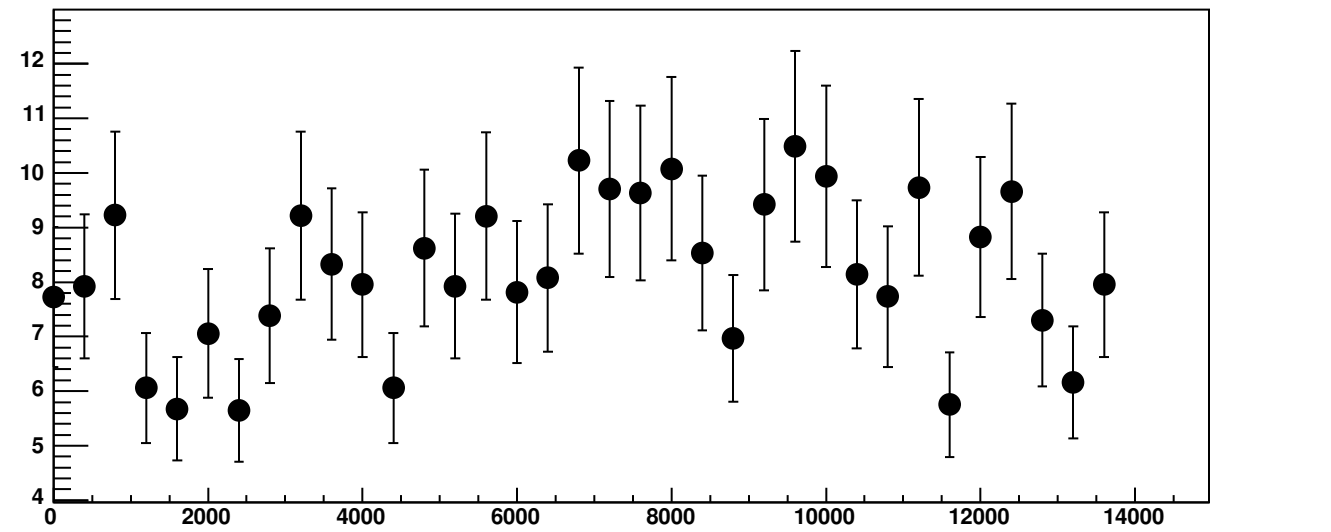

Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

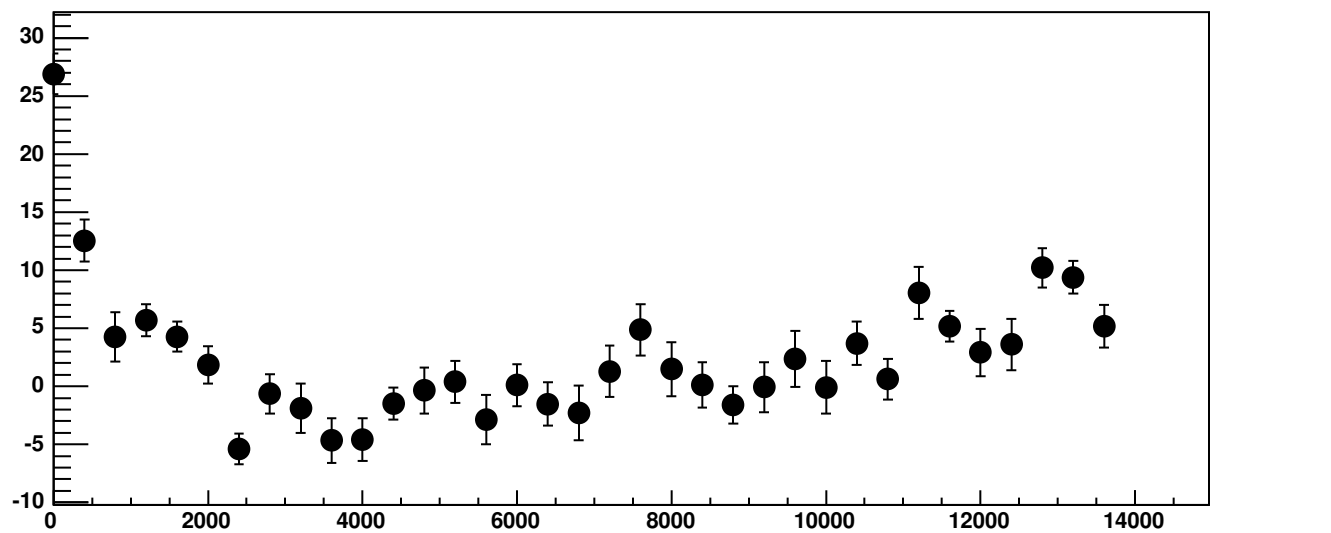
Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

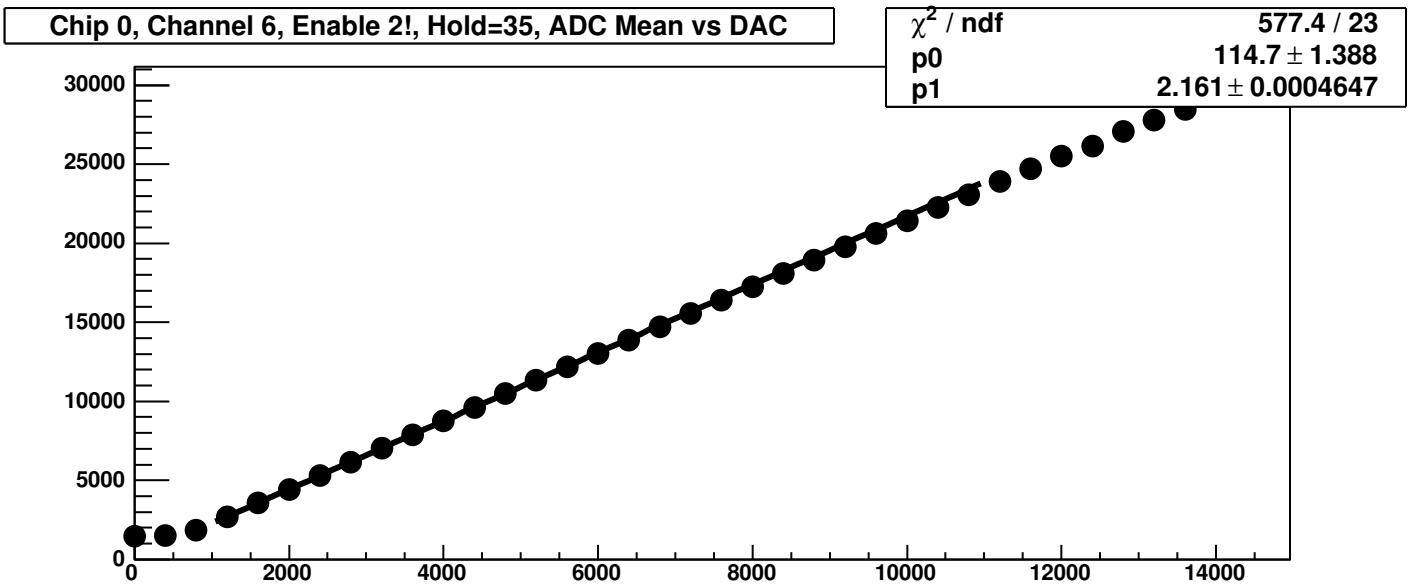


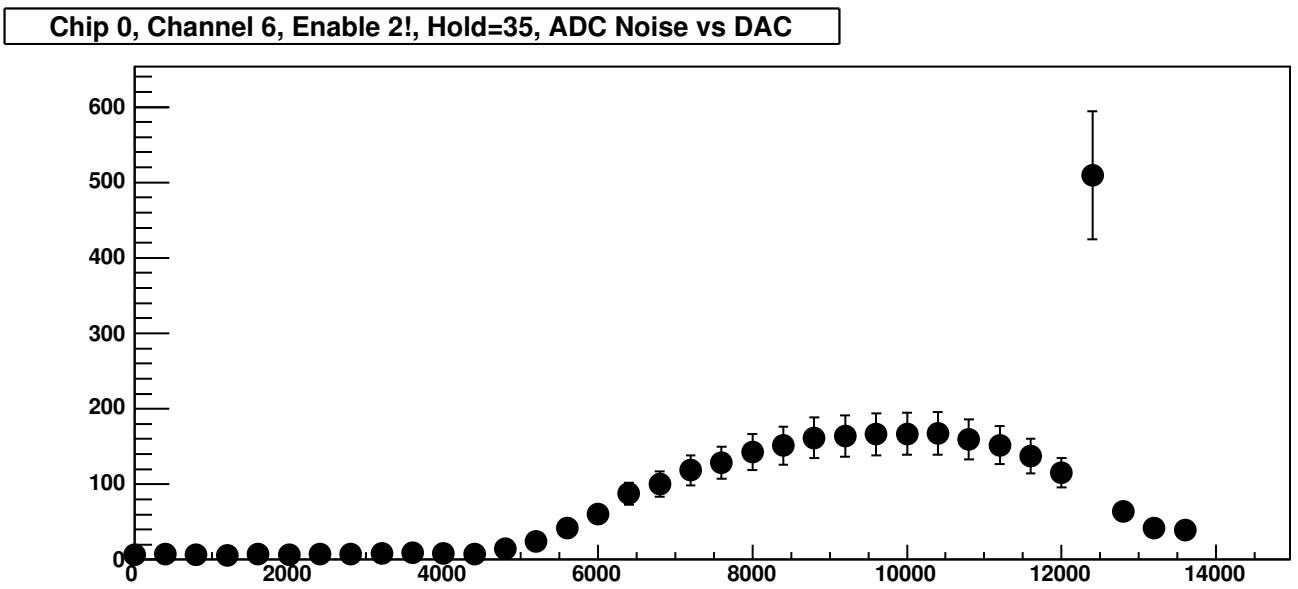
Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

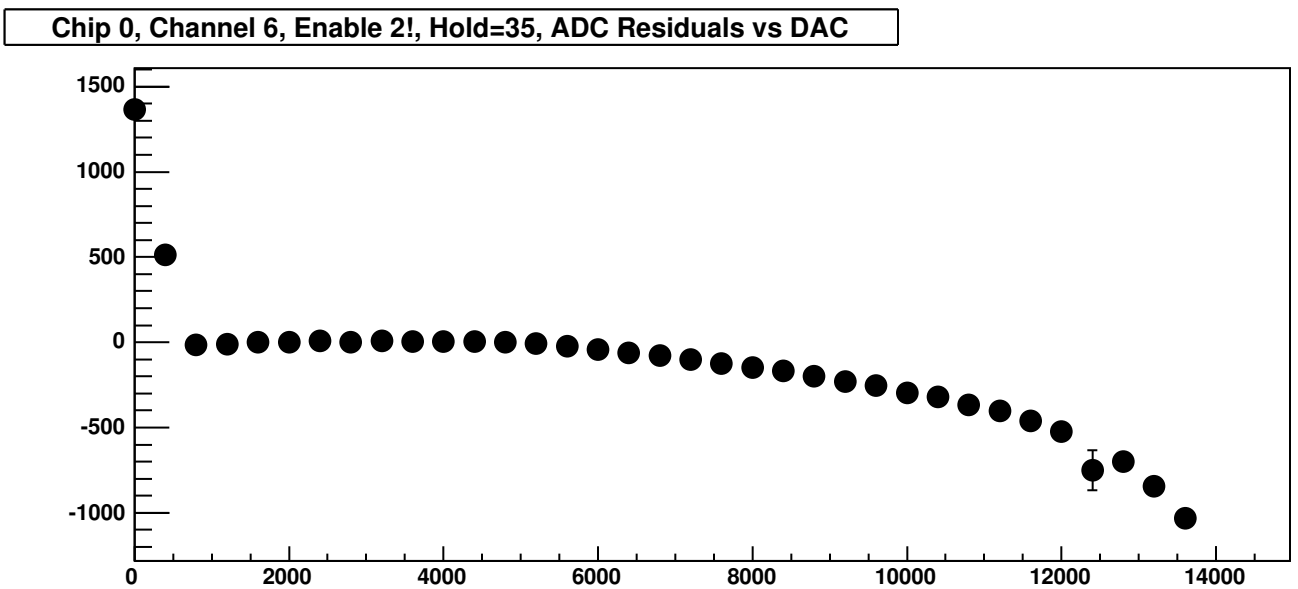


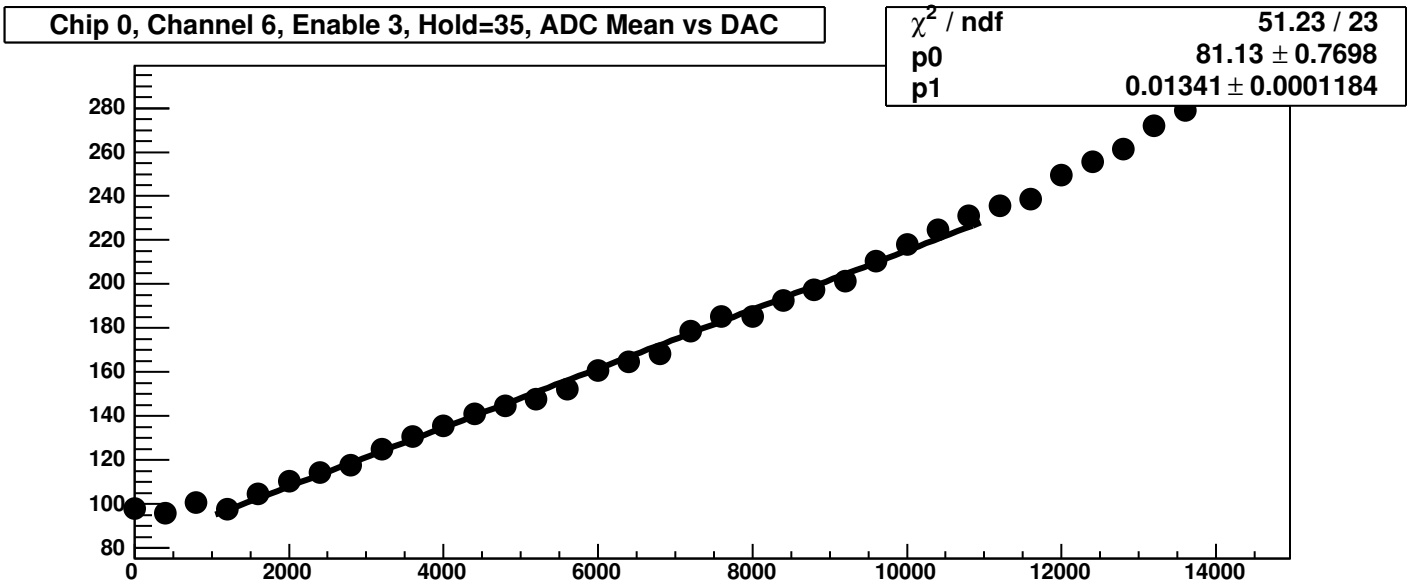
Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

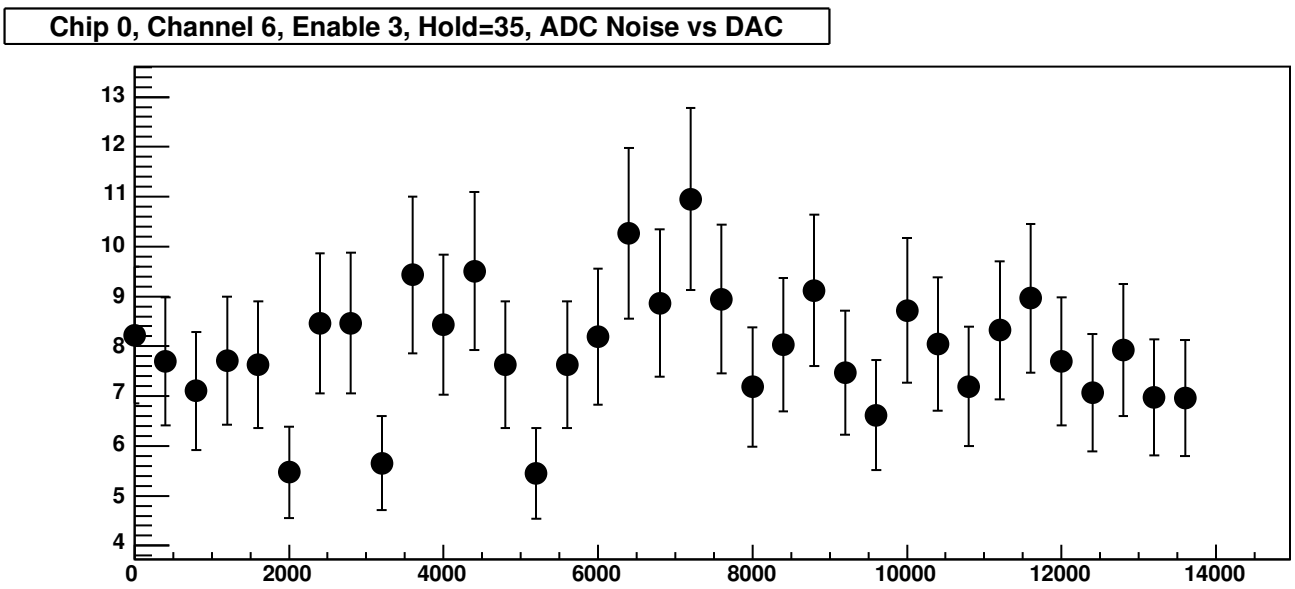


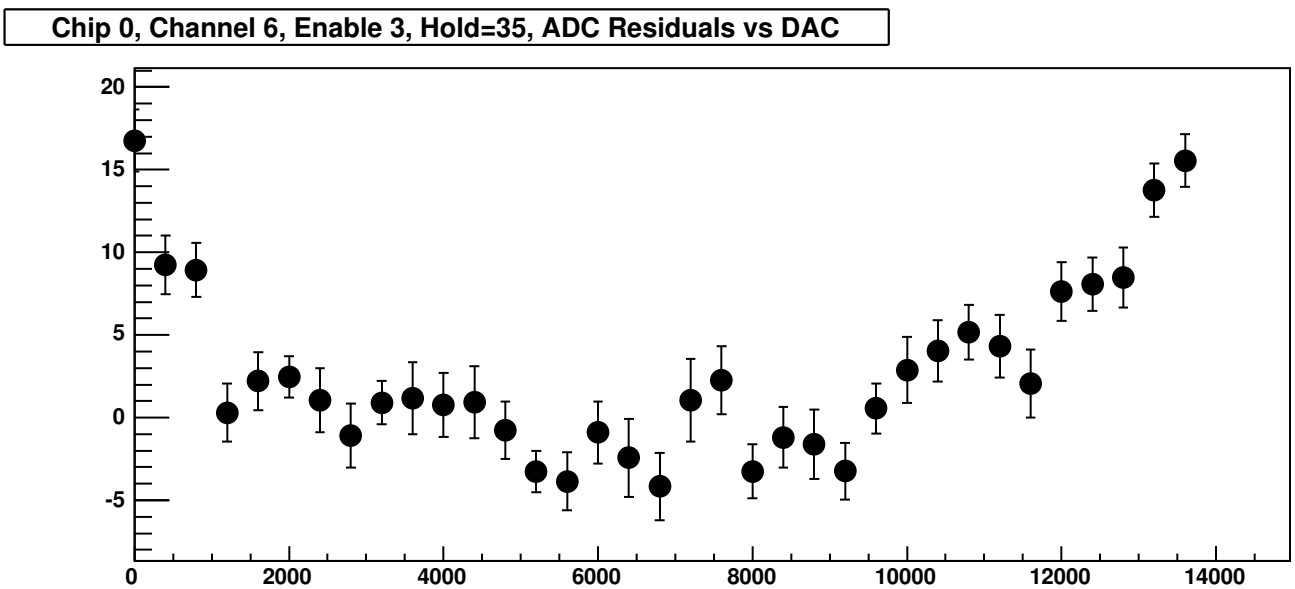

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 577.4 / 23
p0 114.7 ± 1.388
p1 2.161 ± 0.0004647
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

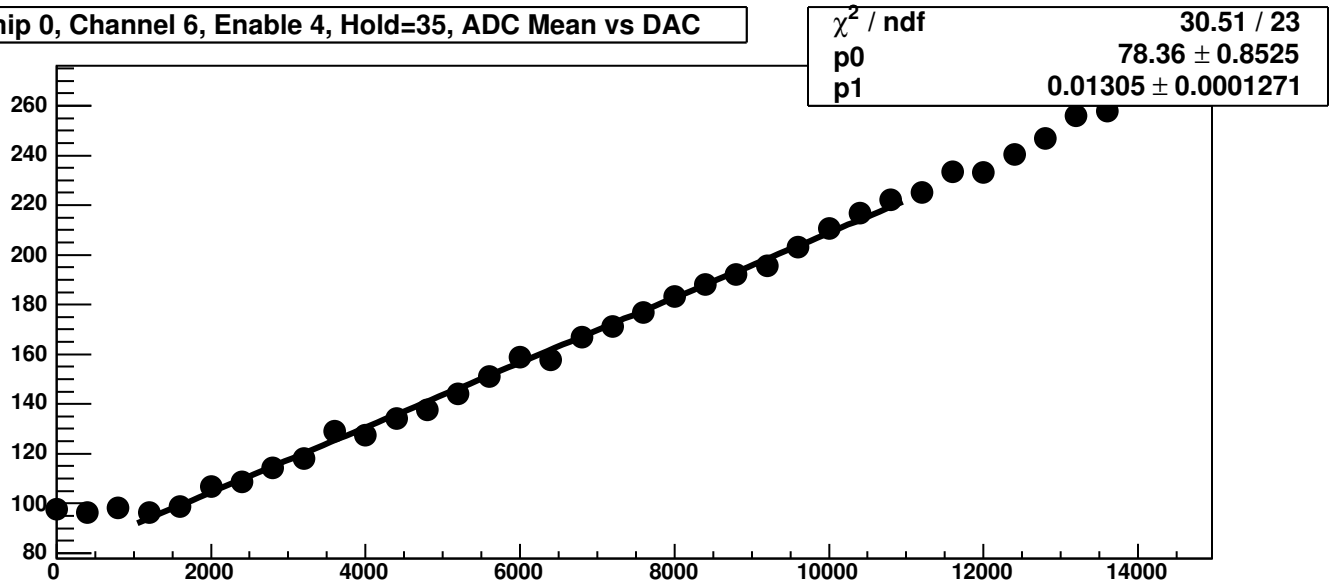

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.23 / 23
p0 81.13 ± 0.7698
p1 0.01341 ± 0.0001184
280
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000

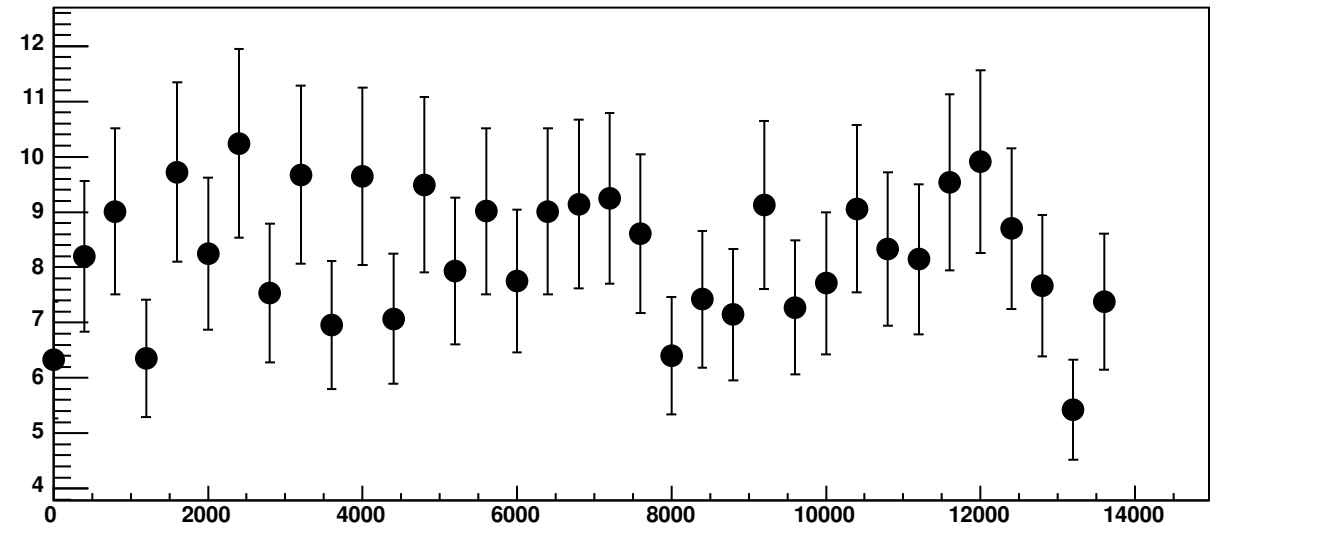

Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

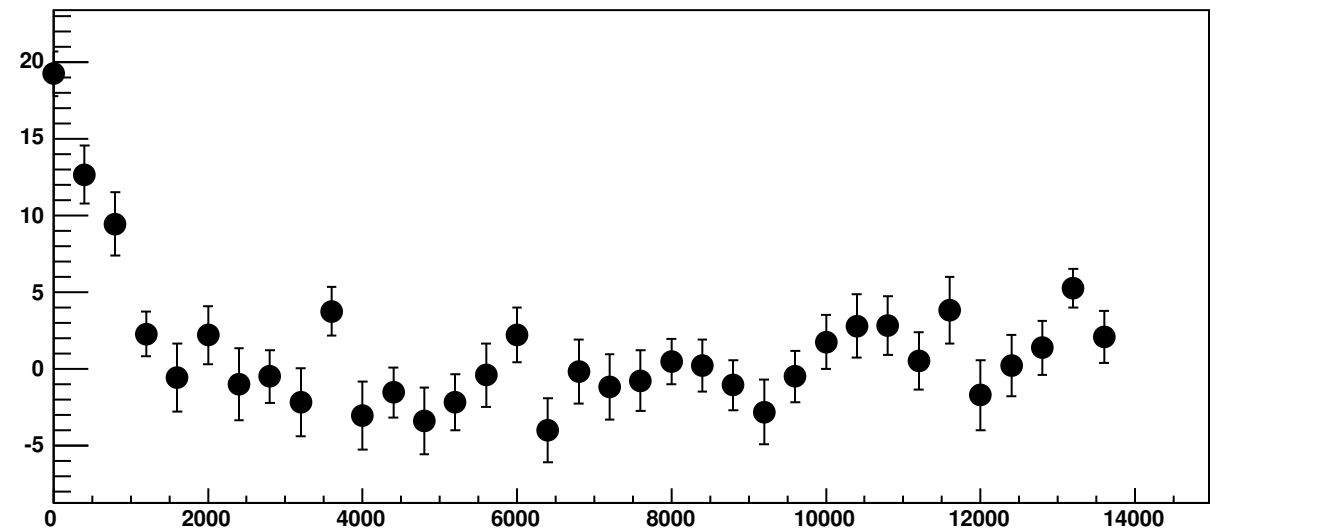
Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

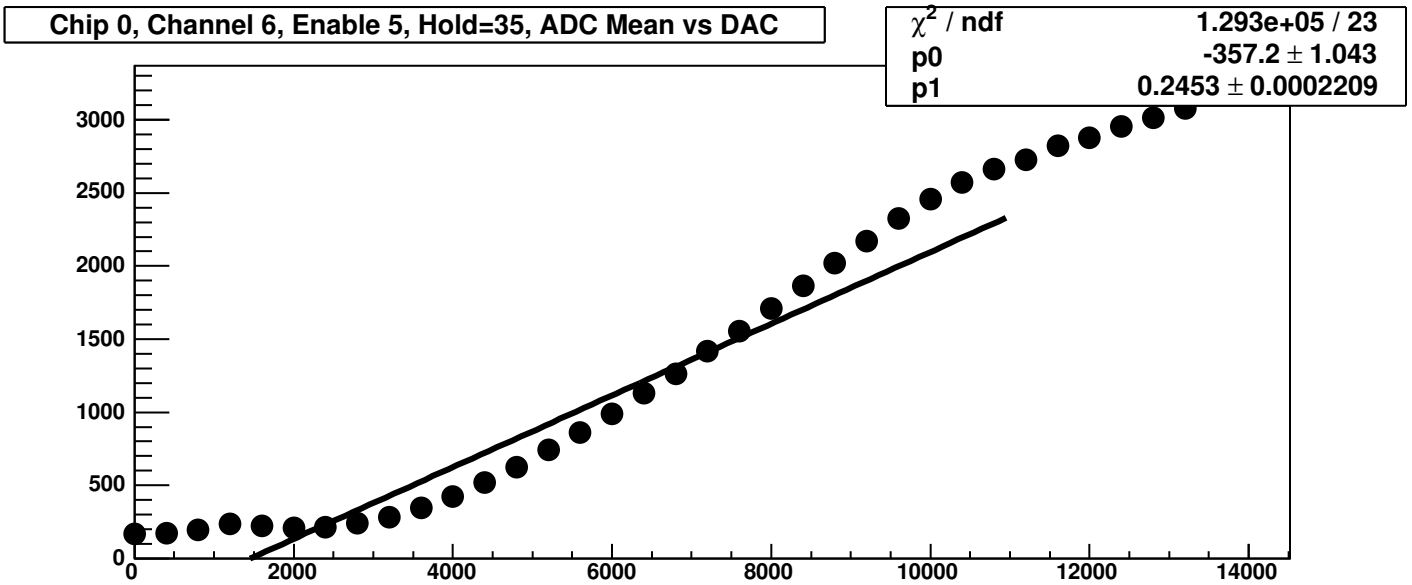


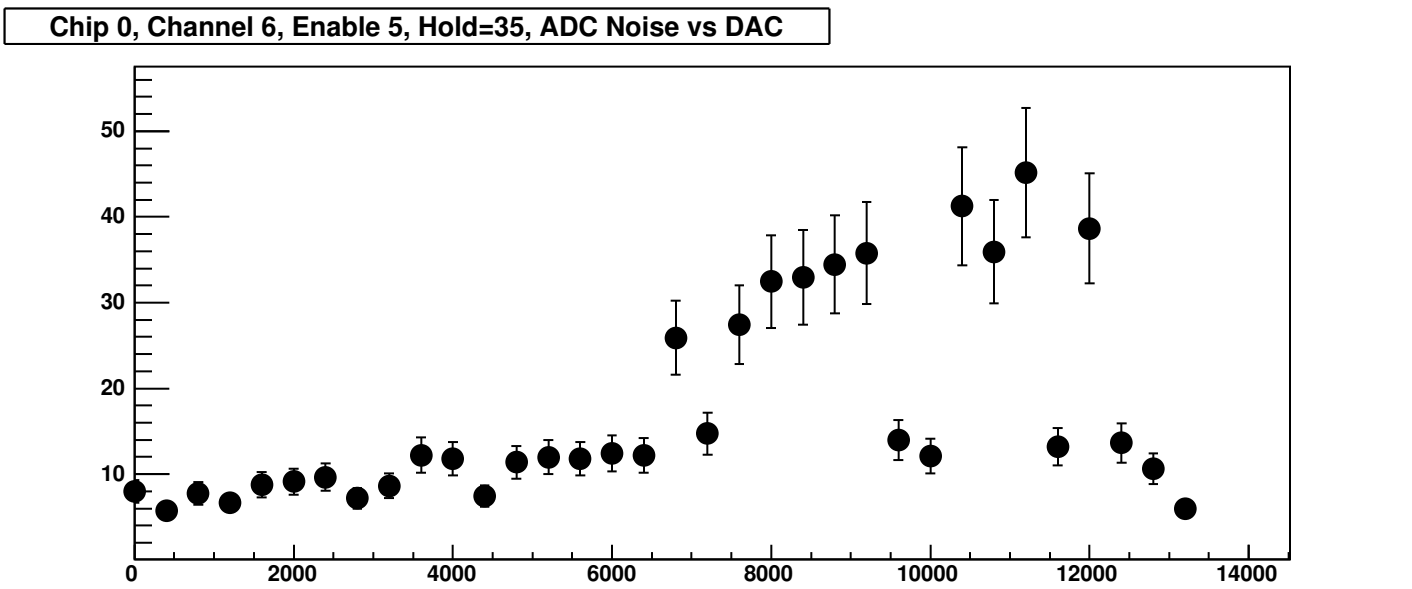
Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

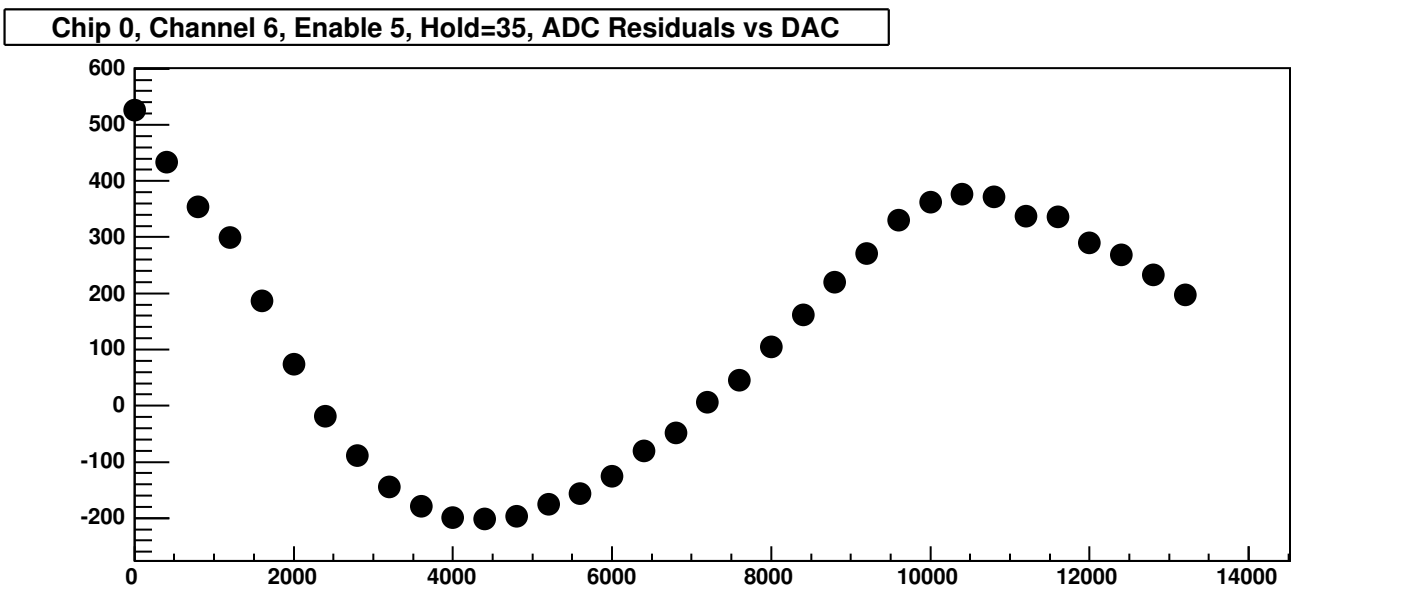


Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

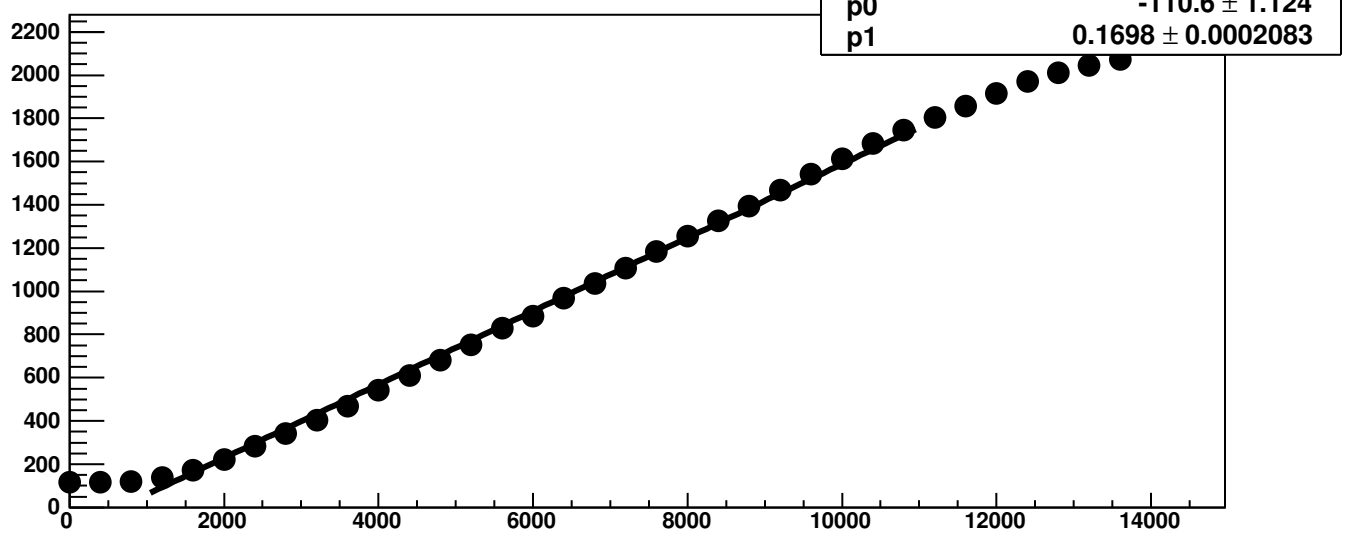



Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.293e+05 / 23
p0 -357.2 ± 1.043
p1 0.2453 ± 0.0002209
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

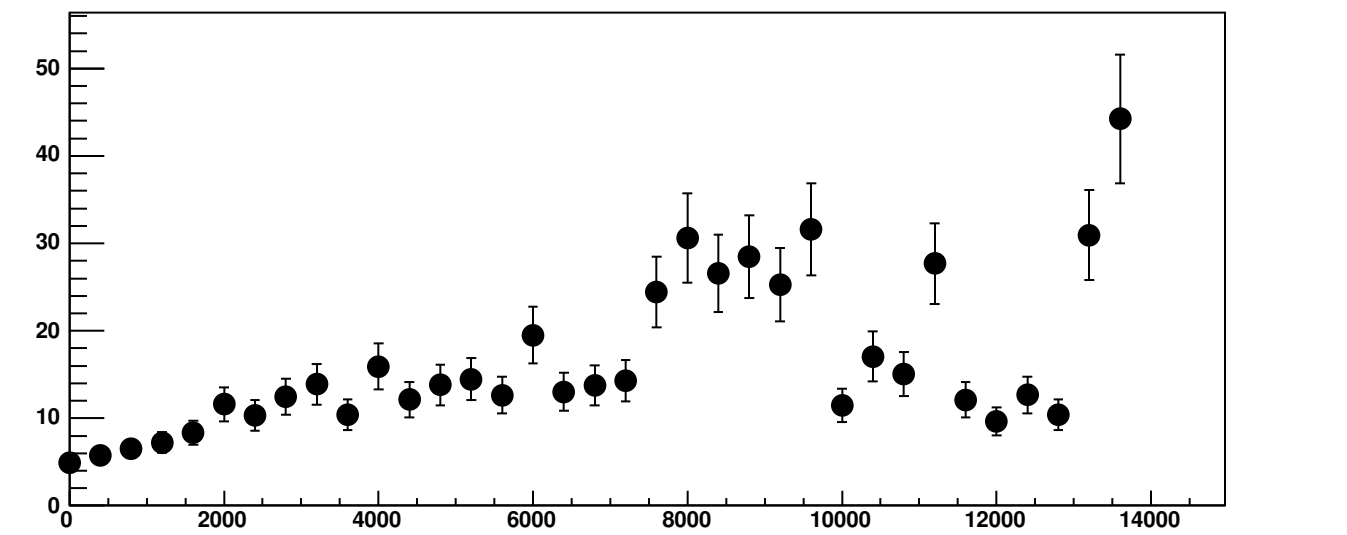

Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

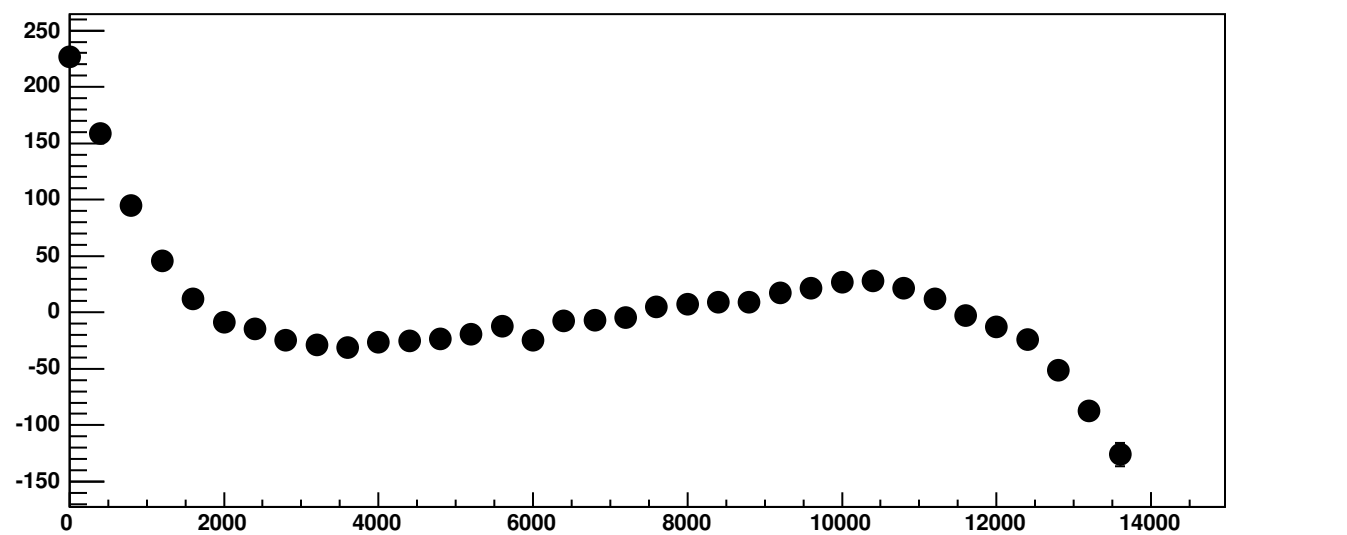
Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



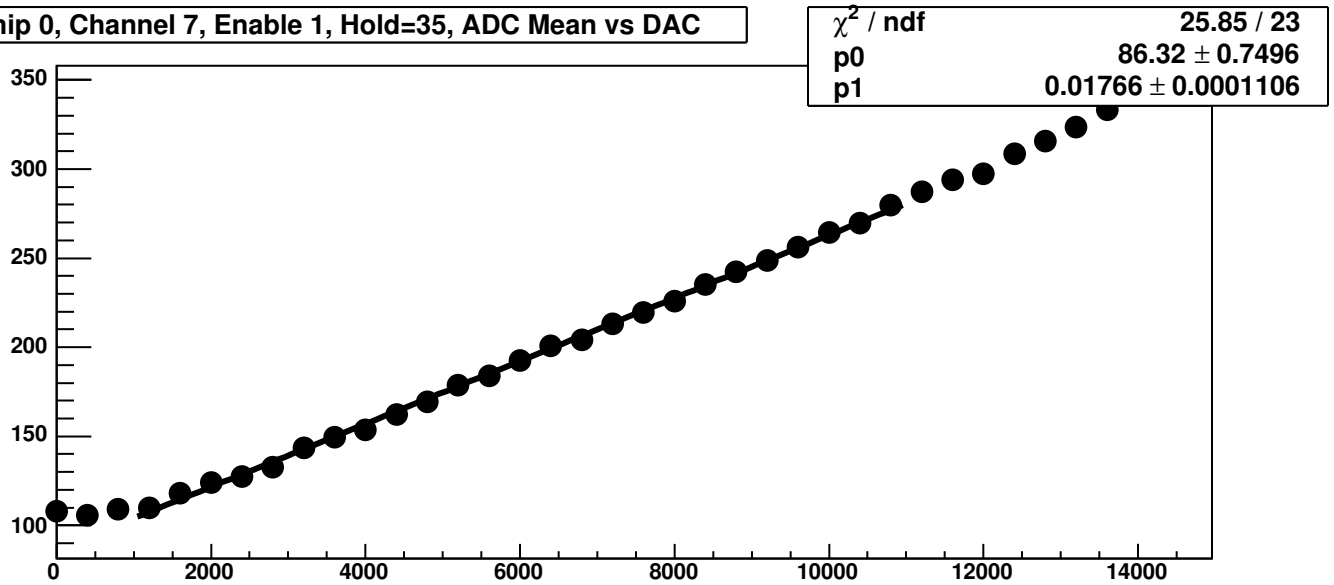
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



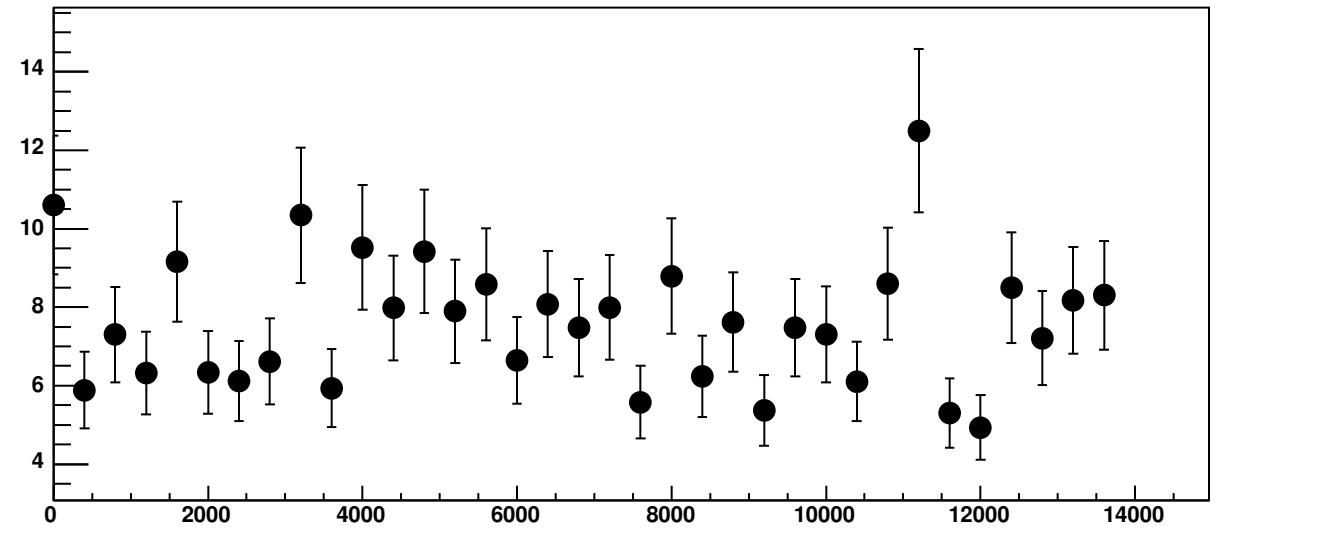
Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



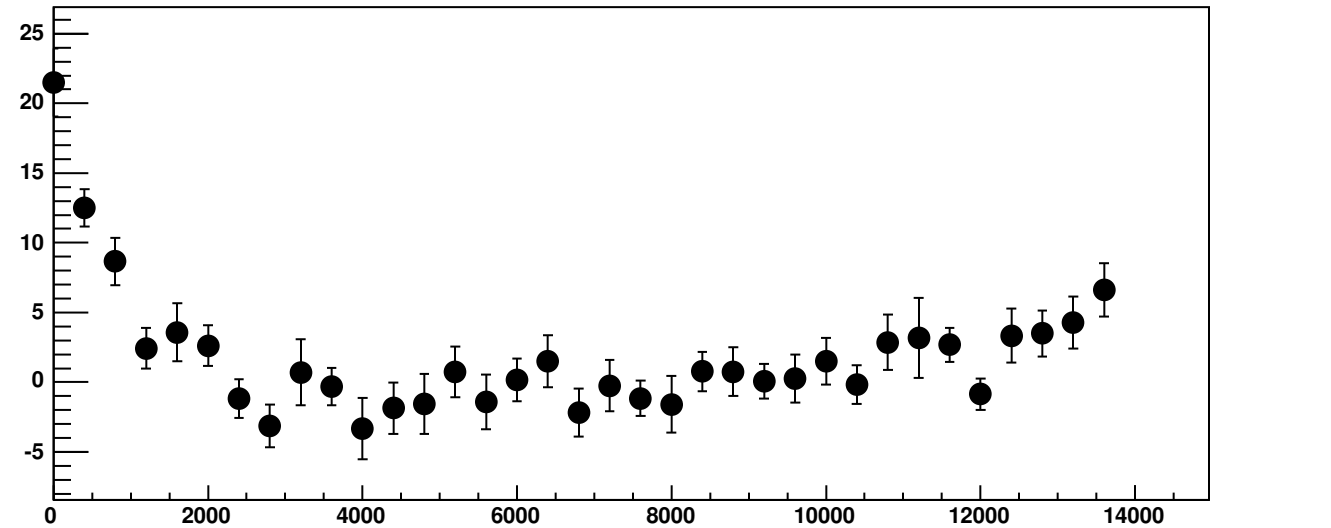
Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

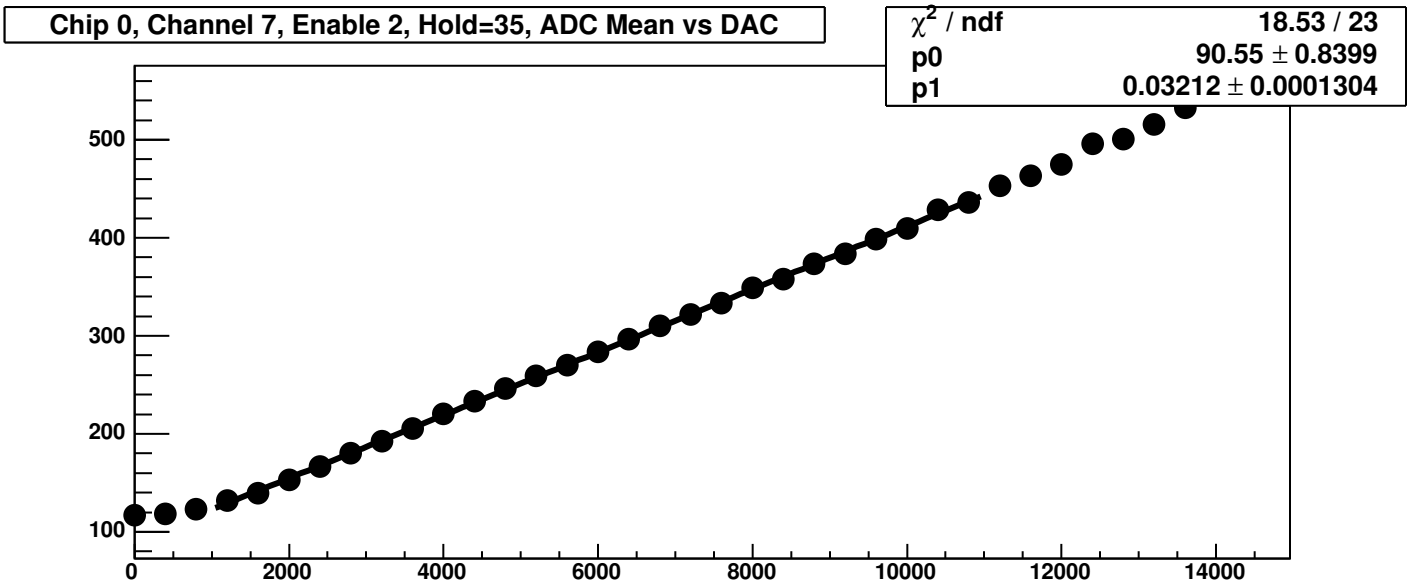


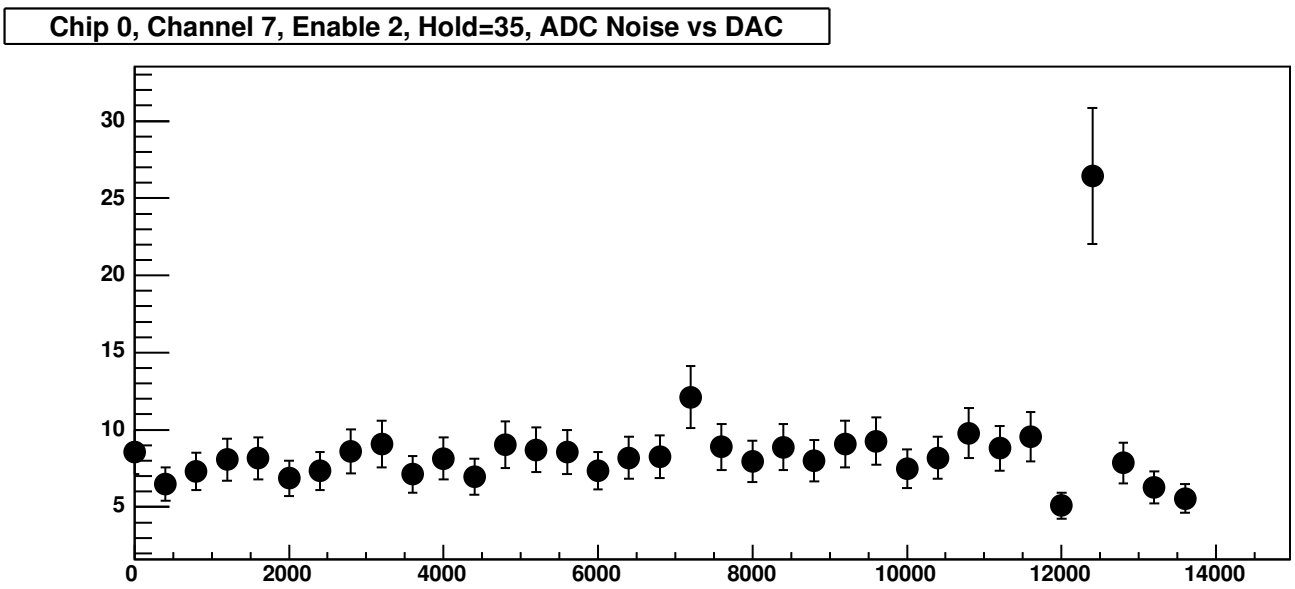
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

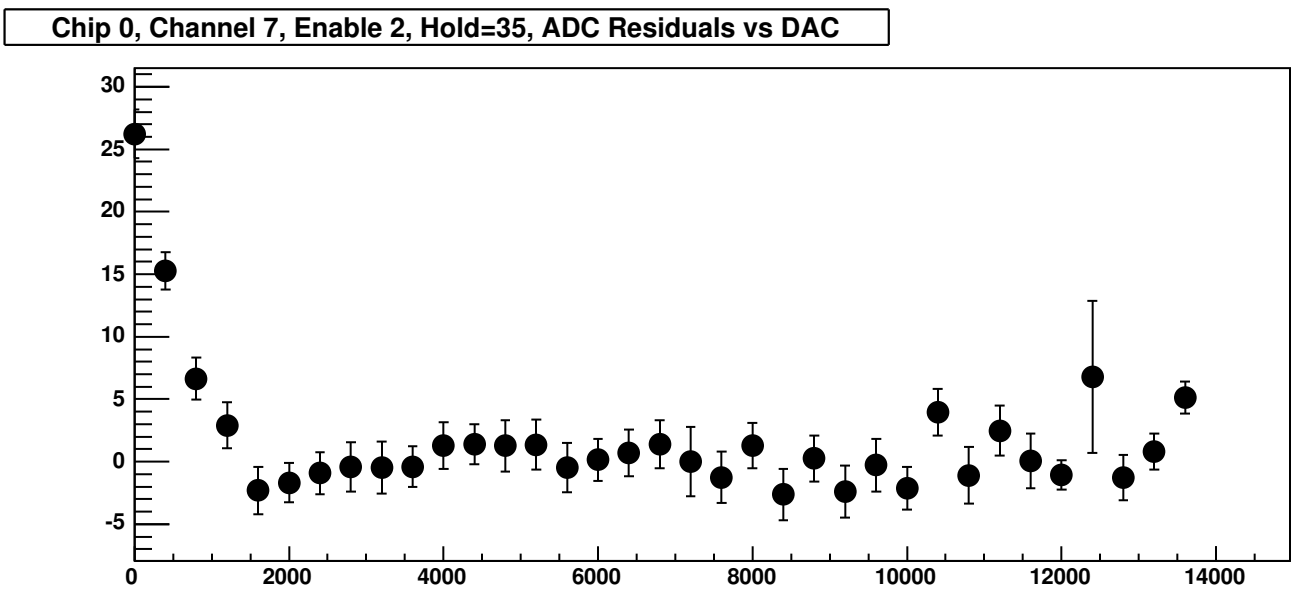


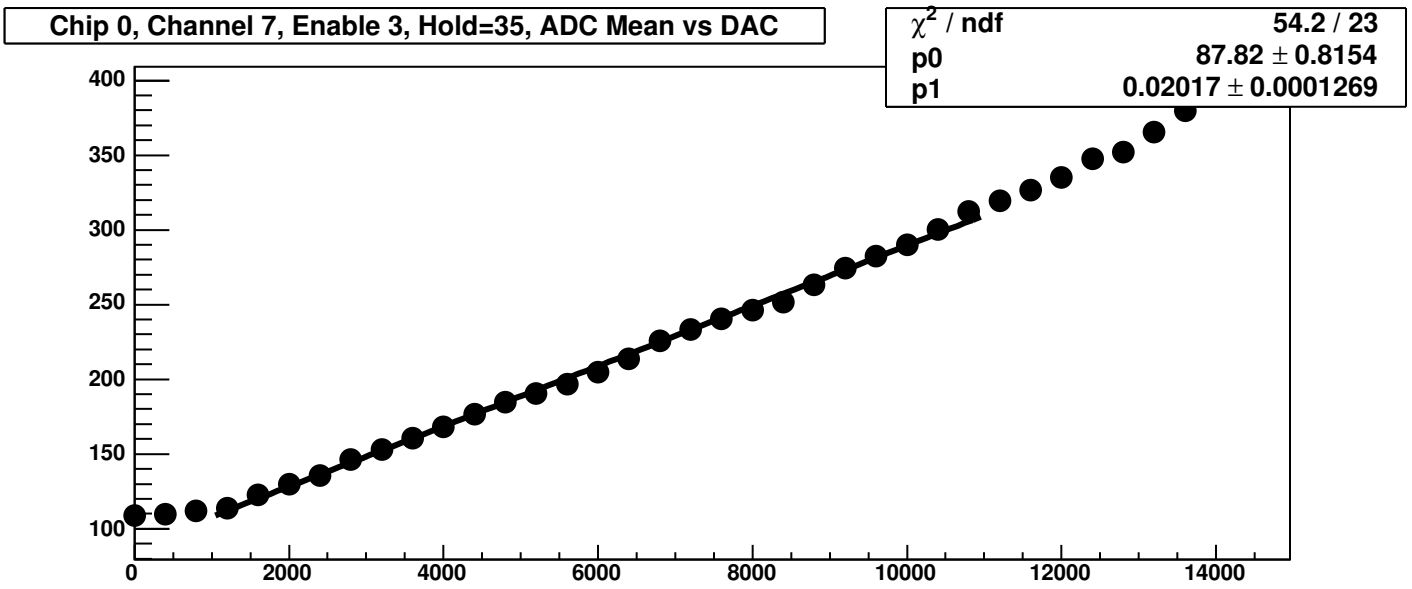
Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

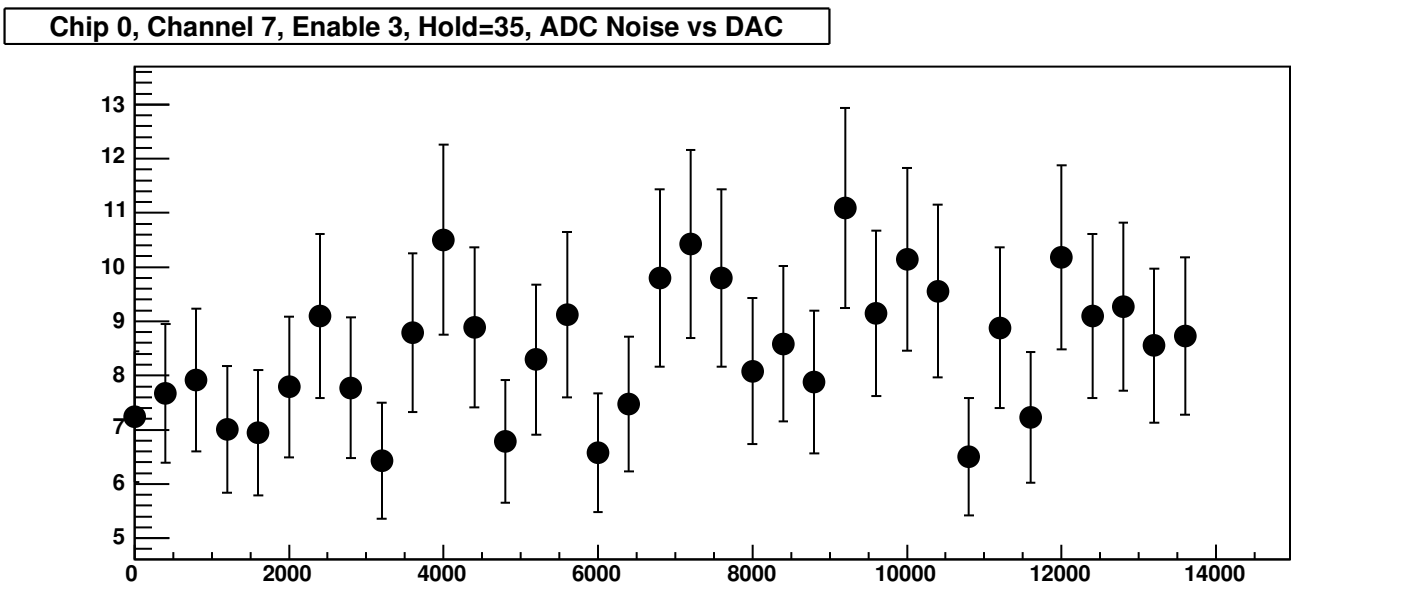


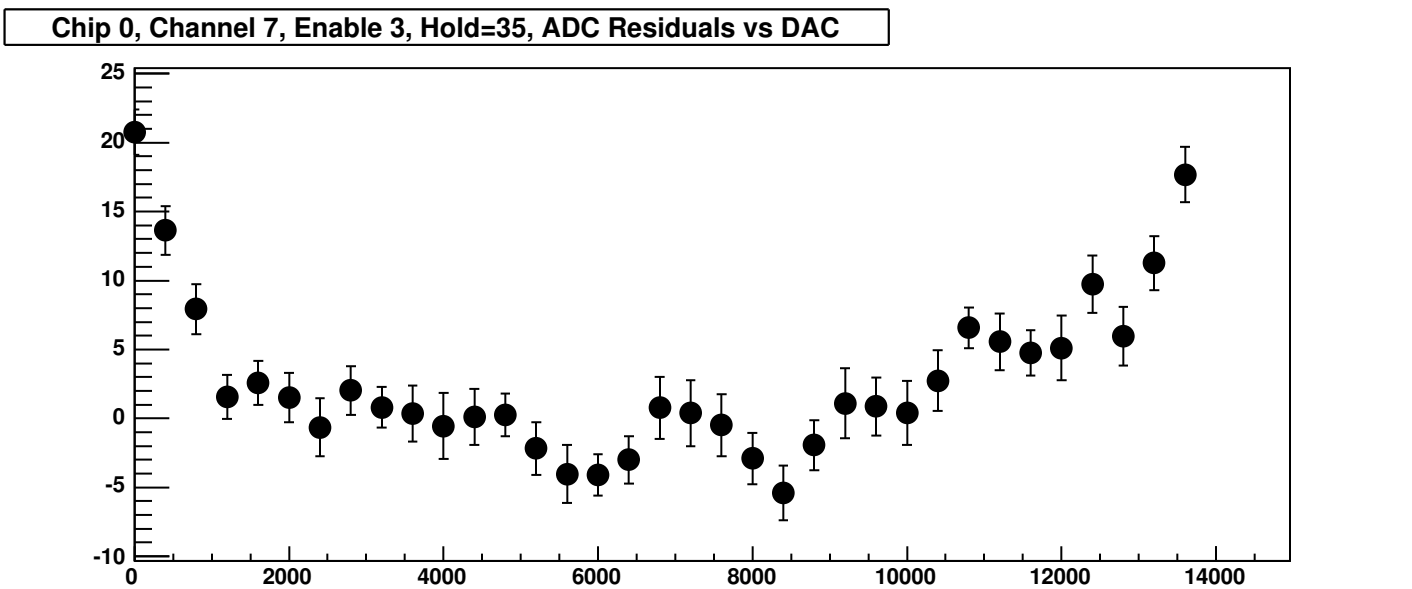

Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.53 / 23
p0 90.55 ± 0.8399
p1 0.03212 ± 0.0001304
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

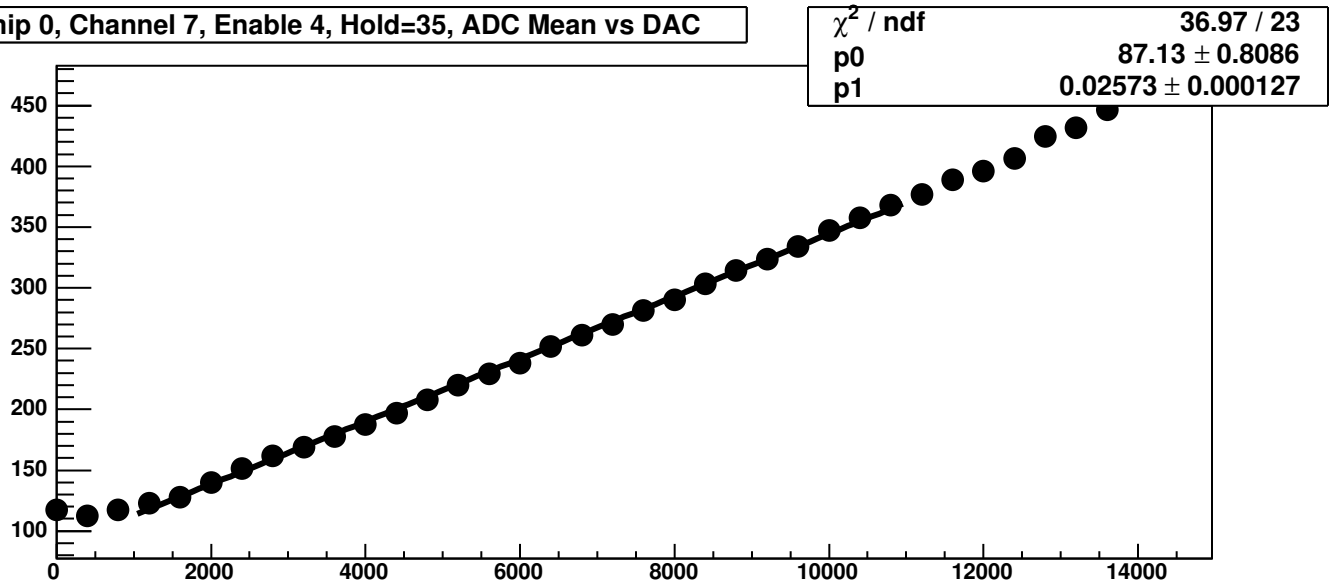

Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 54.2 / 23
p0 87.82 ± 0.8154
p1 0.02017 ± 0.0001269
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

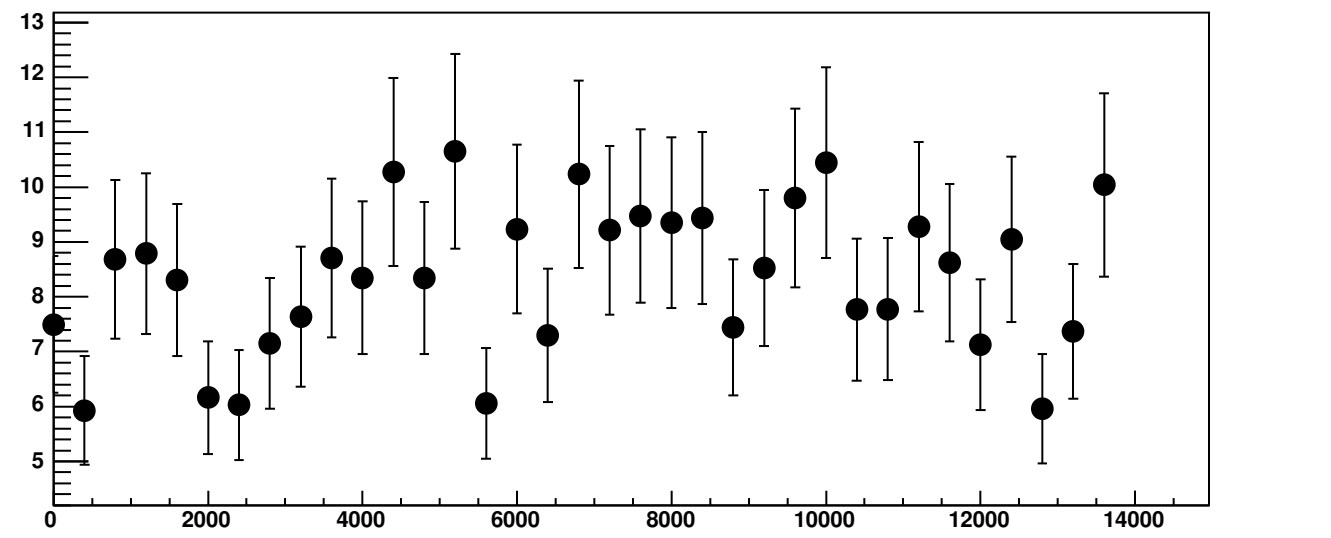

Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

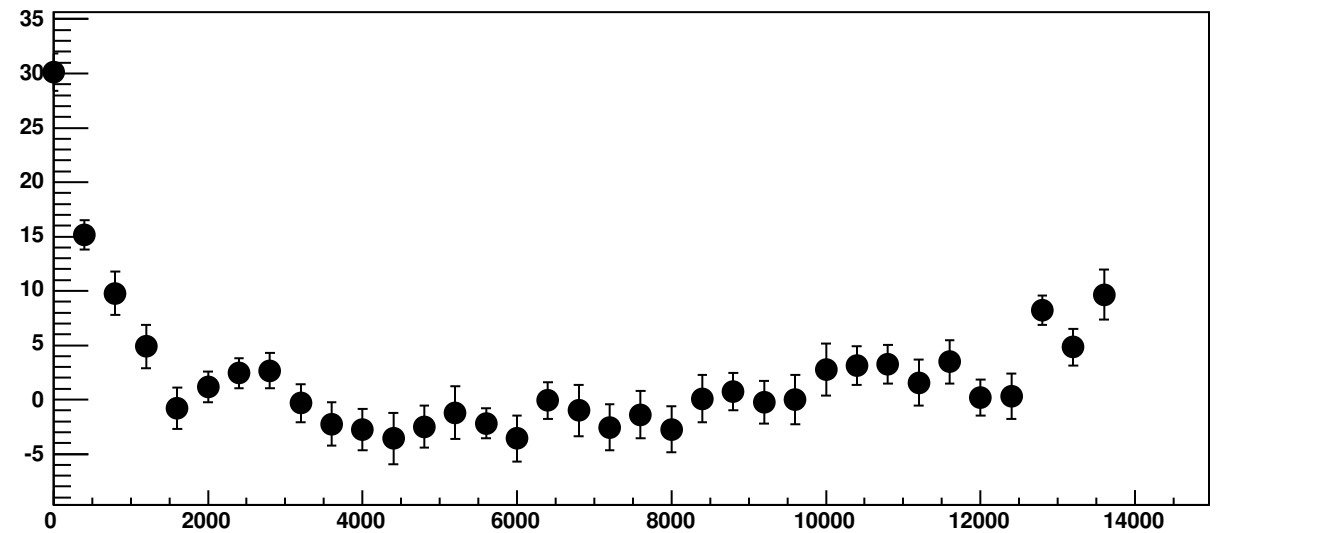
Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

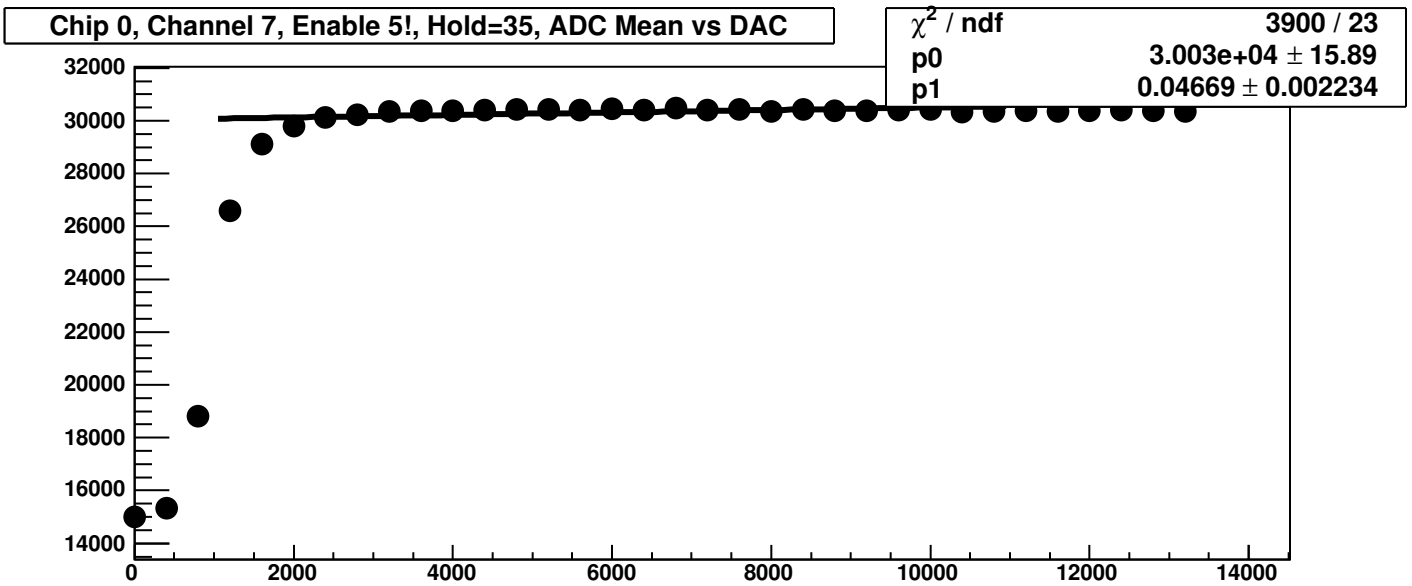


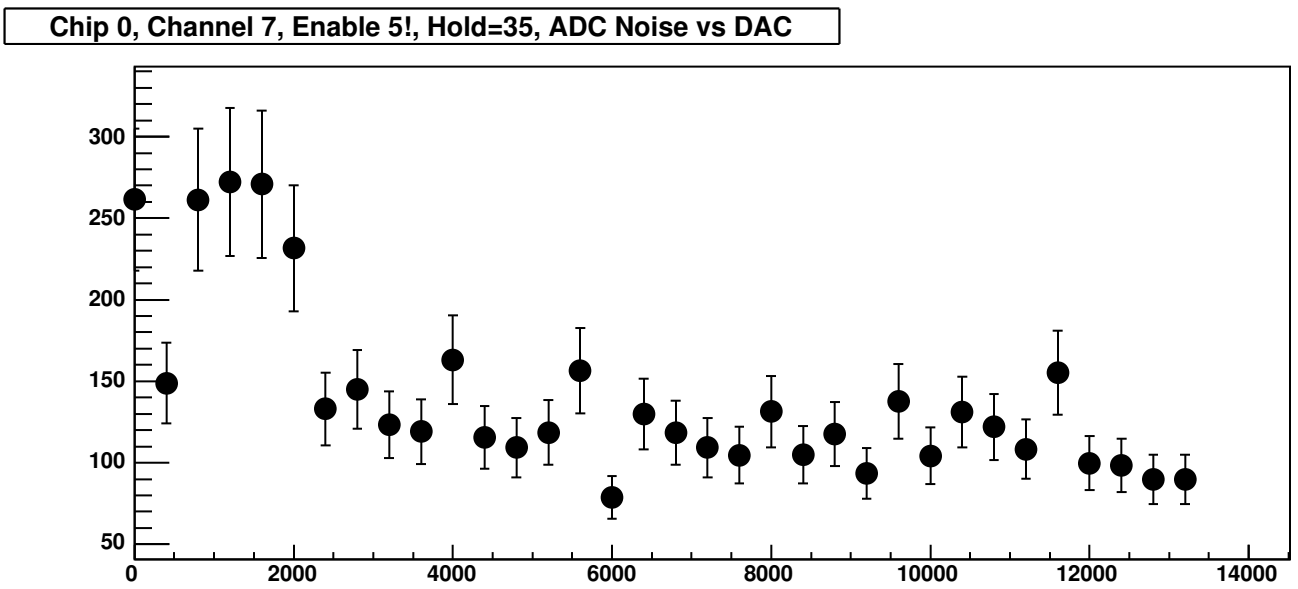
Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

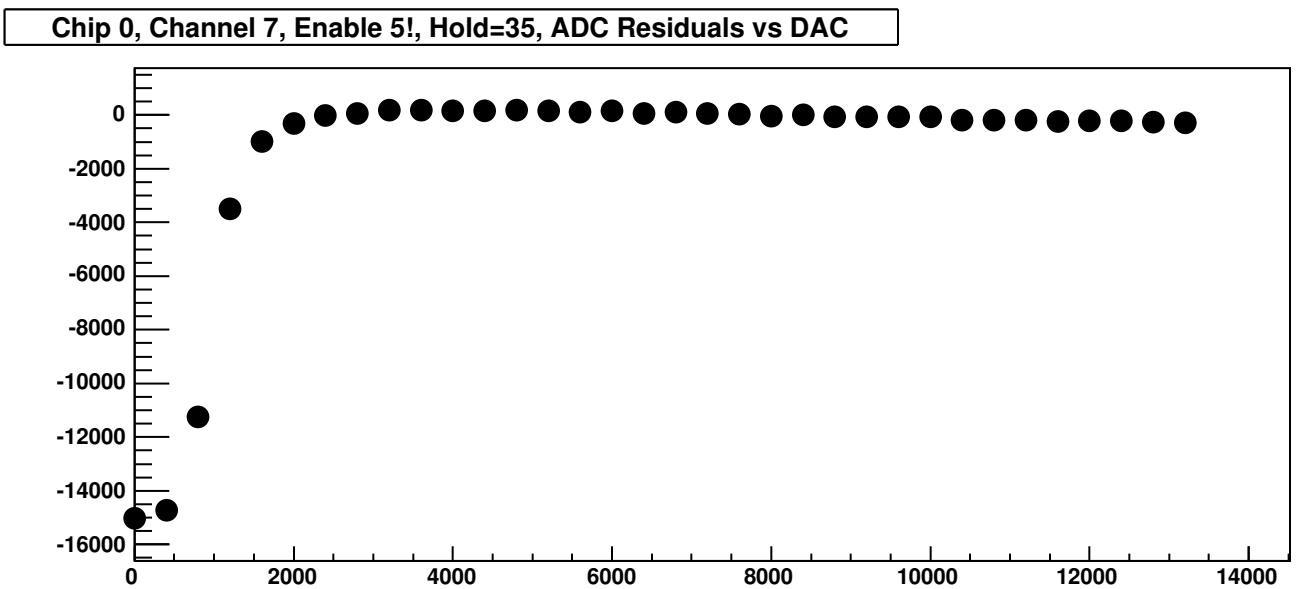


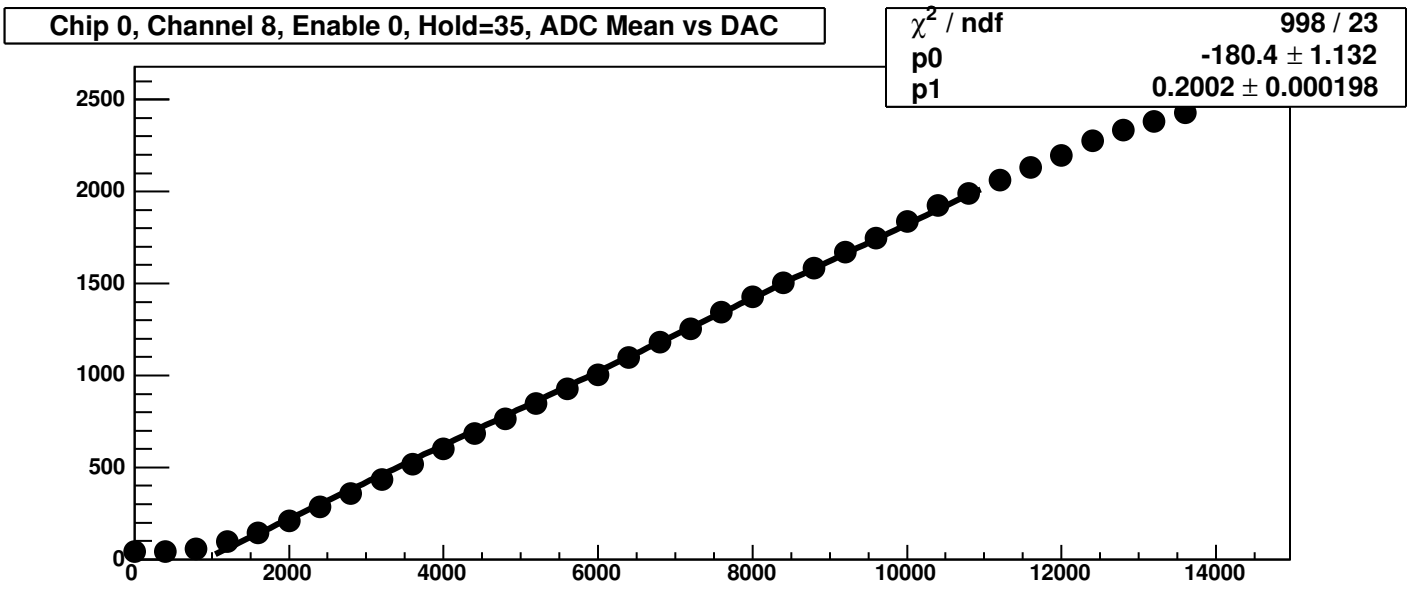
Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

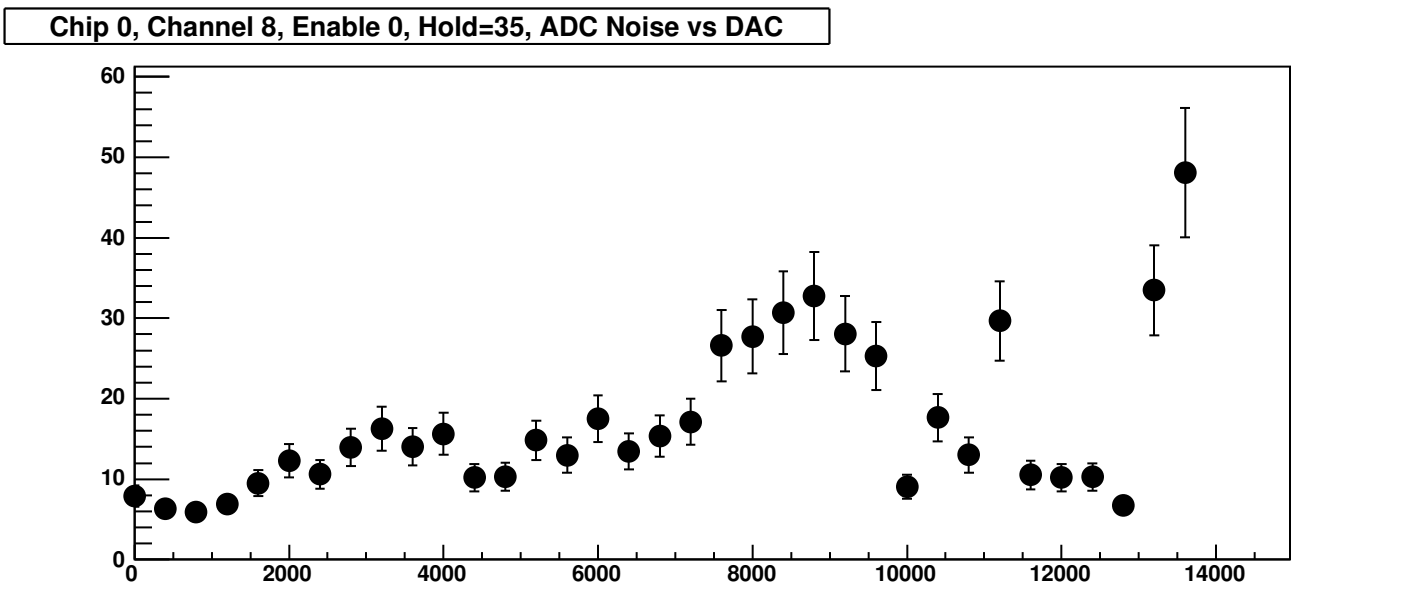


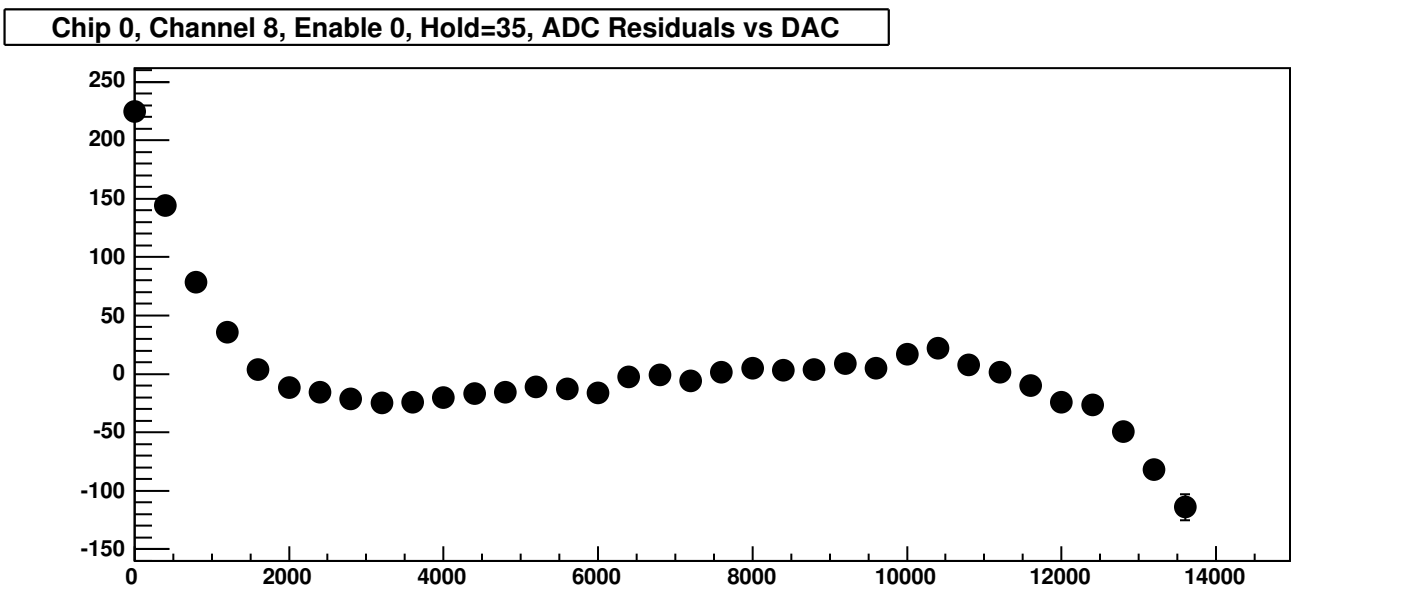

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3900 / 23
p0 3.003e+04 ± 15.89
p1 0.04669 ± 0.002234
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

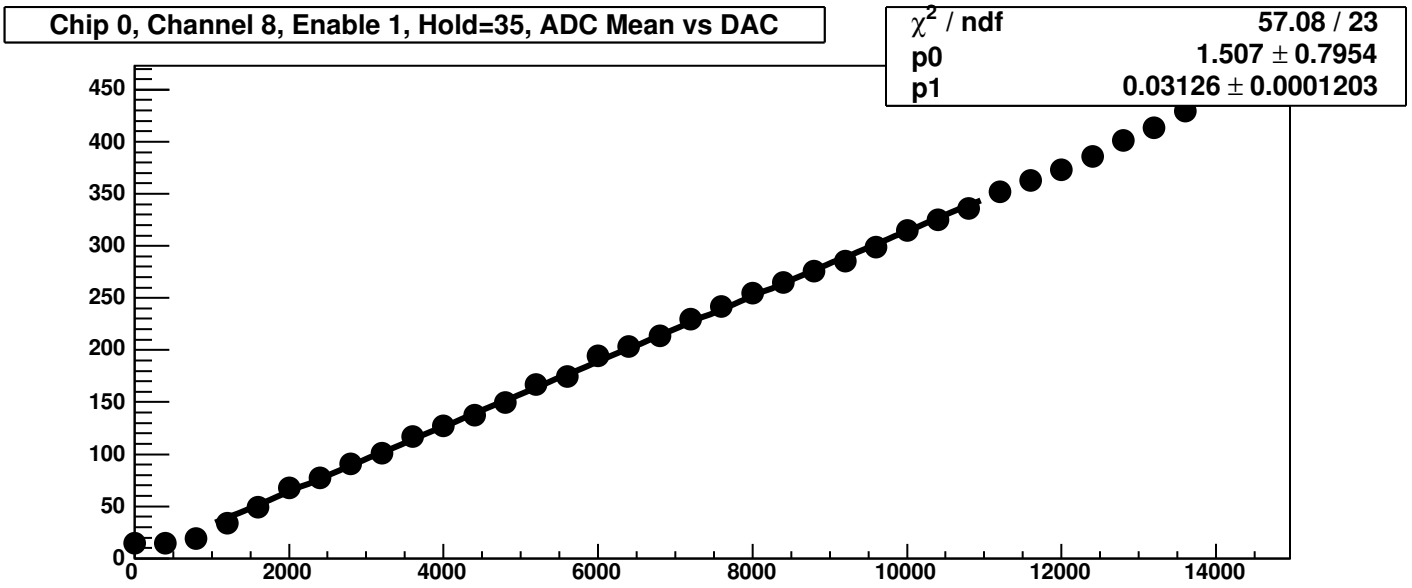

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

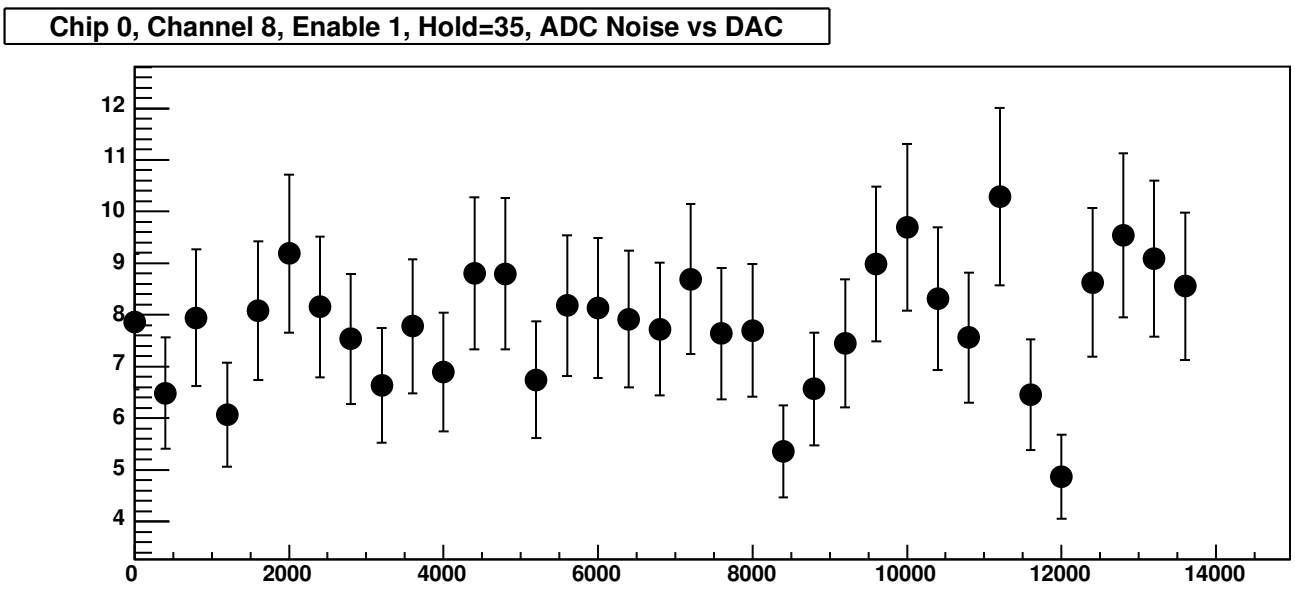

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

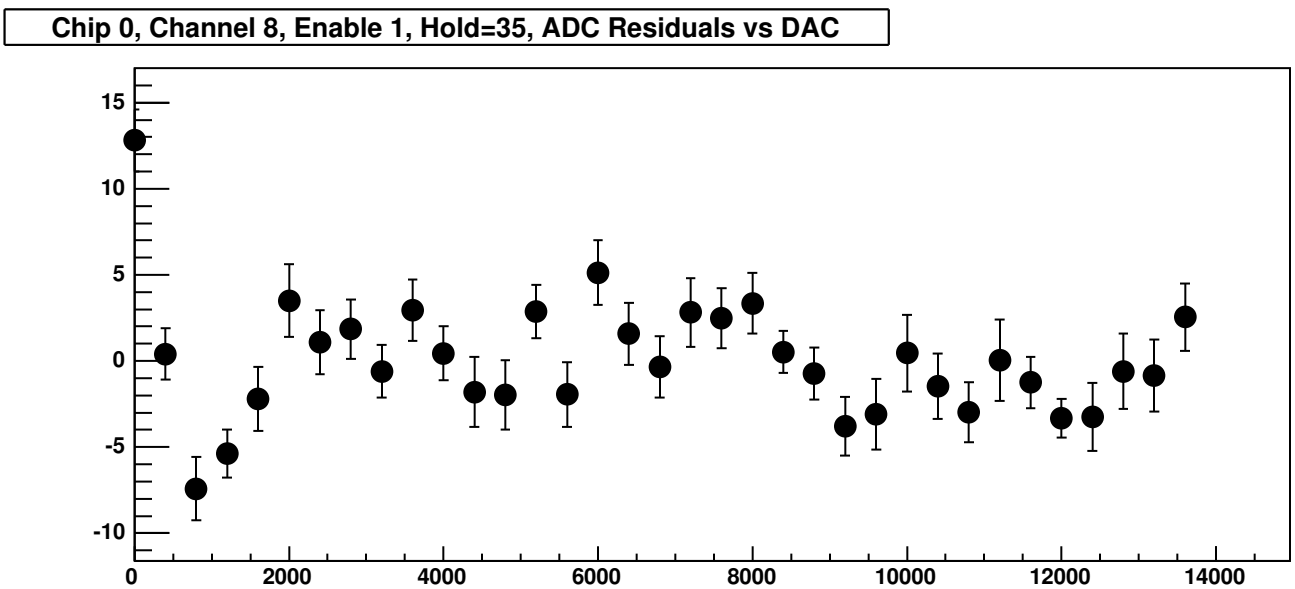

Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 998 / 23
p0 -180.4 ± 1.132
p1 0.2002 ± 0.000198
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

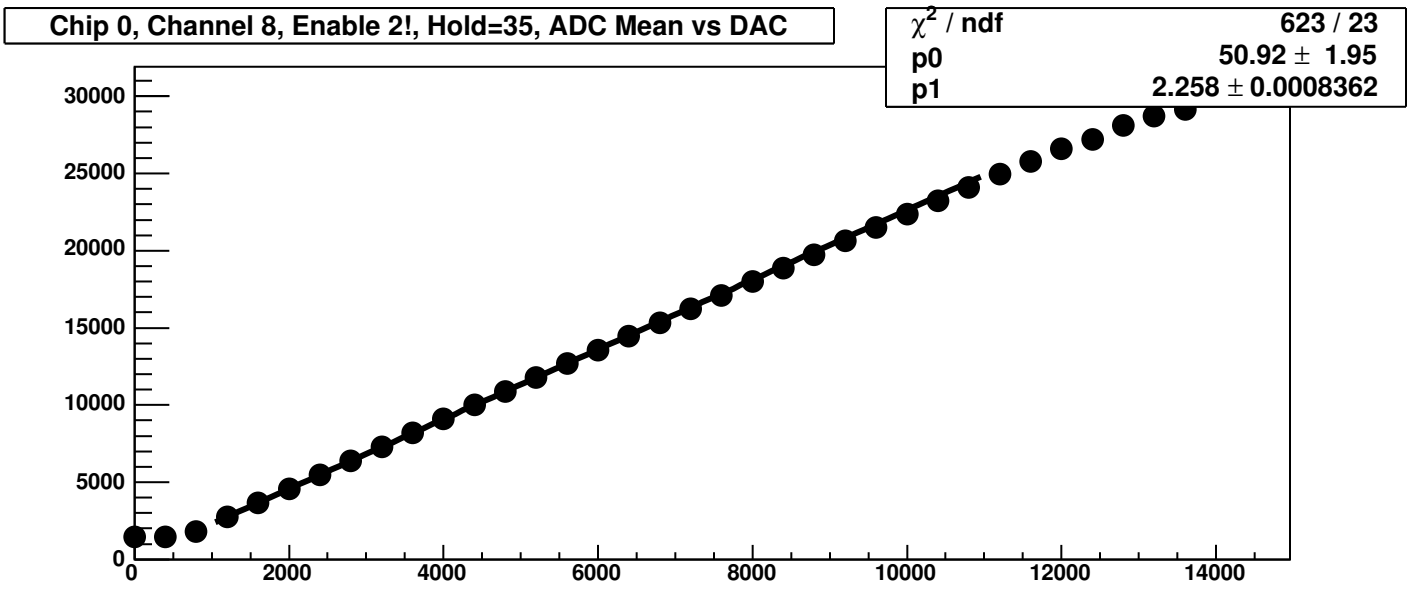

Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

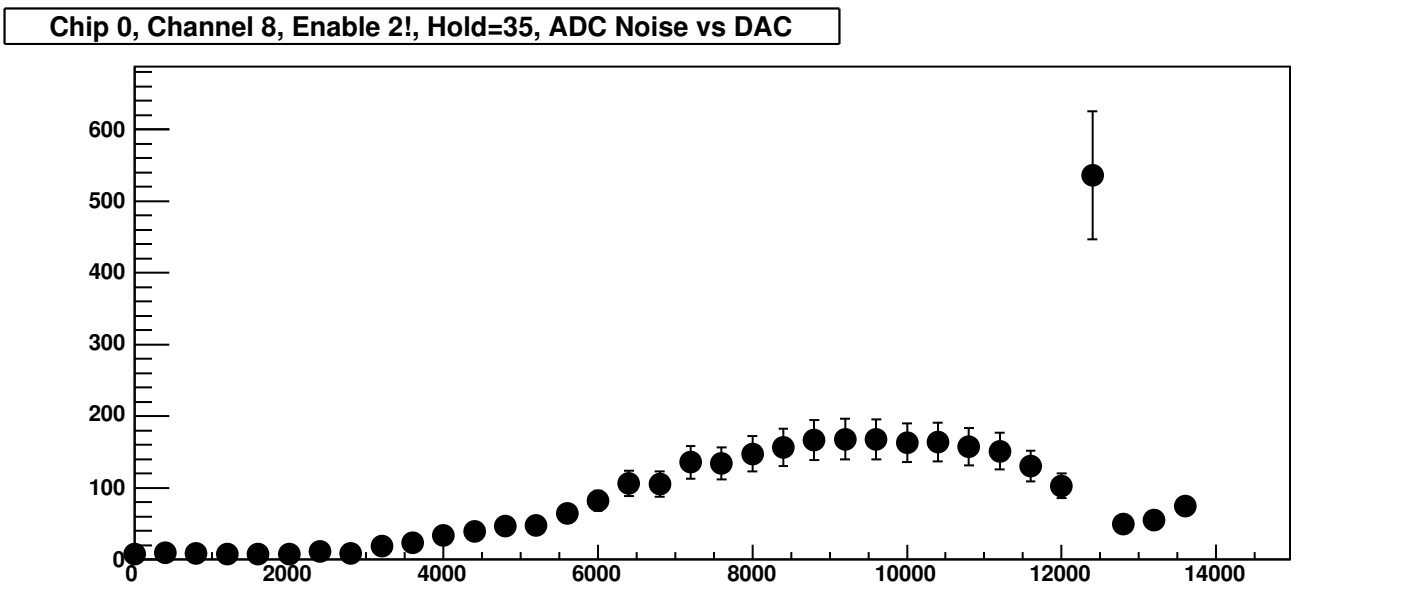

Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

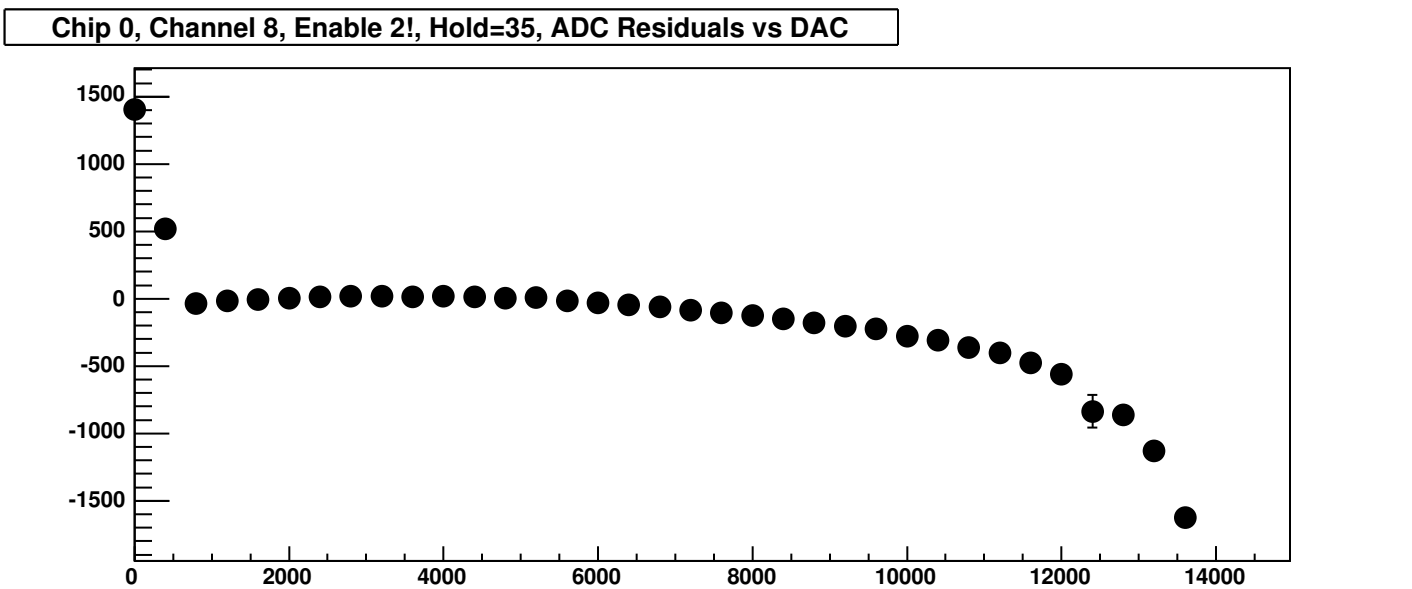

Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 57.08 / 23
p0 1.507 ± 0.7954
p1 0.03126 ± 0.0001203
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

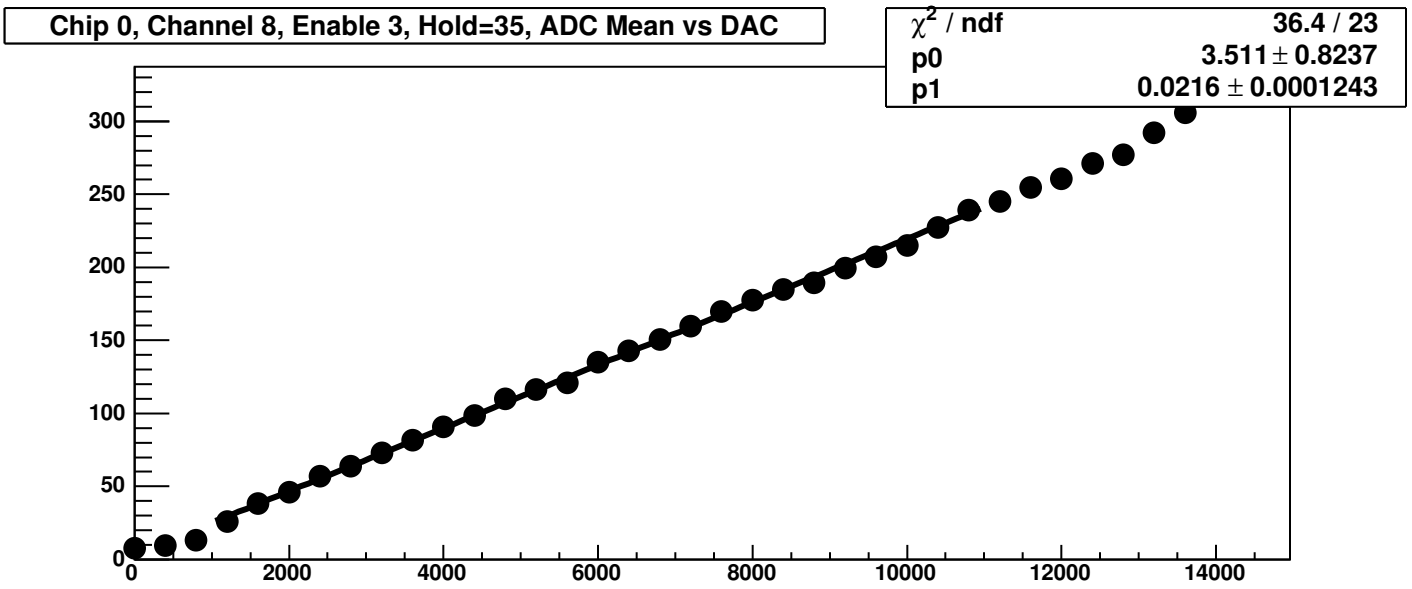

Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

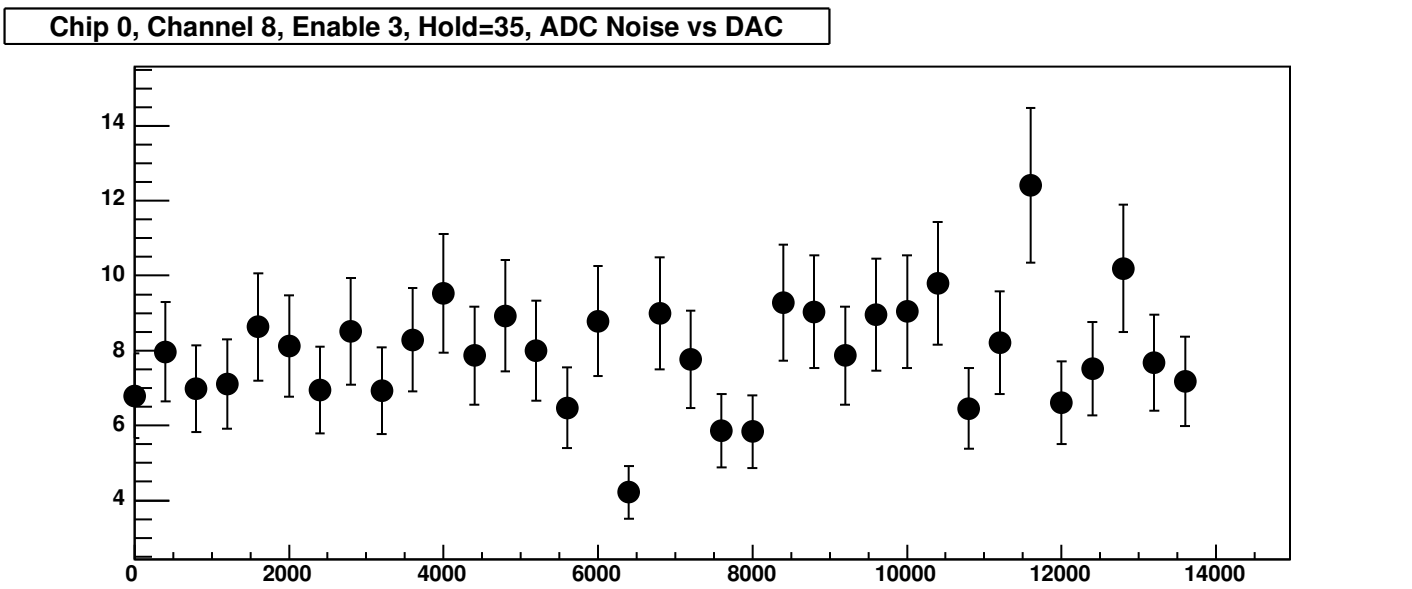

Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

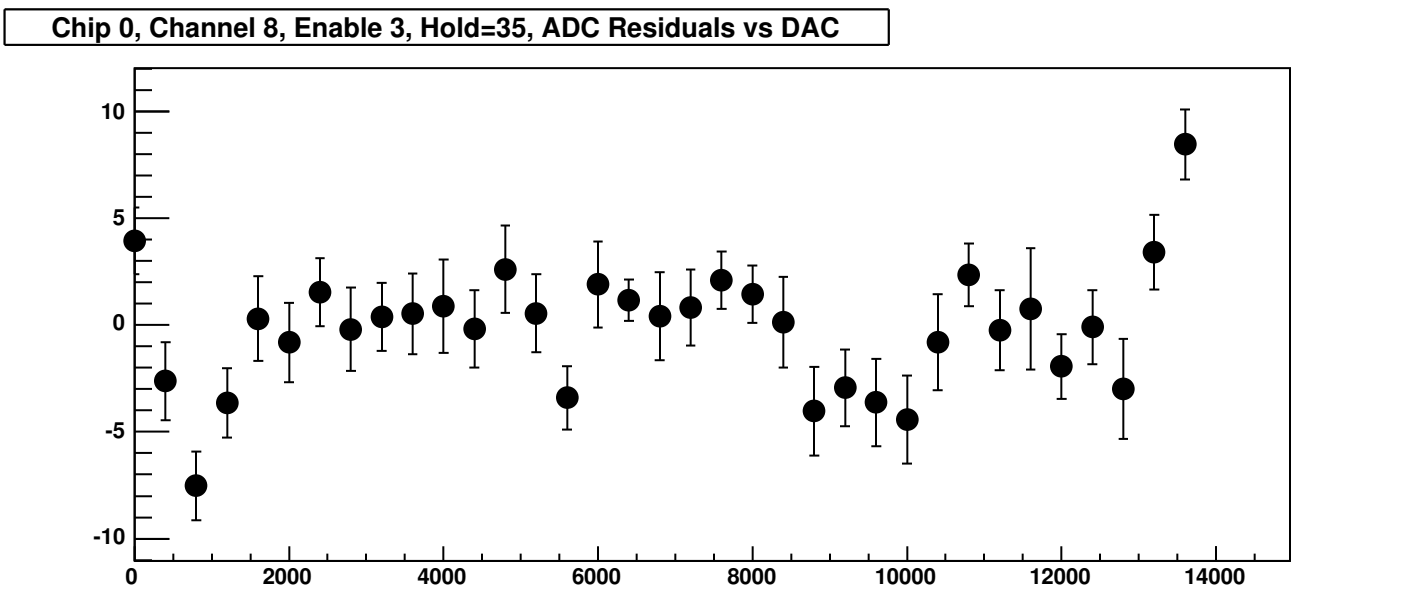

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 623 / 23
p0 50.92 ± 1.95
p1 2.258 ± 0.0008362
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

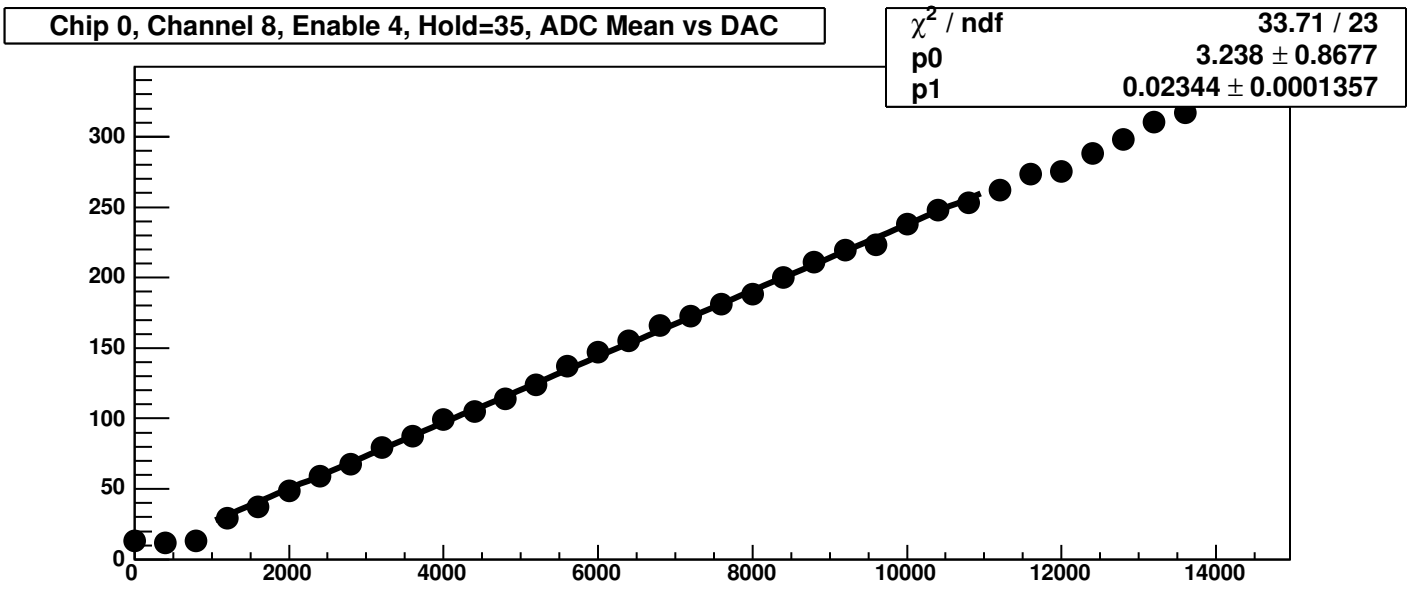

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

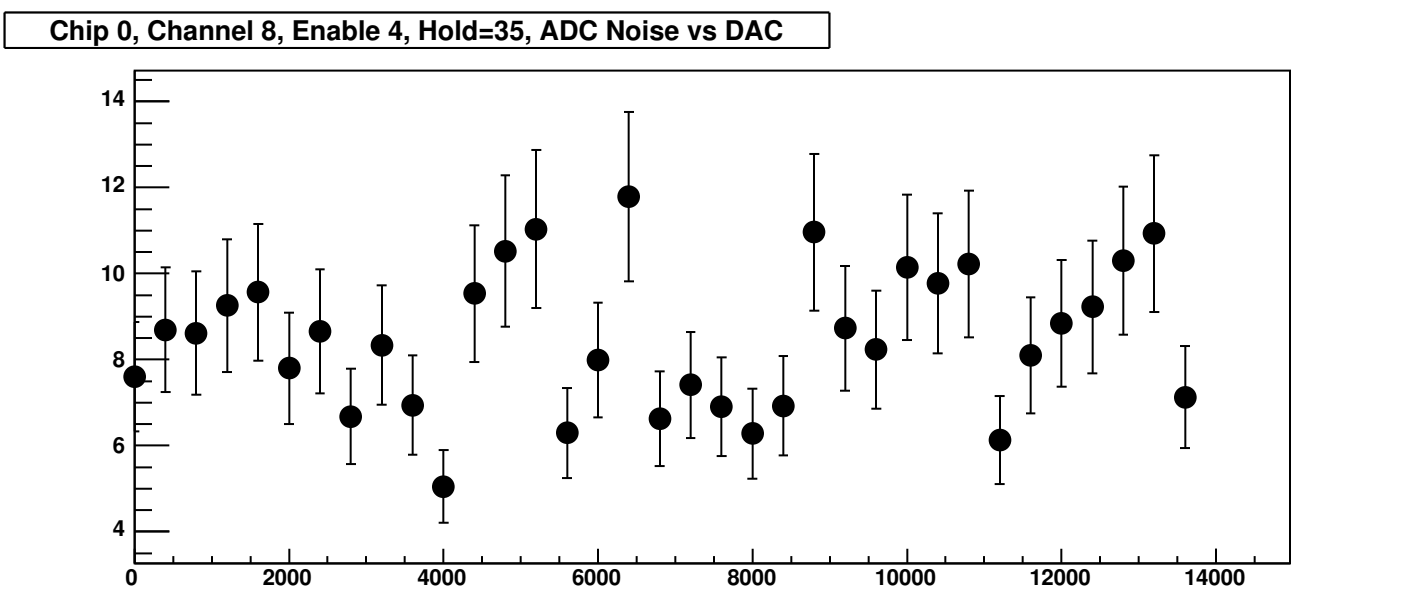

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

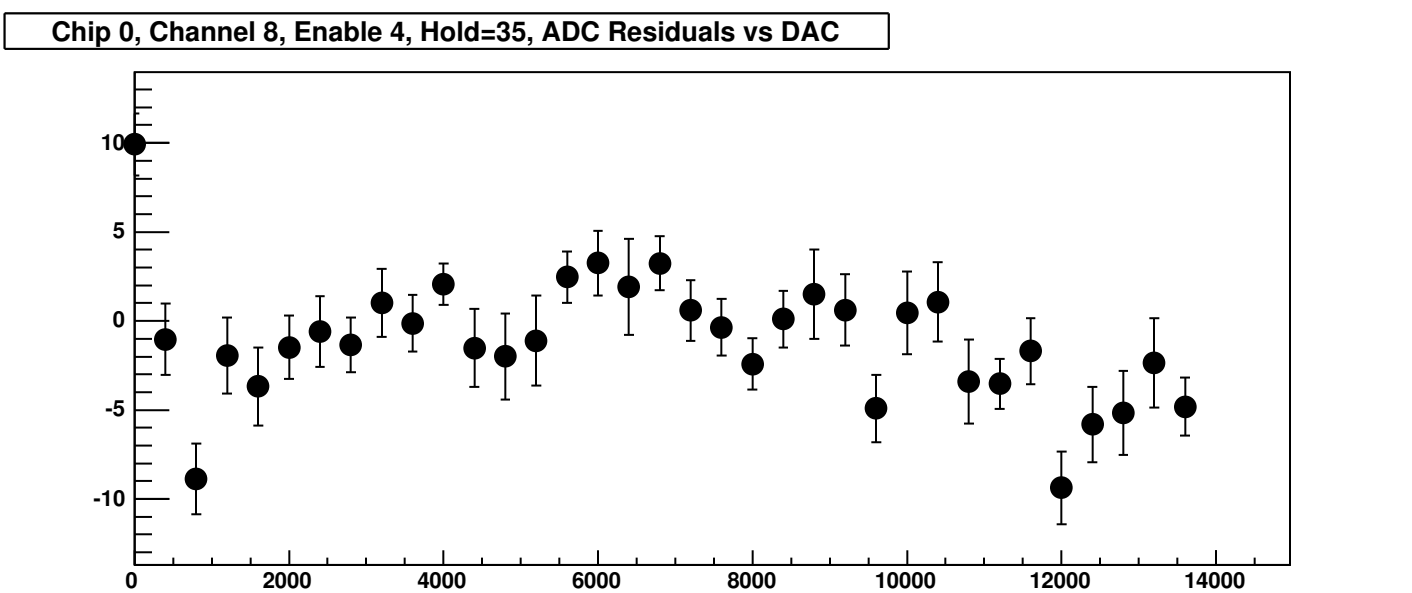

Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.4 / 23
p0 3.511 ± 0.8237
p1 0.0216 ± 0.0001243
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

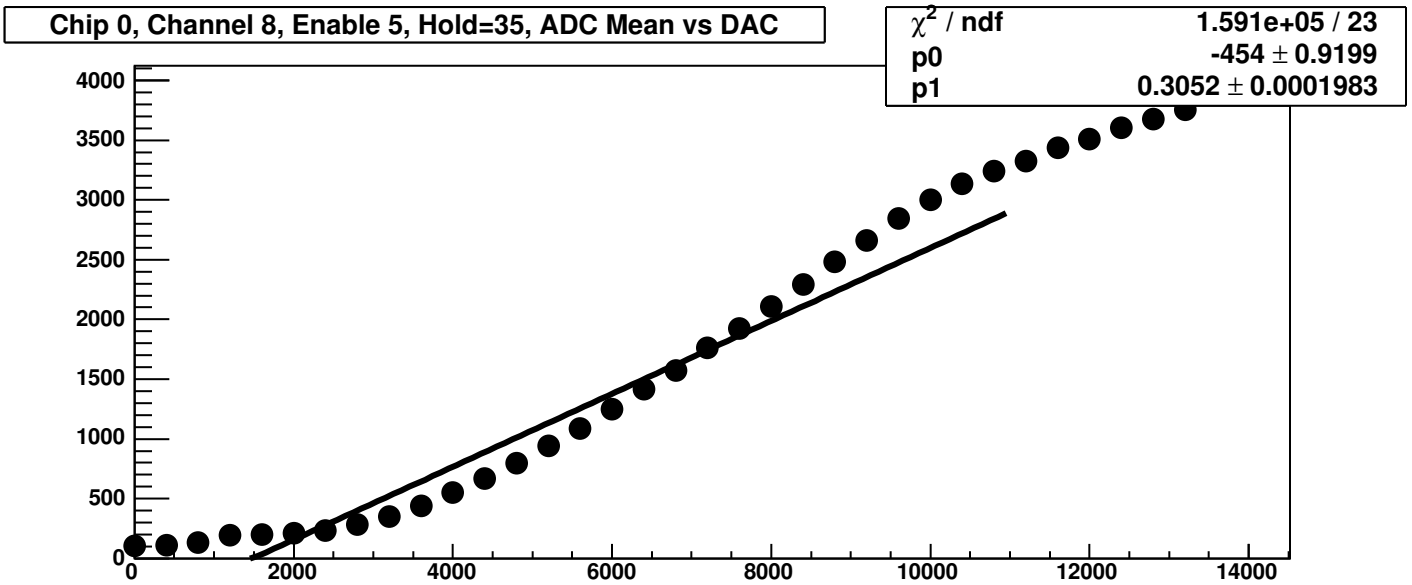

Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

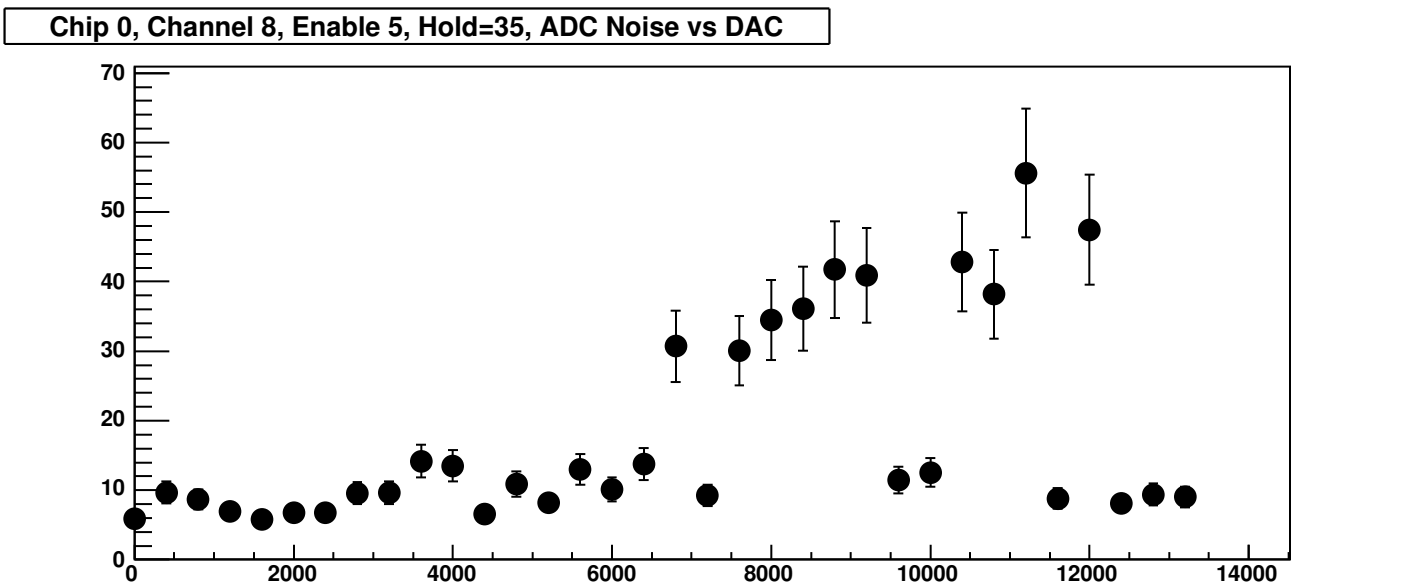

Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

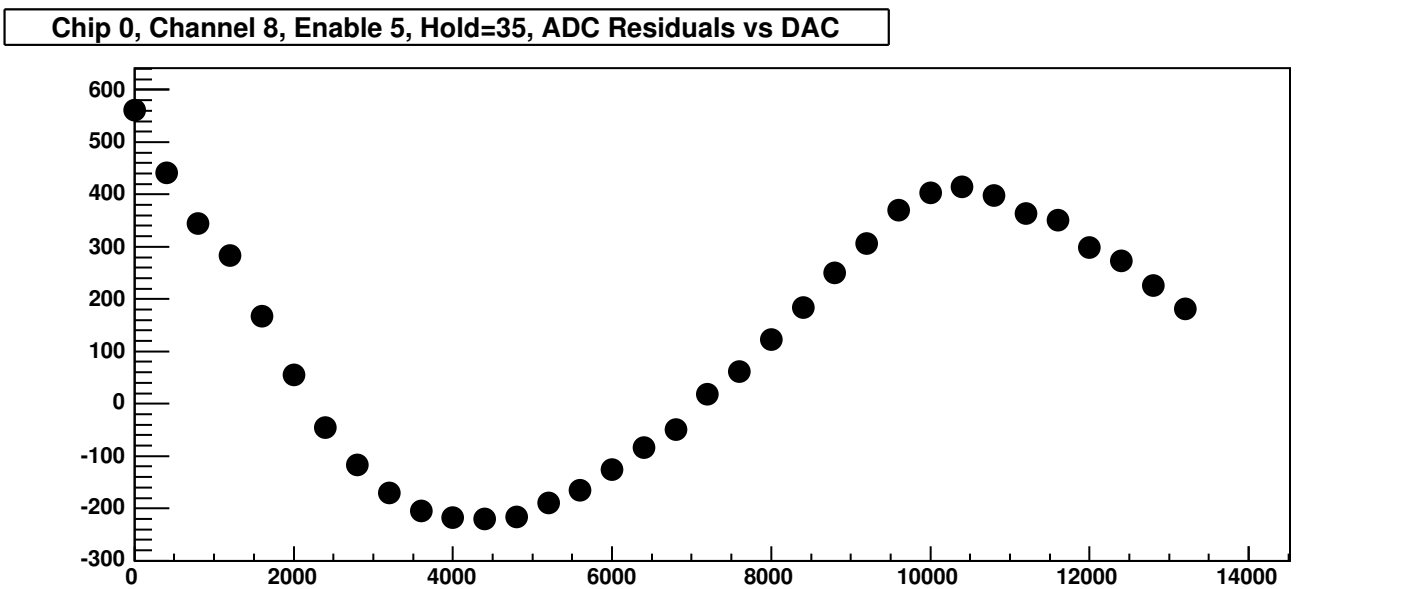

Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.71 / 23
p0 3.238 ± 0.8677
p1 0.02344 ± 0.0001357
0
50
100
150
200
250
300
0
2000
4000
6000
8000
10000
12000
14000

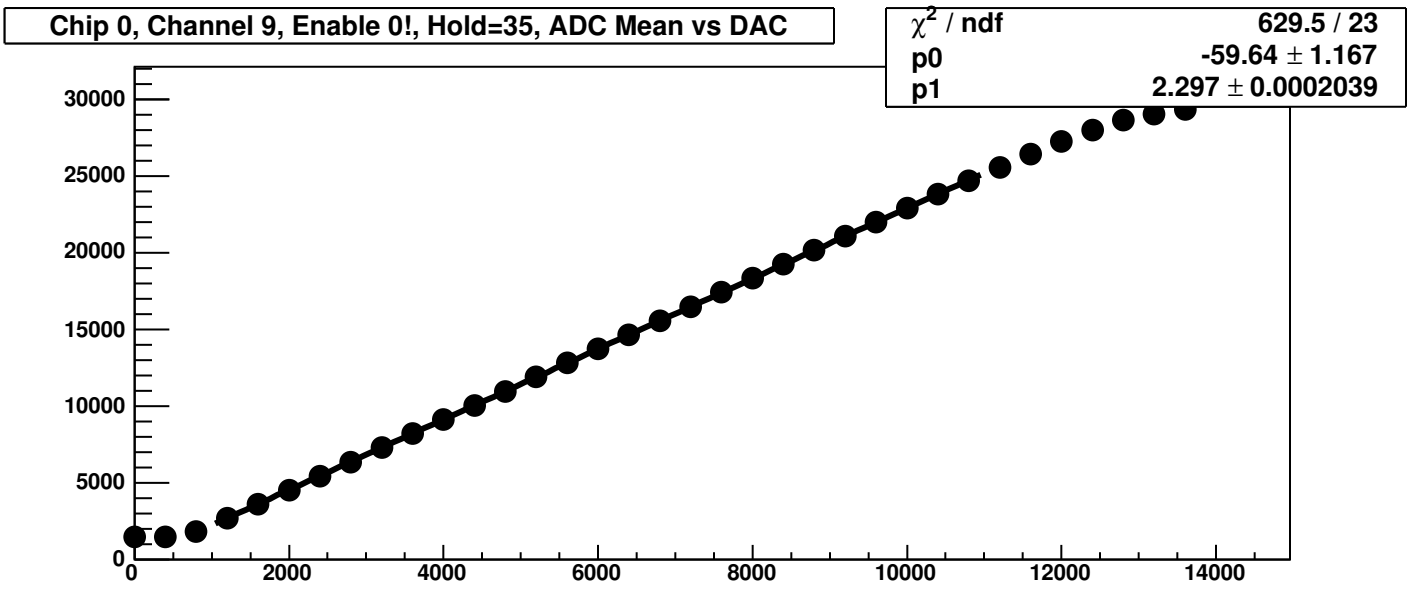

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
4
6
8
10
12
14
0
2000
4000
6000
8000
10000
12000
14000

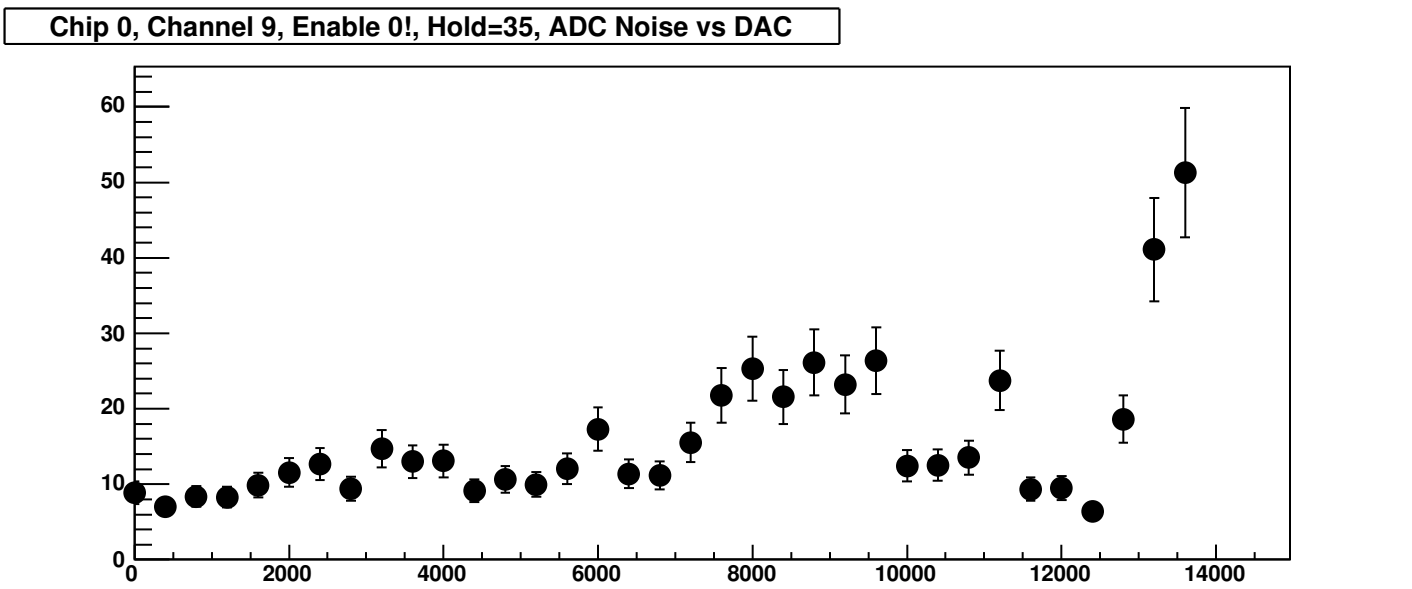

Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
-10
-5
0
5
10
0
2000
4000
6000
8000
10000
12000
14000

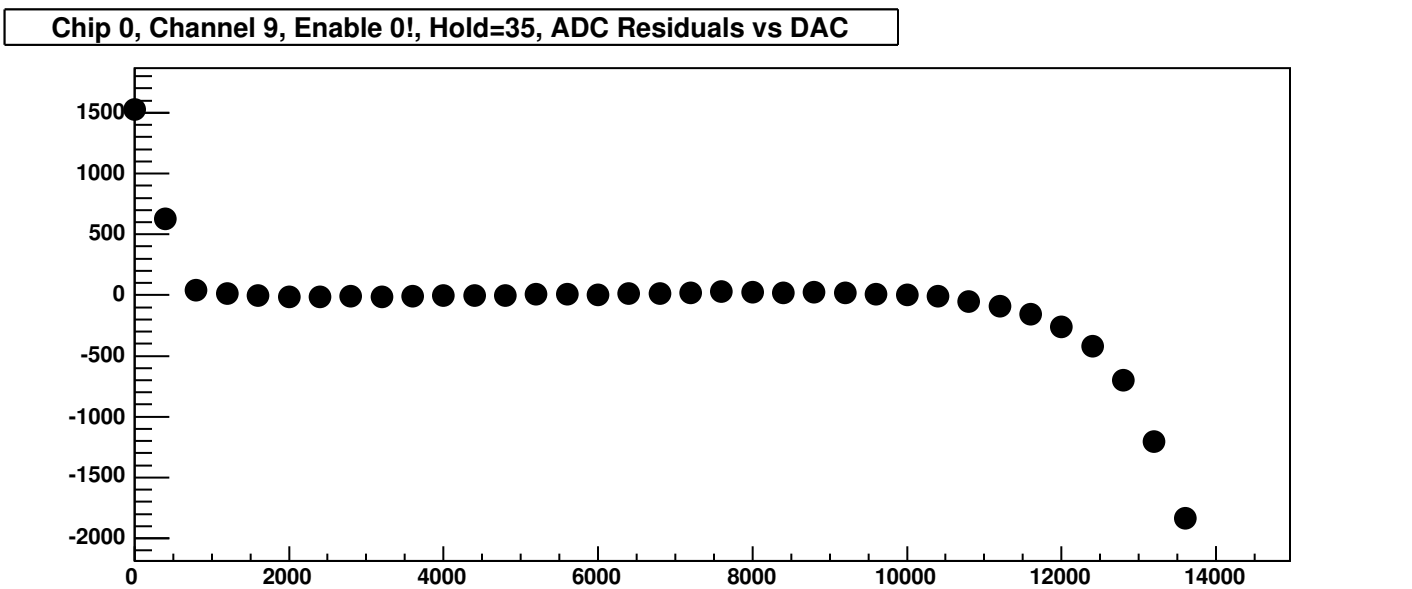

Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.591e+05 / 23
p0 -454 ± 0.9199
p1 0.3052 ± 0.0001983
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

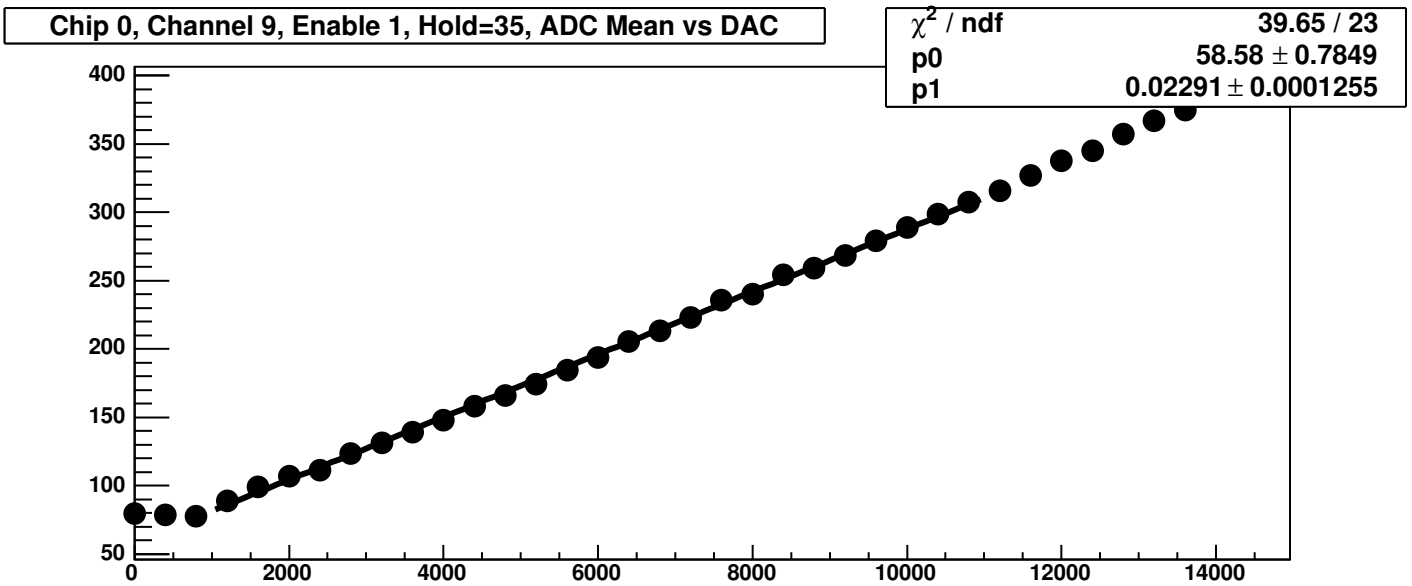

Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

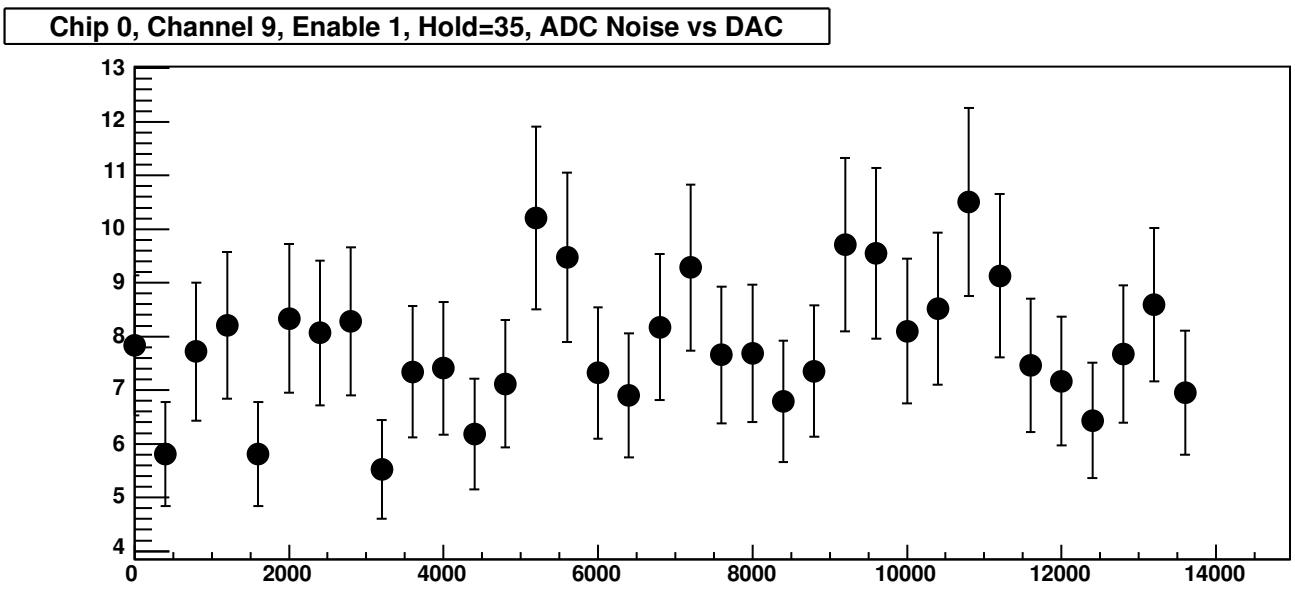

Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

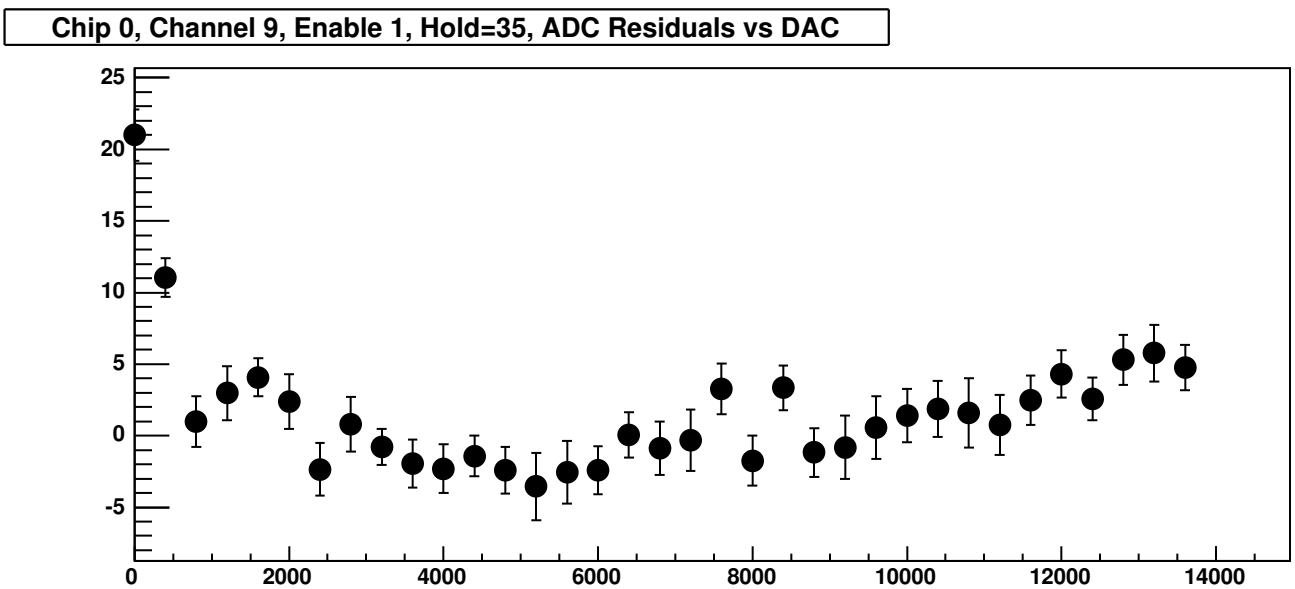

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 629.5 / 23
p0 -59.64 ± 1.167
p1 2.297 ± 0.0002039
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

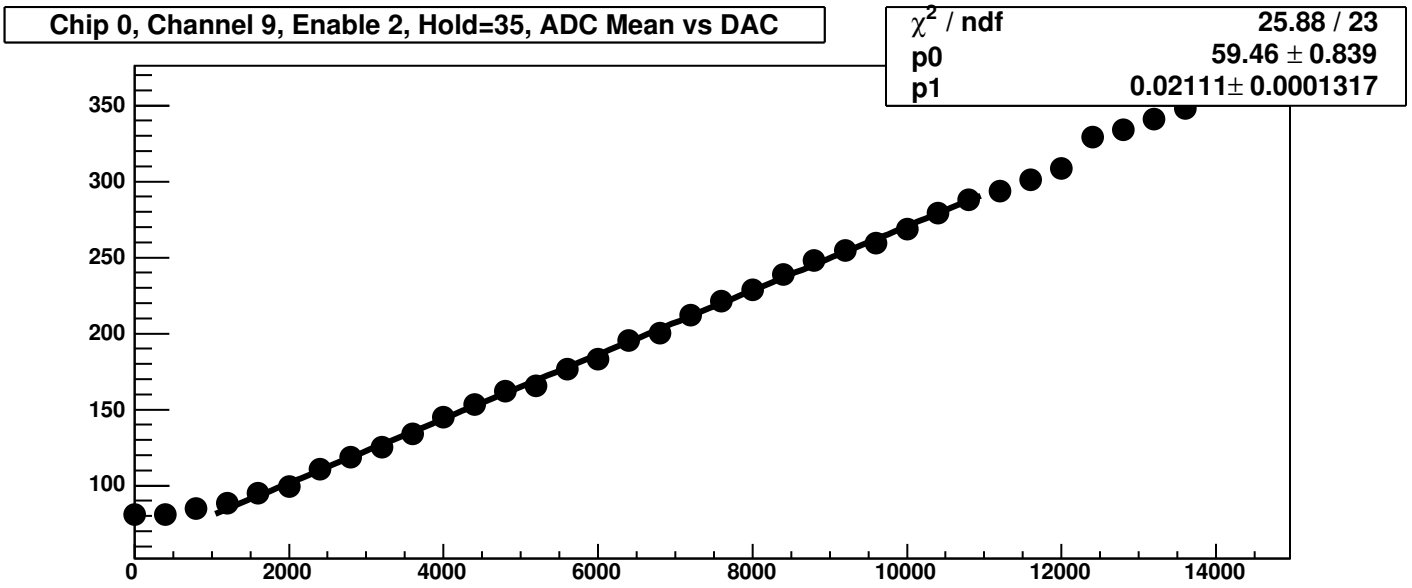

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

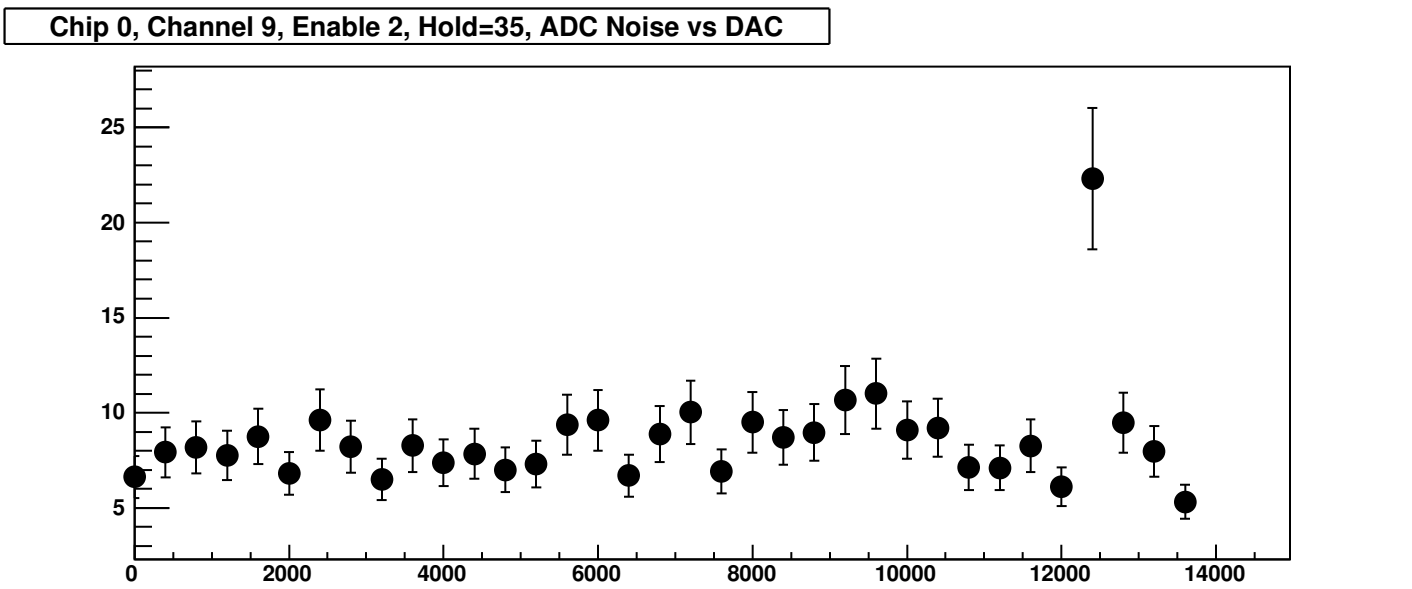

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

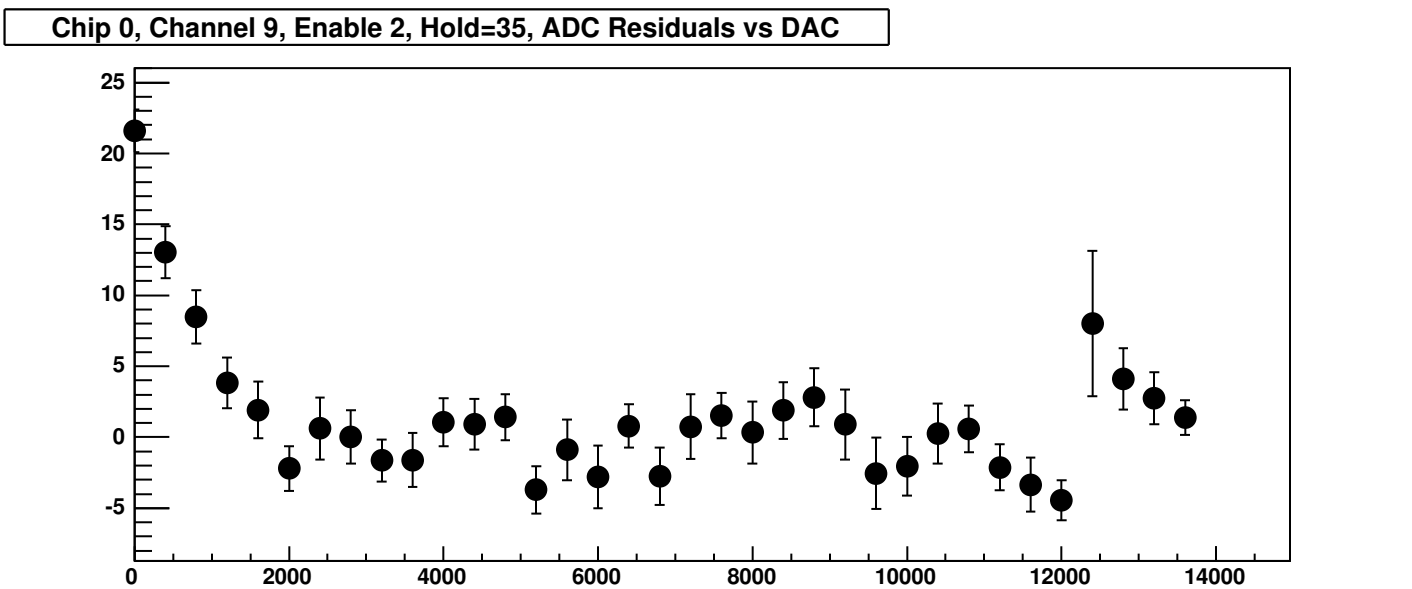

Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.65 / 23
p0 58.58 ± 0.7849
p1 0.02291 ± 0.0001255
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

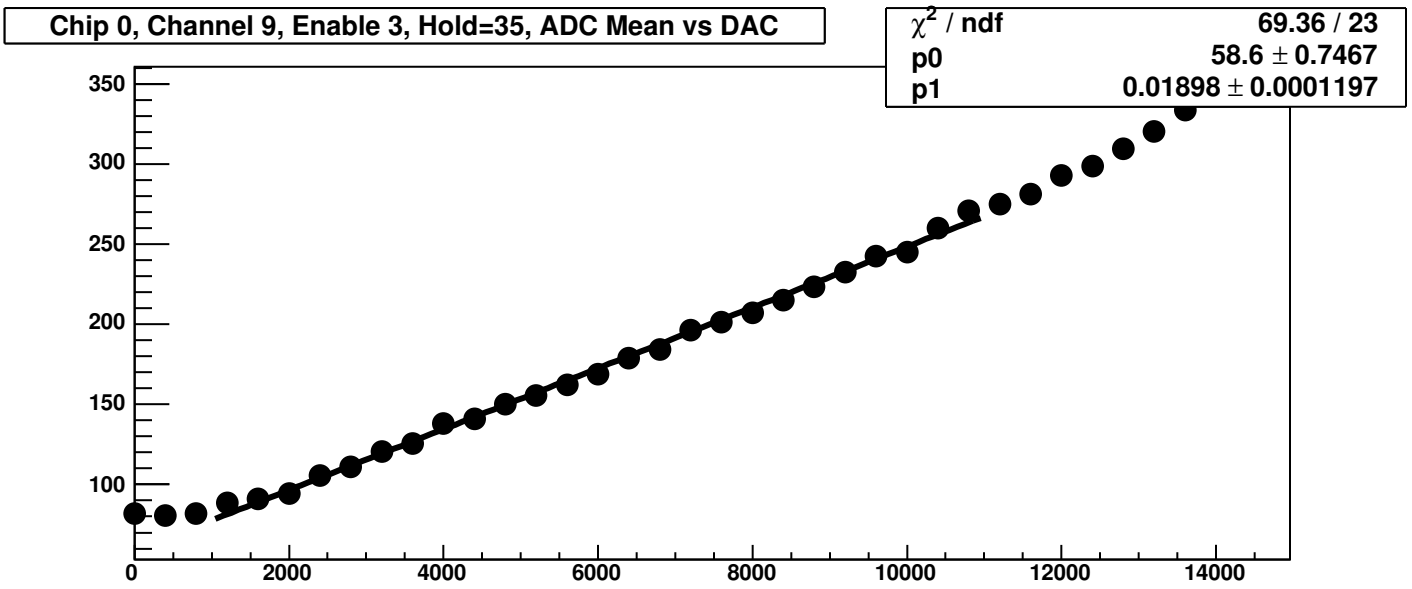

Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

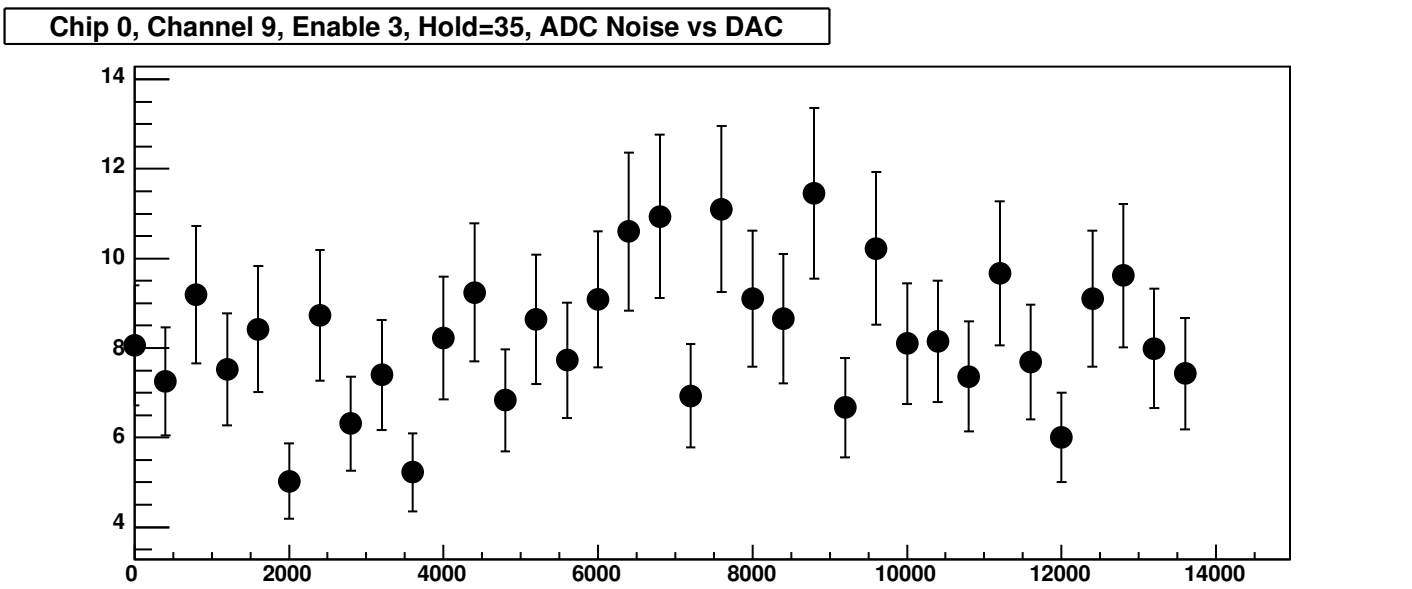

Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

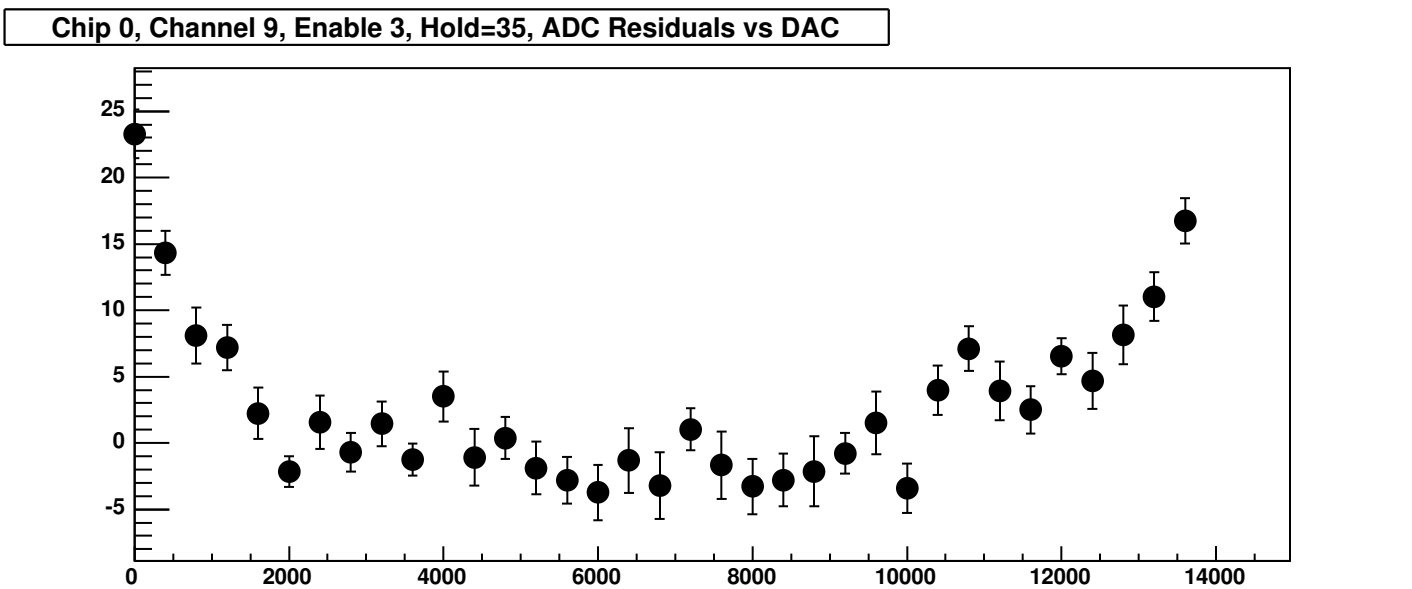

Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.88 / 23
p0 59.46 ± 0.839
p1 0.02111± 0.0001317
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

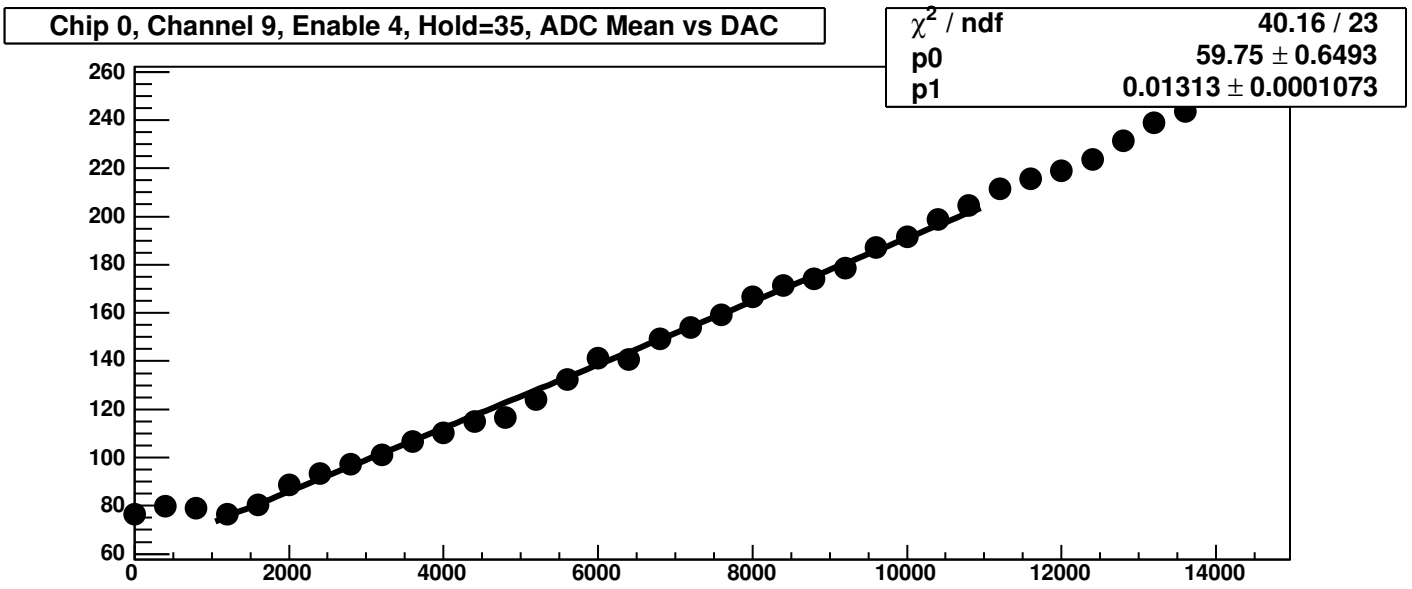

Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

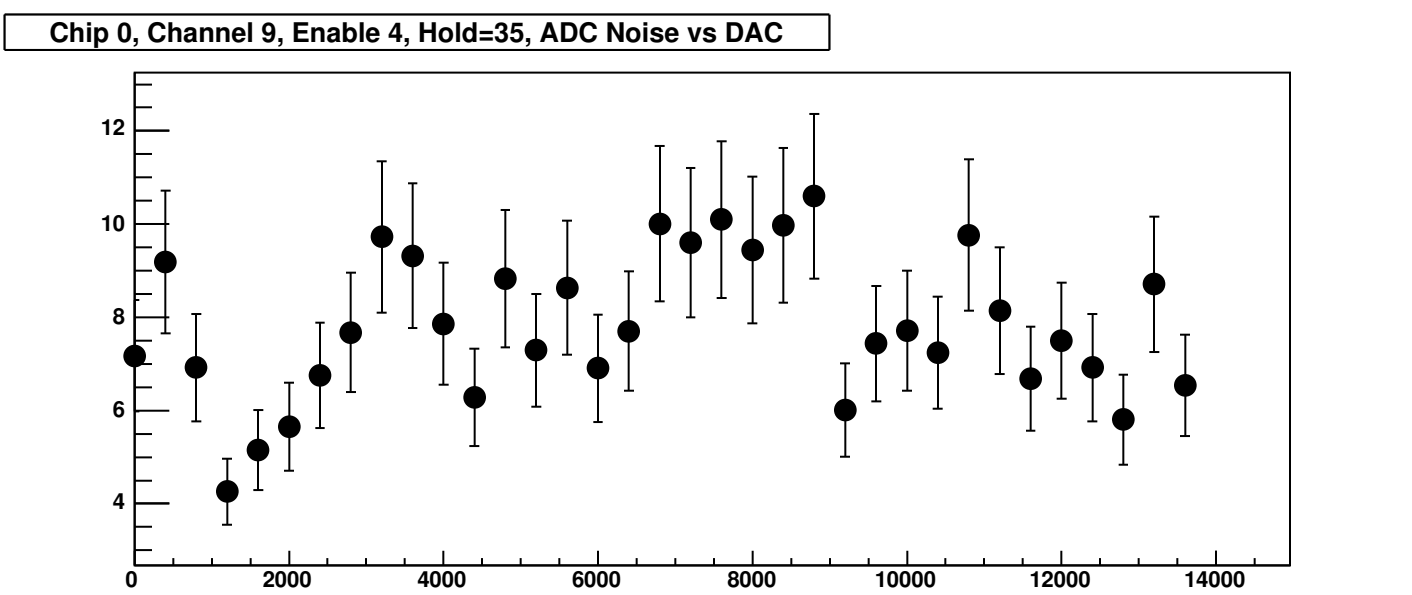

Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

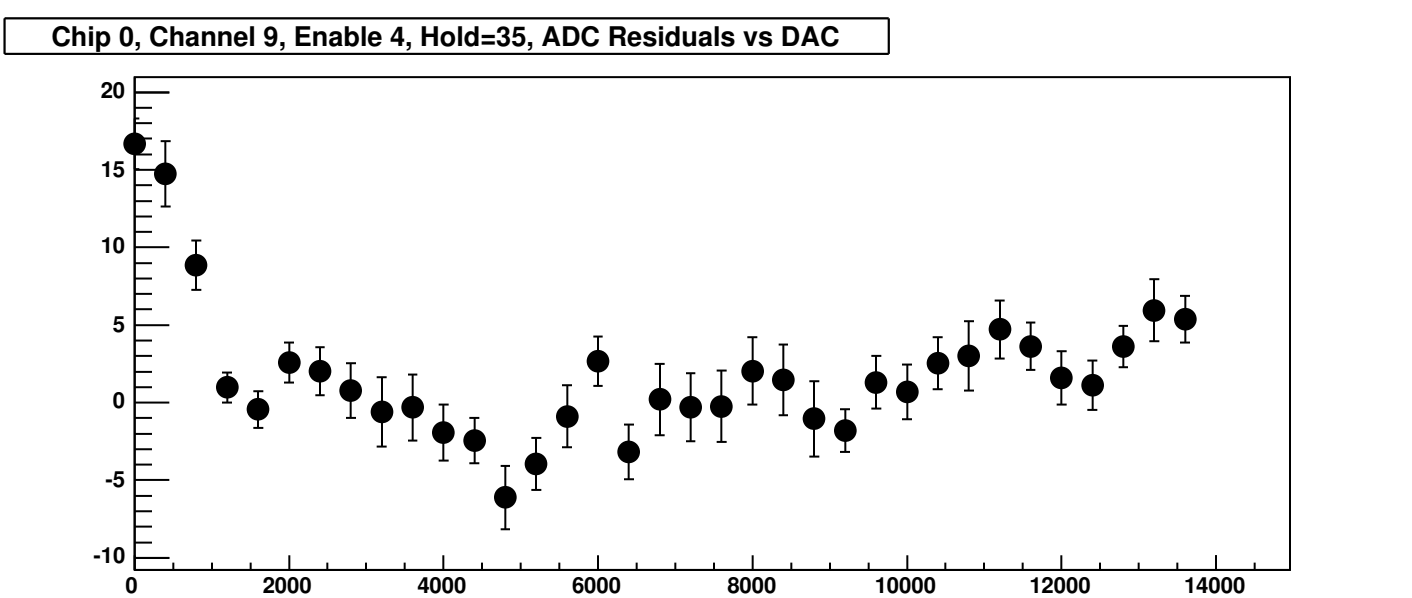

Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 69.36 / 23
p0 58.6 ± 0.7467
p1 0.01898 ± 0.0001197
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

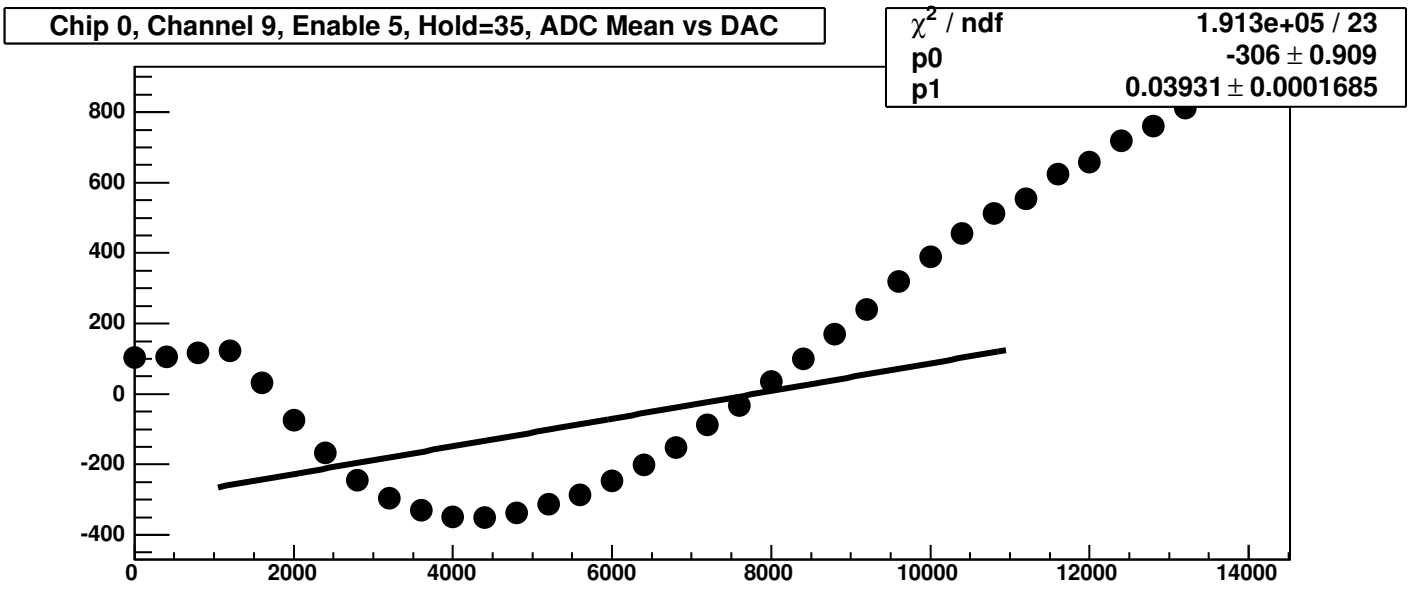

Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

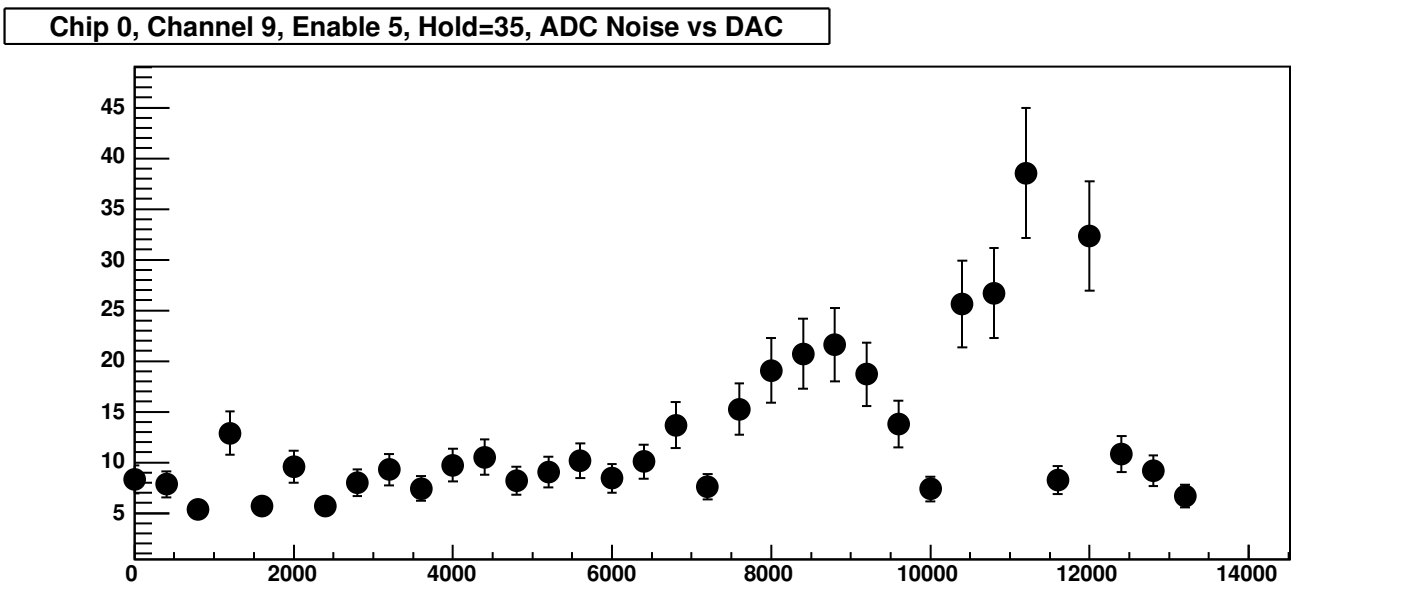

Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

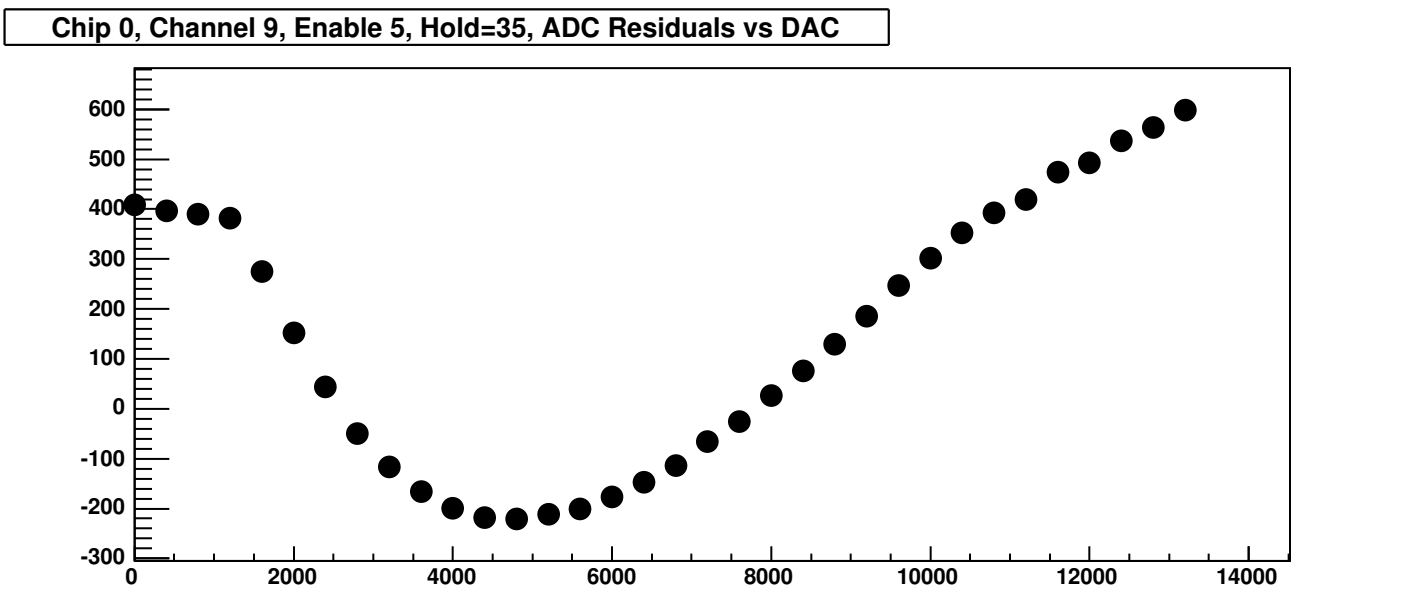

Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.16 / 23
p0 59.75 ± 0.6493
p1 0.01313 ± 0.0001073
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

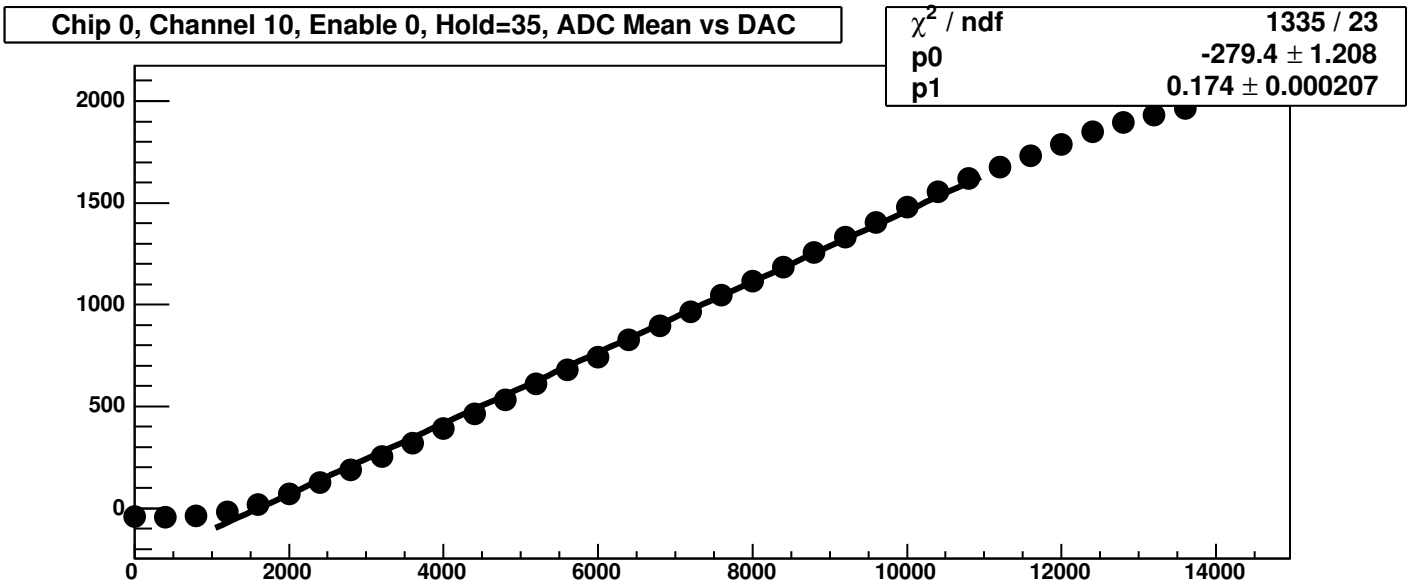

Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

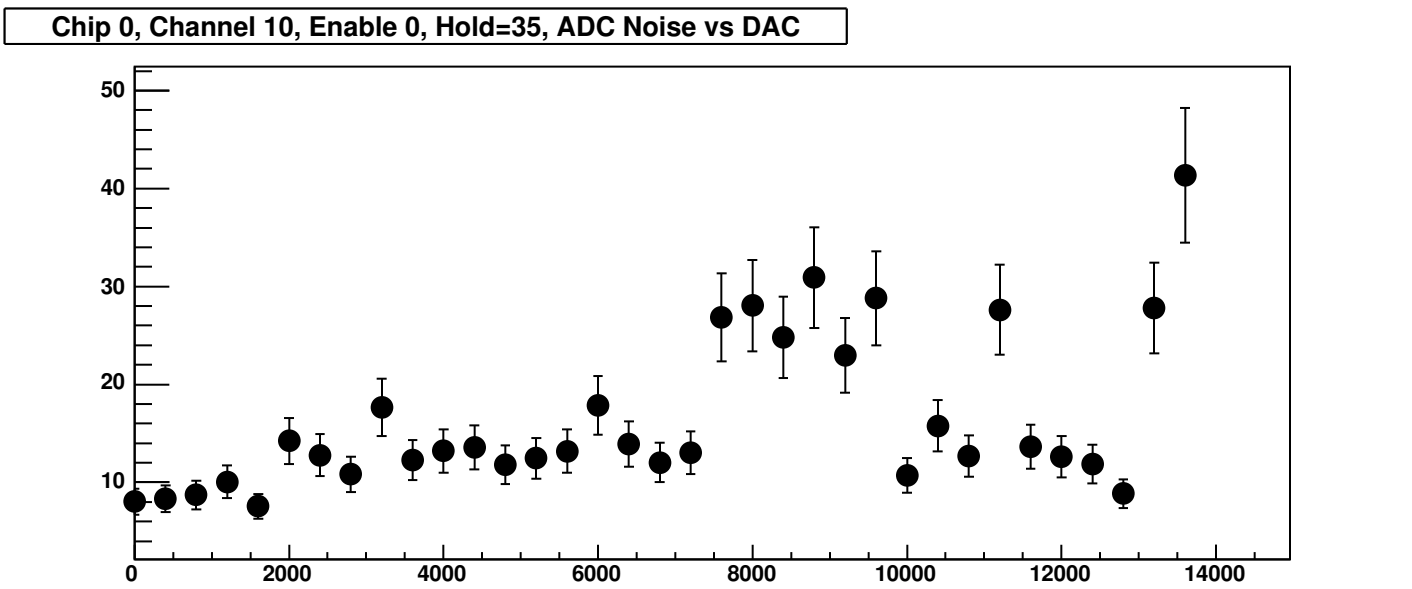

Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

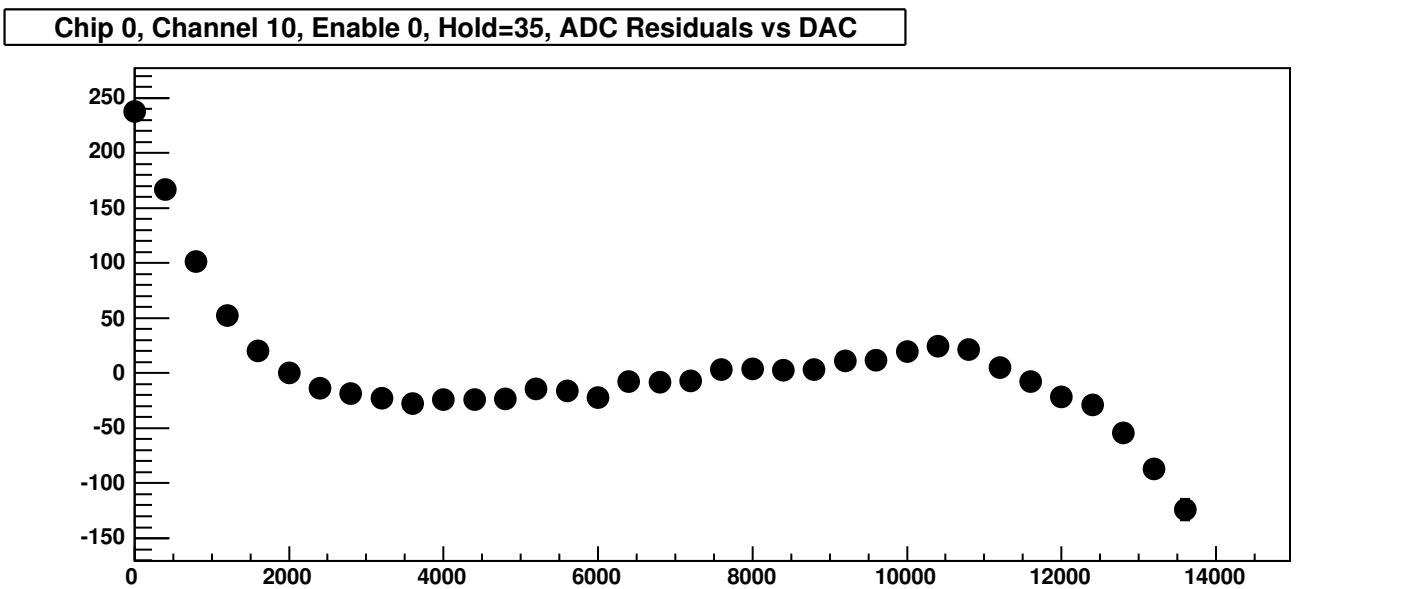

Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.913e+05 / 23
p0 -306 ± 0.909
p1 0.03931 ± 0.0001685
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

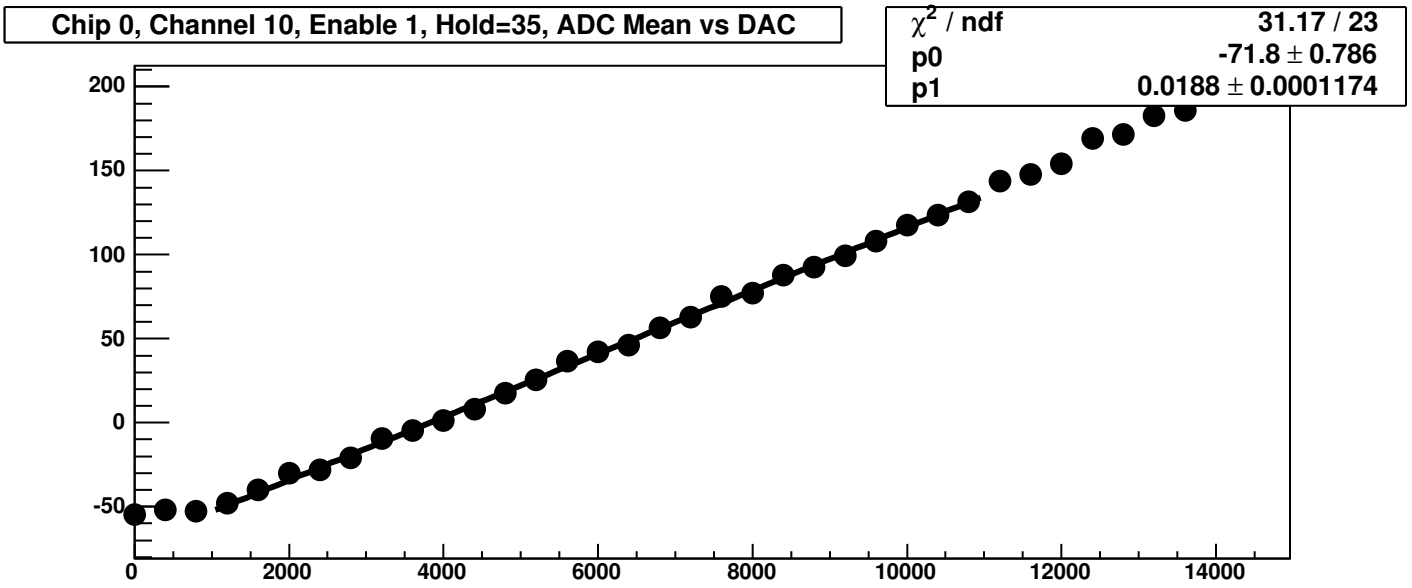

Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

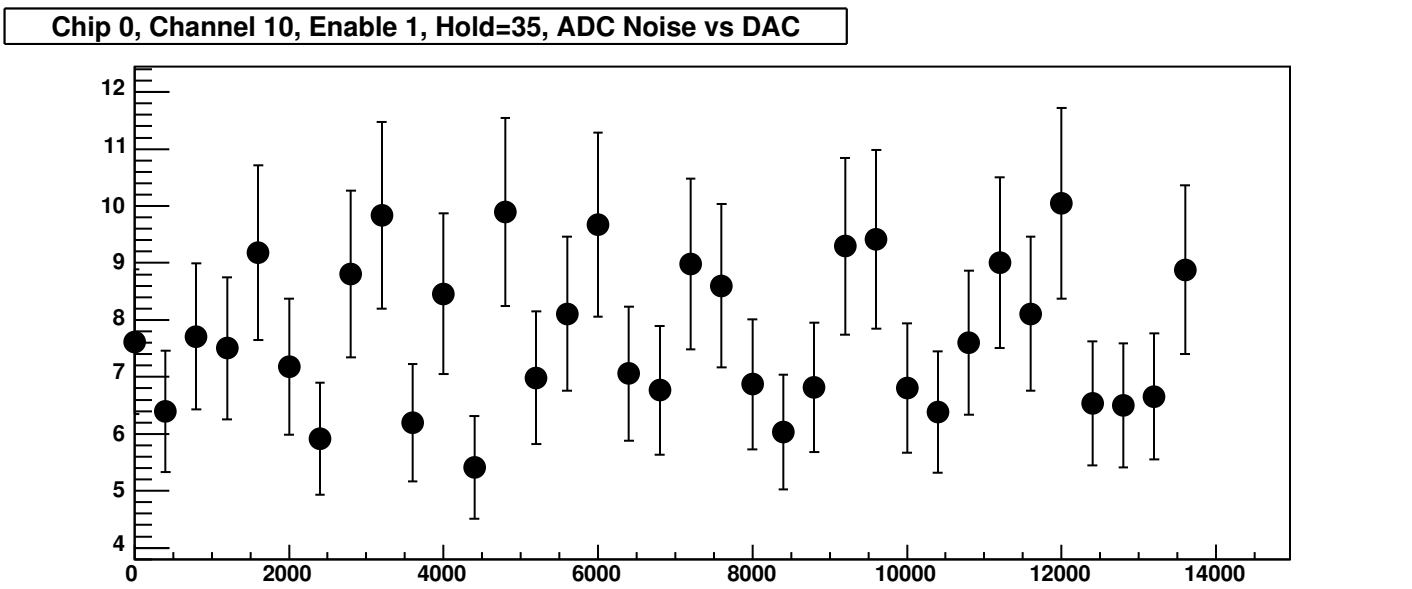

Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

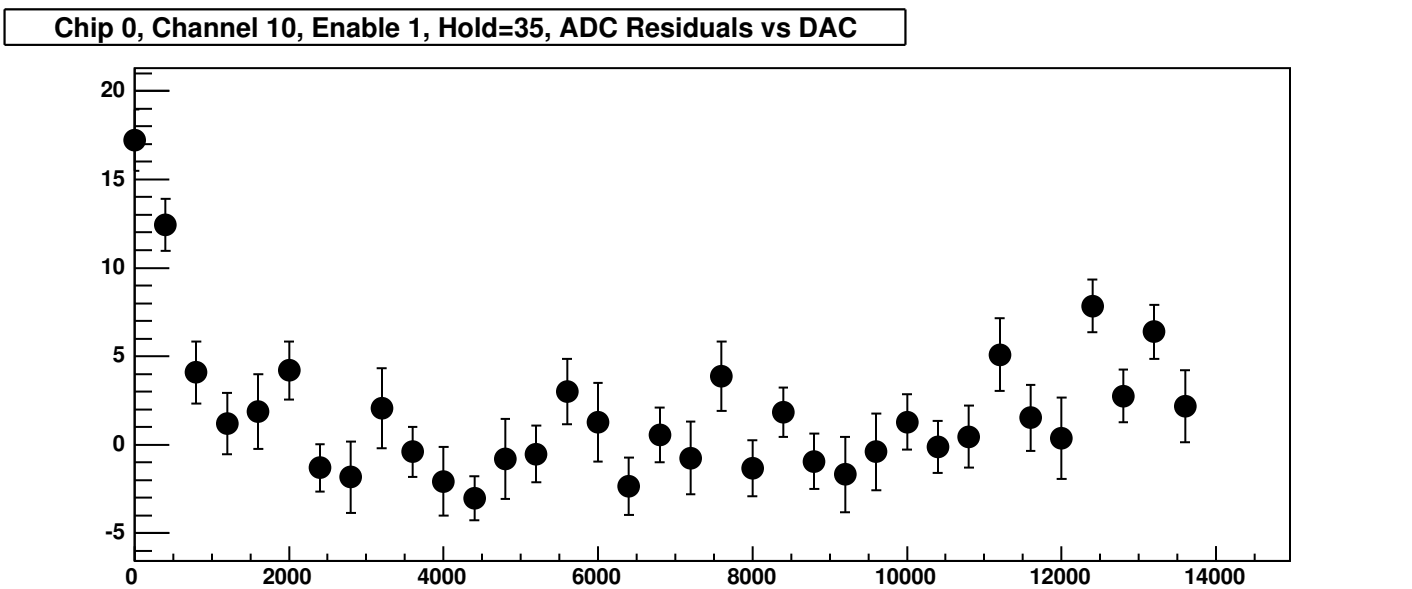

Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1335 / 23
p0 -279.4 ± 1.208
p1 0.174 ± 0.000207
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

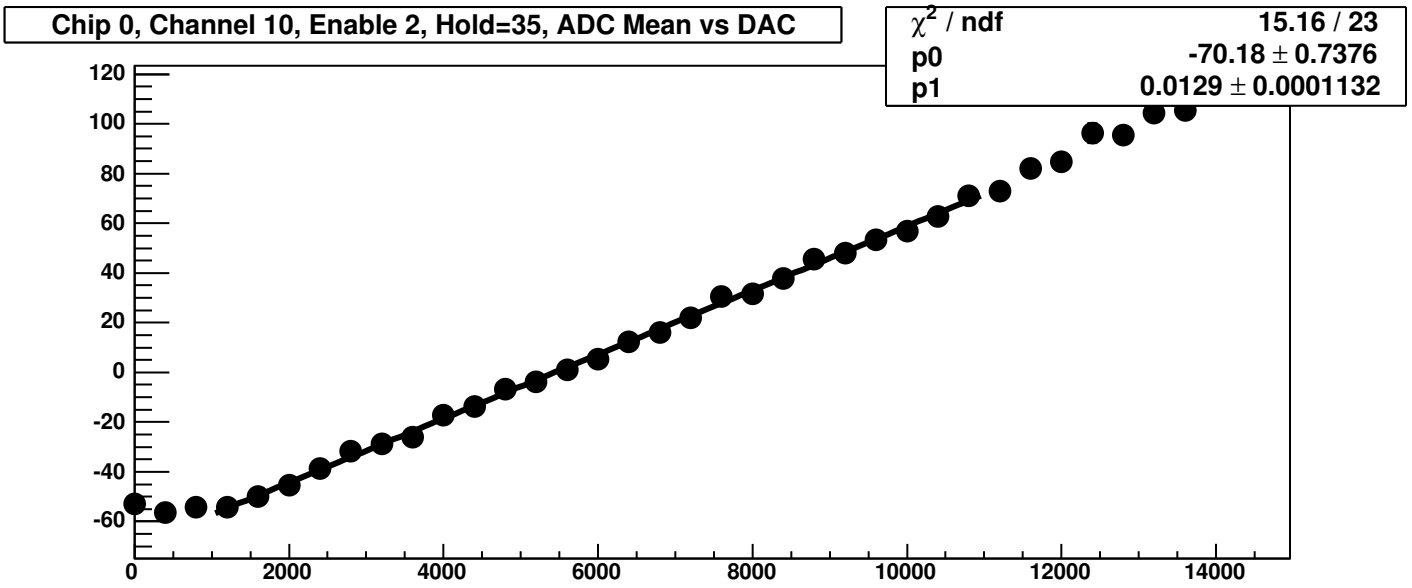

Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

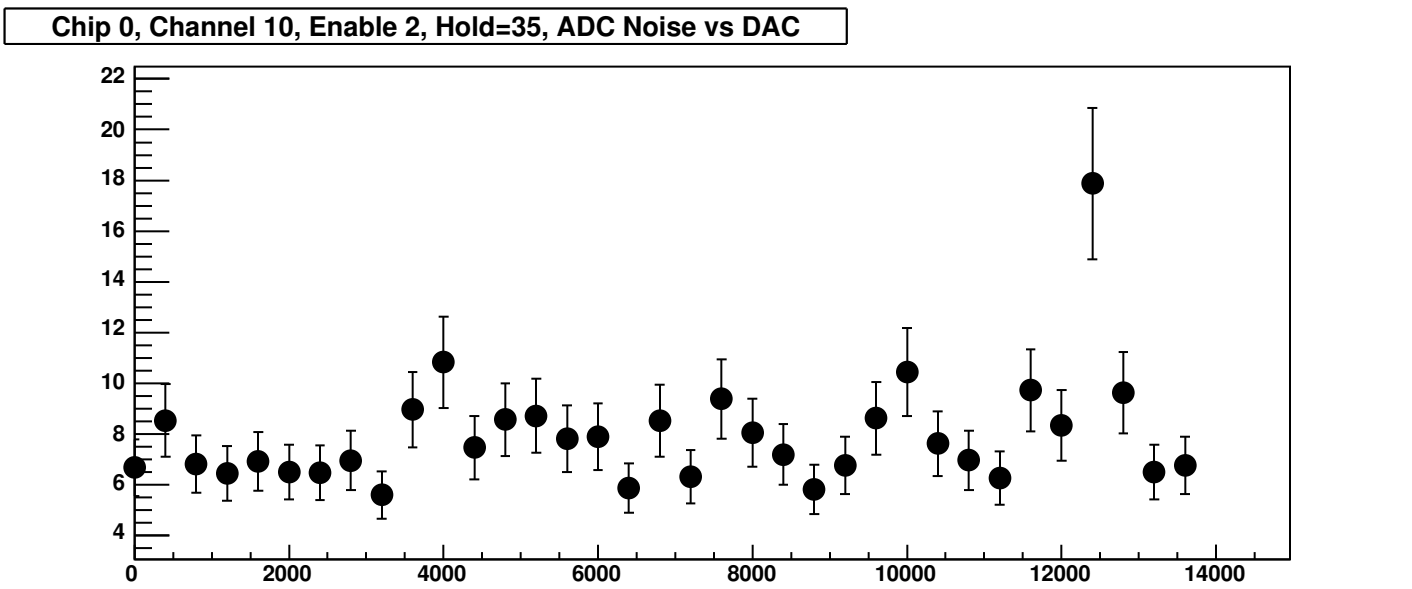

Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

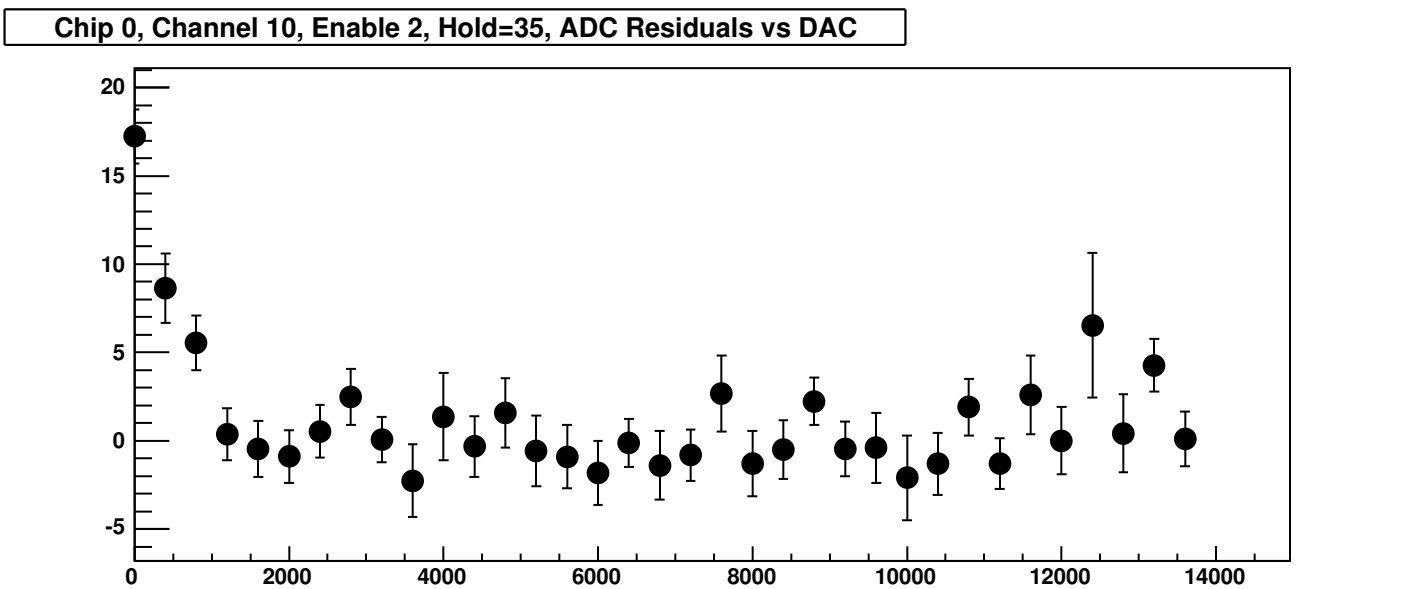

Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.17 / 23
p0 -71.8 ± 0.786
p1 0.0188 ± 0.0001174
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

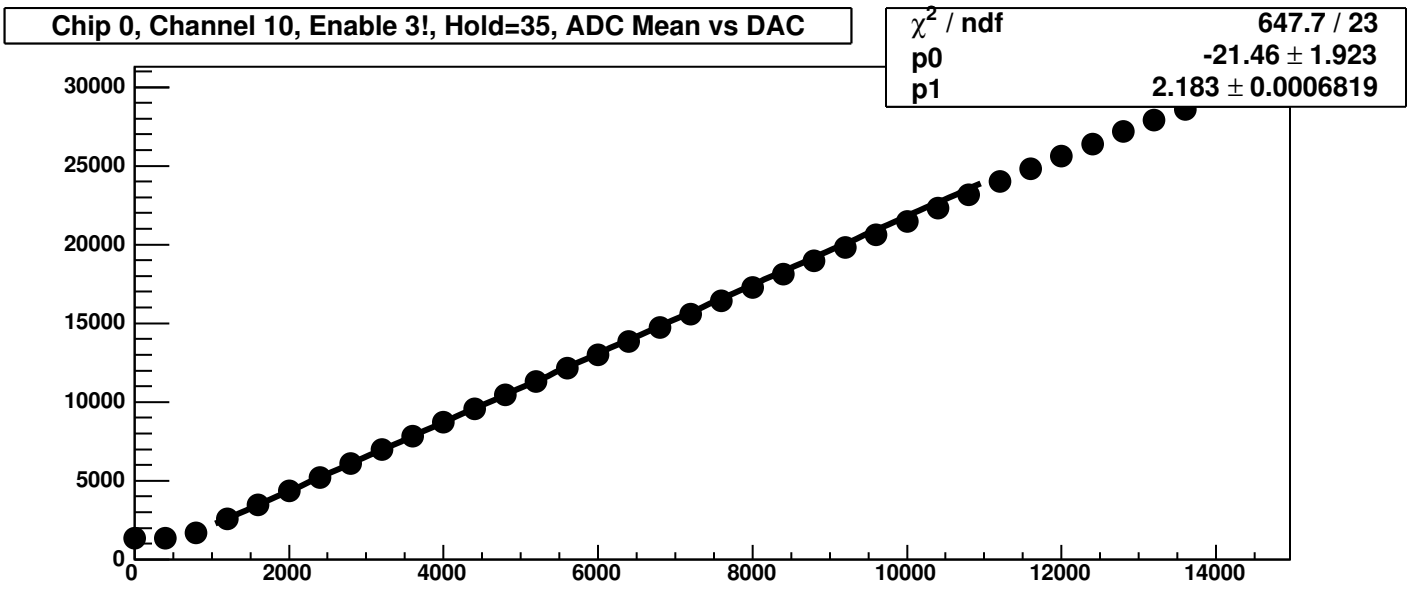

Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

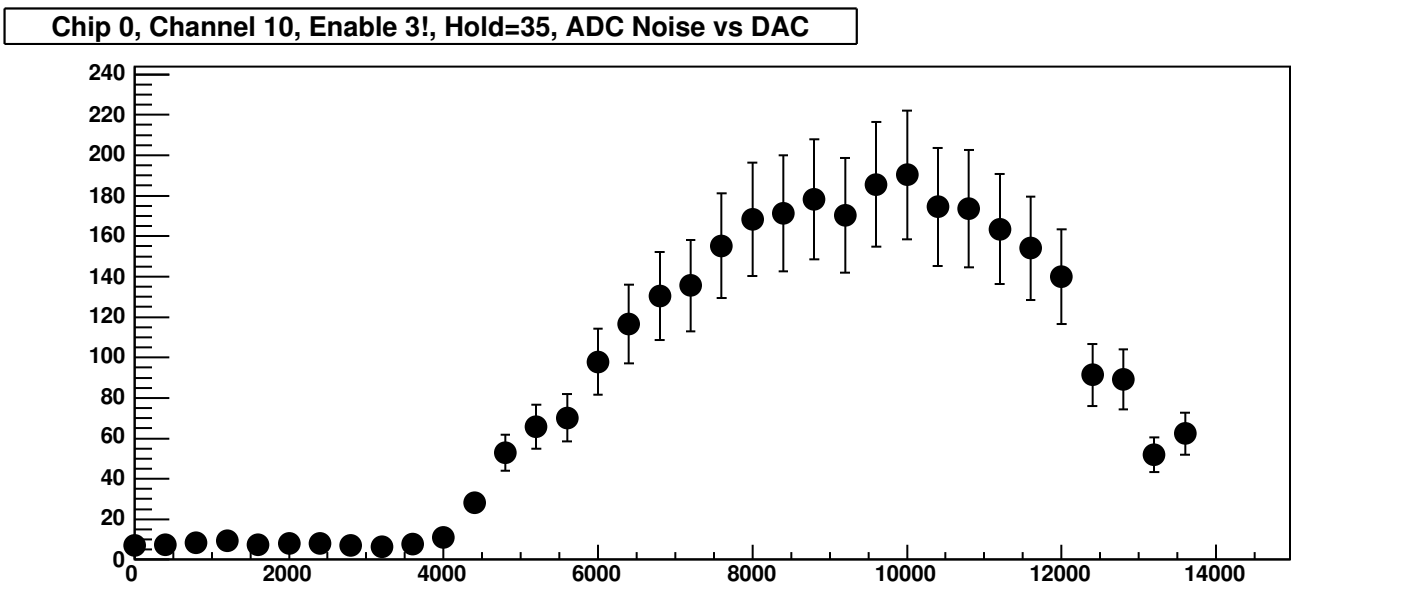

Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

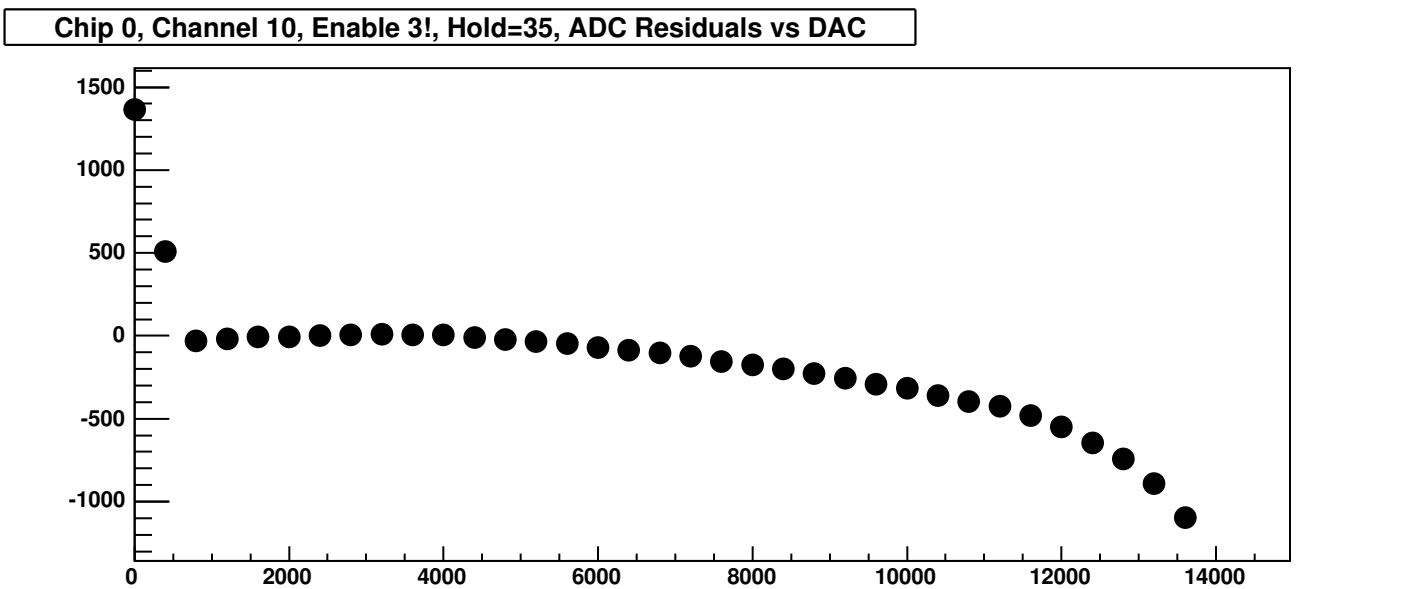

Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.16 / 23
p0 -70.18 ± 0.7376
p1 0.0129 ± 0.0001132
120
100
80
60
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

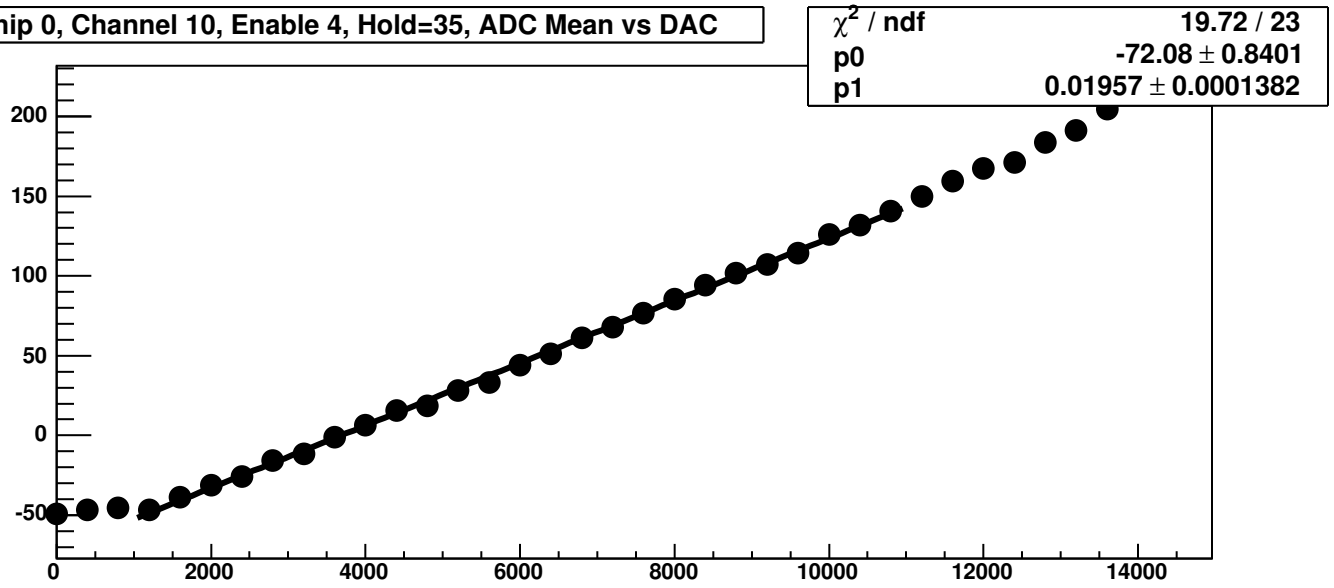

Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 647.7 / 23
p0 -21.46 ± 1.923
p1 2.183 ± 0.0006819
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

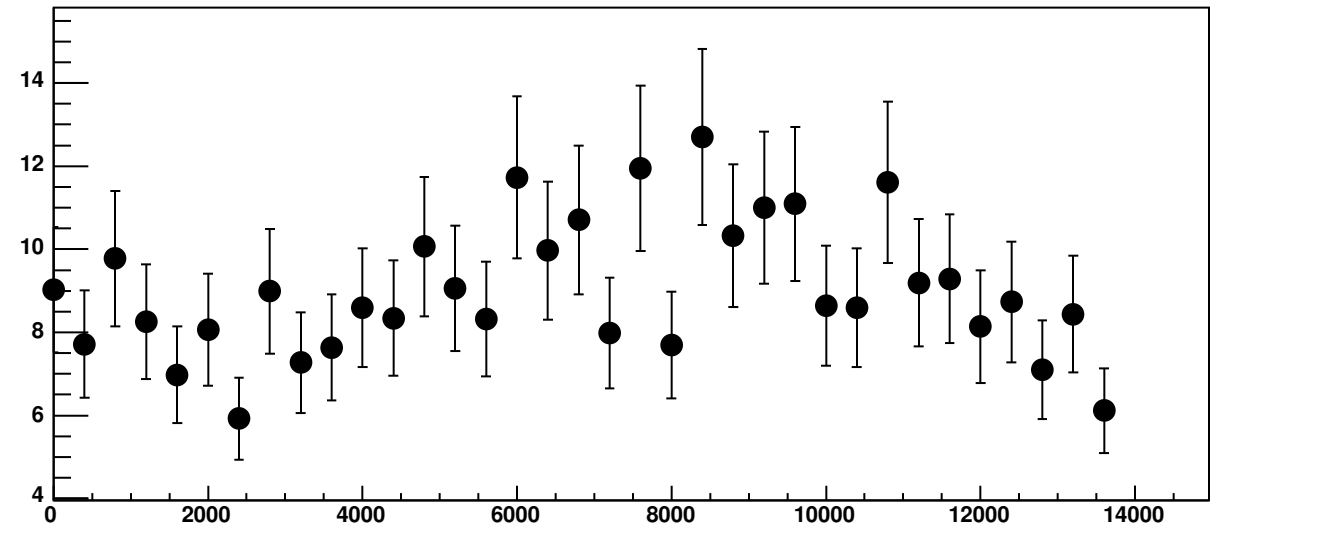

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

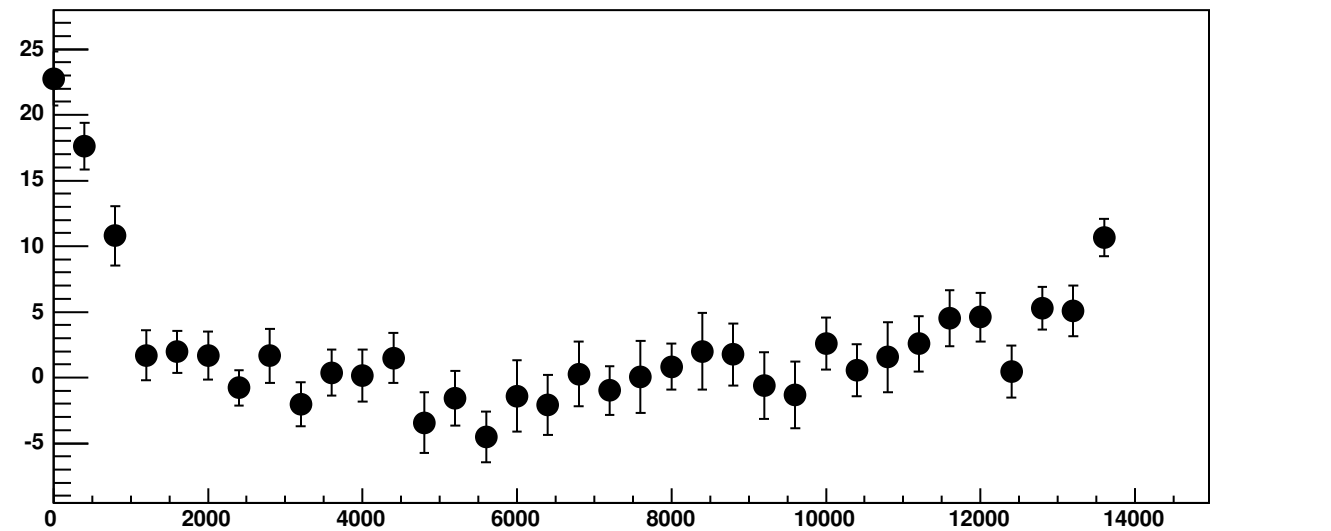
Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

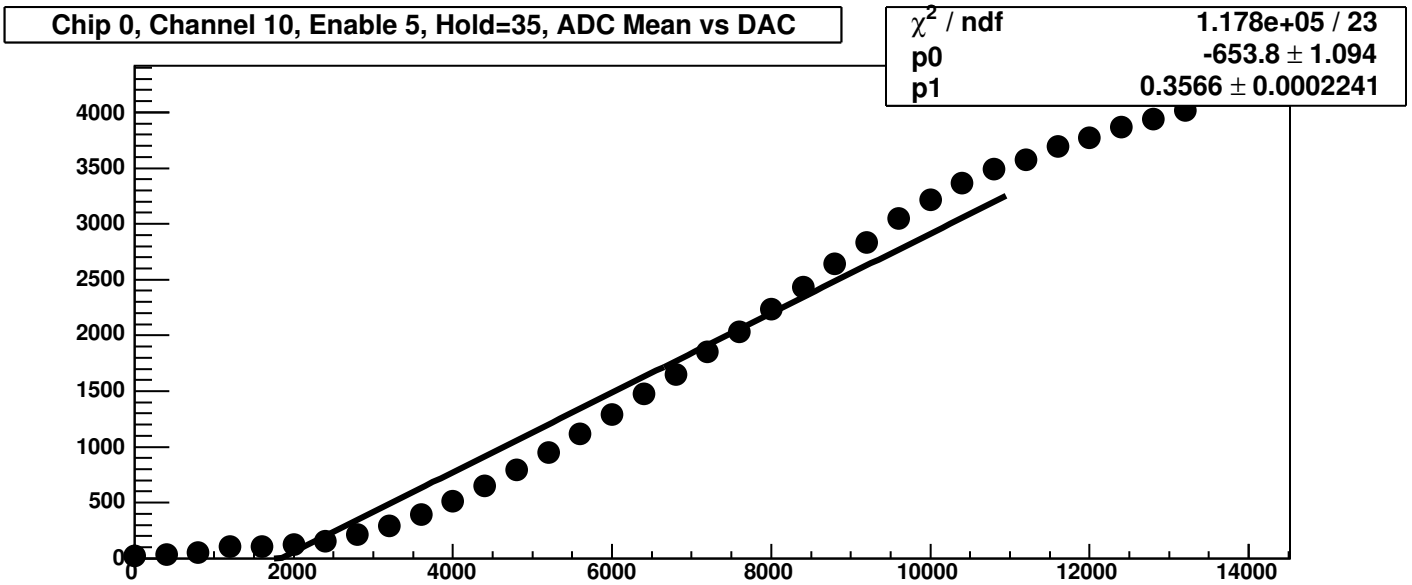


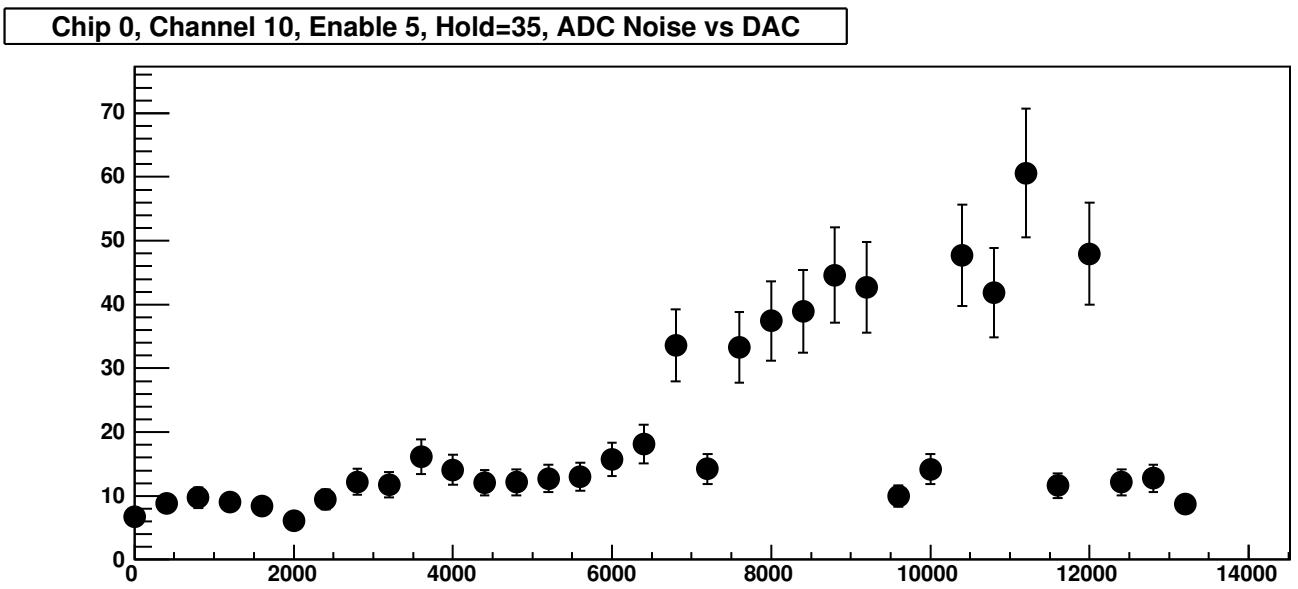
Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

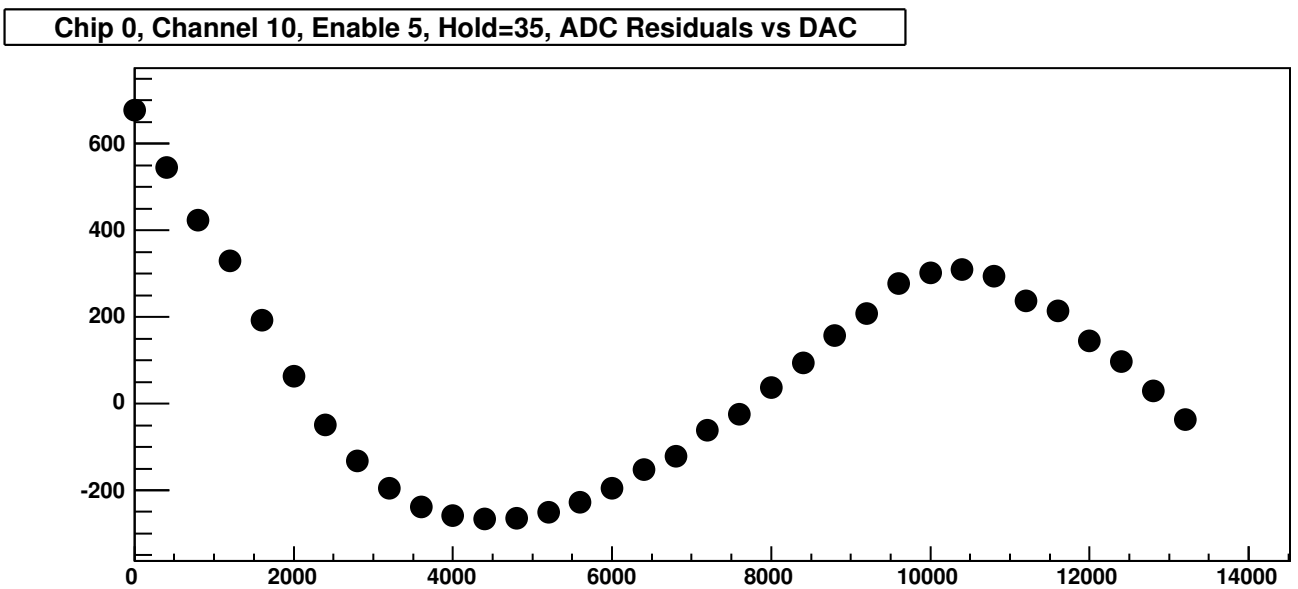


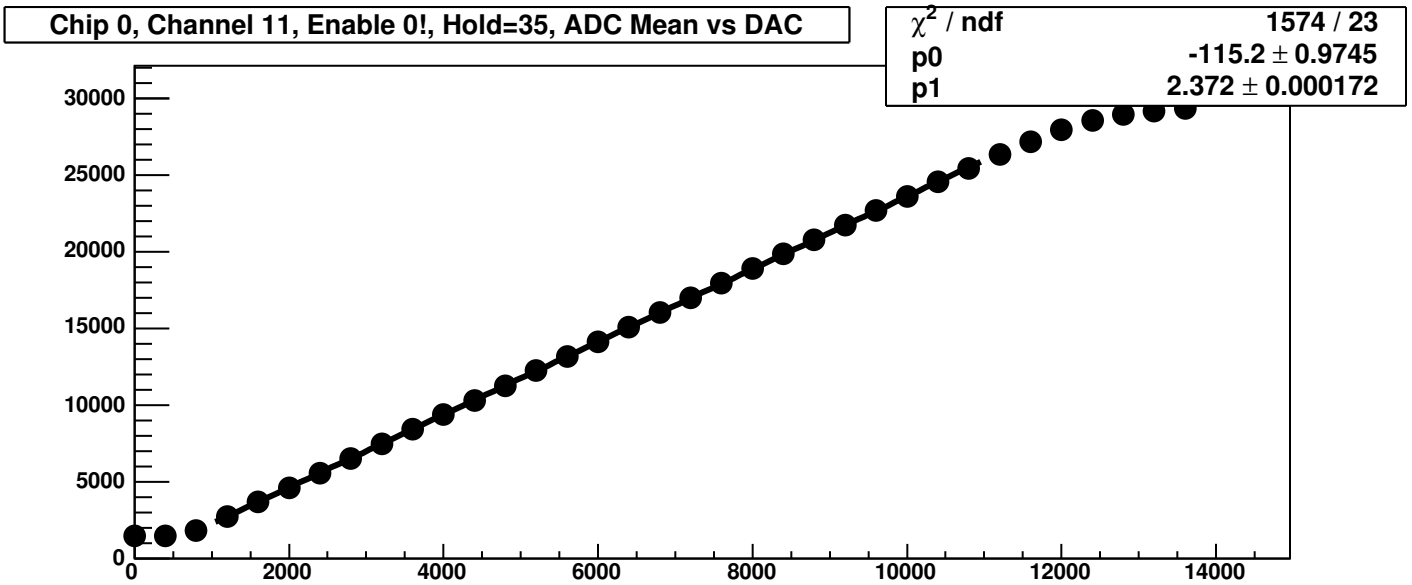
Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

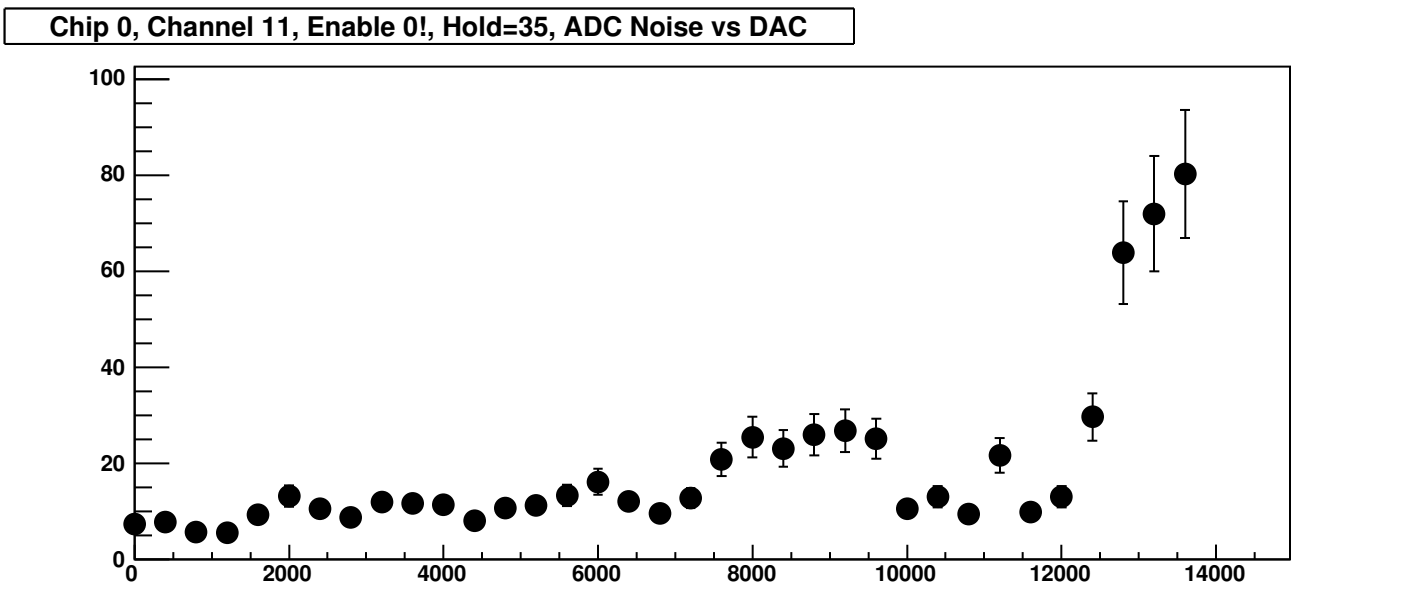


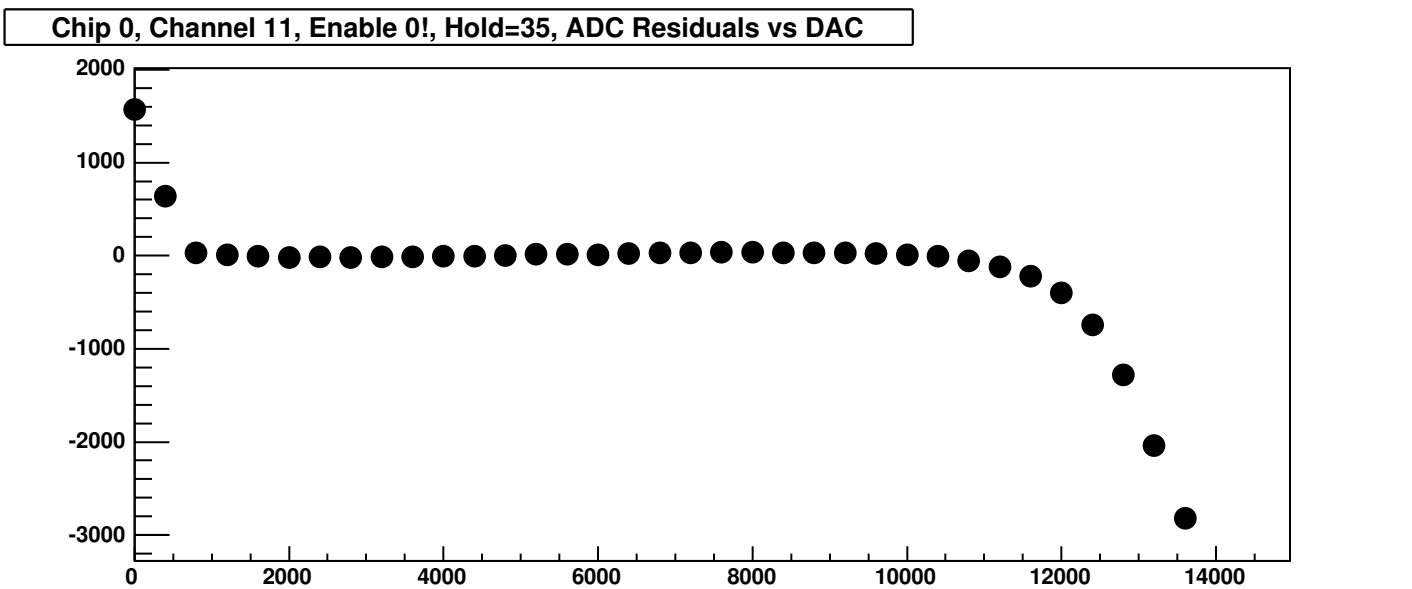

Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.178e+05 / 23
p0 -653.8 ± 1.094
p1 0.3566 ± 0.0002241
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

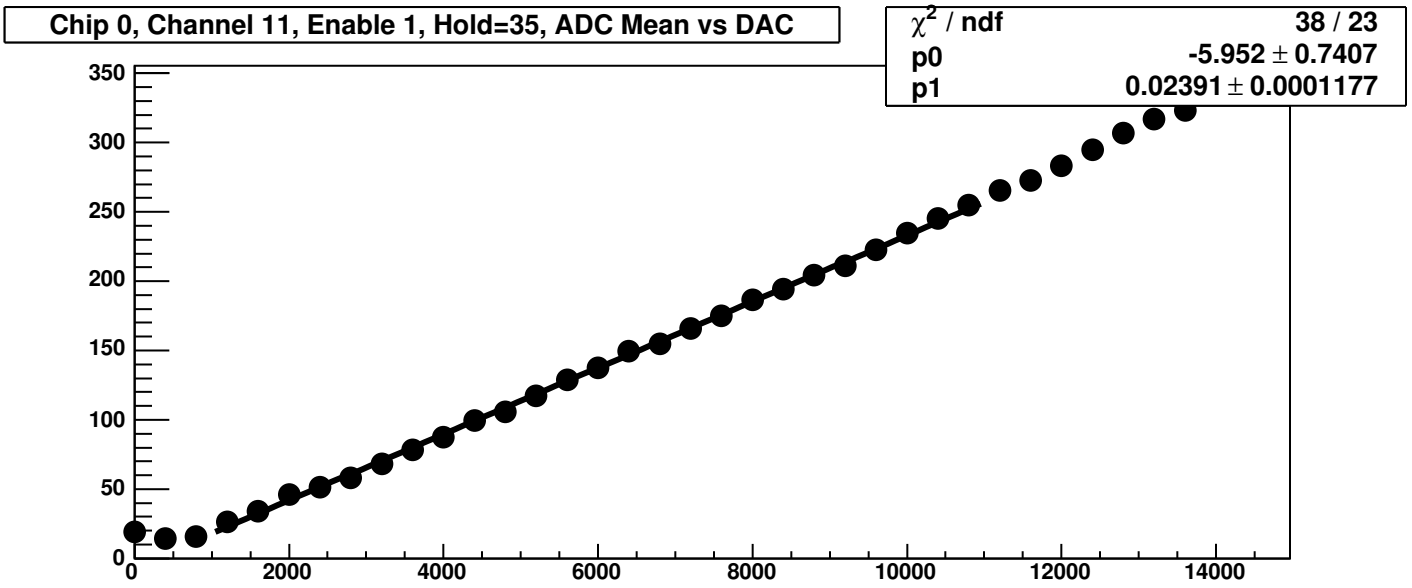

Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

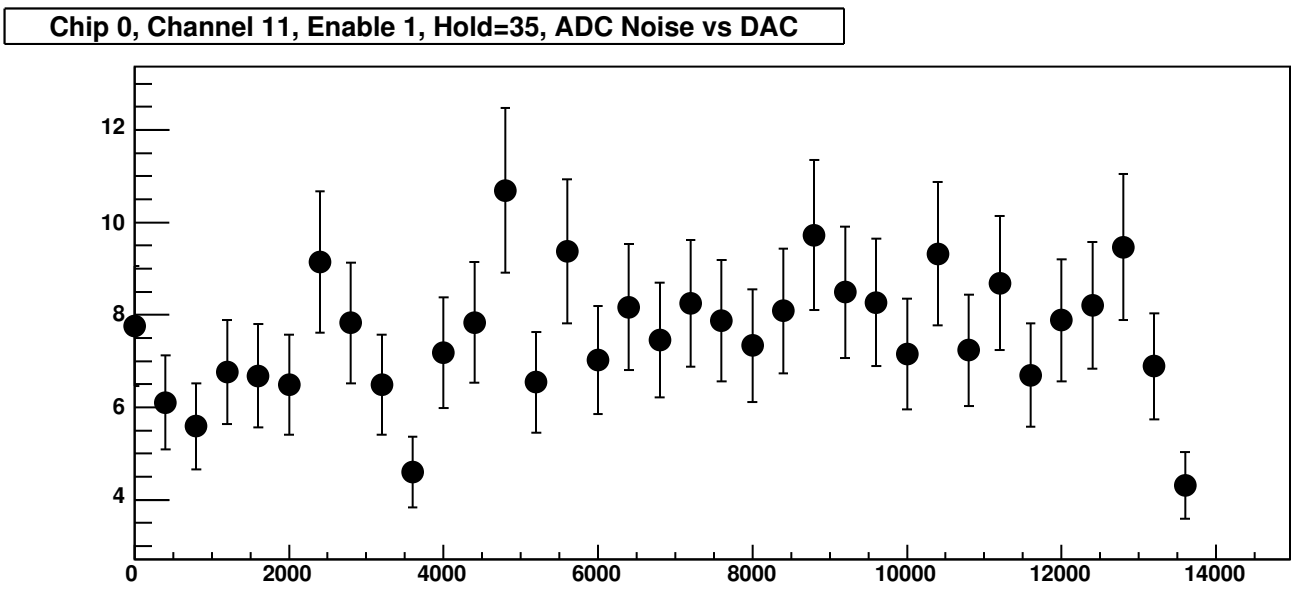

Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

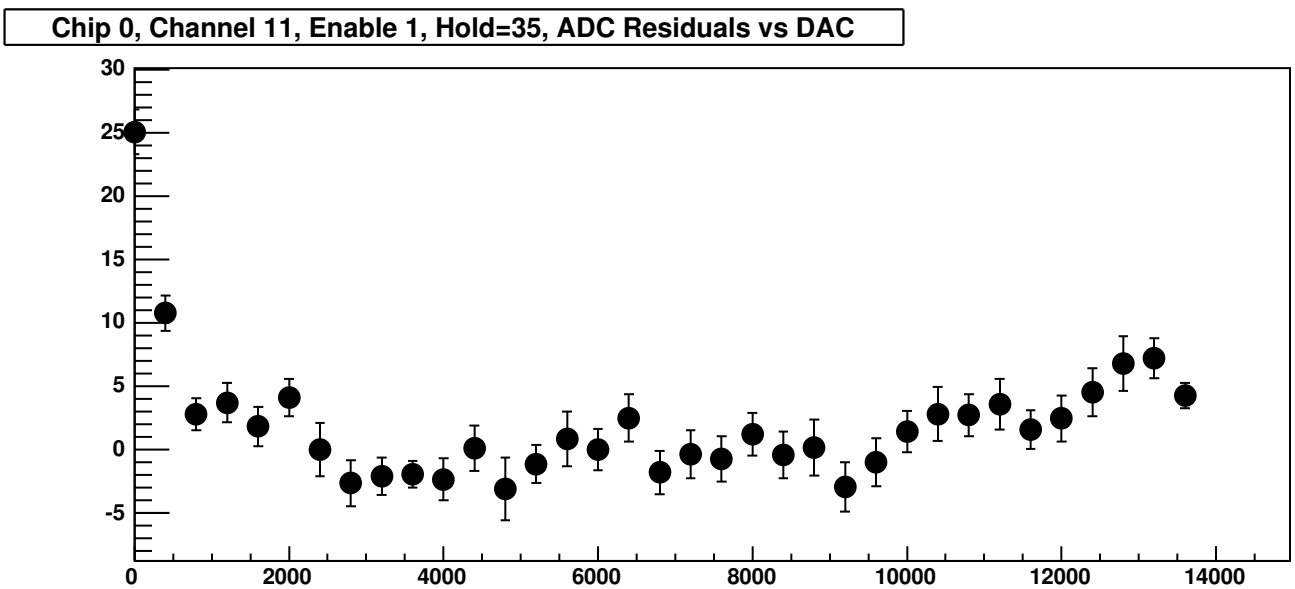

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1574 / 23
p0 -115.2 ± 0.9745
p1 2.372 ± 0.000172
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

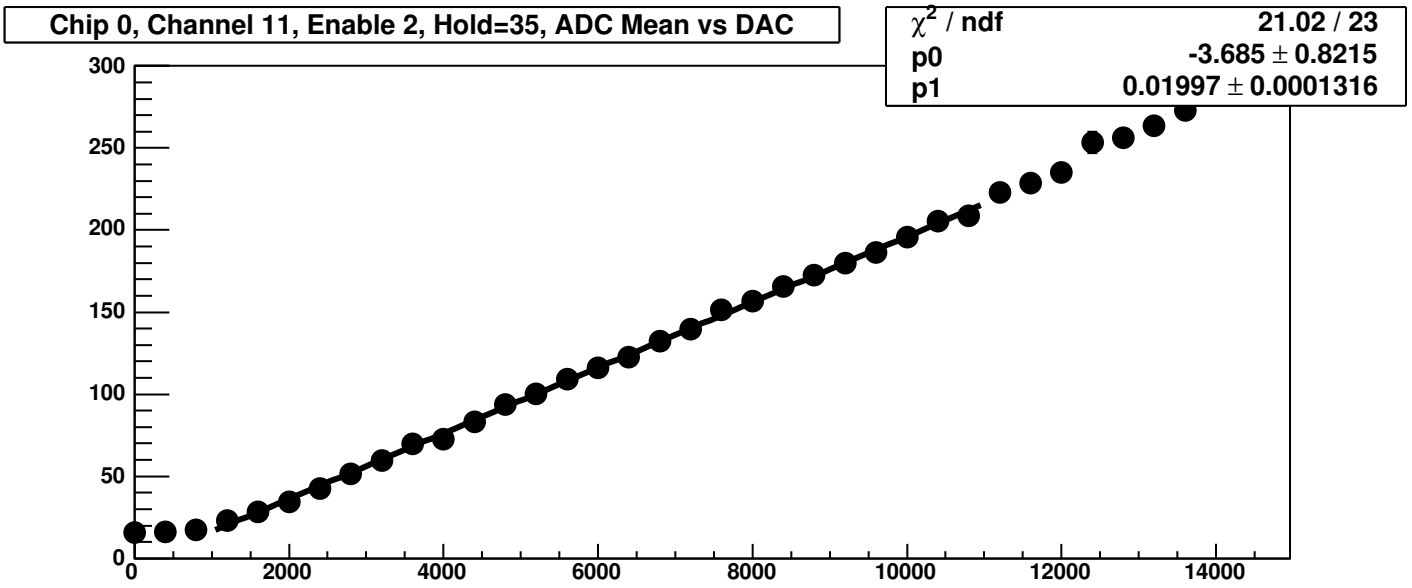

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

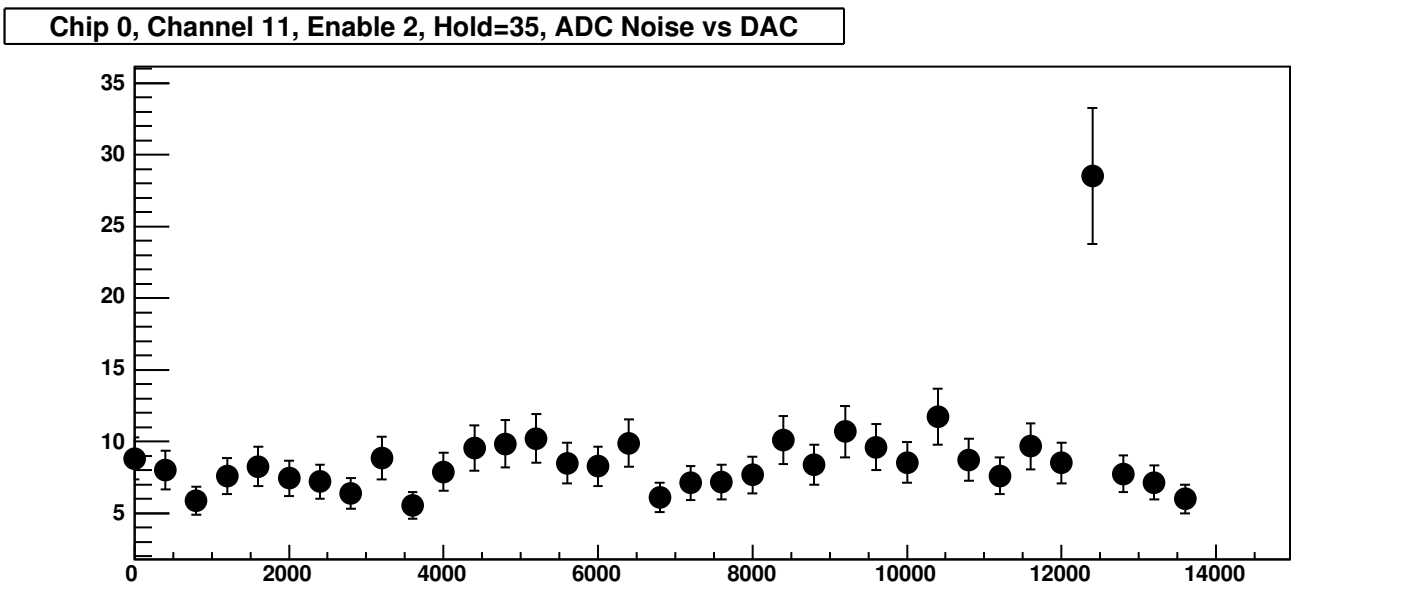

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

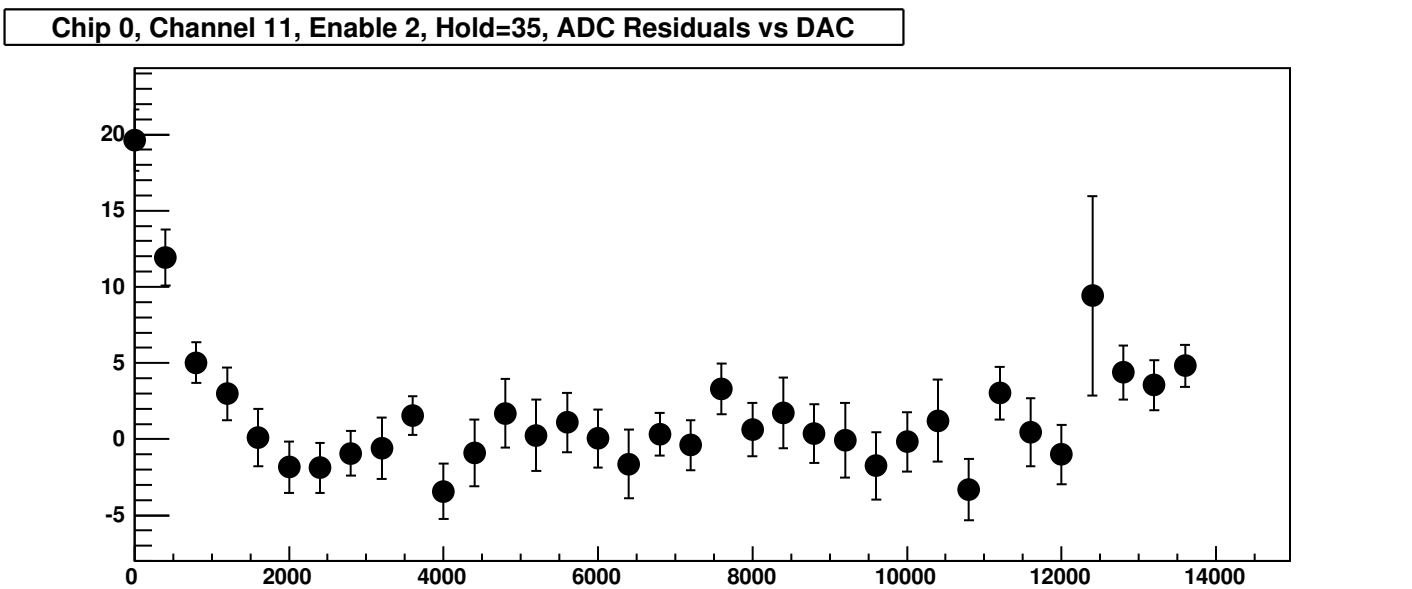

Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38 / 23
p0 -5.952 ± 0.7407
p1 0.02391 ± 0.0001177
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

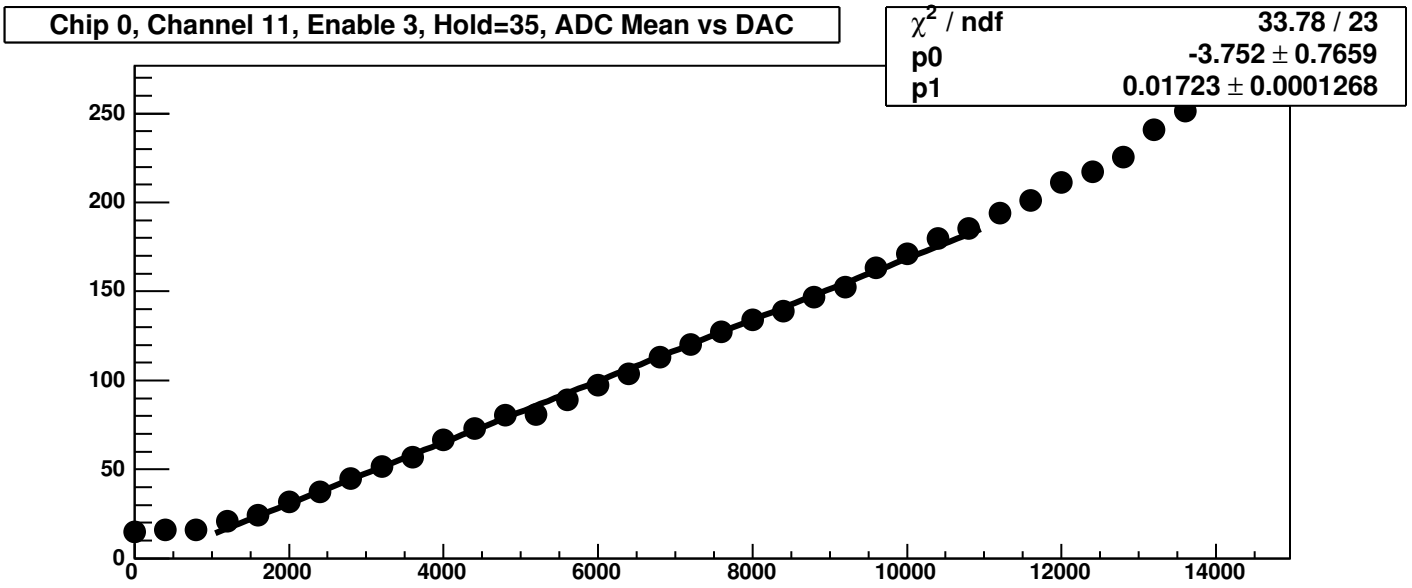

Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

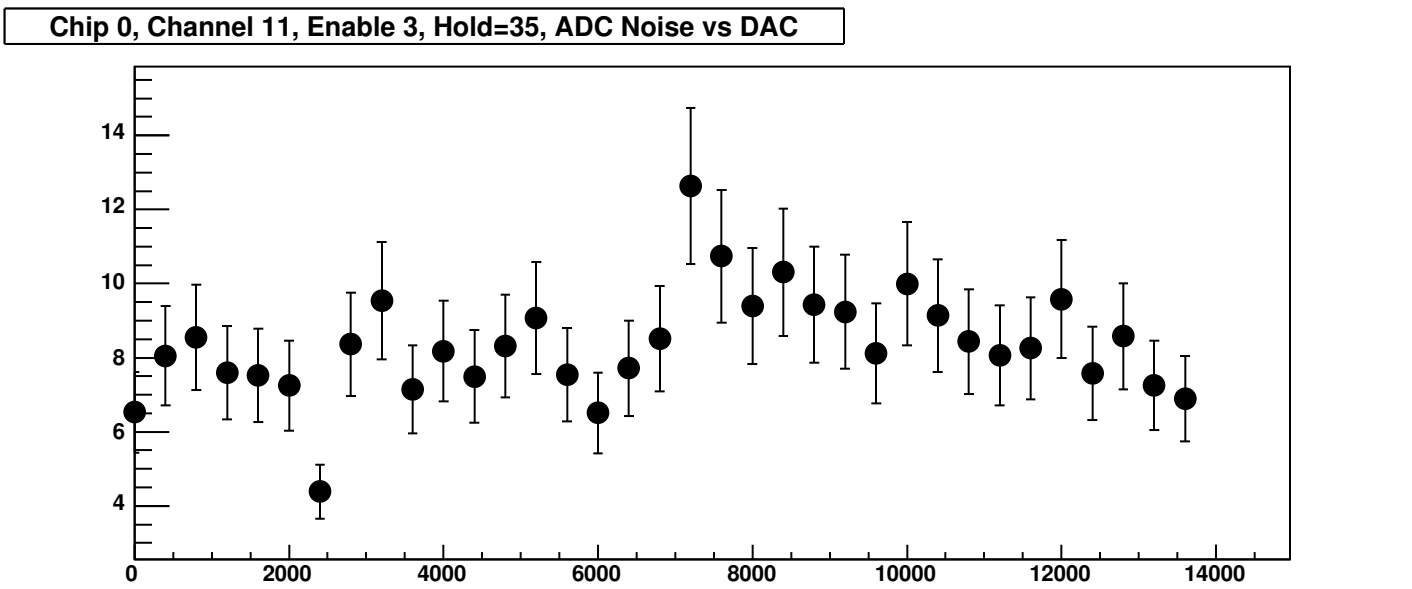

Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

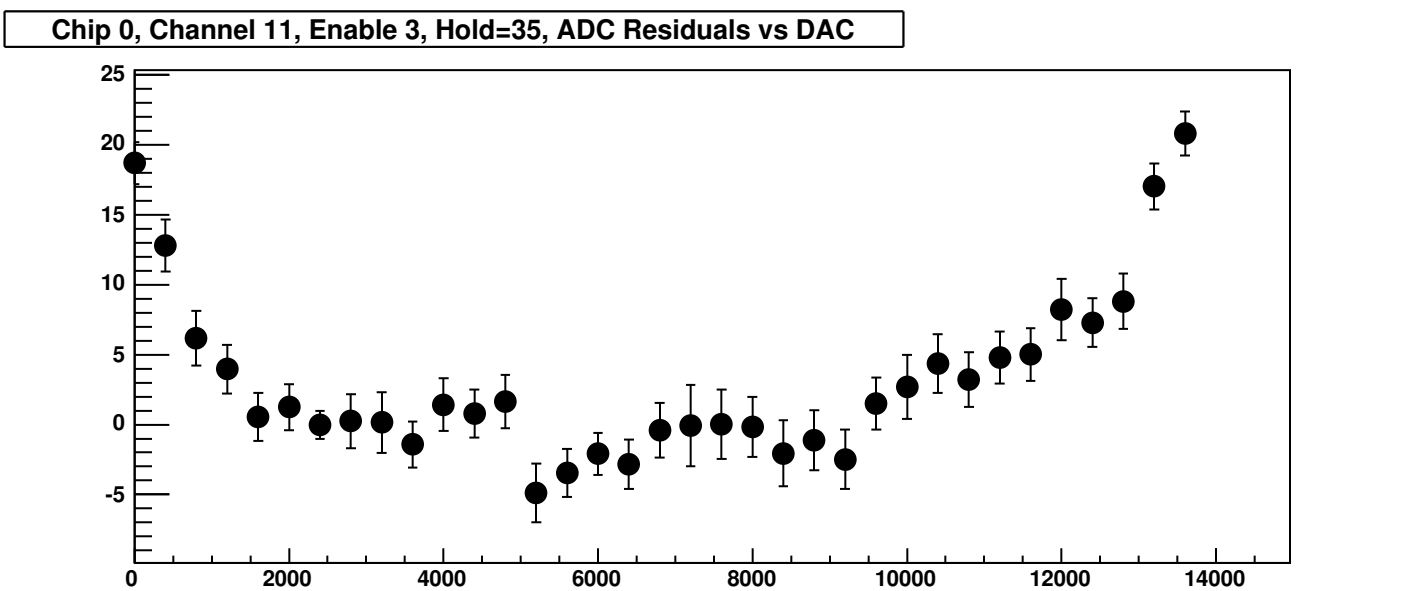

Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.02 / 23
p0 -3.685 ± 0.8215
p1 0.01997 ± 0.0001316
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

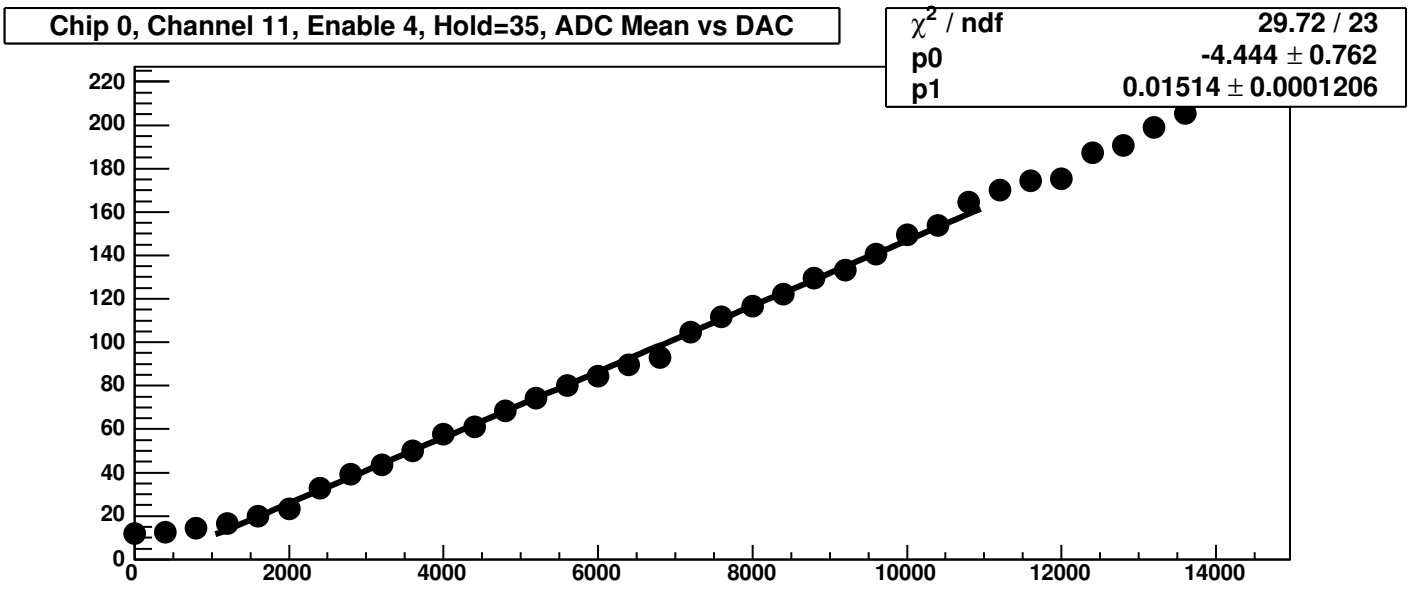

Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

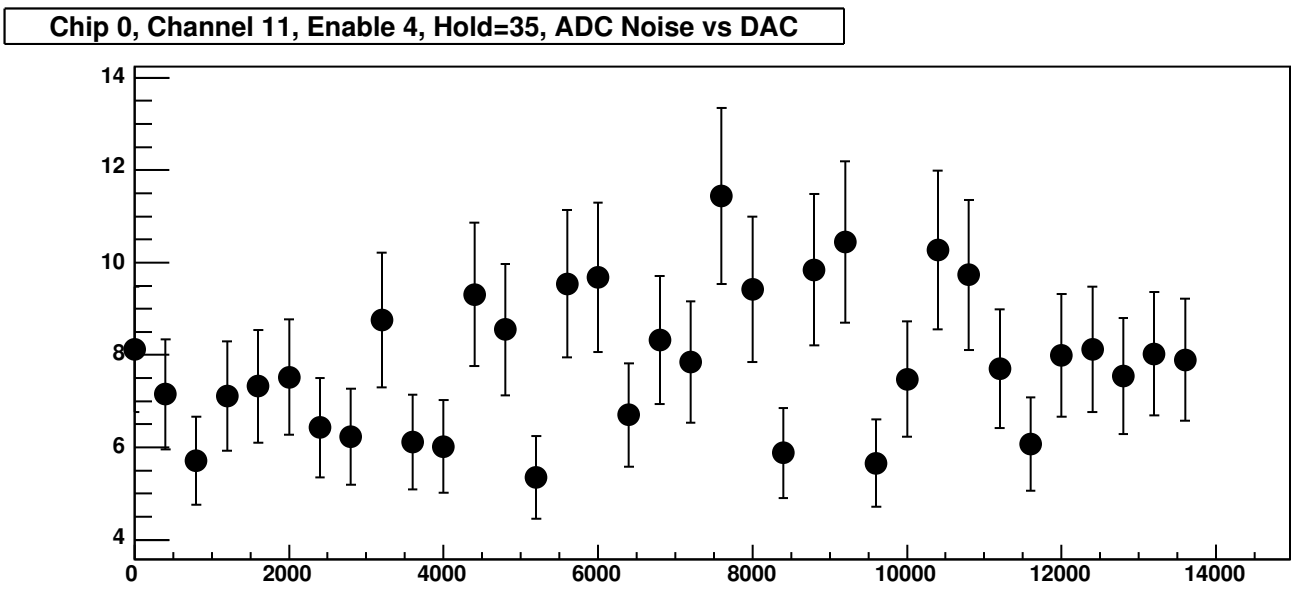

Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

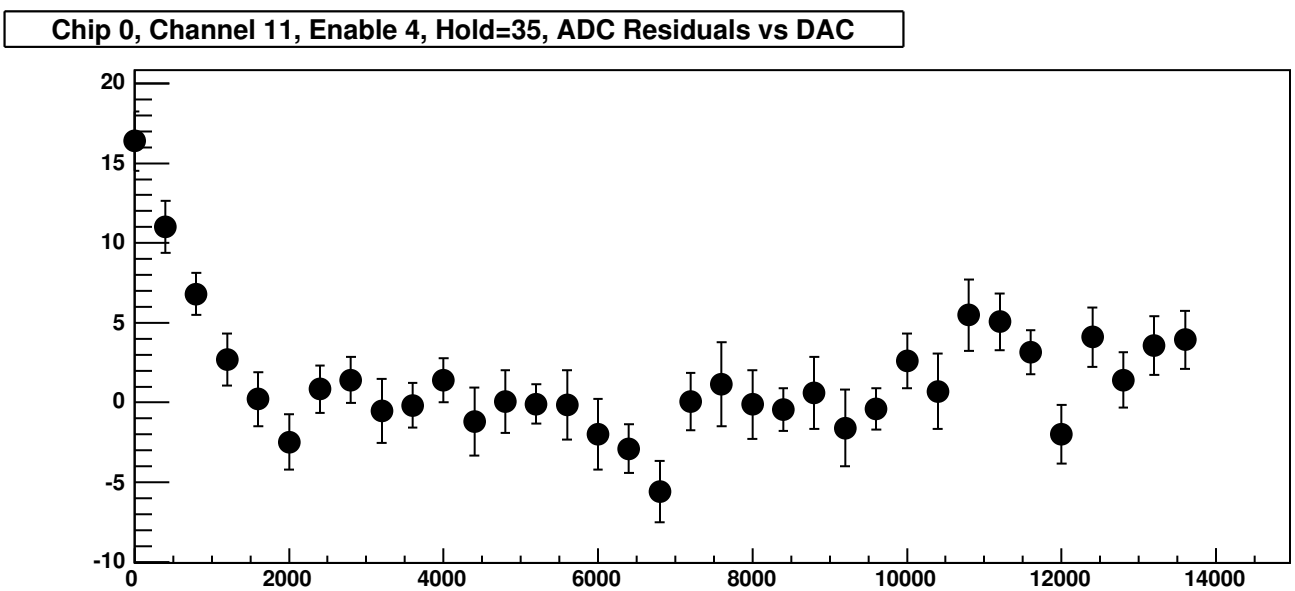

Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.78 / 23
p0 -3.752 ± 0.7659
p1 0.01723 ± 0.0001268
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

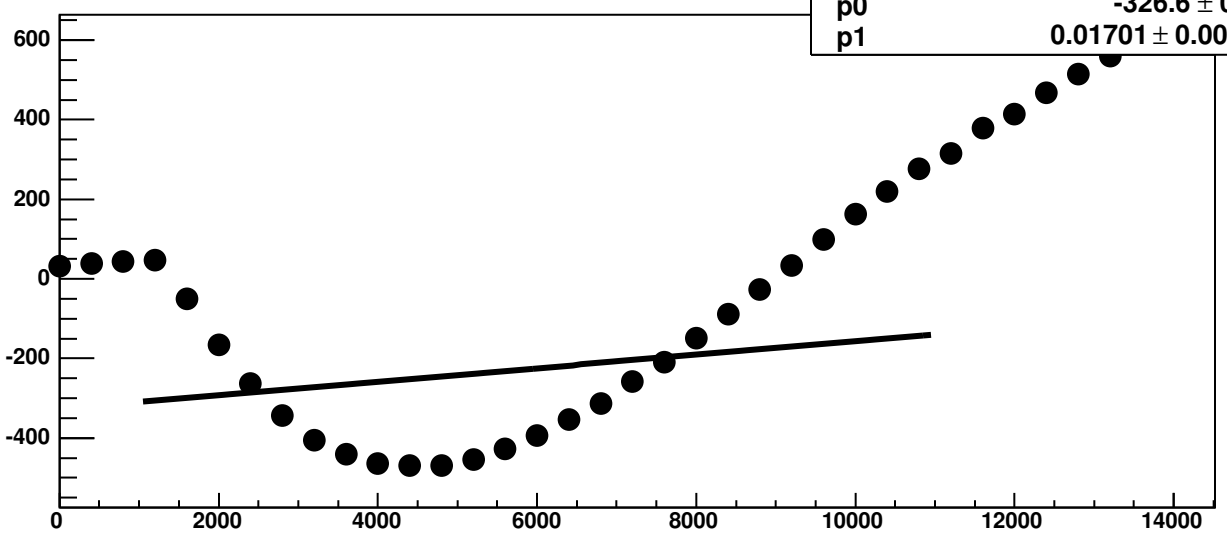

Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.72 / 23
p0 -4.444 ± 0.762
p1 0.01514 ± 0.0001206
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

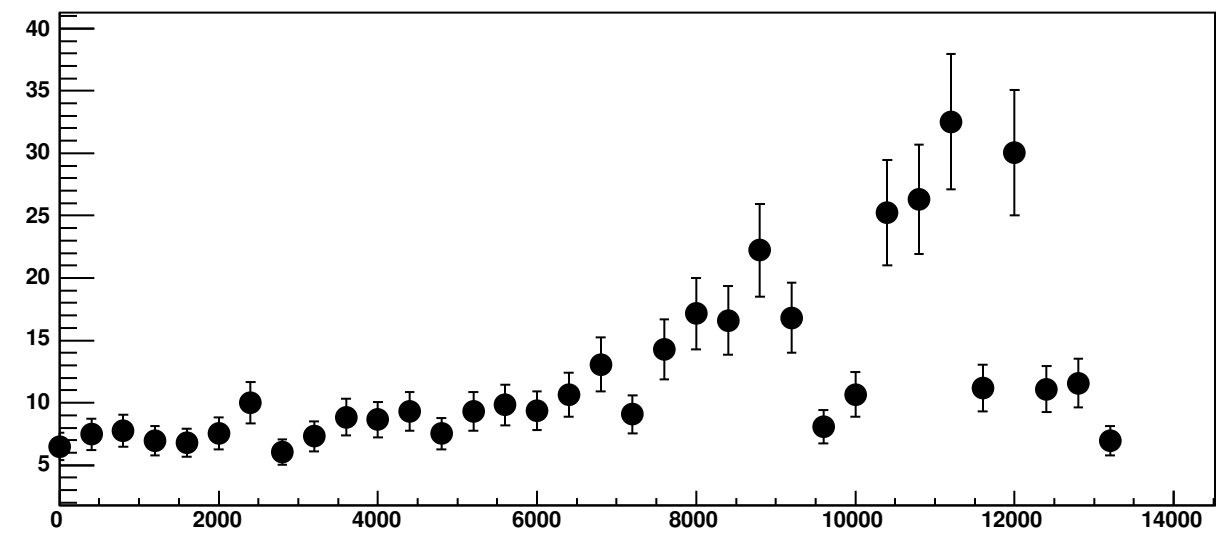

Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

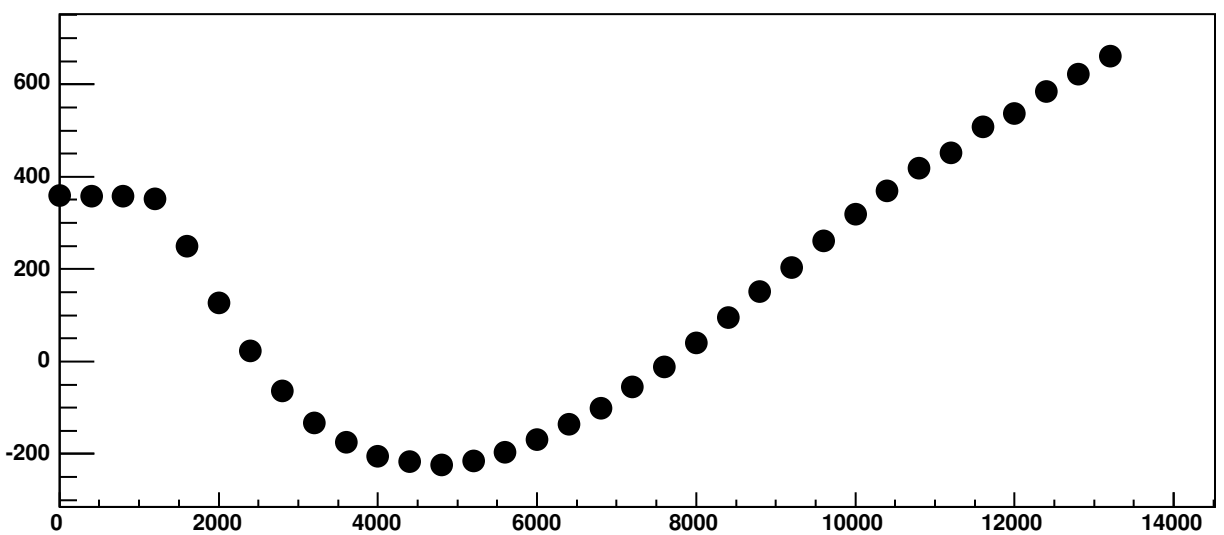
χ^2 / ndf	2.128e+05 / 23
p0	-326.6 ± 0.8803
p1	0.01701 ± 0.0001659

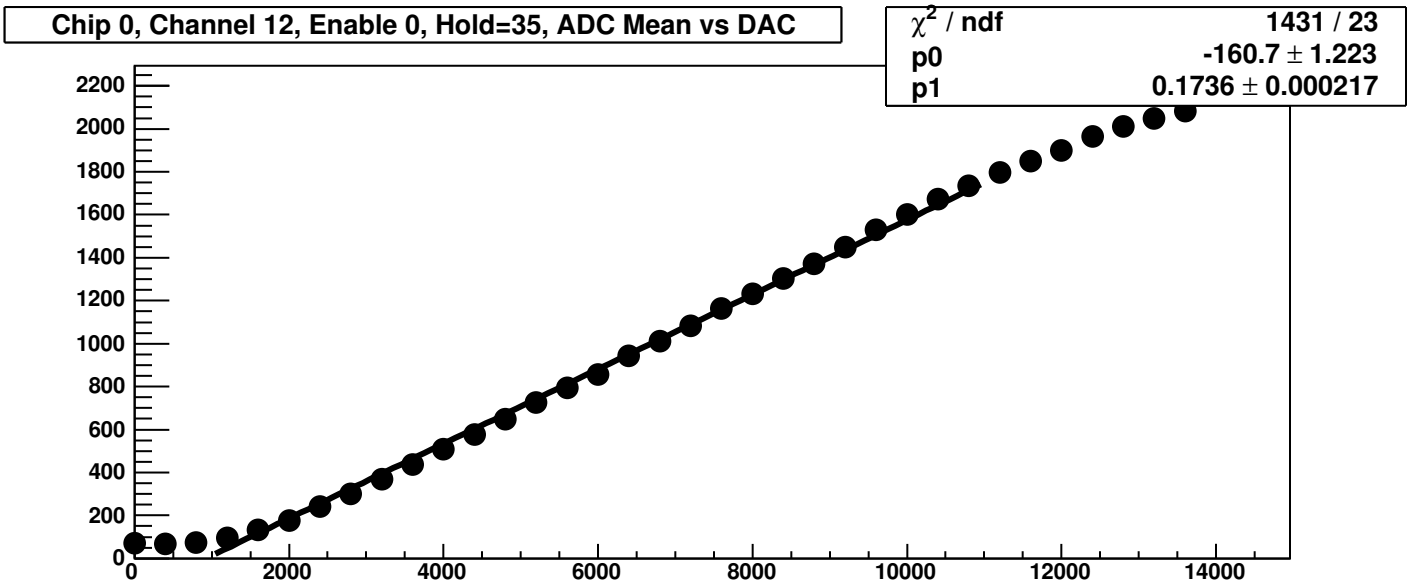


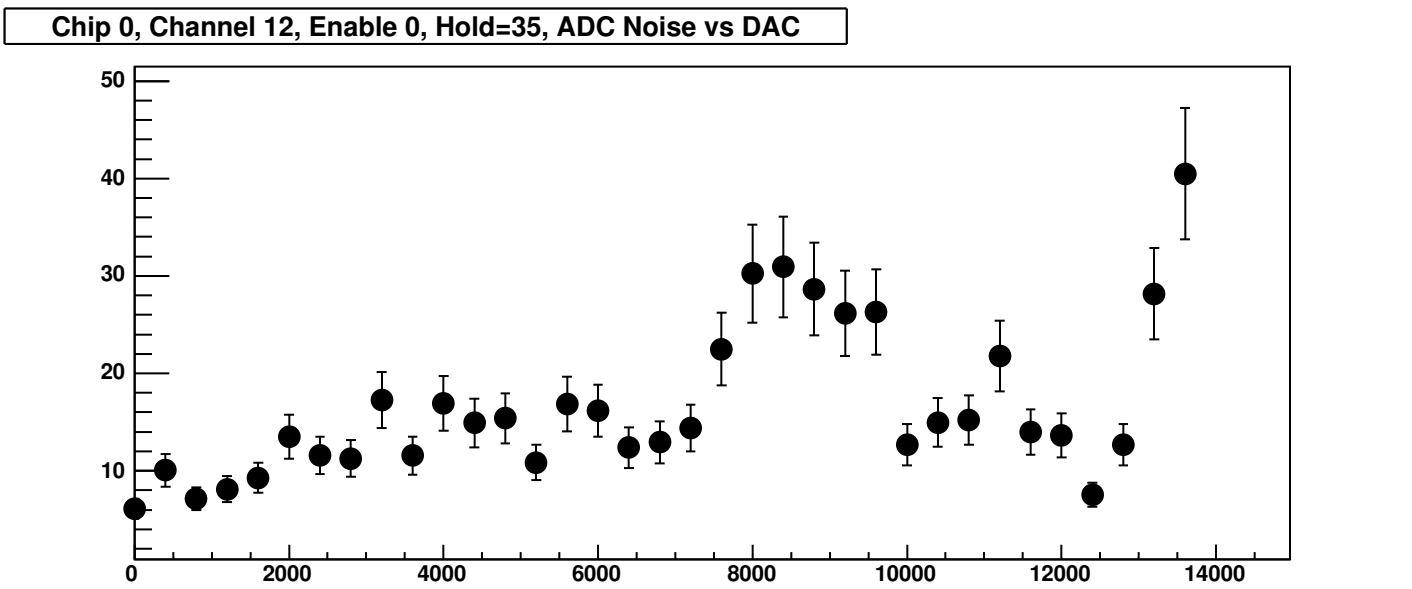
Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

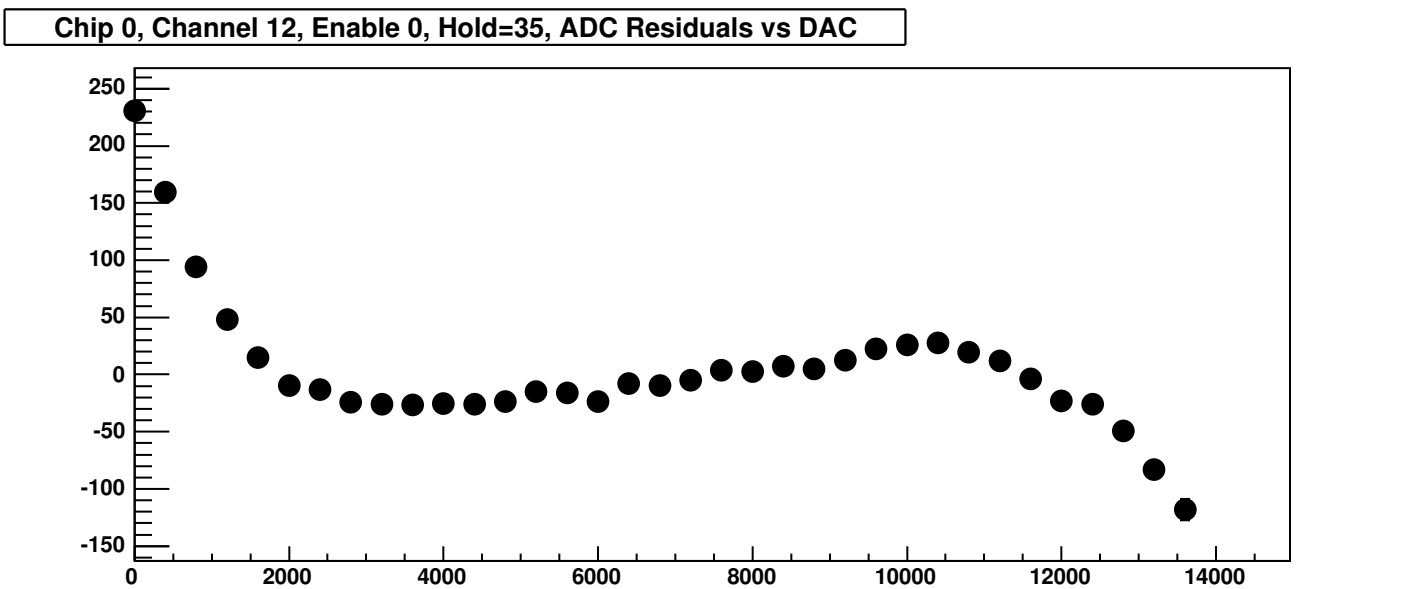


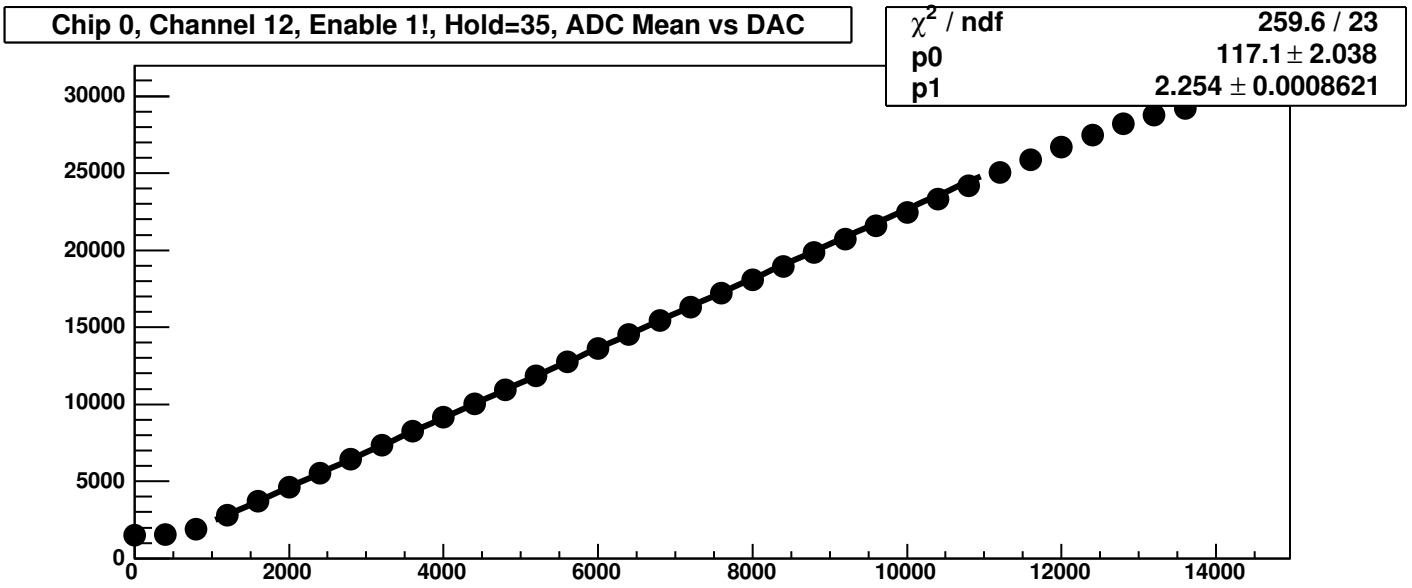
Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

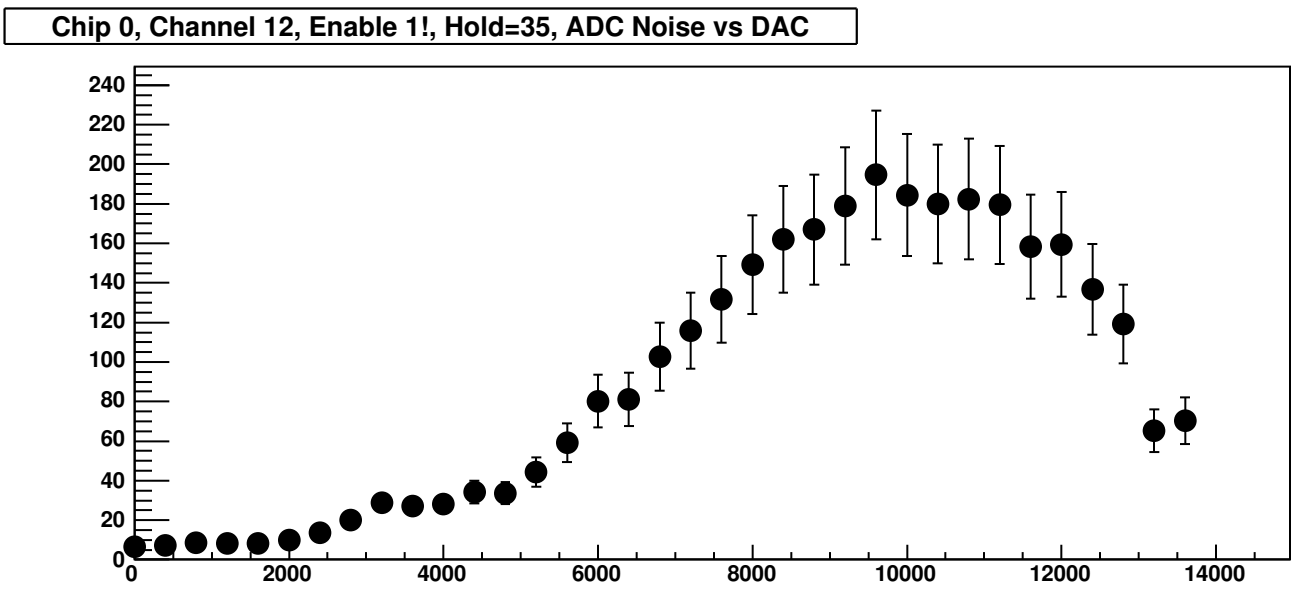


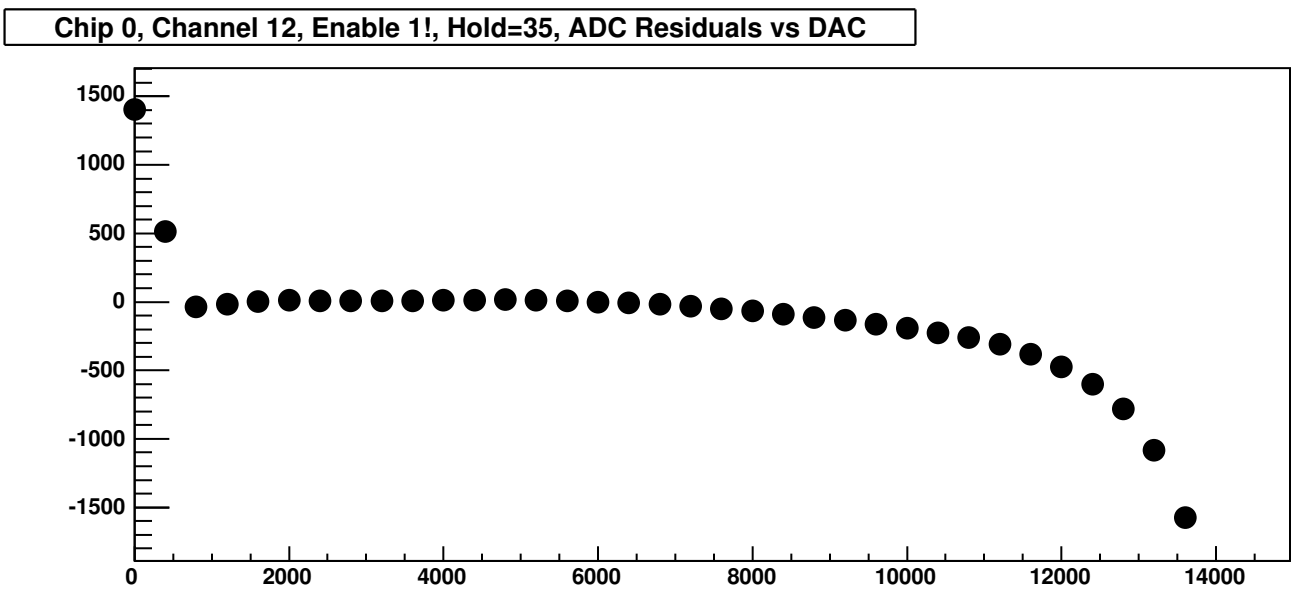

Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1431 / 23
p0 -160.7 ± 1.223
p1 0.1736 ± 0.000217
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

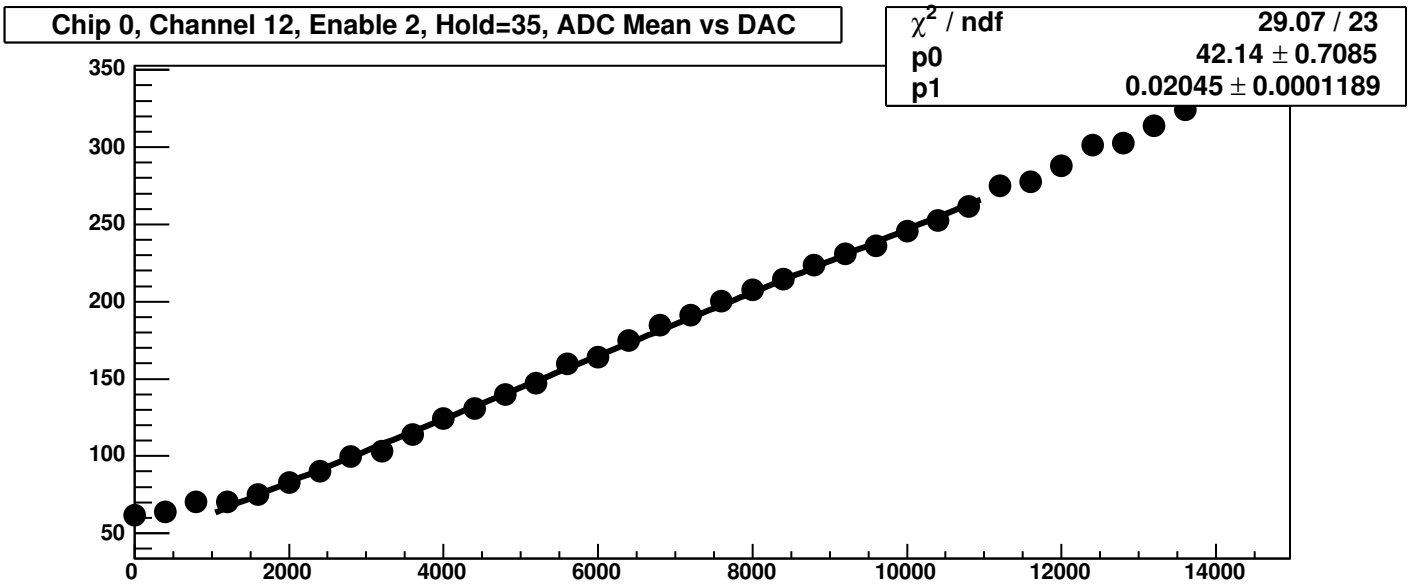

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

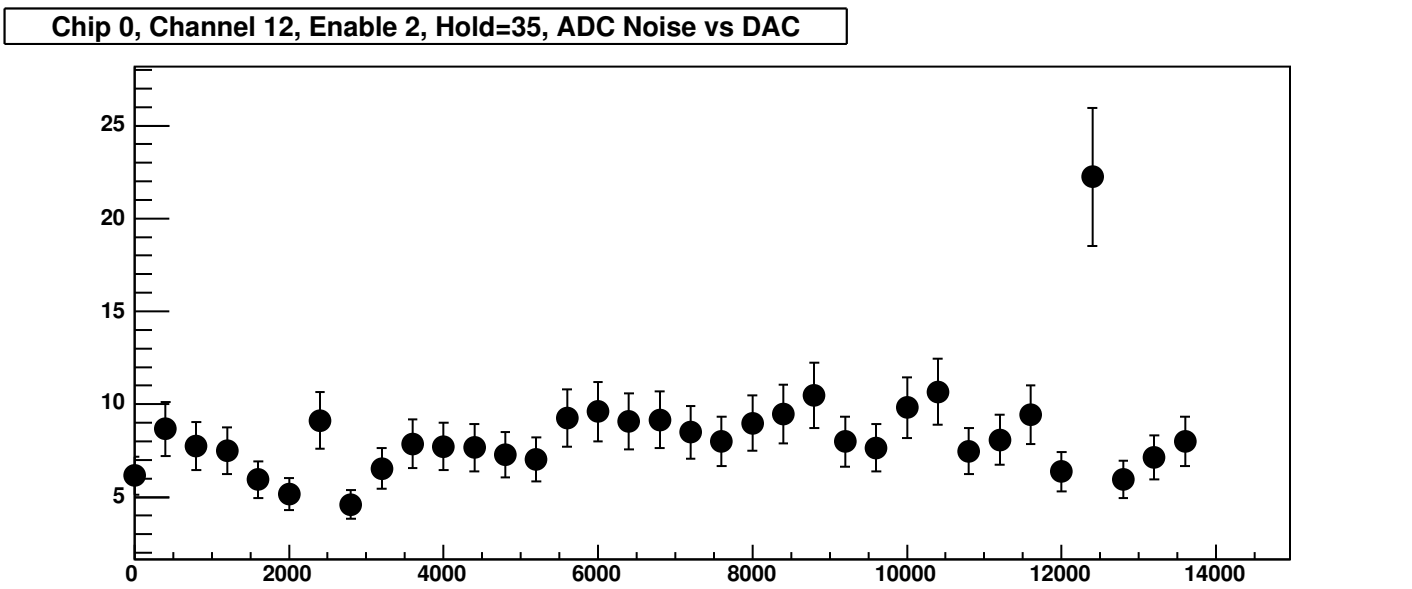

Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

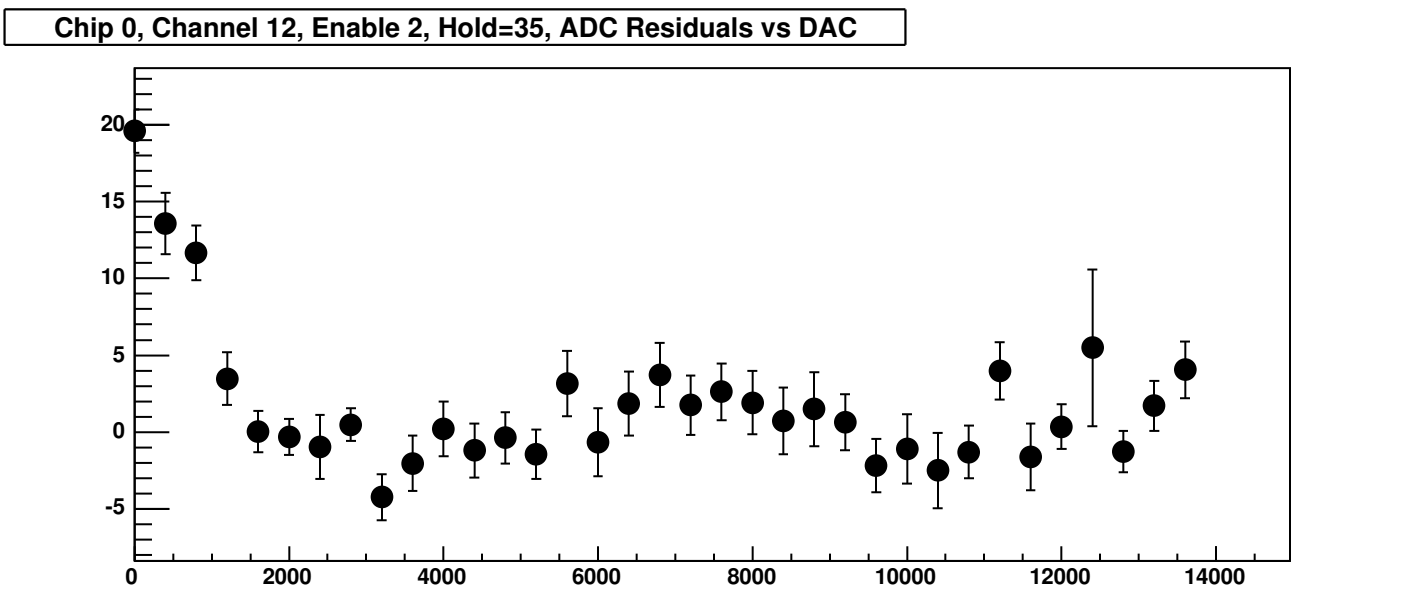

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 259.6 / 23
p0 117.1 ± 2.038
p1 2.254 ± 0.0008621
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

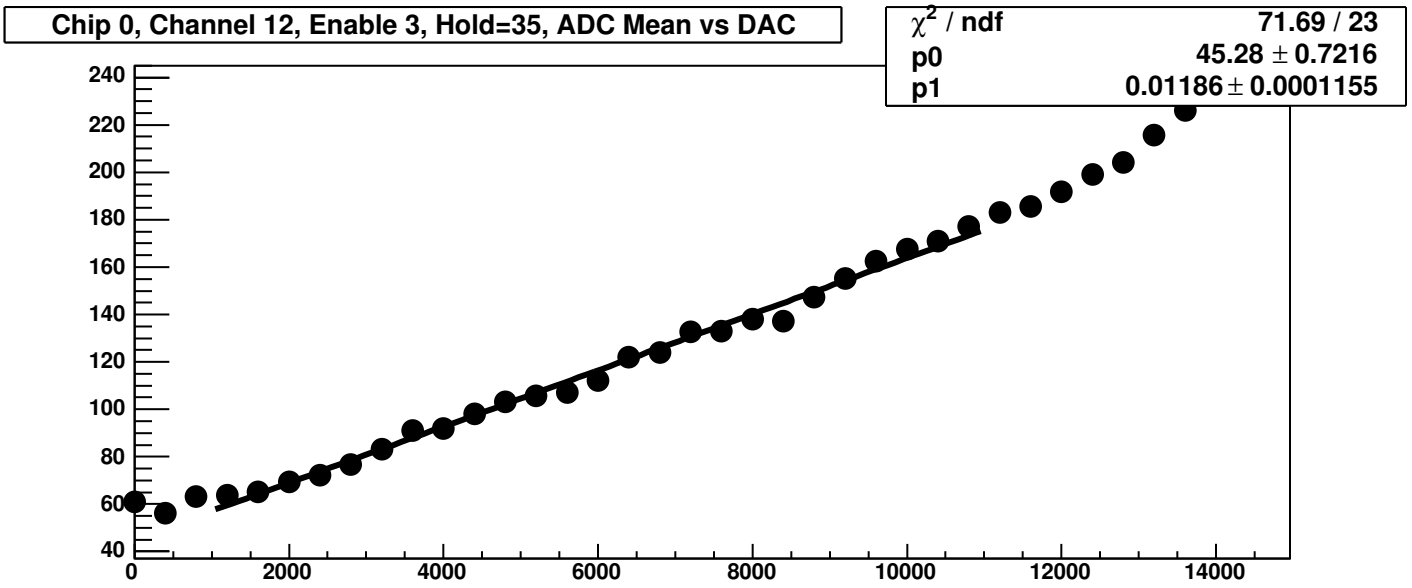

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

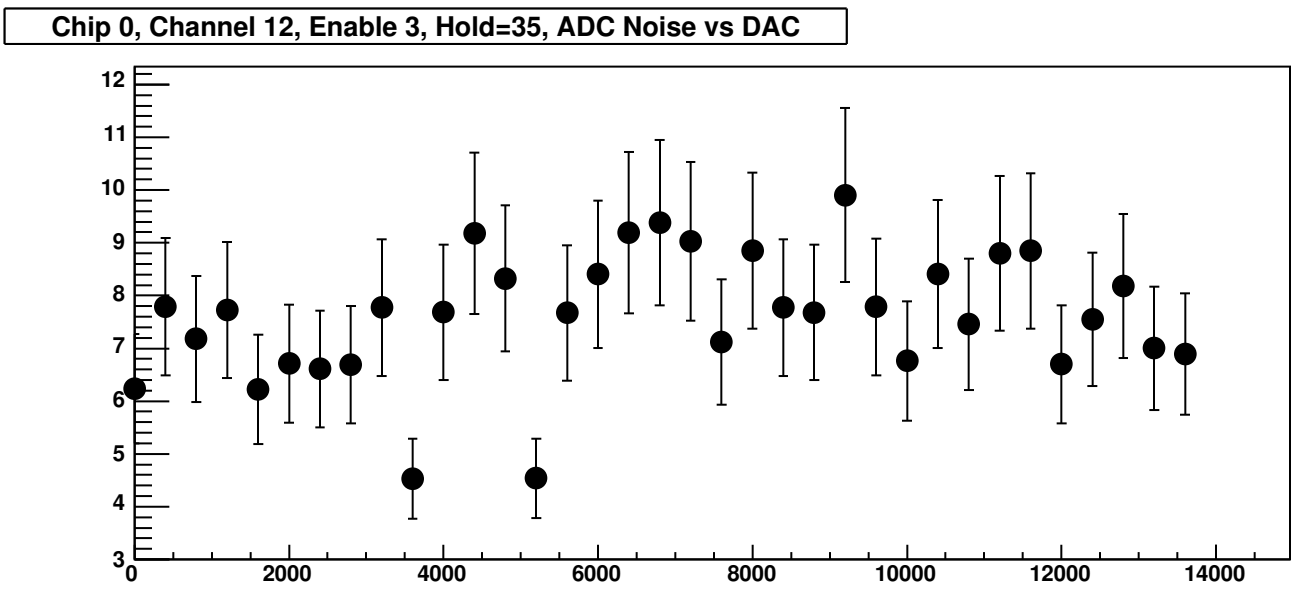

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

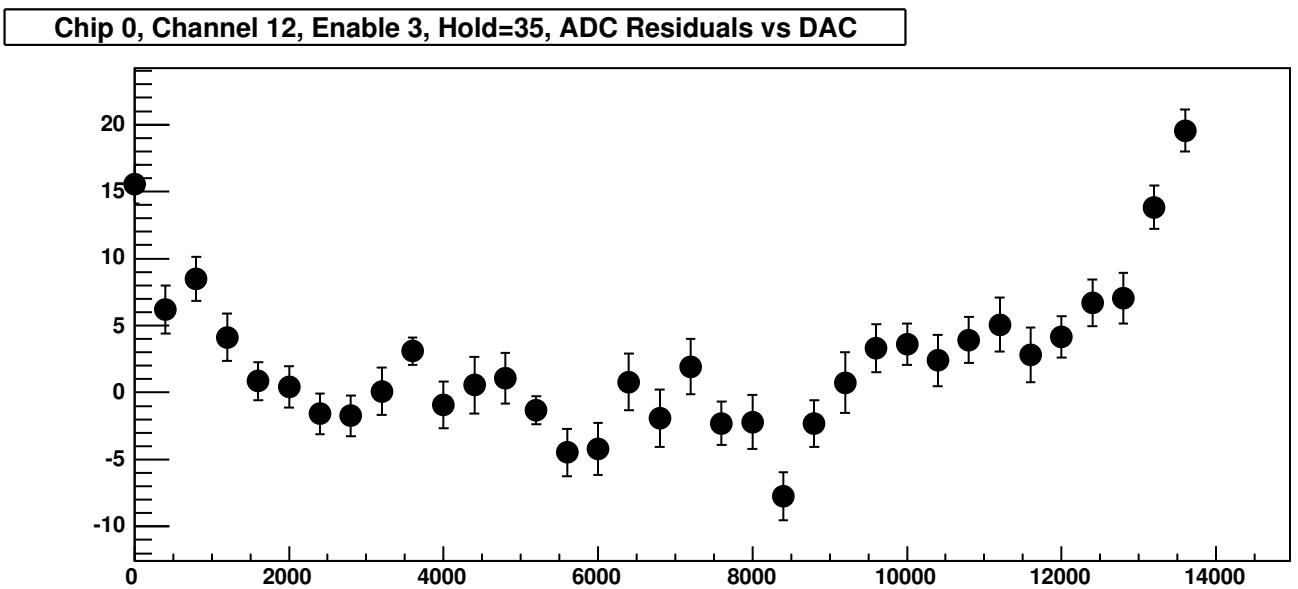

Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.07 / 23
p0 42.14 ± 0.7085
p1 0.02045 ± 0.0001189
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

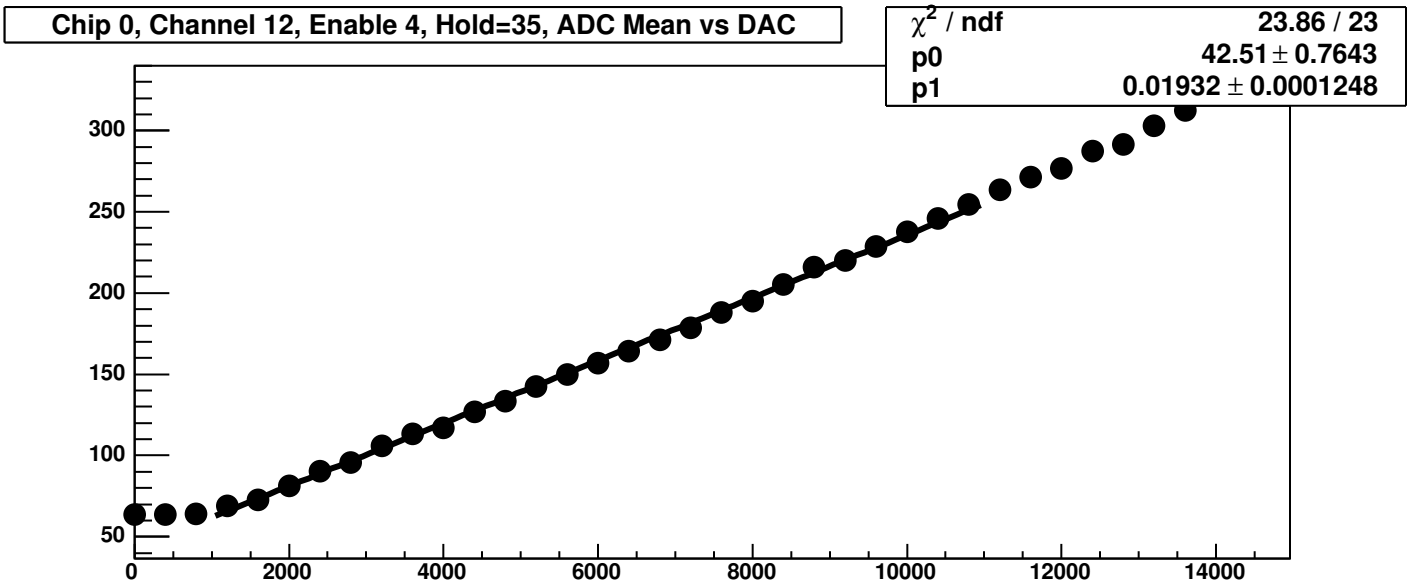

Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

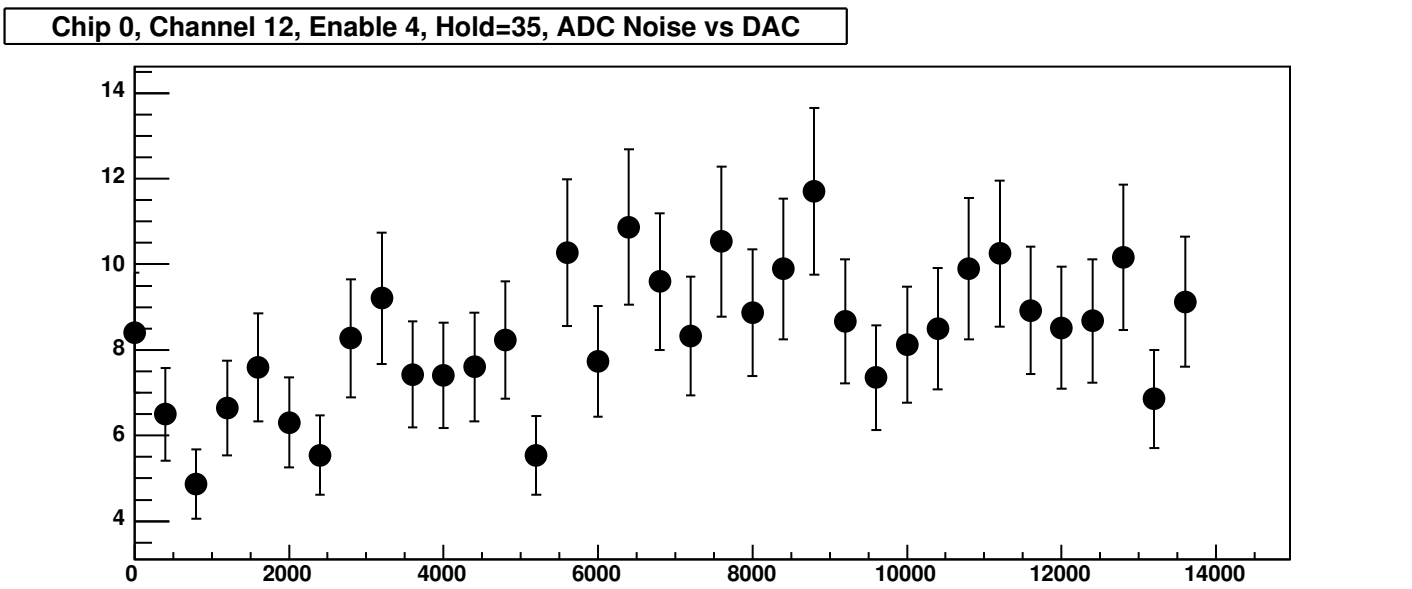

Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

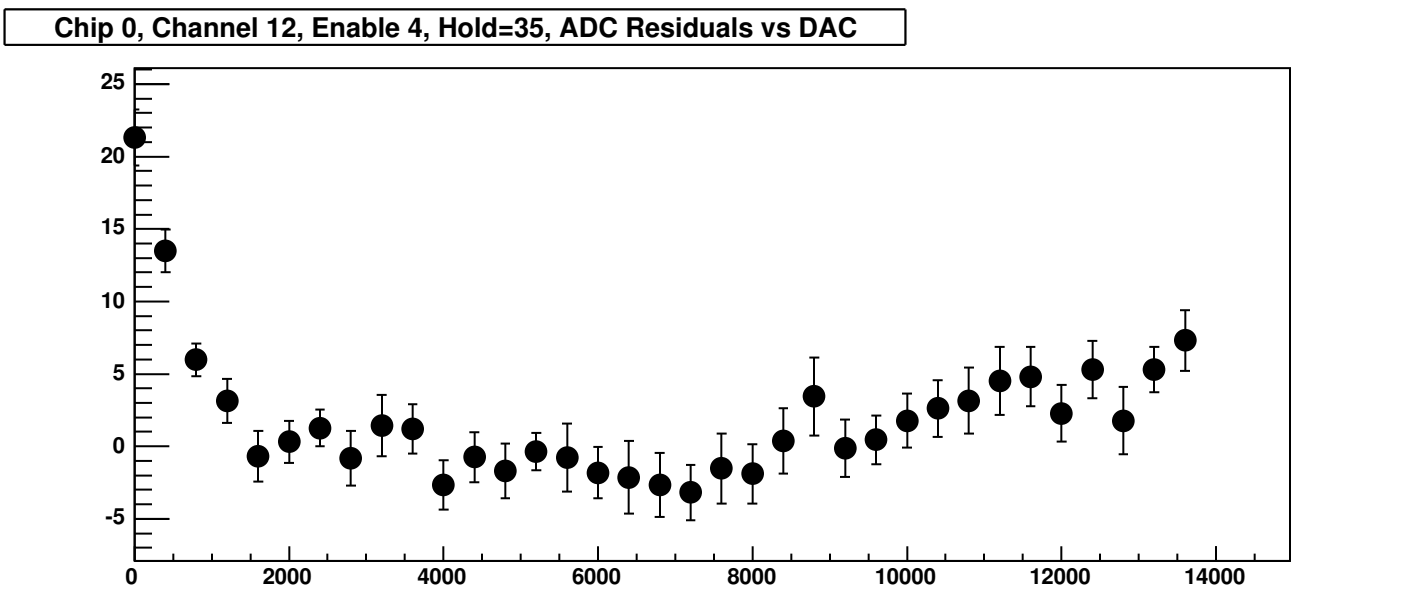

Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 71.69 / 23
p0 45.28 ± 0.7216
p1 0.01186 ± 0.0001155
240
220
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

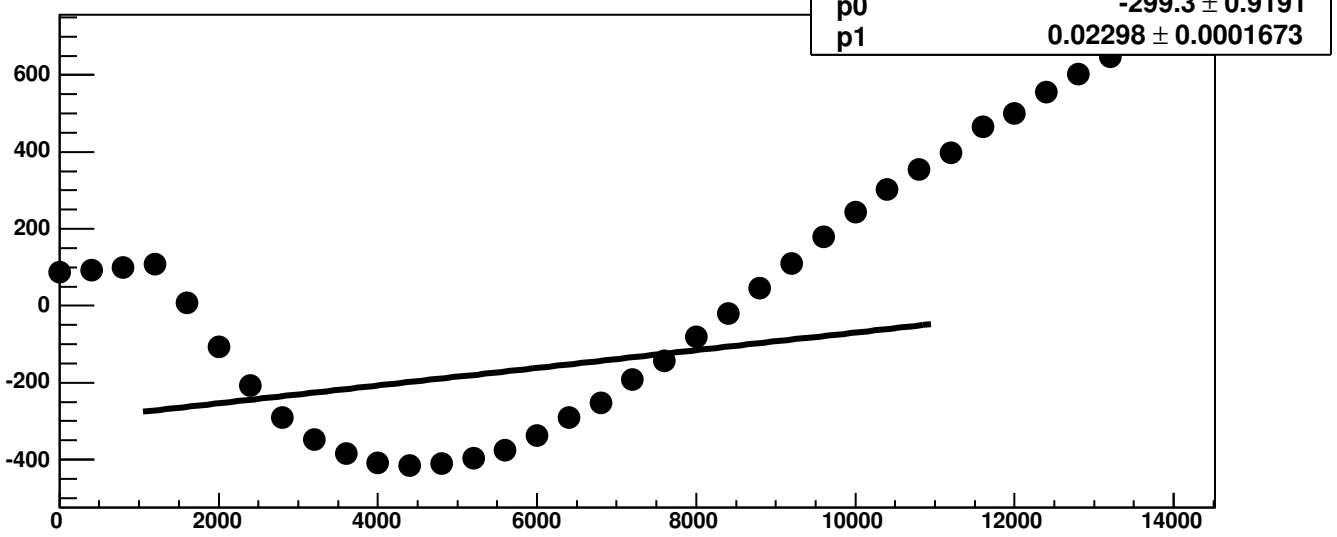

Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.86 / 23
p0 42.51 ± 0.7643
p1 0.01932 ± 0.0001248
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

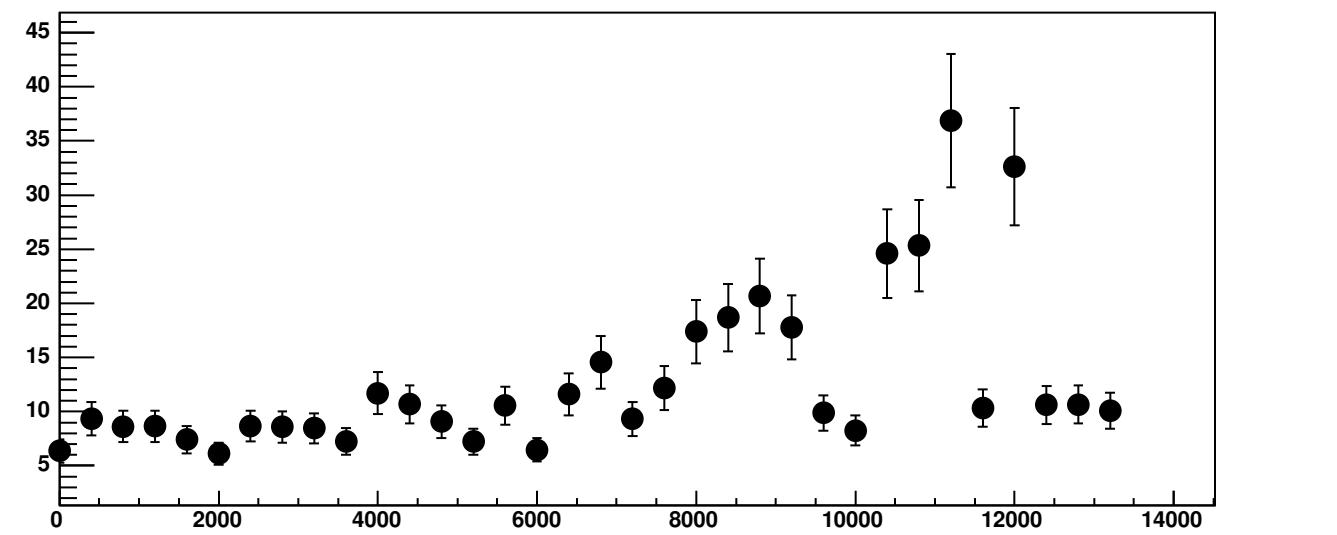

Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

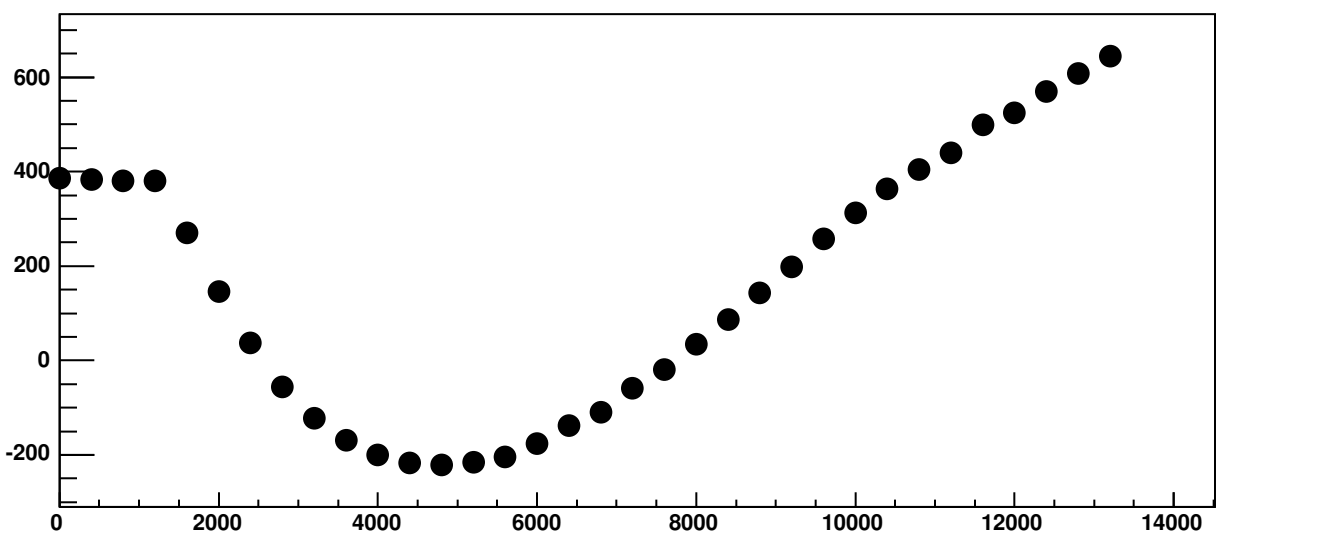
Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

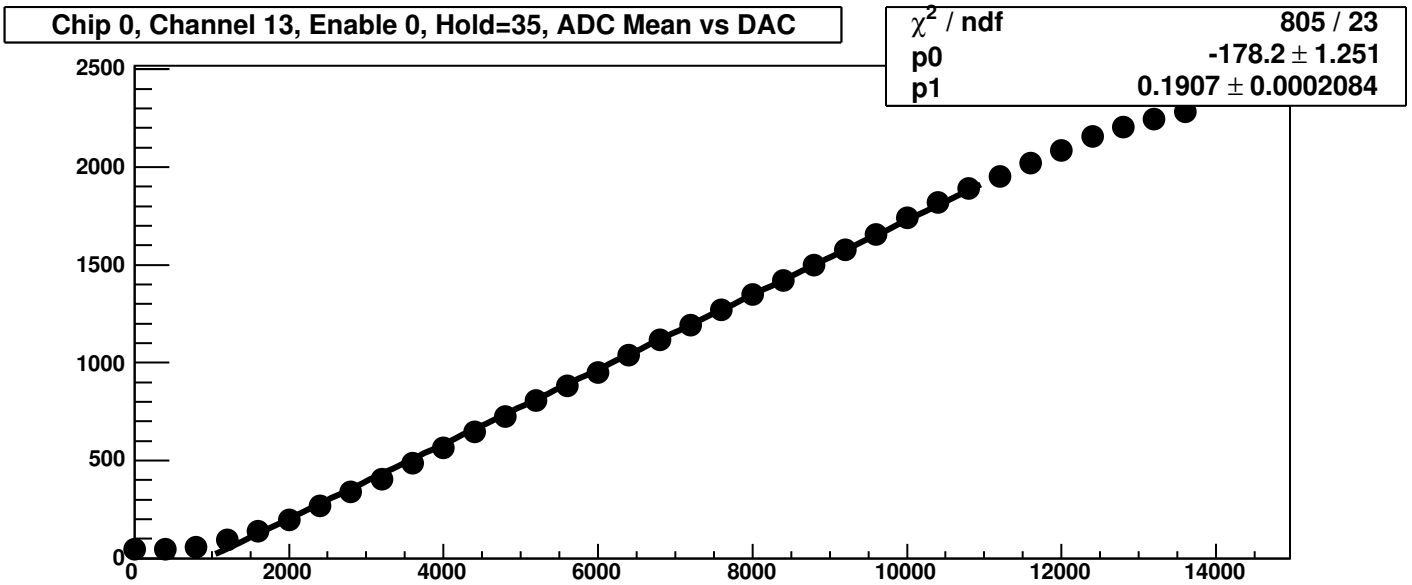


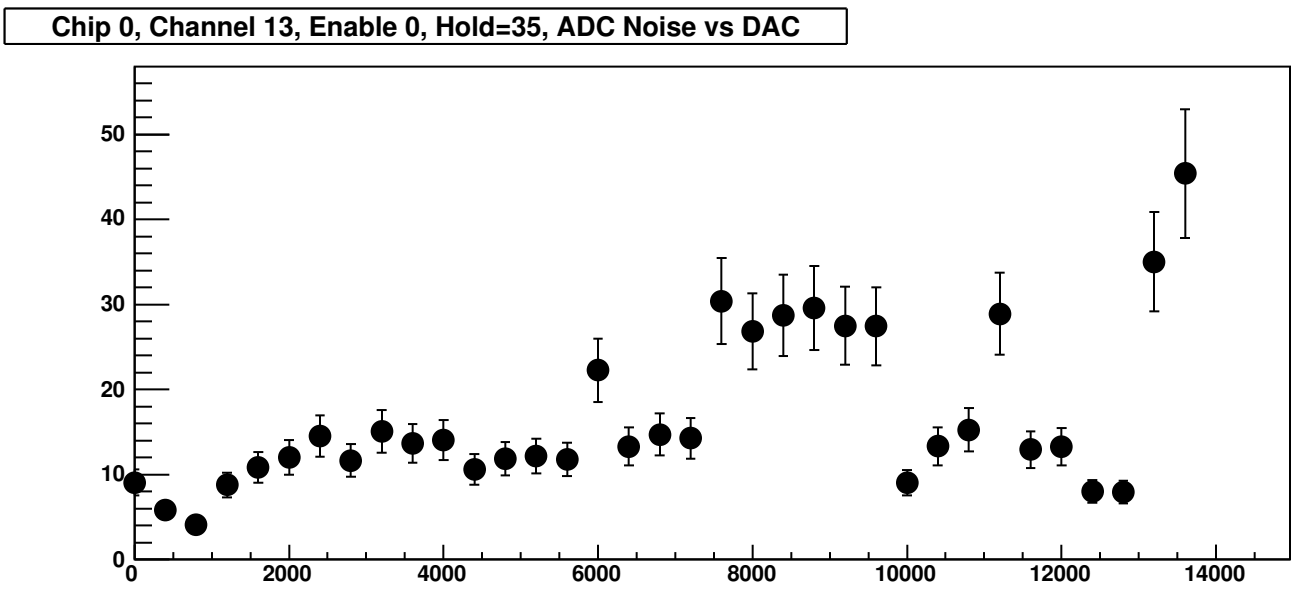
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

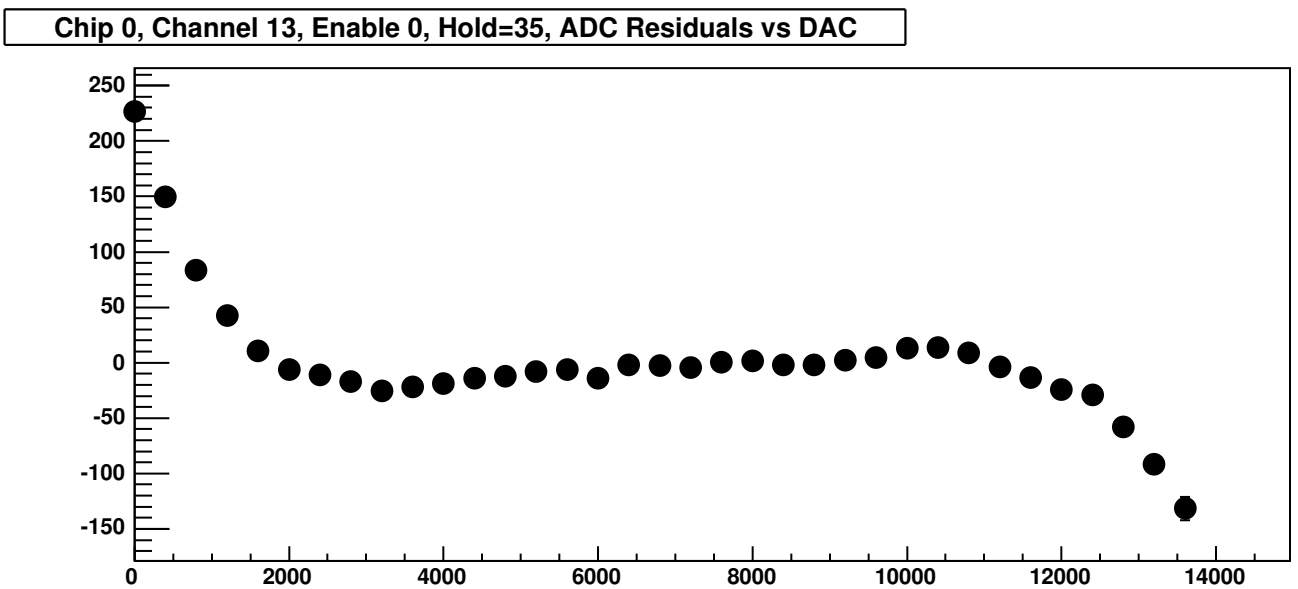


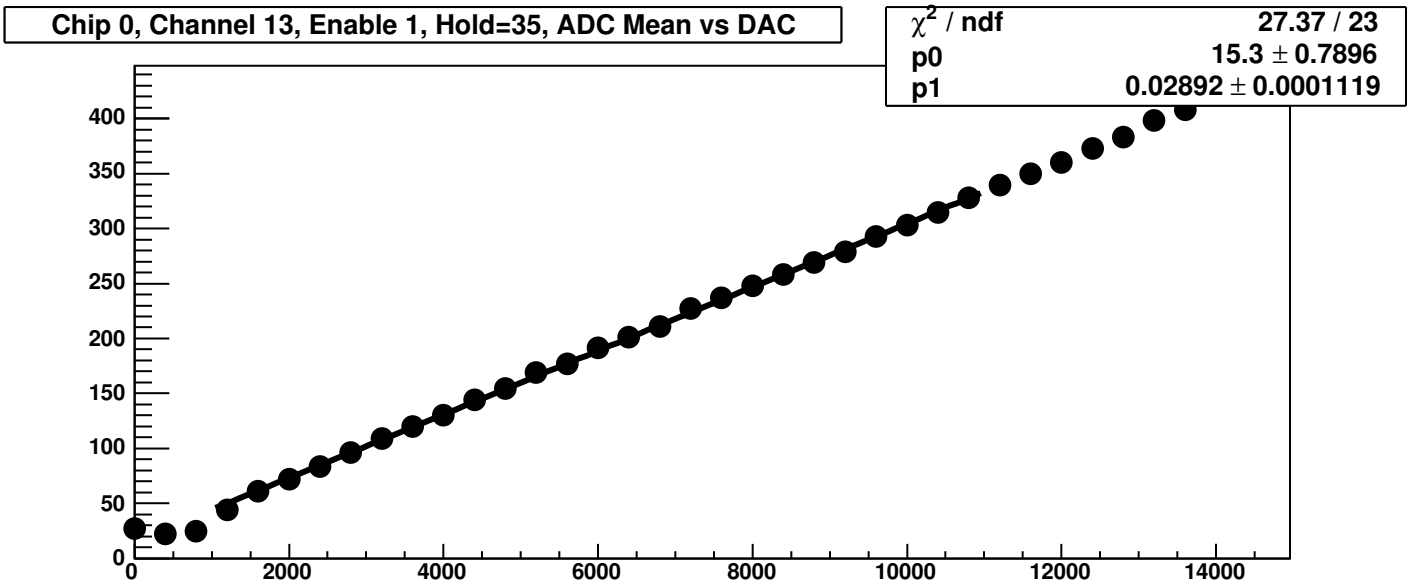
Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

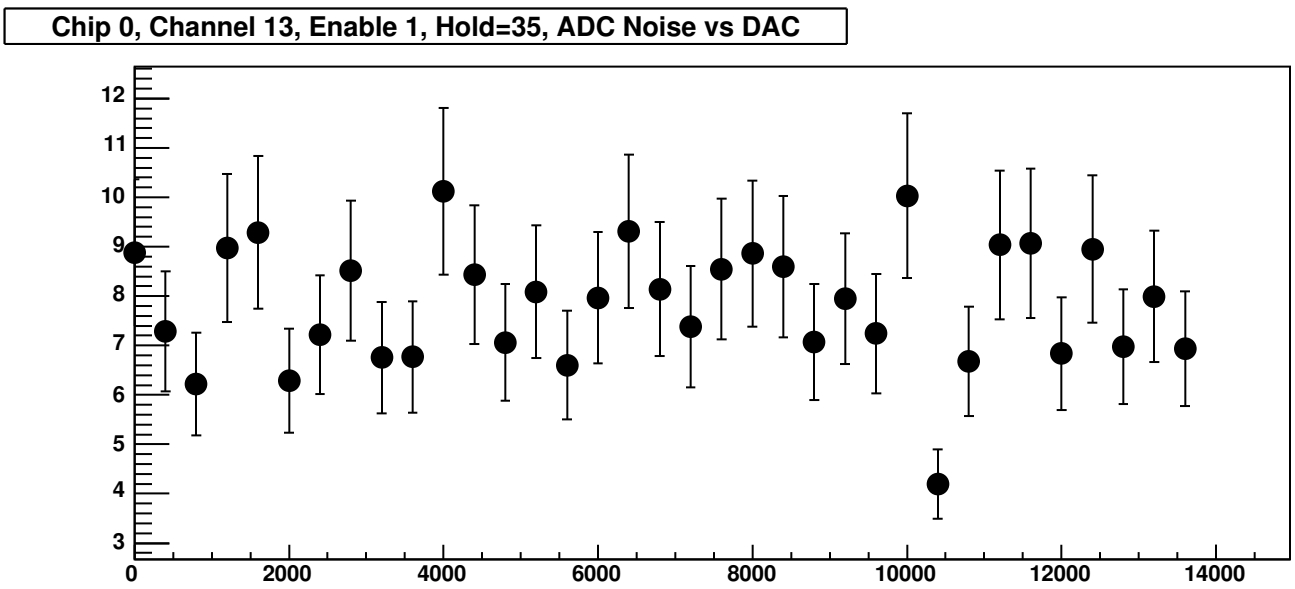


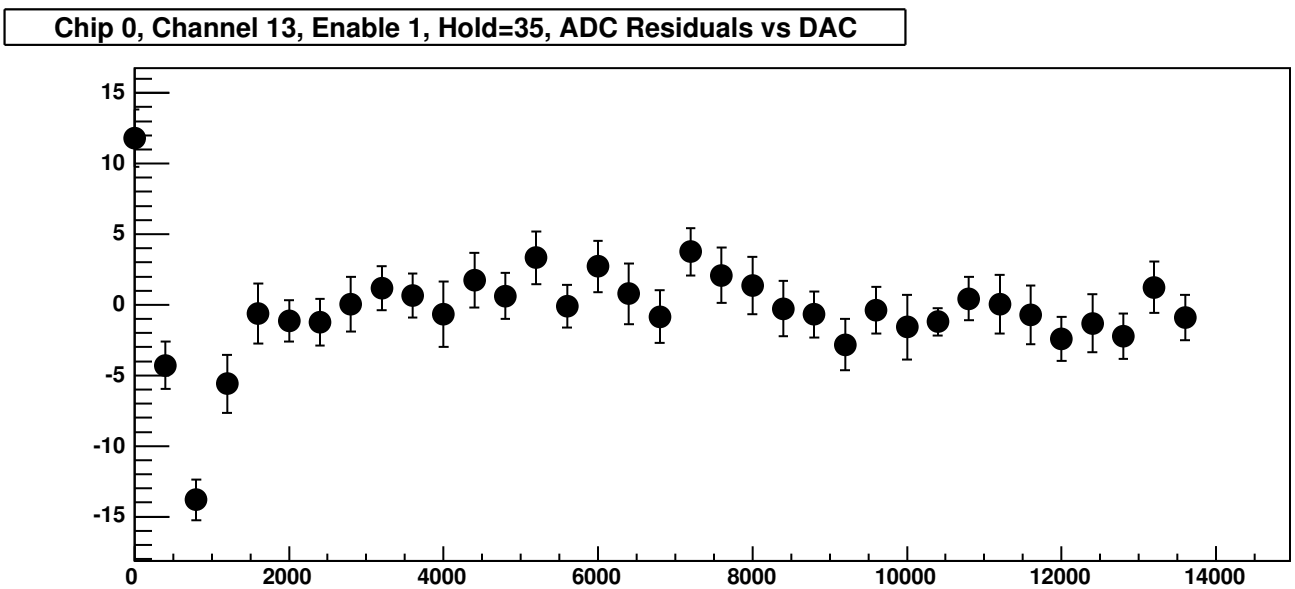

Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 805 / 23
p0 -178.2 ± 1.251
p1 0.1907 ± 0.0002084
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

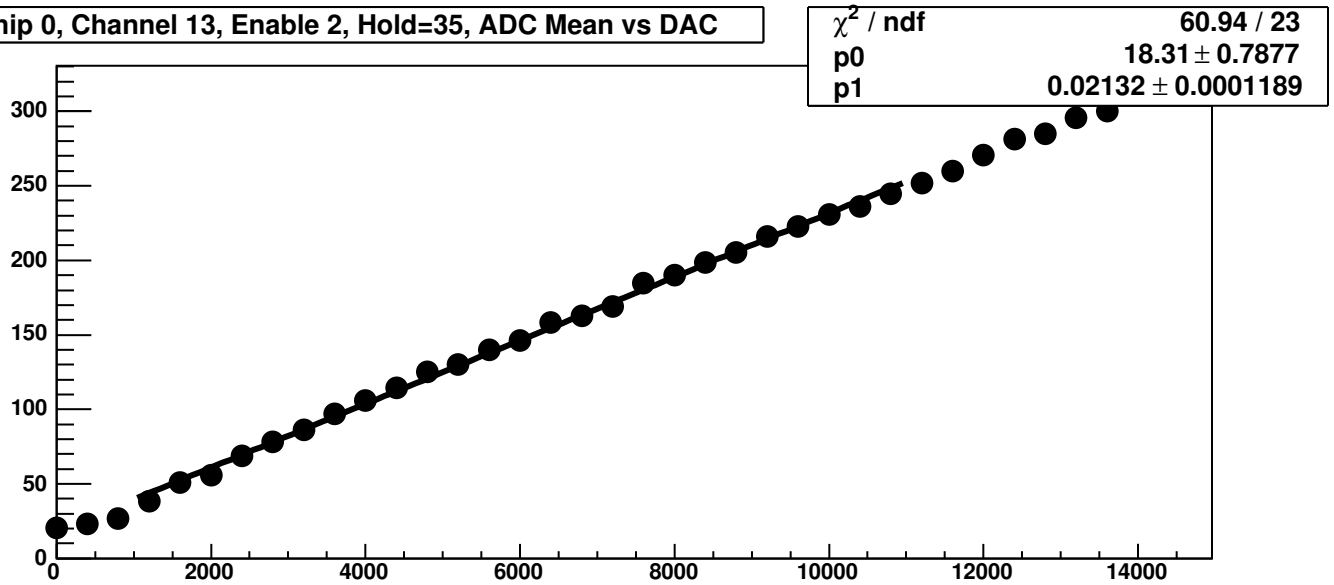

Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.37 / 23
p0 15.3 ± 0.7896
p1 0.02892 ± 0.0001119
0 50 100 150 200 250 300 350 400
0 2000 4000 6000 8000 10000 12000 14000

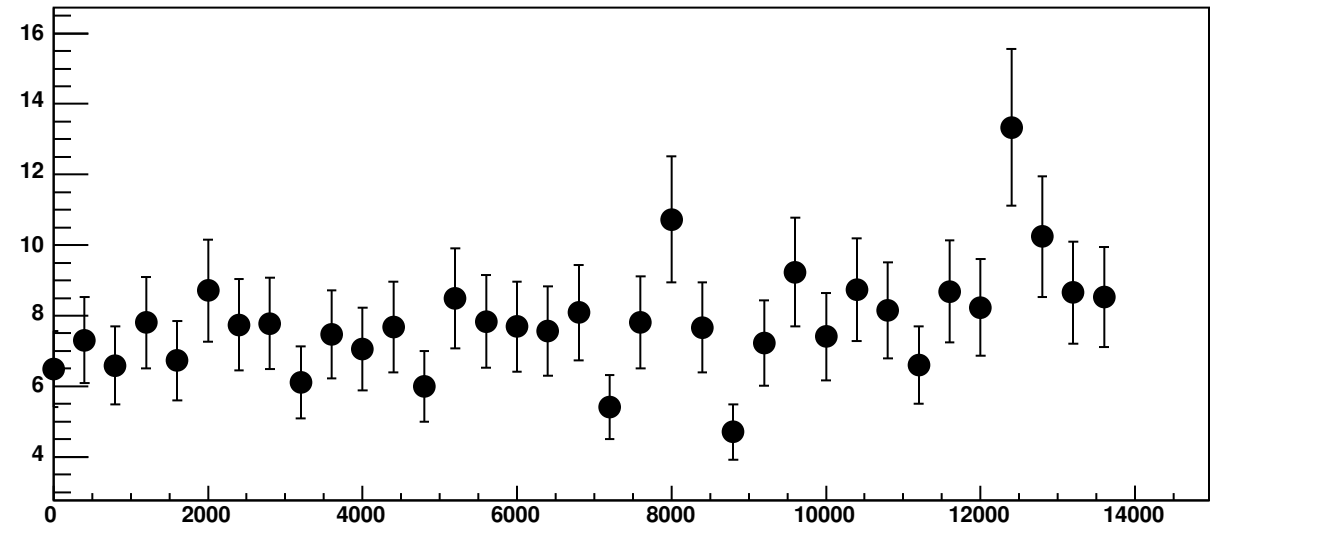

Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
3 4 5 6 7 8 9 10 11 12
0 2000 4000 6000 8000 10000 12000 14000


Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
-15 -10 -5 0 5 10 15
0 2000 4000 6000 8000 10000 12000 14000

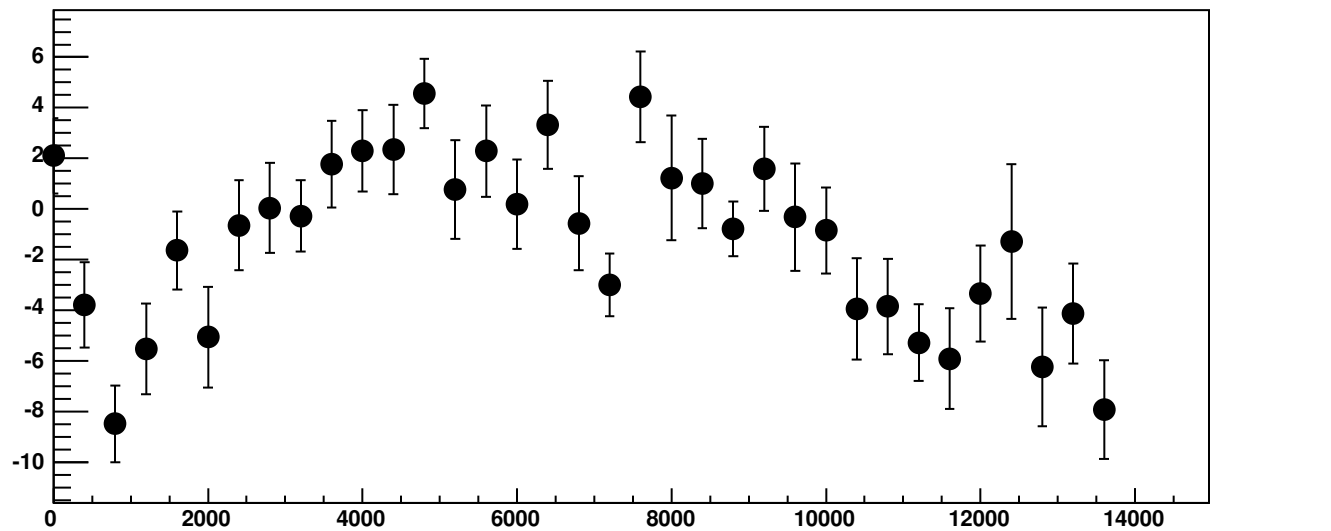
Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

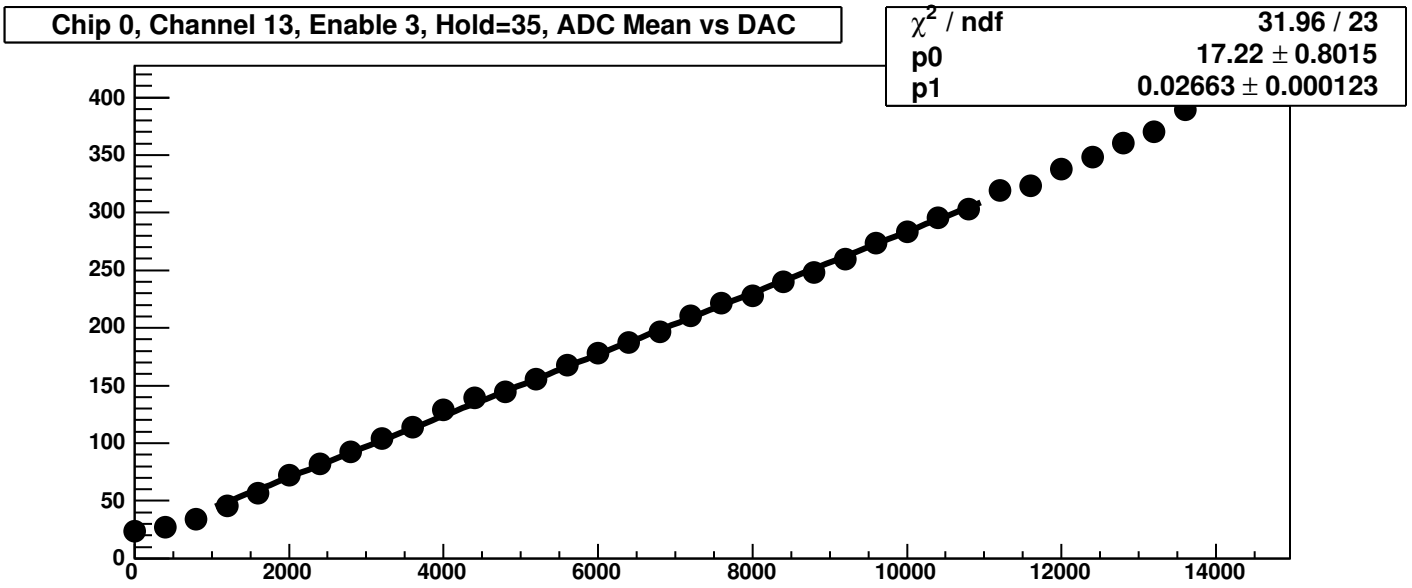


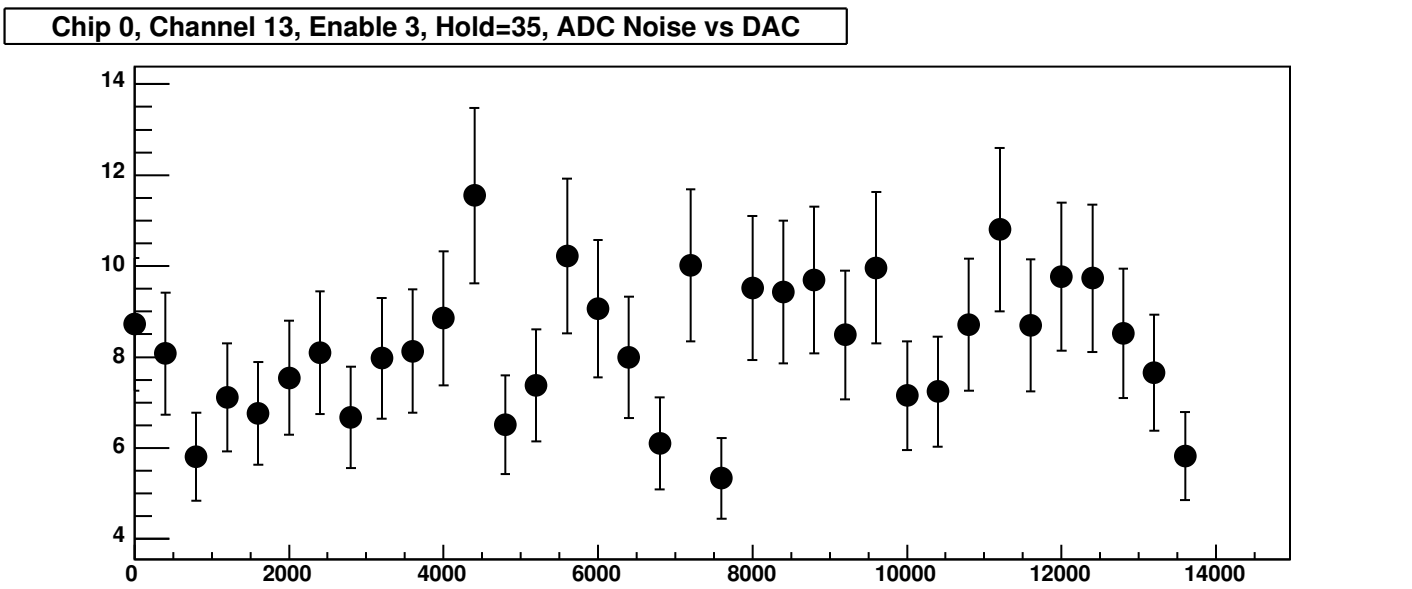
Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

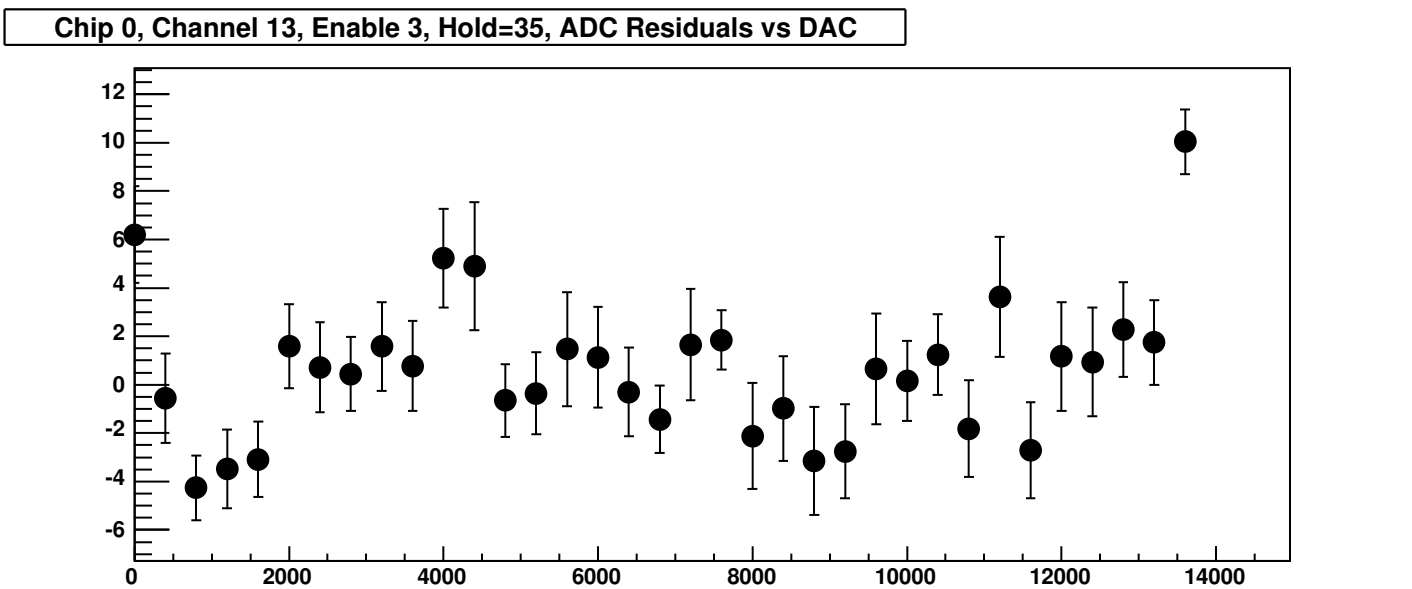


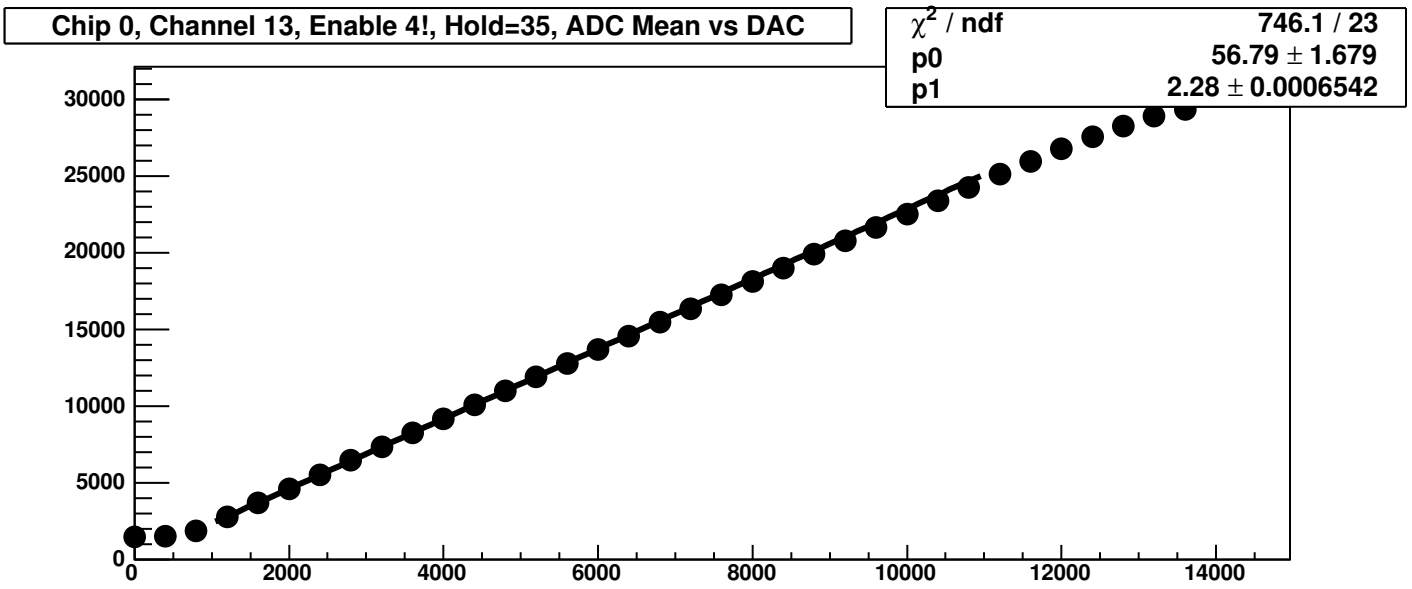
Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

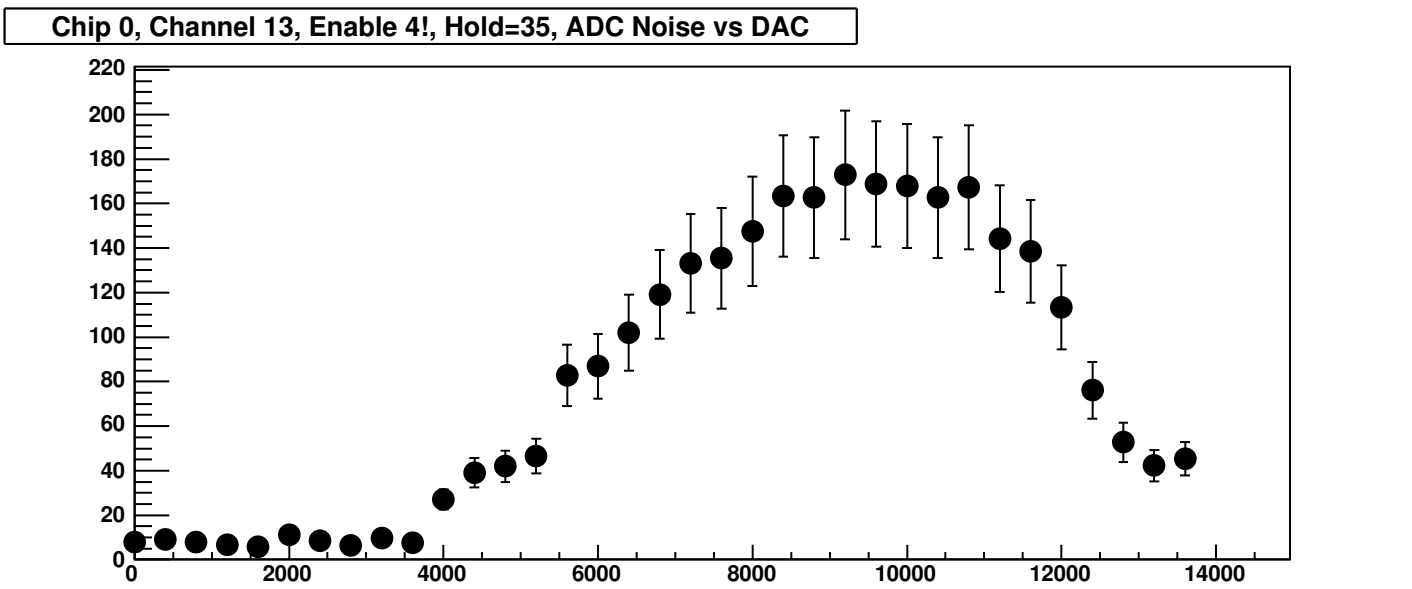


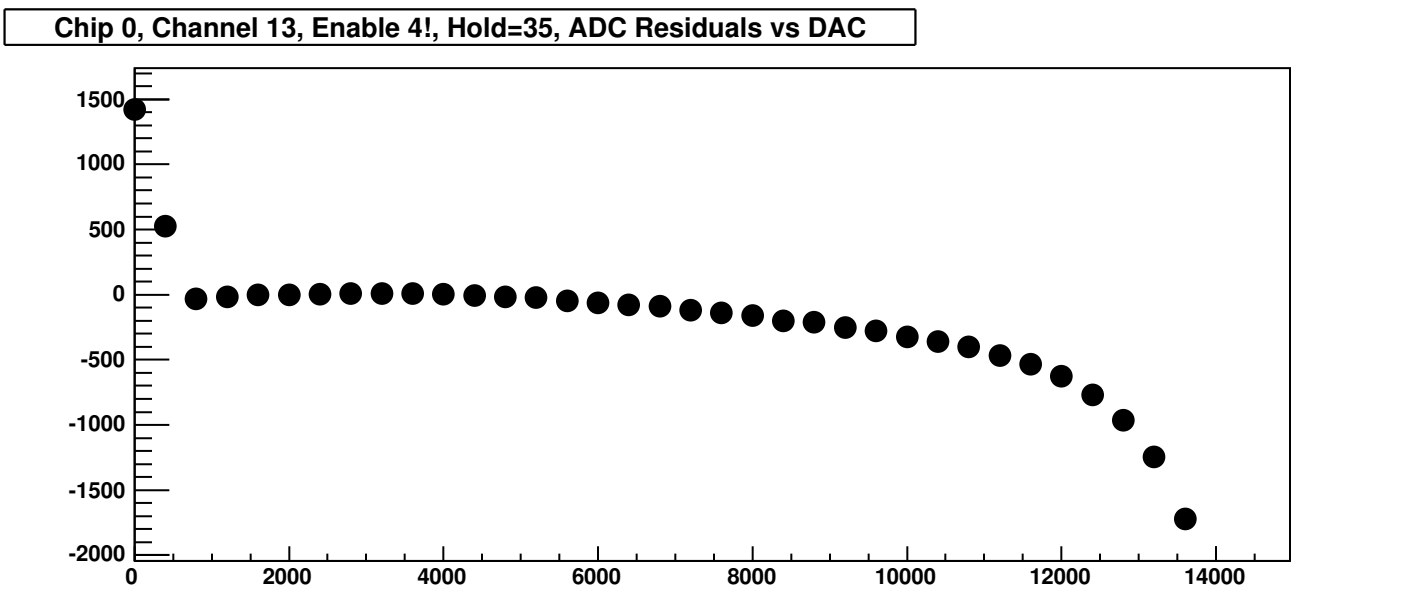

Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.96 / 23
p0 17.22 ± 0.8015
p1 0.02663 ± 0.000123
0 50 100 150 200 250 300 350 400
0 2000 4000 6000 8000 10000 12000 14000


Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
4 6 8 10 12 14
0 2000 4000 6000 8000 10000 12000 14000

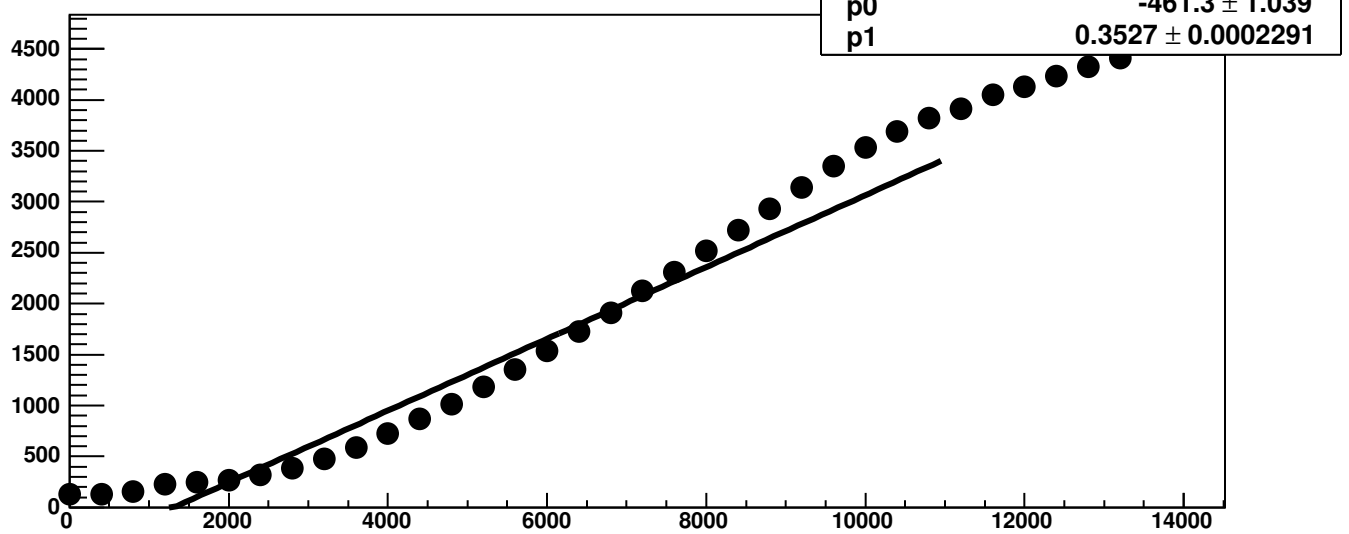

Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
-6 -4 -2 0 2 4 6 8 10 12
0 2000 4000 6000 8000 10000 12000 14000


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 746.1 / 23
p0 56.79 ± 1.679
p1 2.28 ± 0.0006542
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

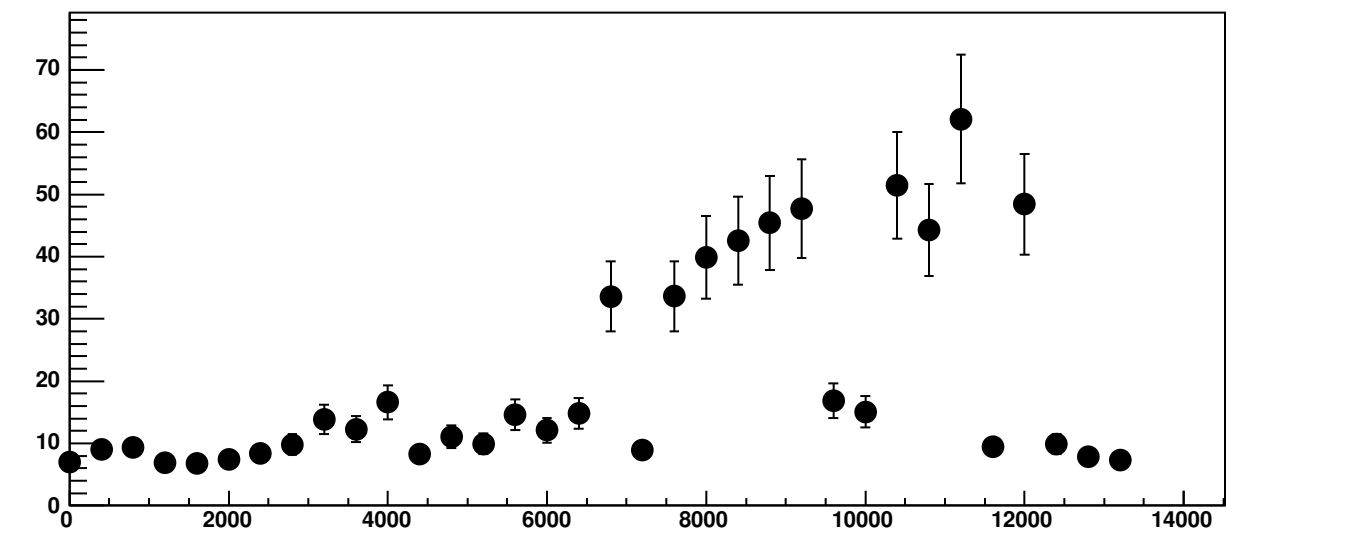

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

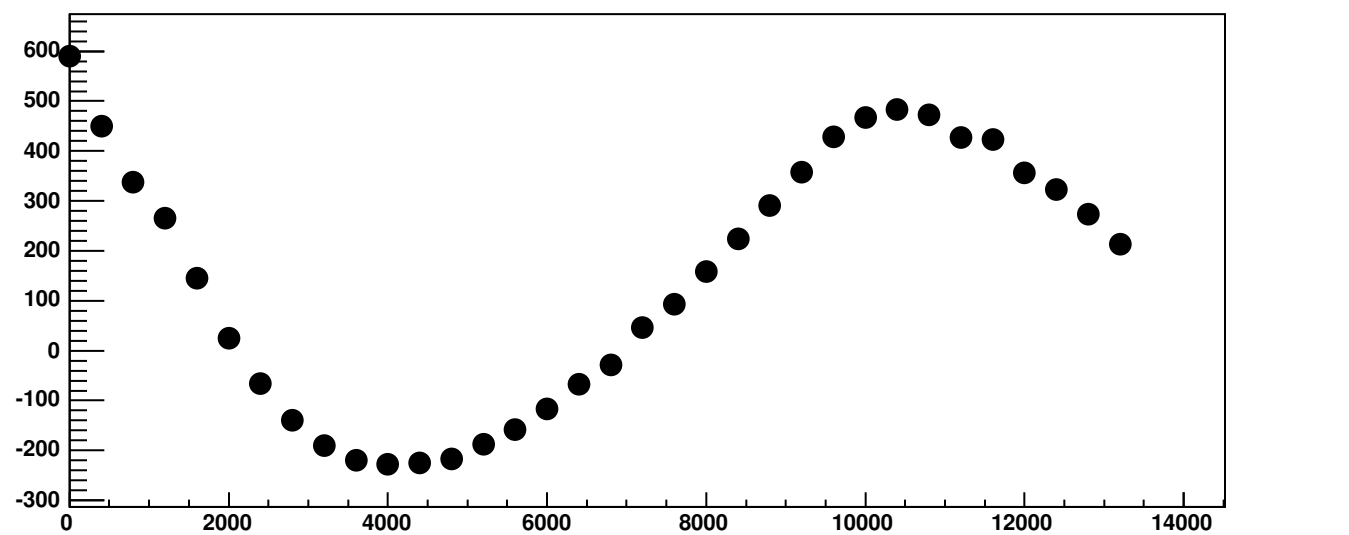
Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

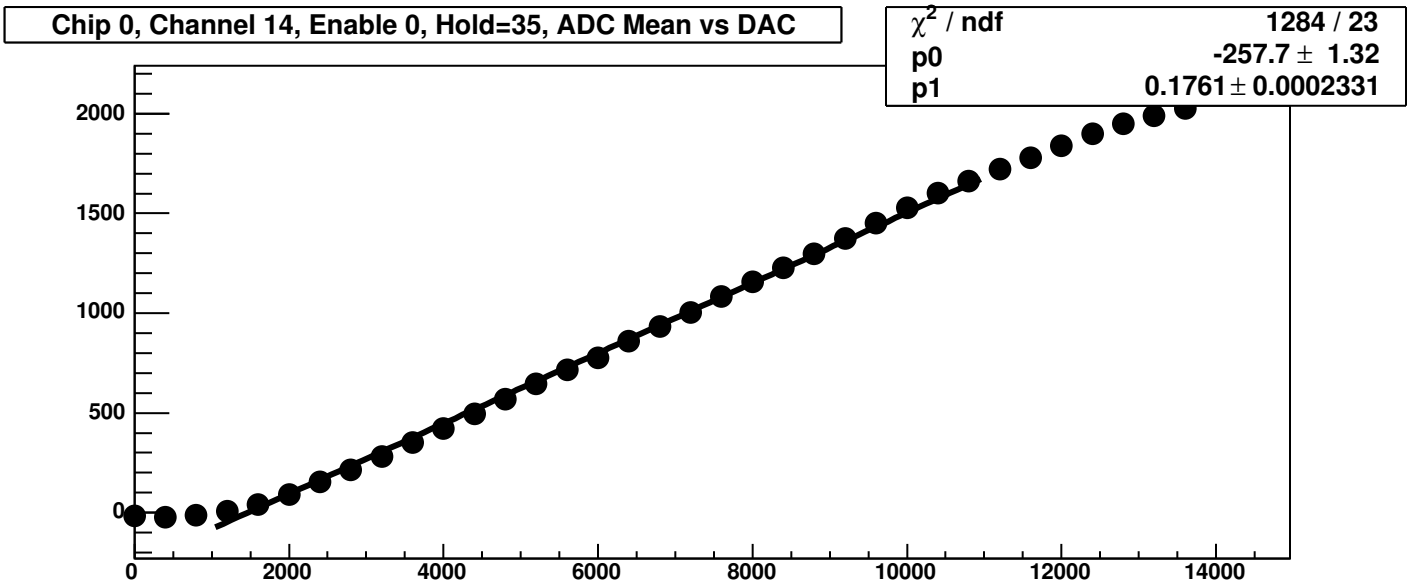


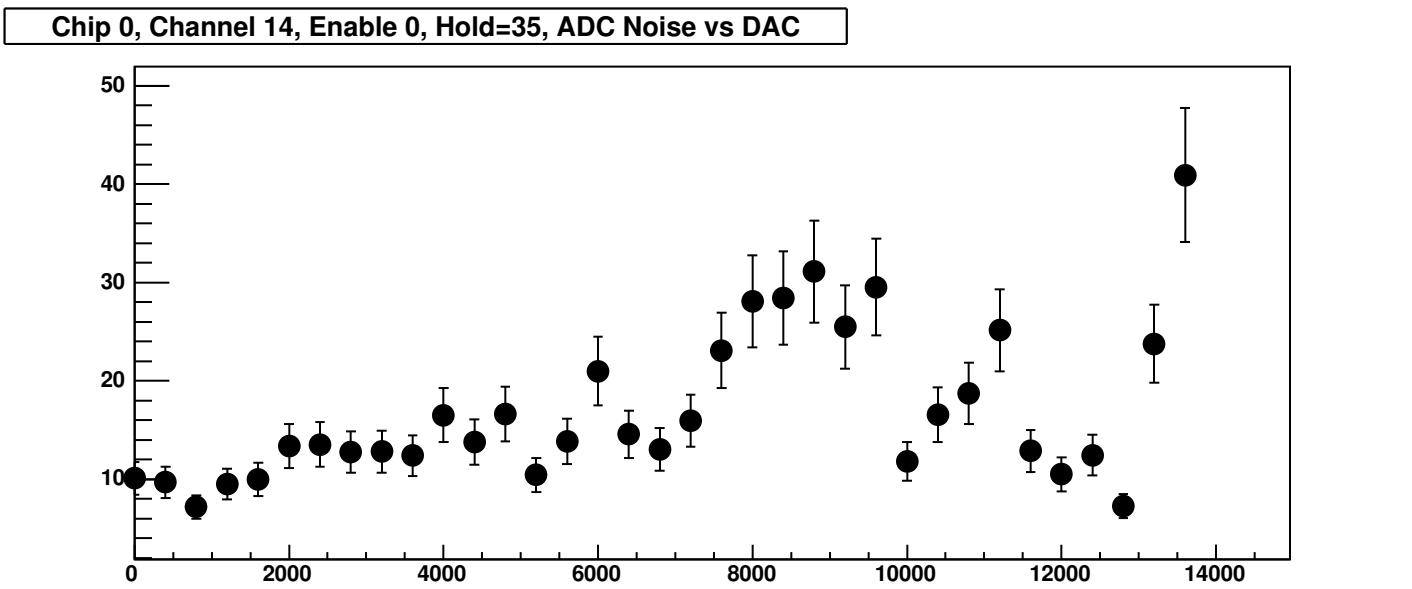
Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

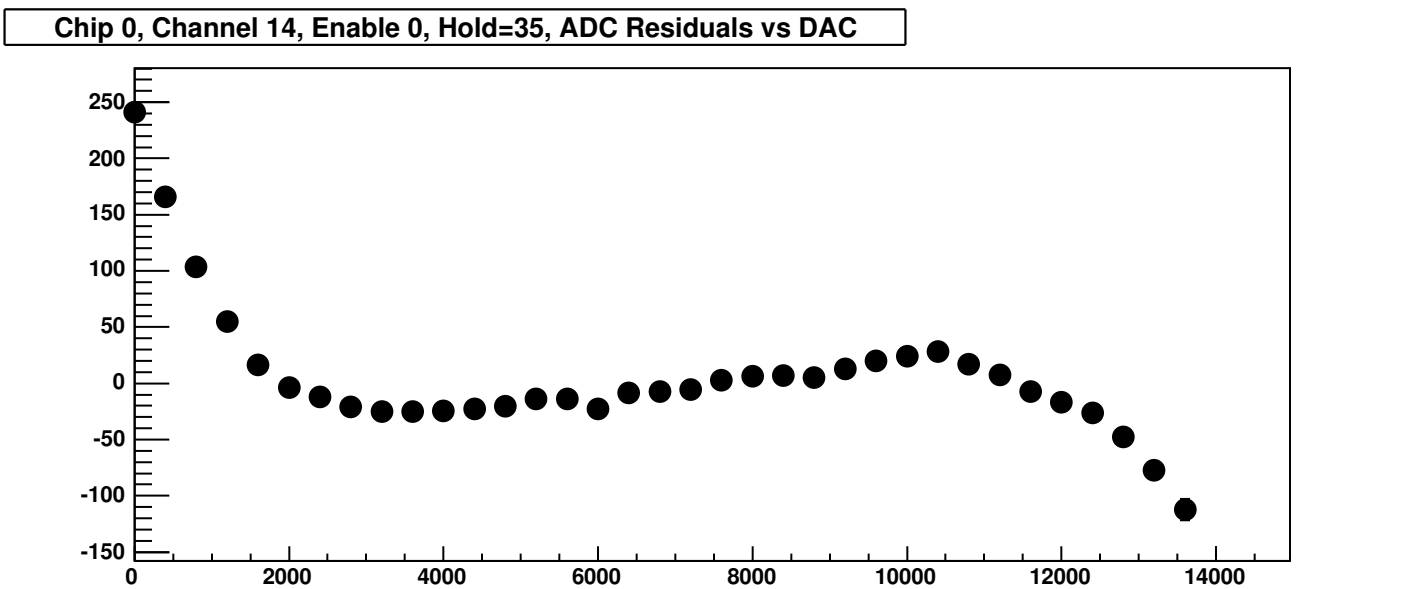


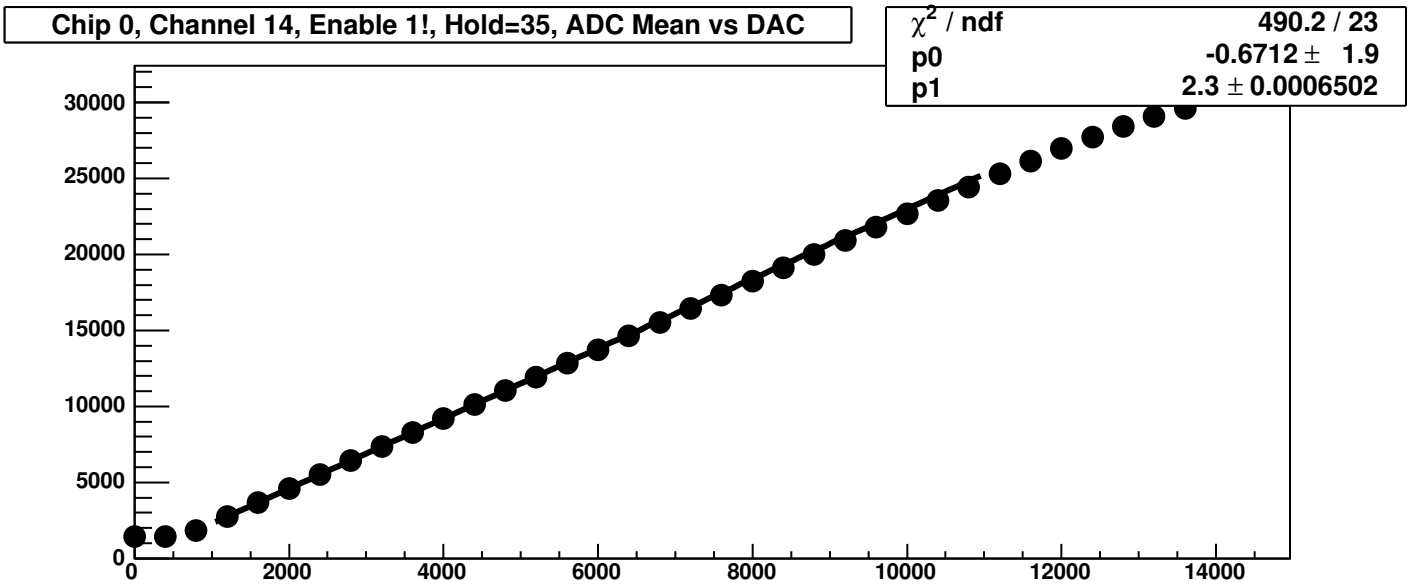
Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

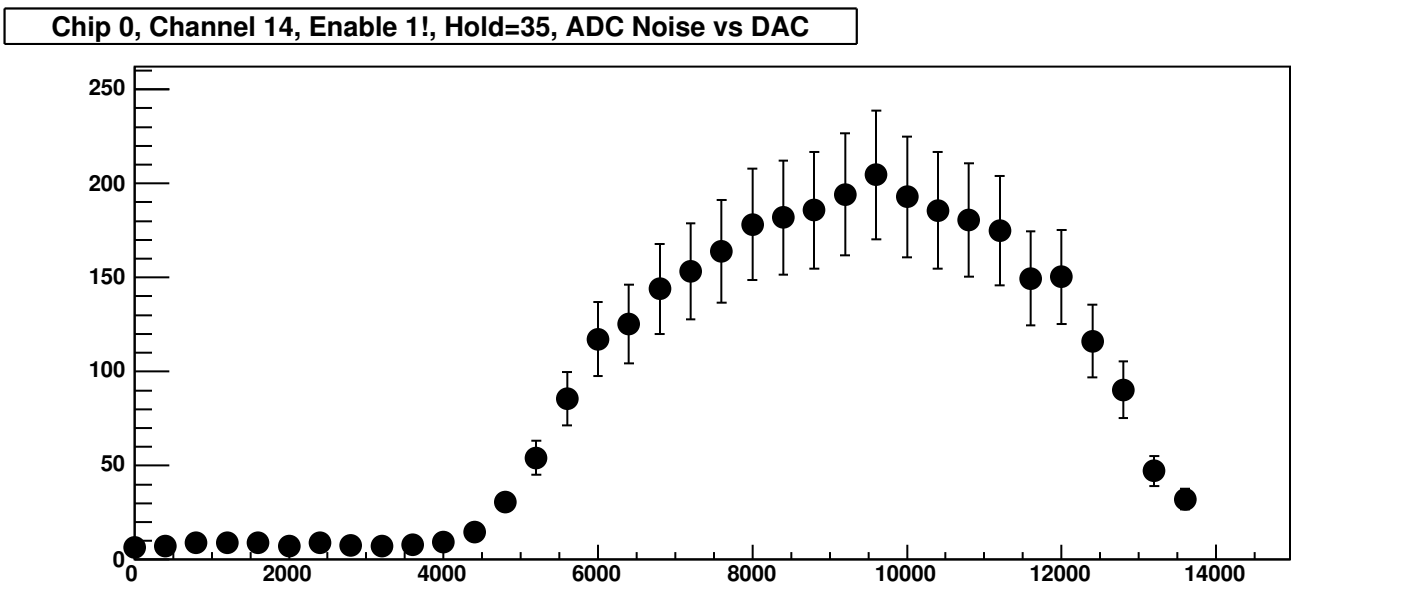


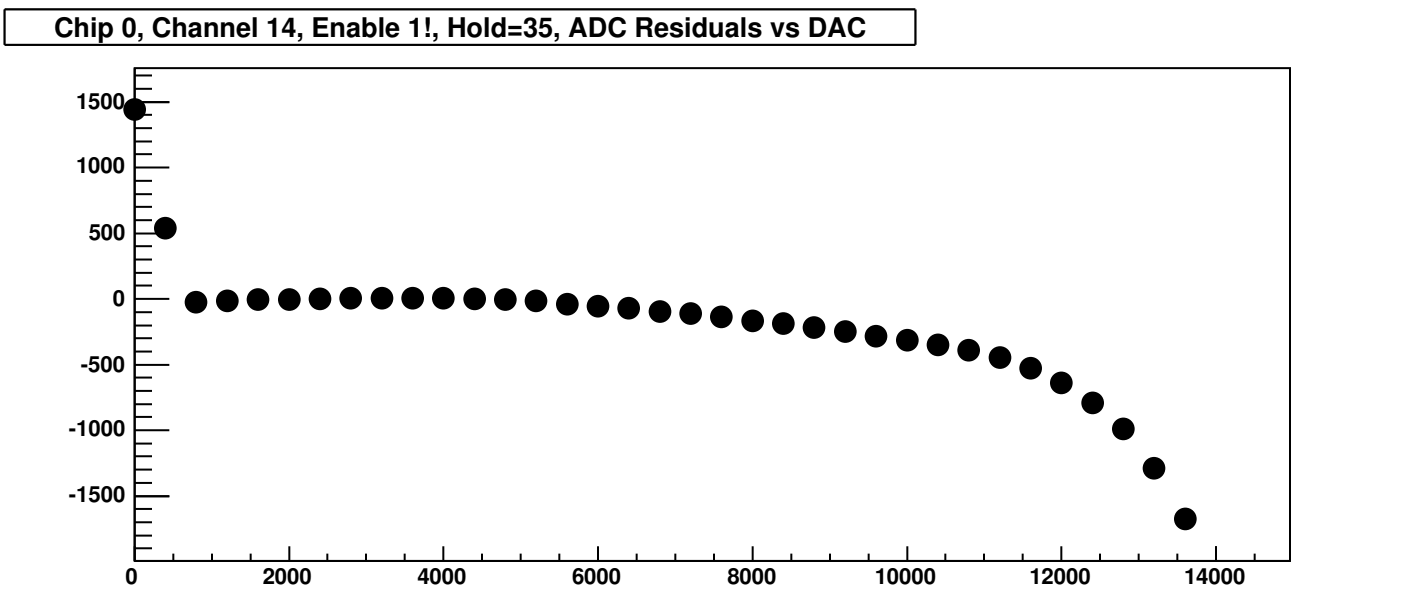

Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1284 / 23
p0 -257.7 ± 1.32
p1 0.1761 ± 0.0002331
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

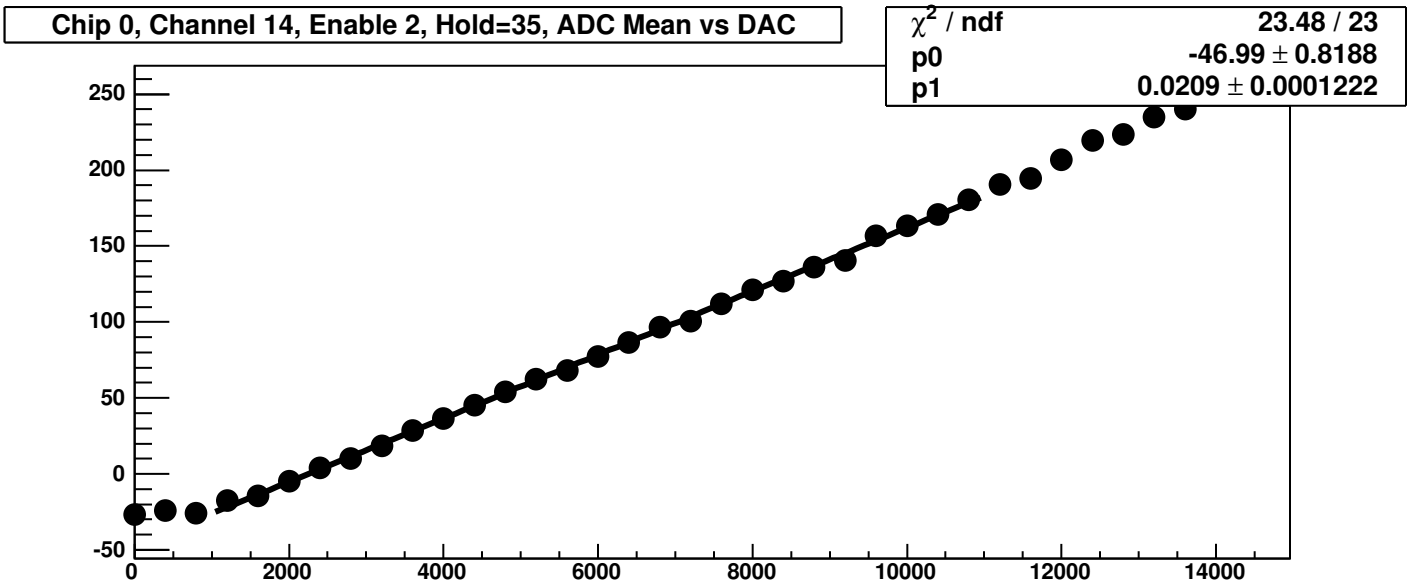

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

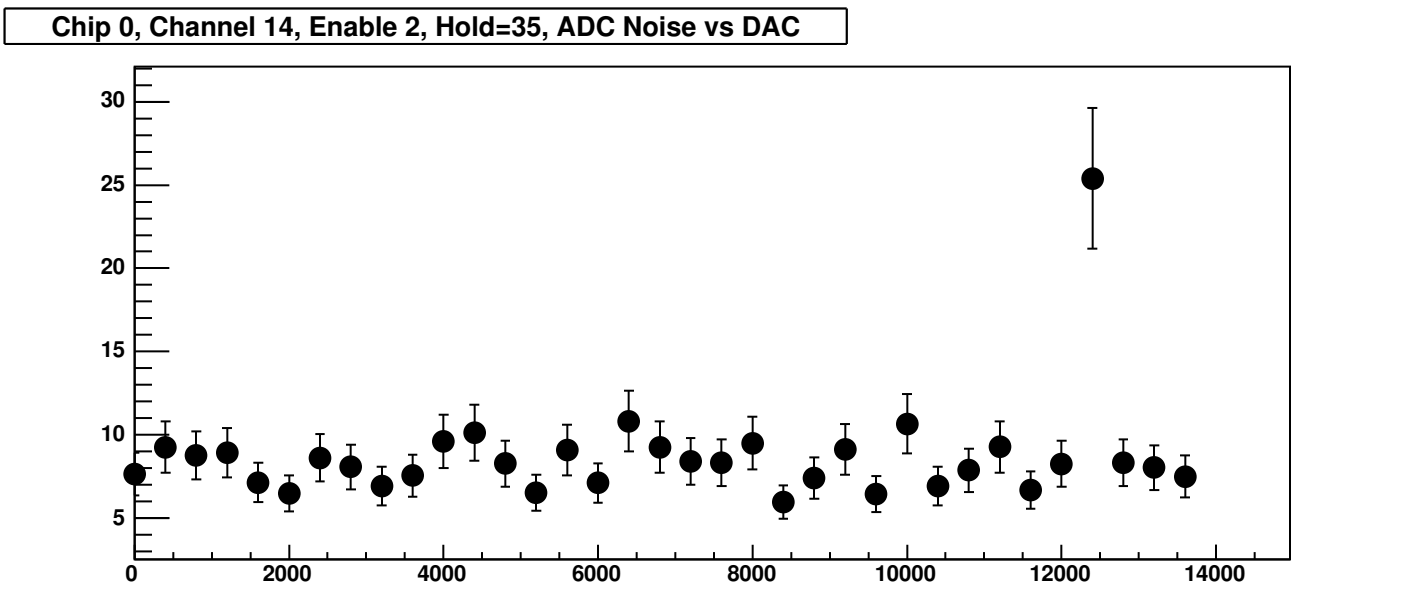

Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

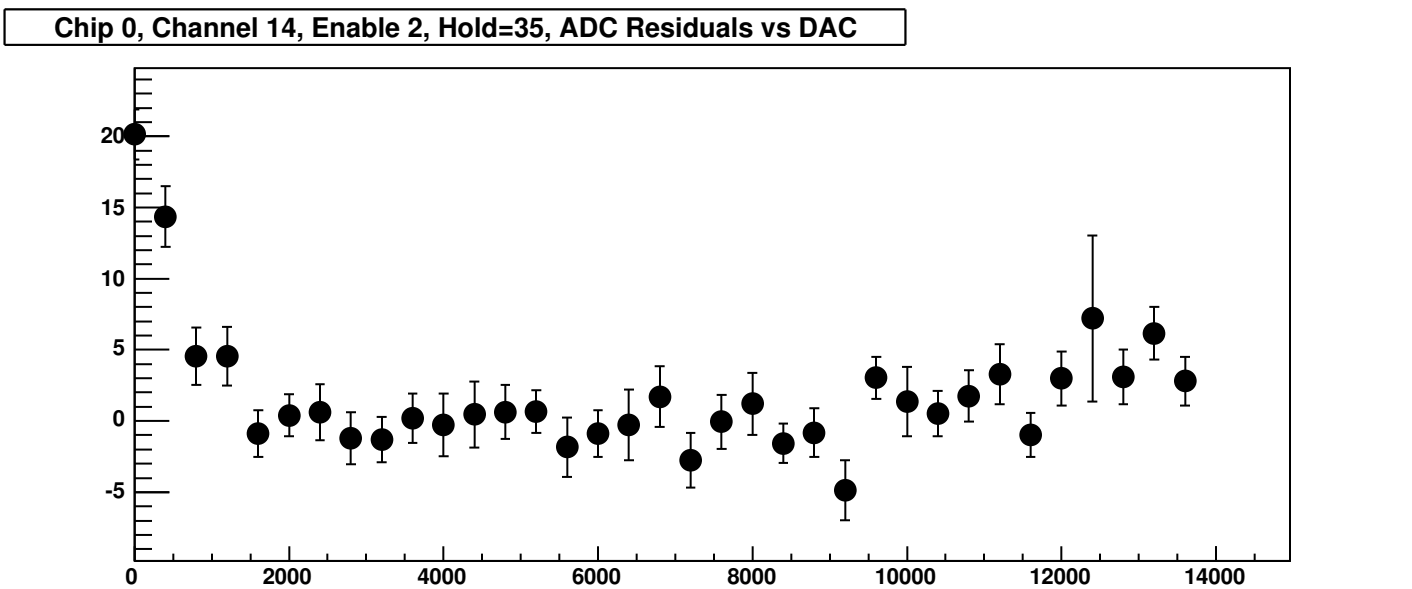

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 490.2 / 23
p0 -0.6712 ± 1.9
p1 2.3 ± 0.0006502
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

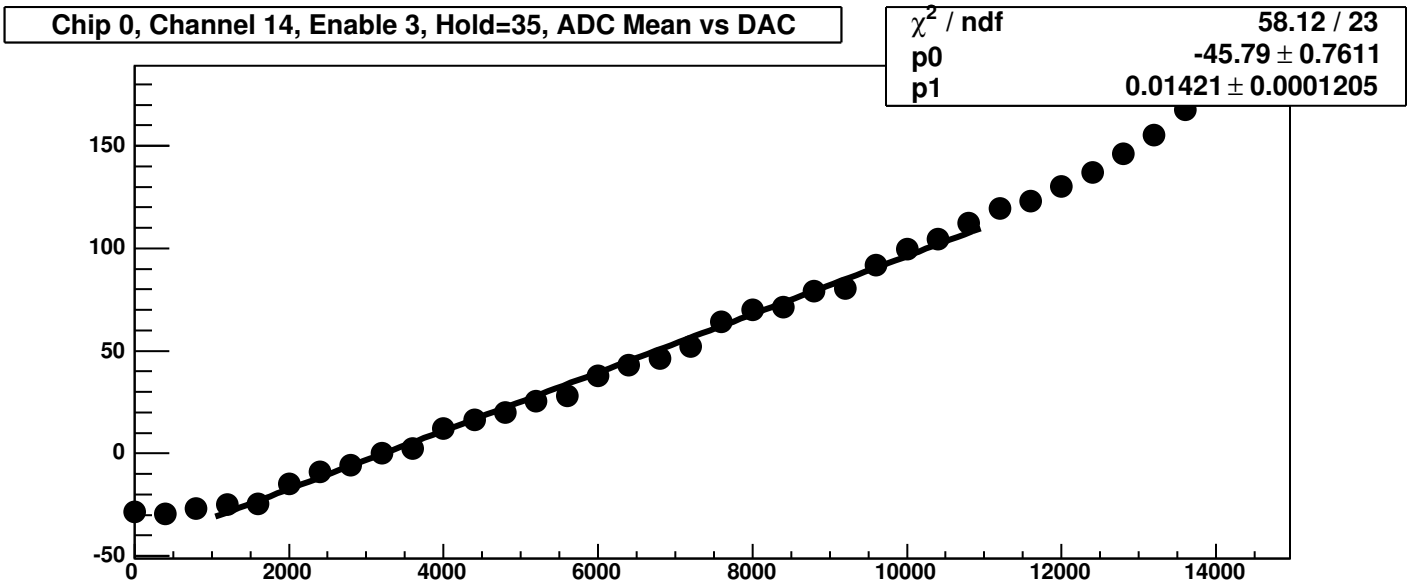

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

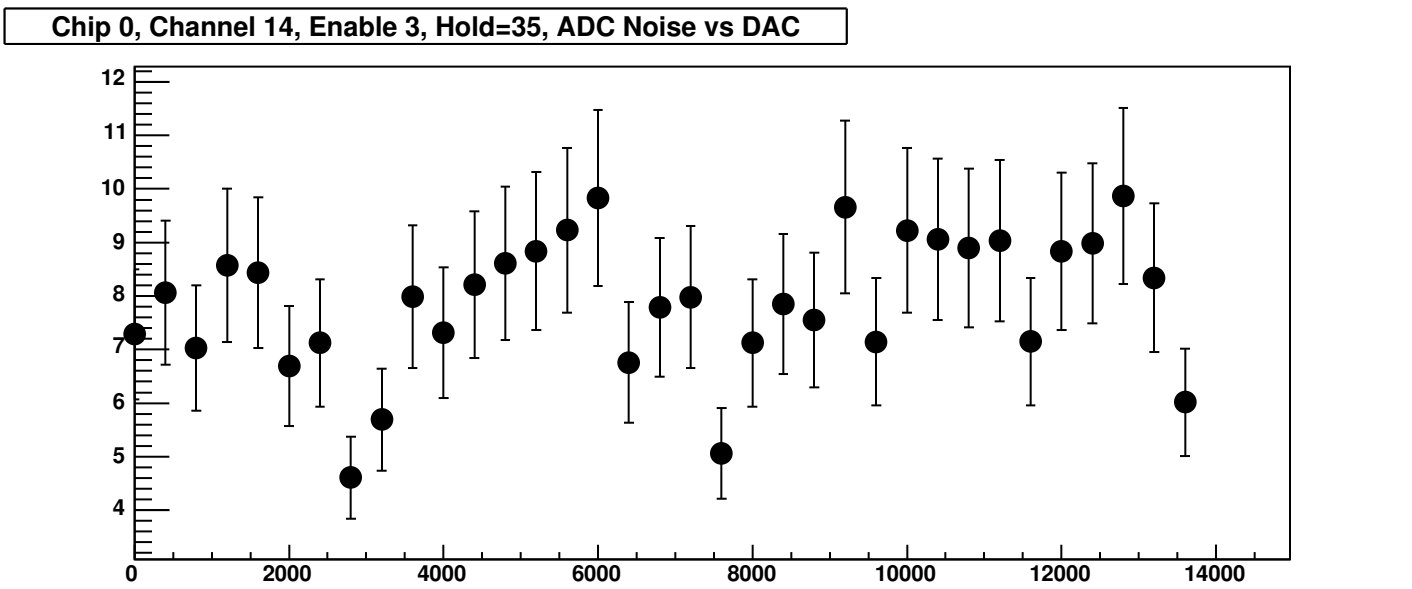

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

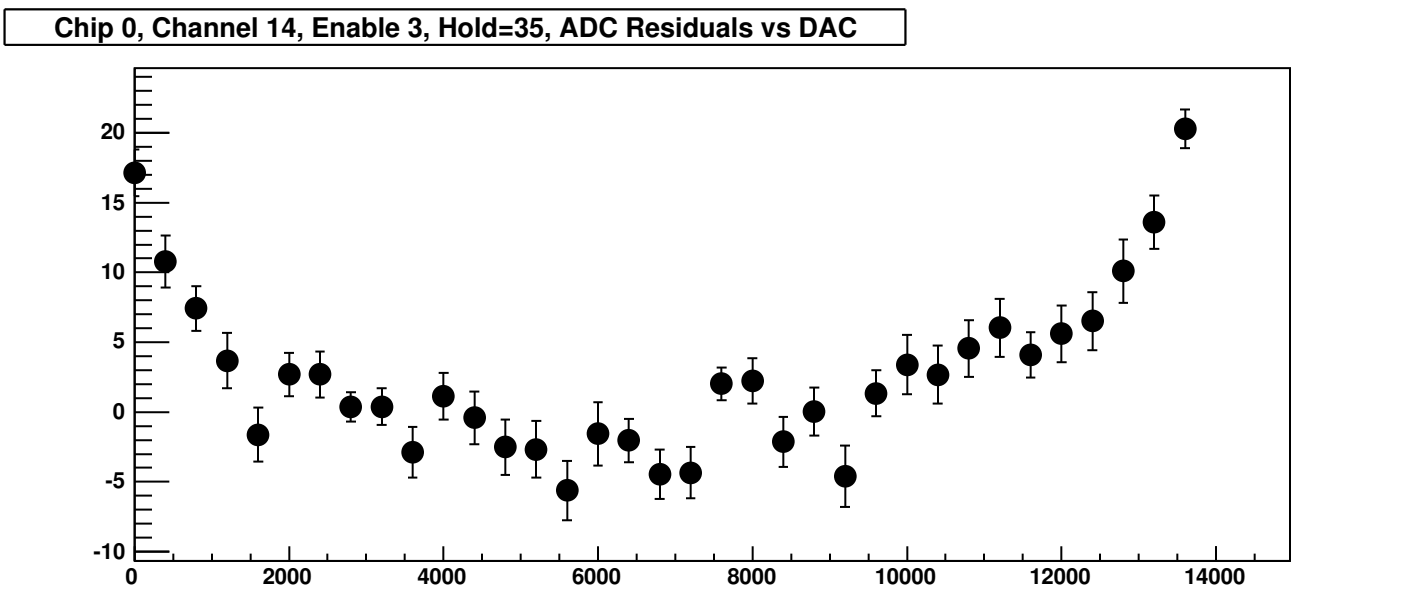

Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.48 / 23
p0 -46.99 ± 0.8188
p1 0.0209 ± 0.0001222
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

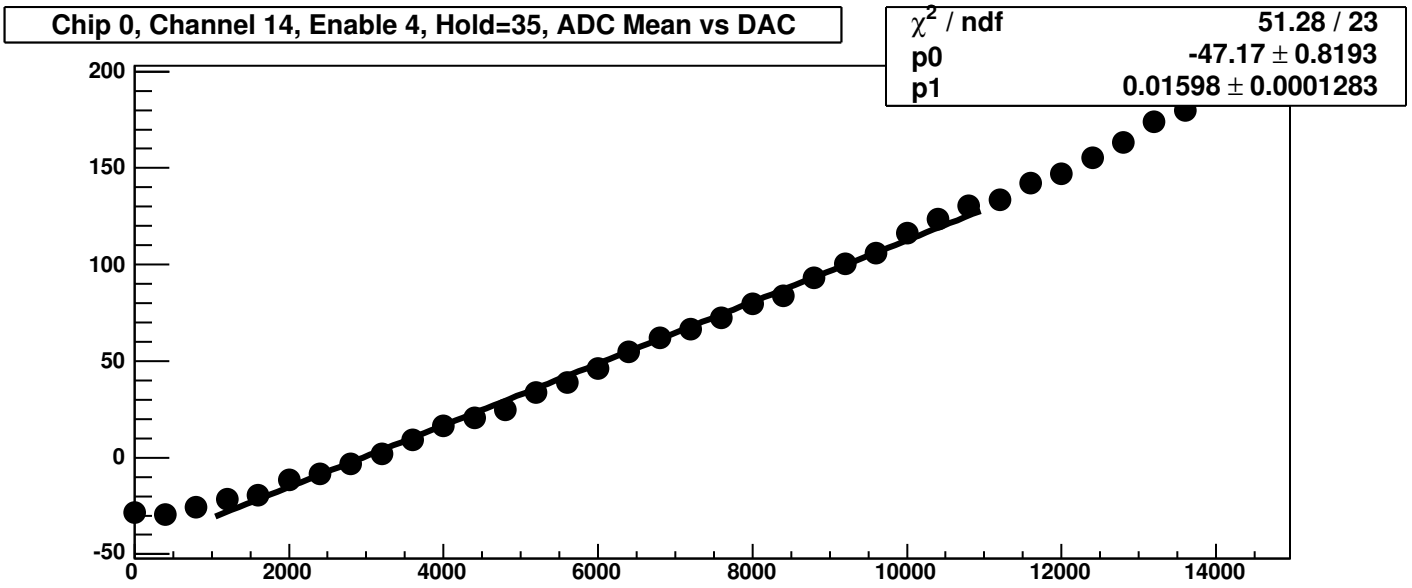

Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

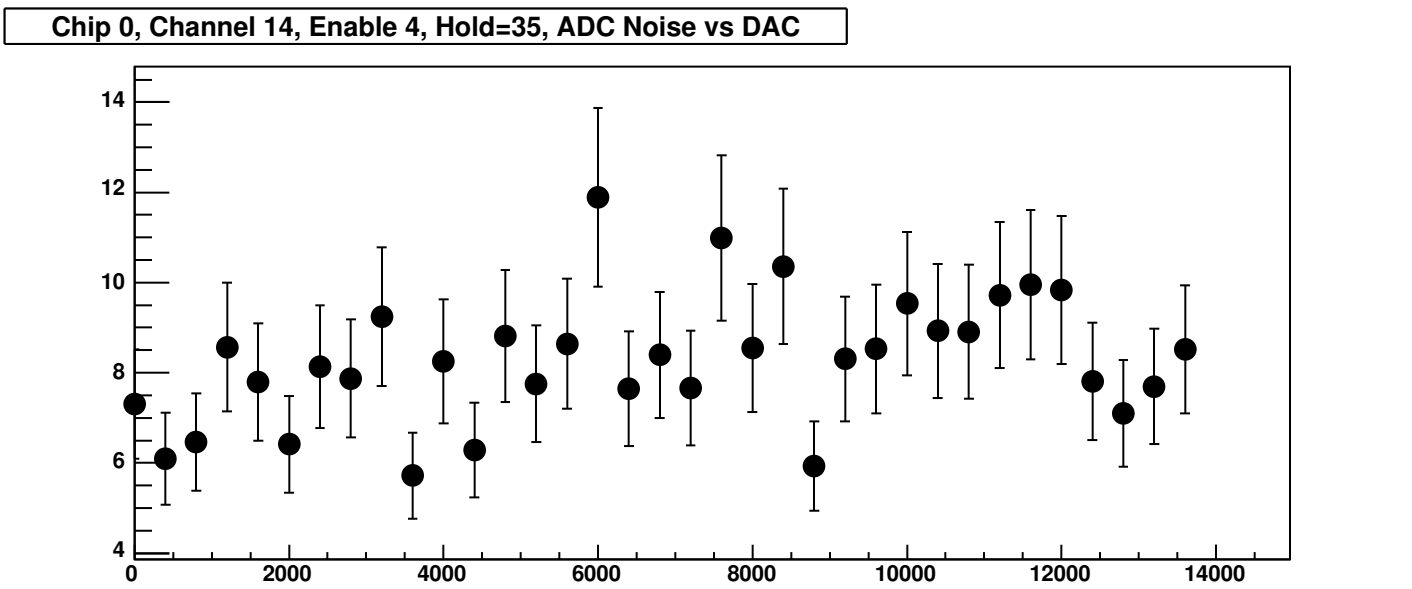

Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

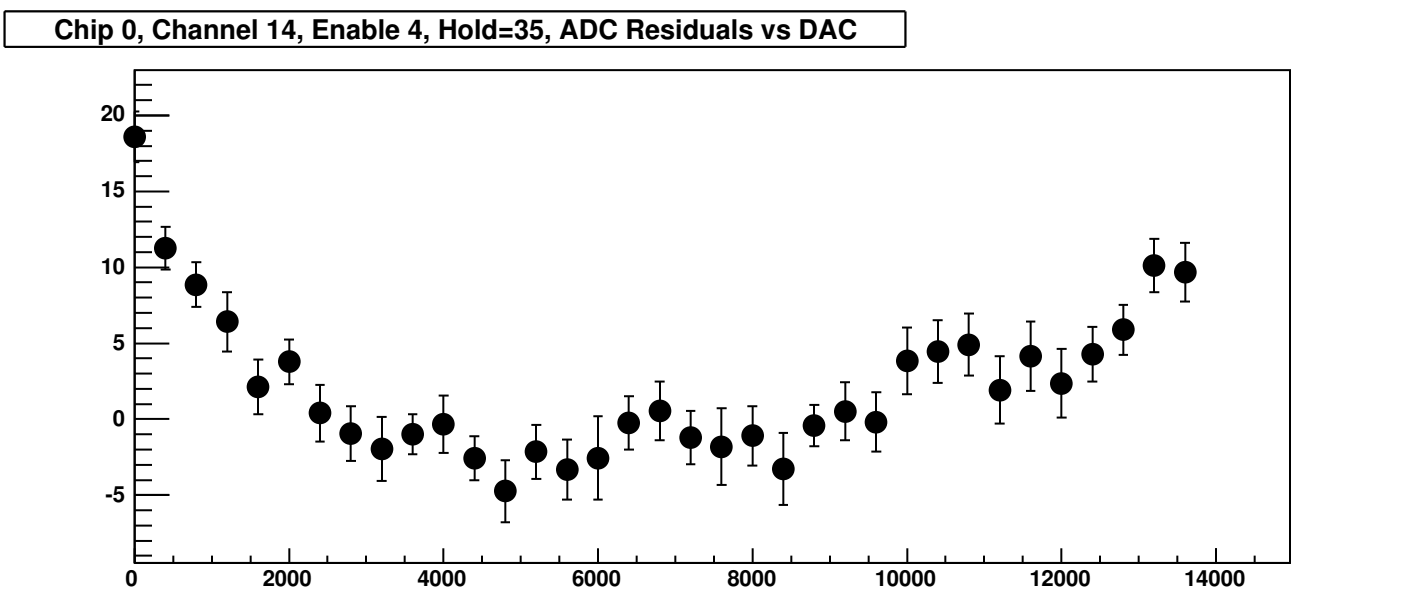

Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 58.12 / 23
p0 -45.79 ± 0.7611
p1 0.01421 ± 0.0001205
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

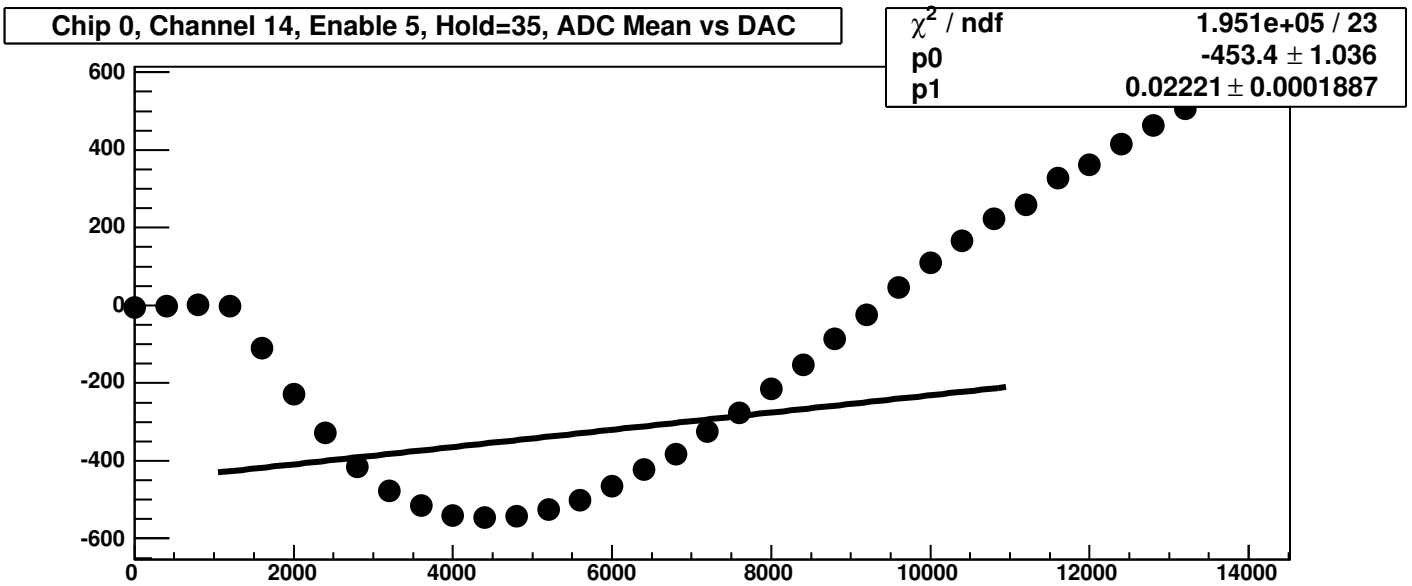

Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

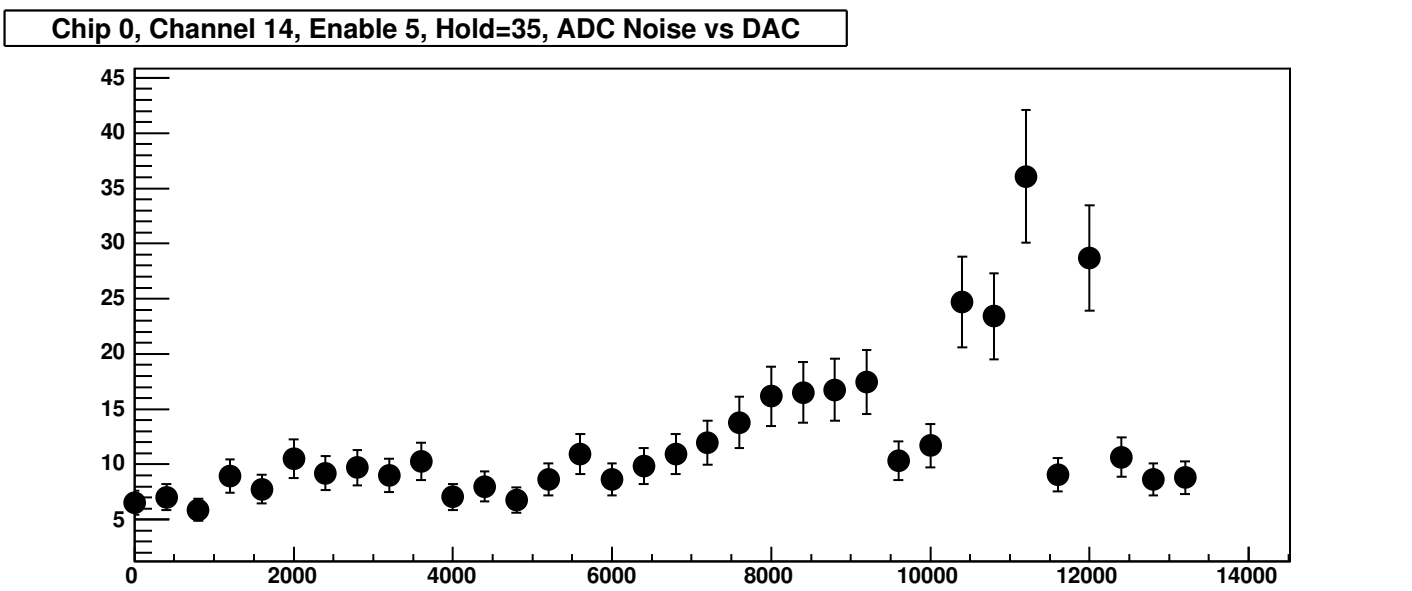

Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

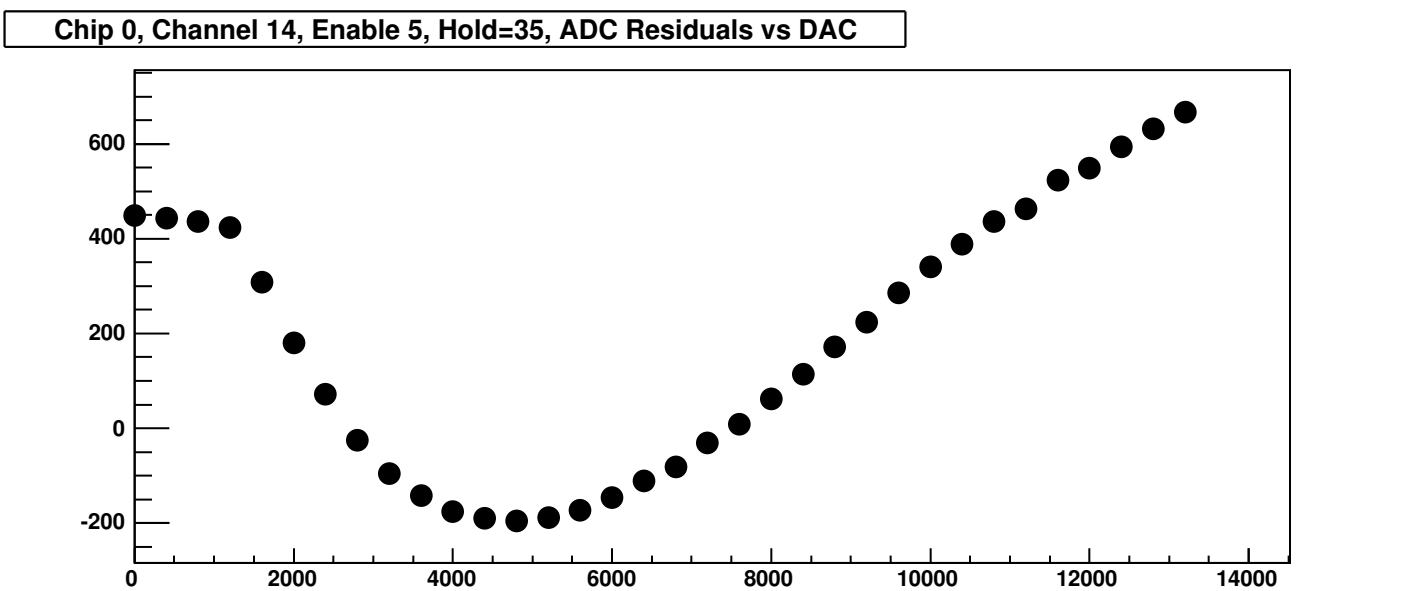

Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 51.28 / 23
p0 -47.17 ± 0.8193
p1 0.01598 ± 0.0001283
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

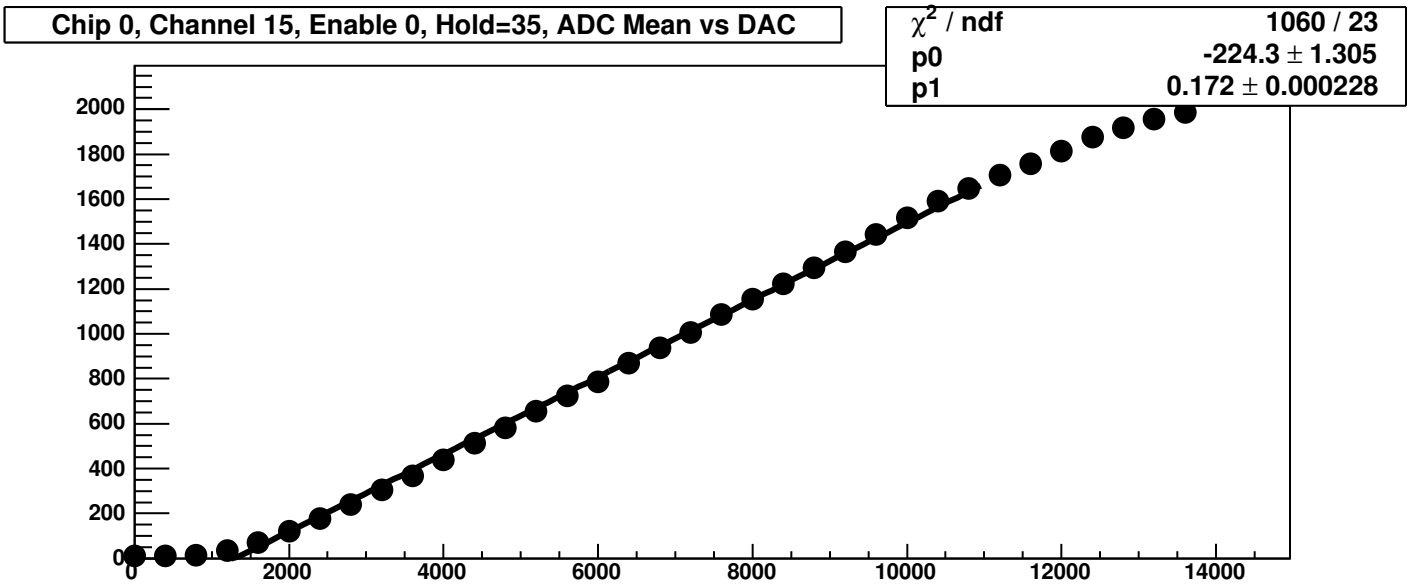

Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

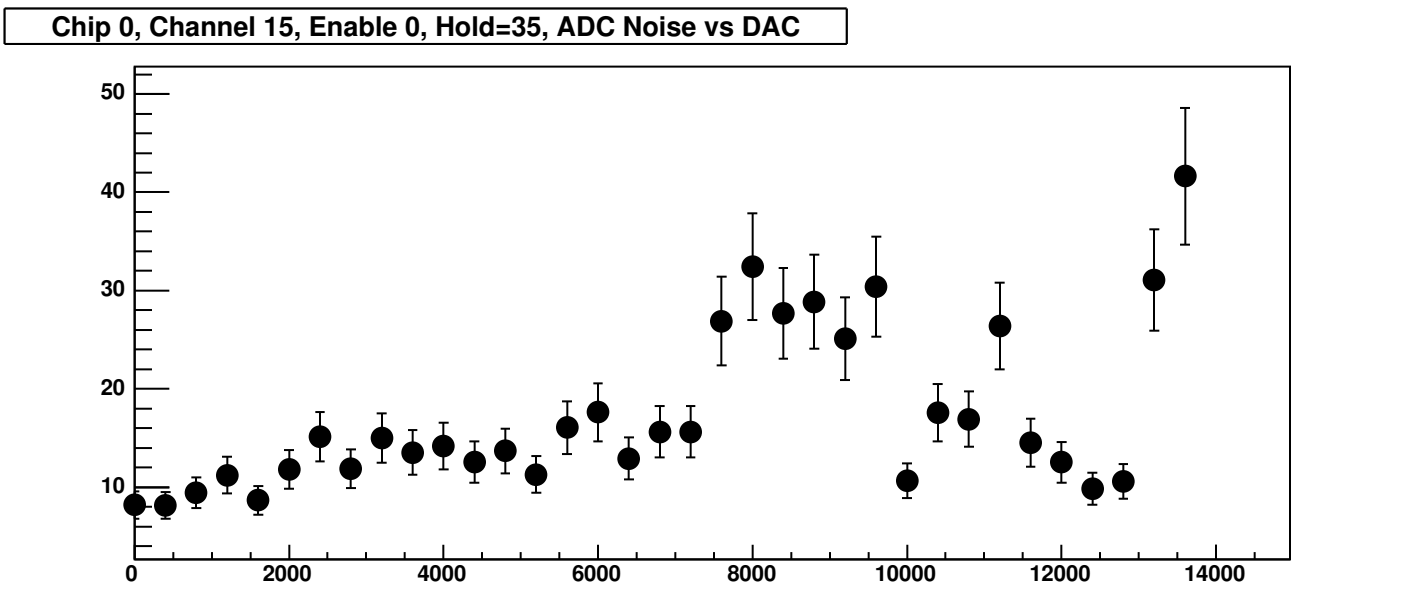

Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

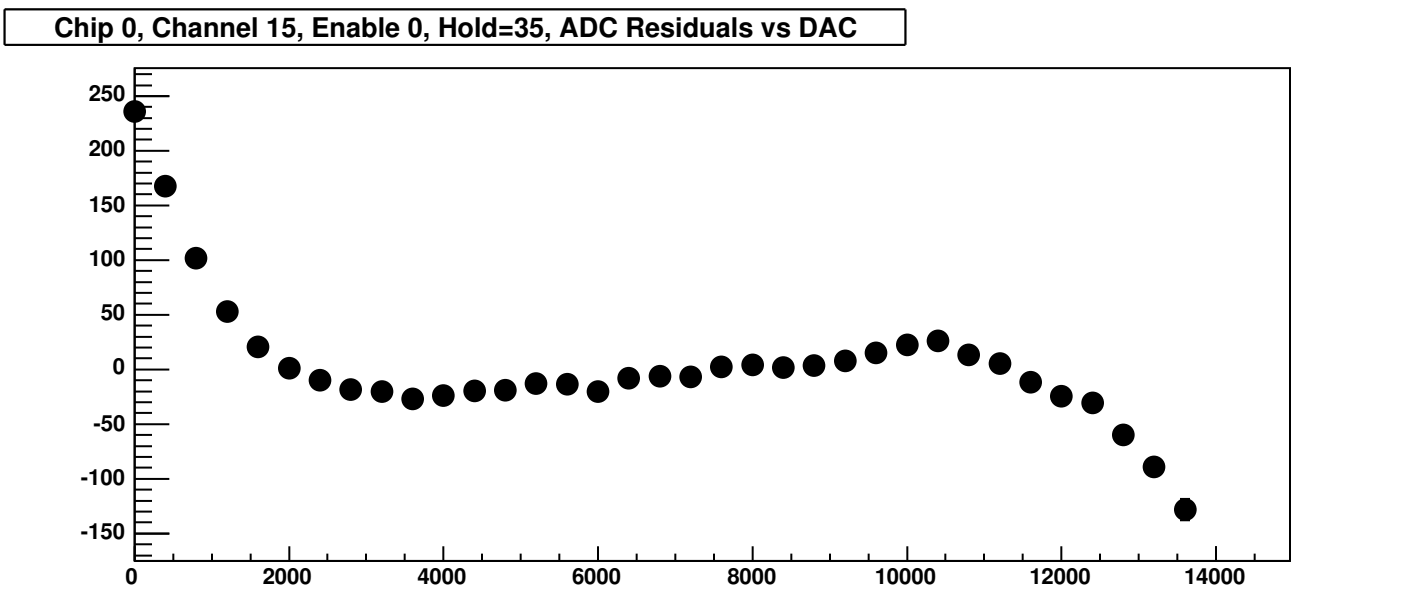

Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.951e+05 / 23
p0 -453.4 ± 1.036
p1 0.02221 ± 0.0001887
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

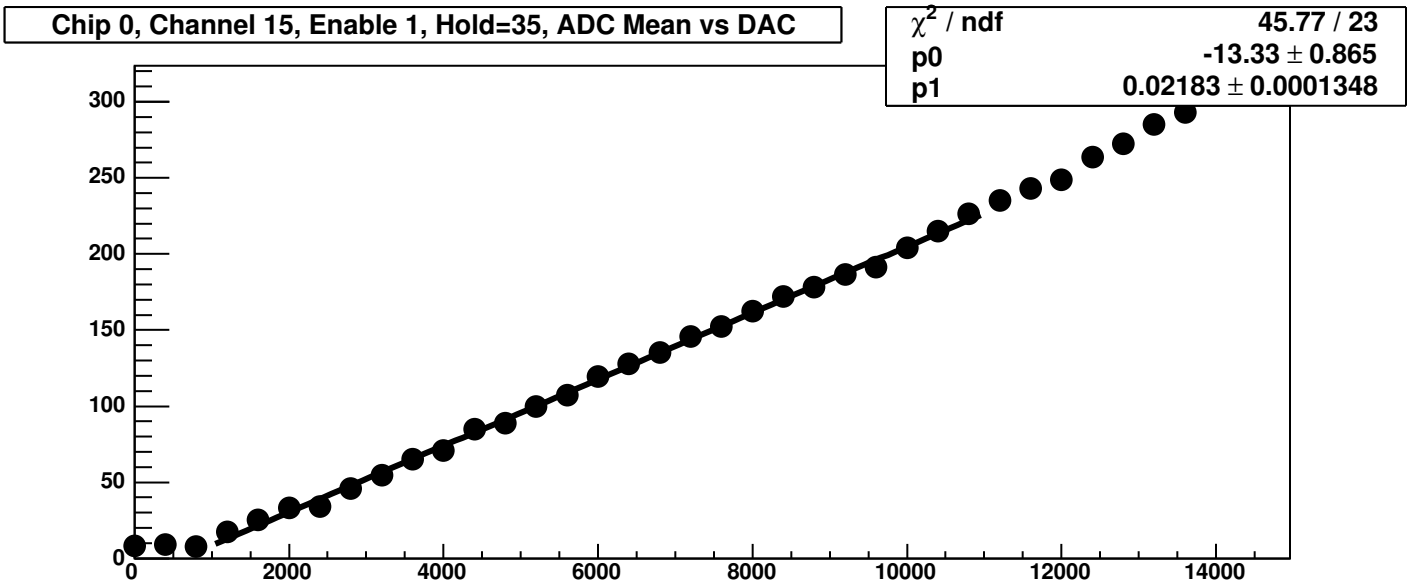

Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

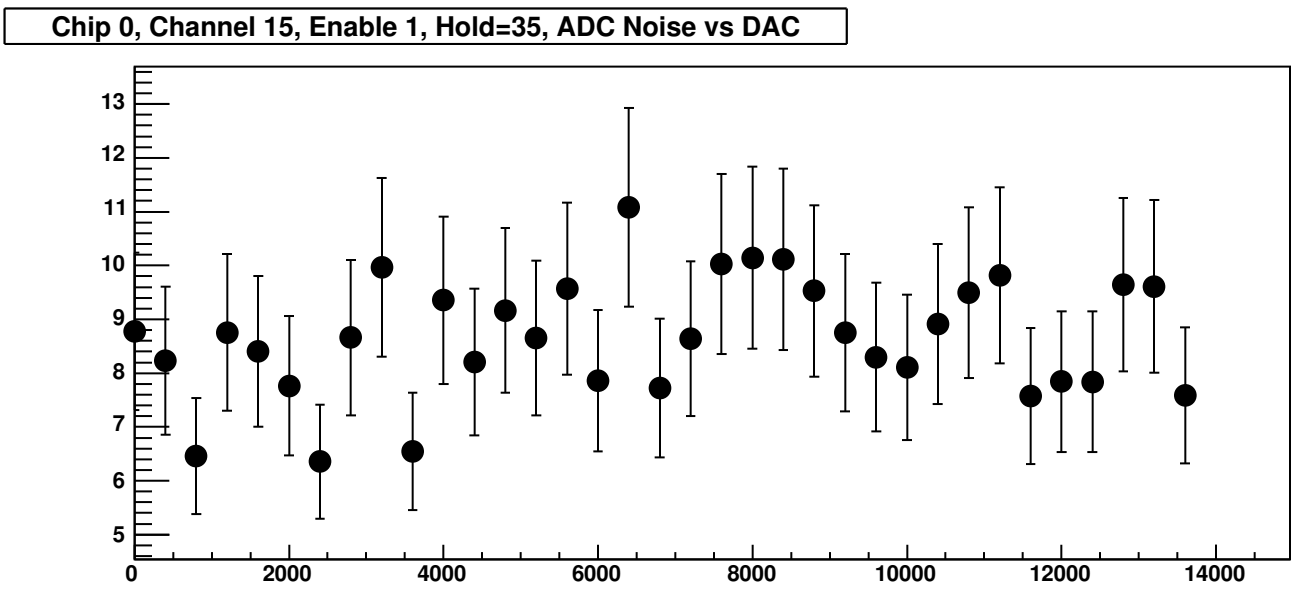

Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

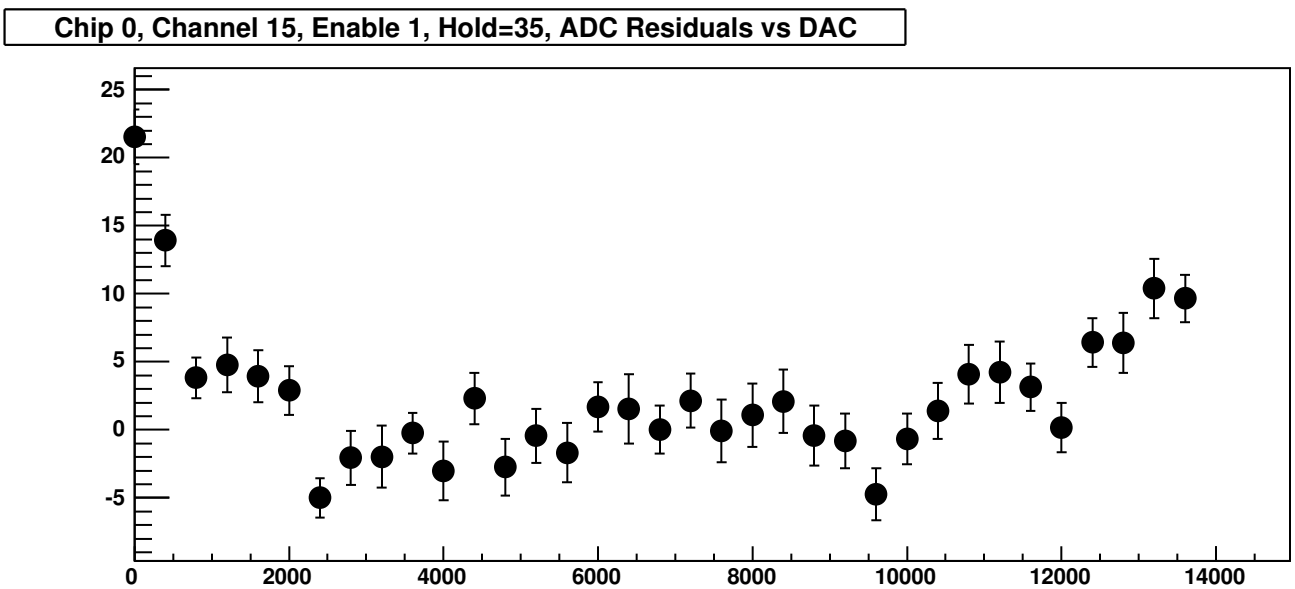

Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1060 / 23
p0 -224.3 ± 1.305
p1 0.172 ± 0.000228
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

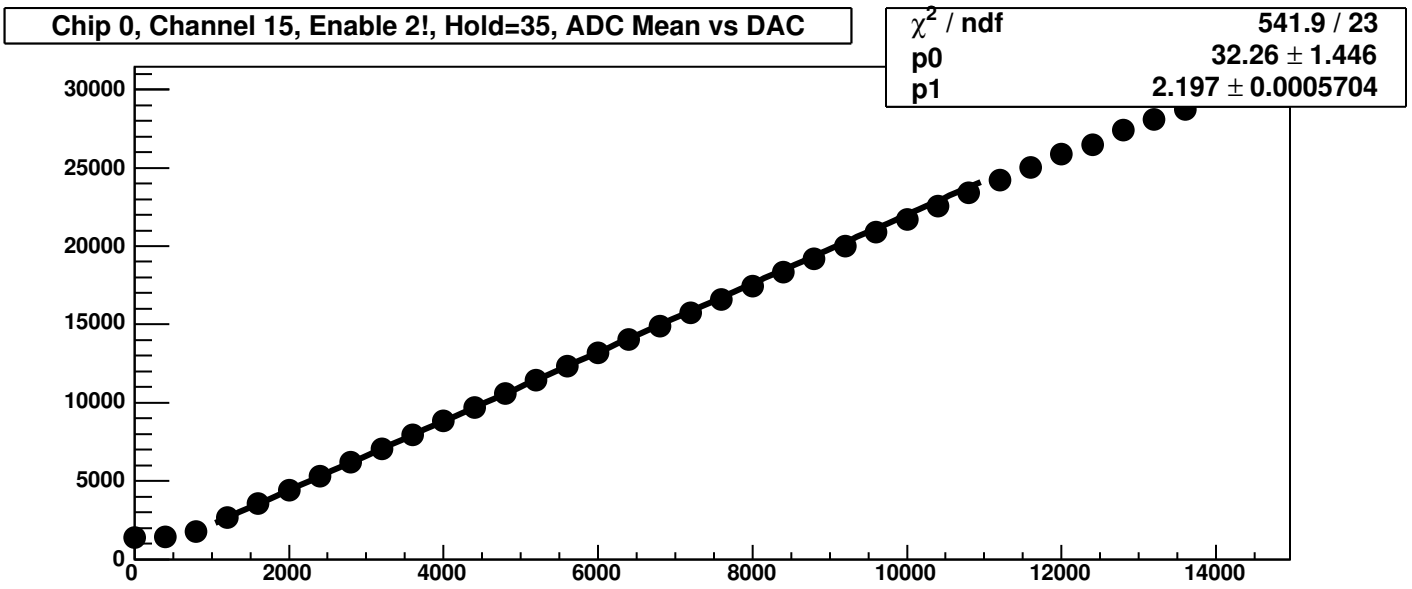

Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

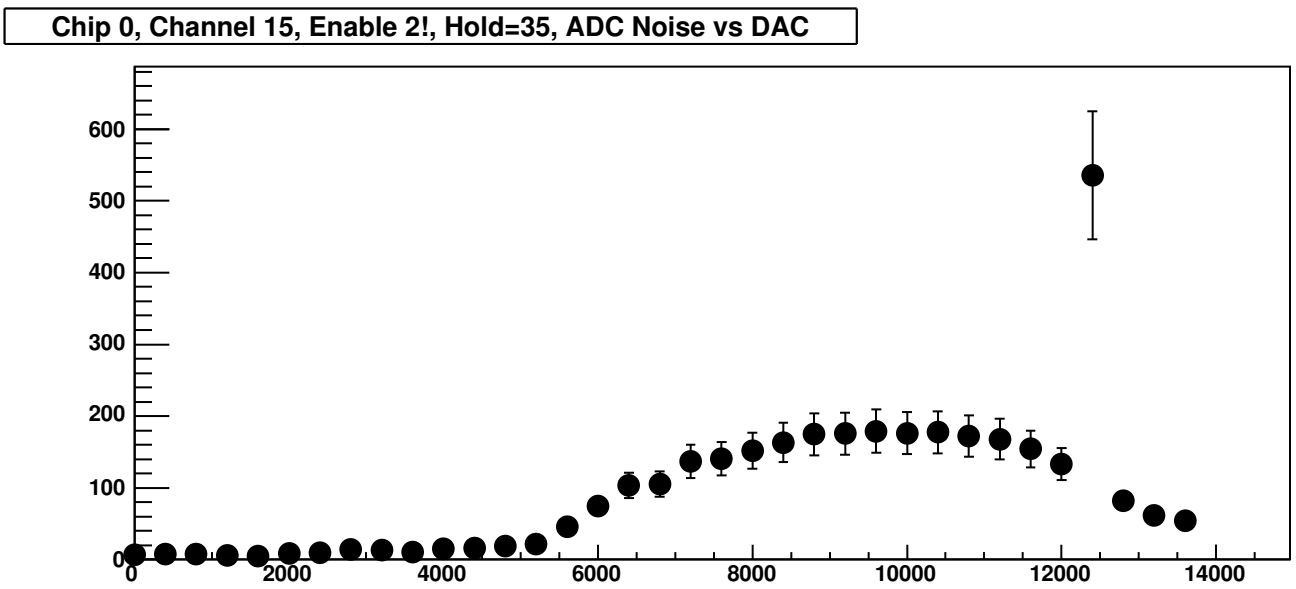

Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

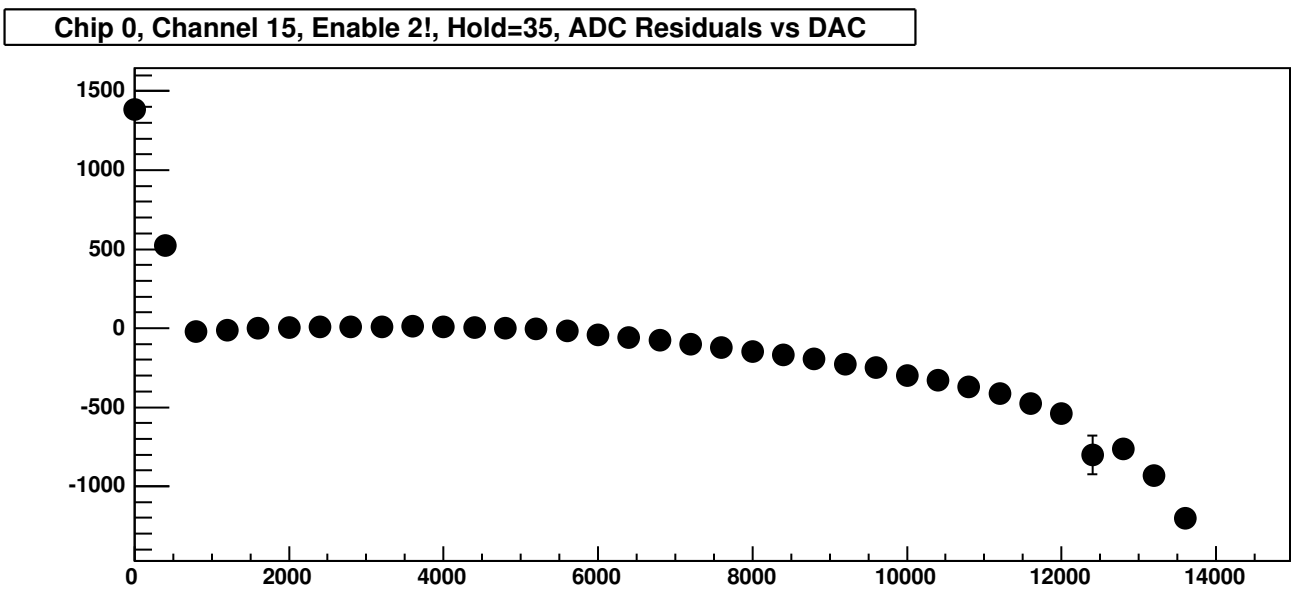

Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.77 / 23
p0 -13.33 ± 0.865
p1 0.02183 ± 0.0001348
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

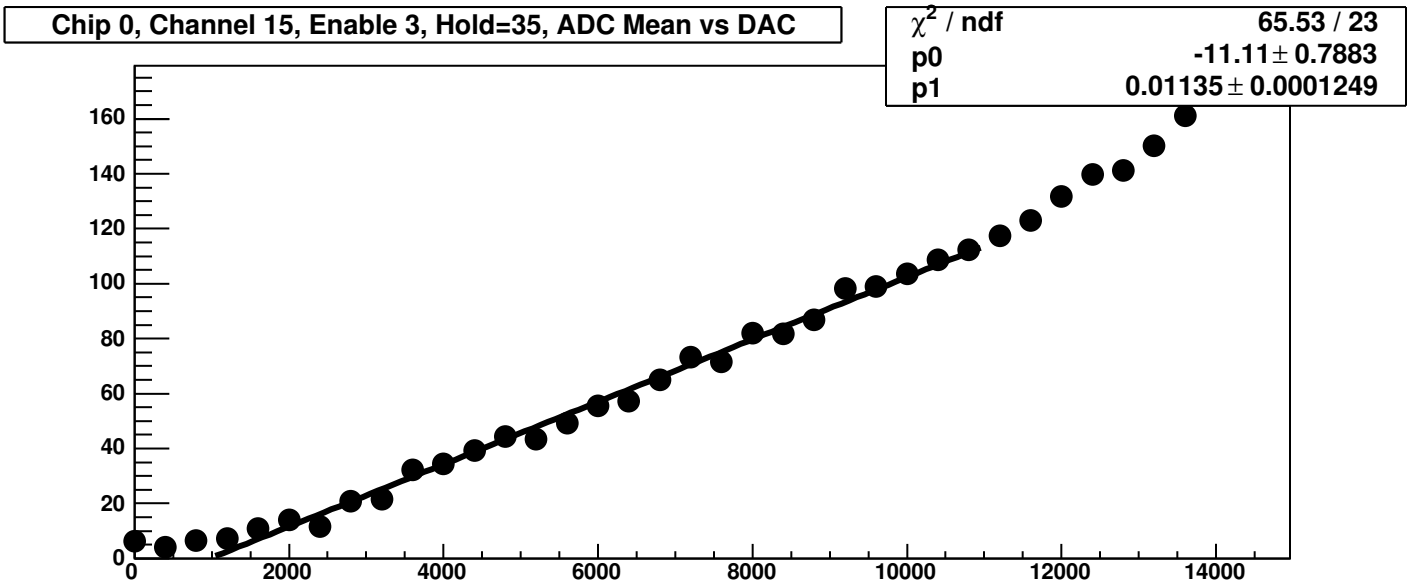

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

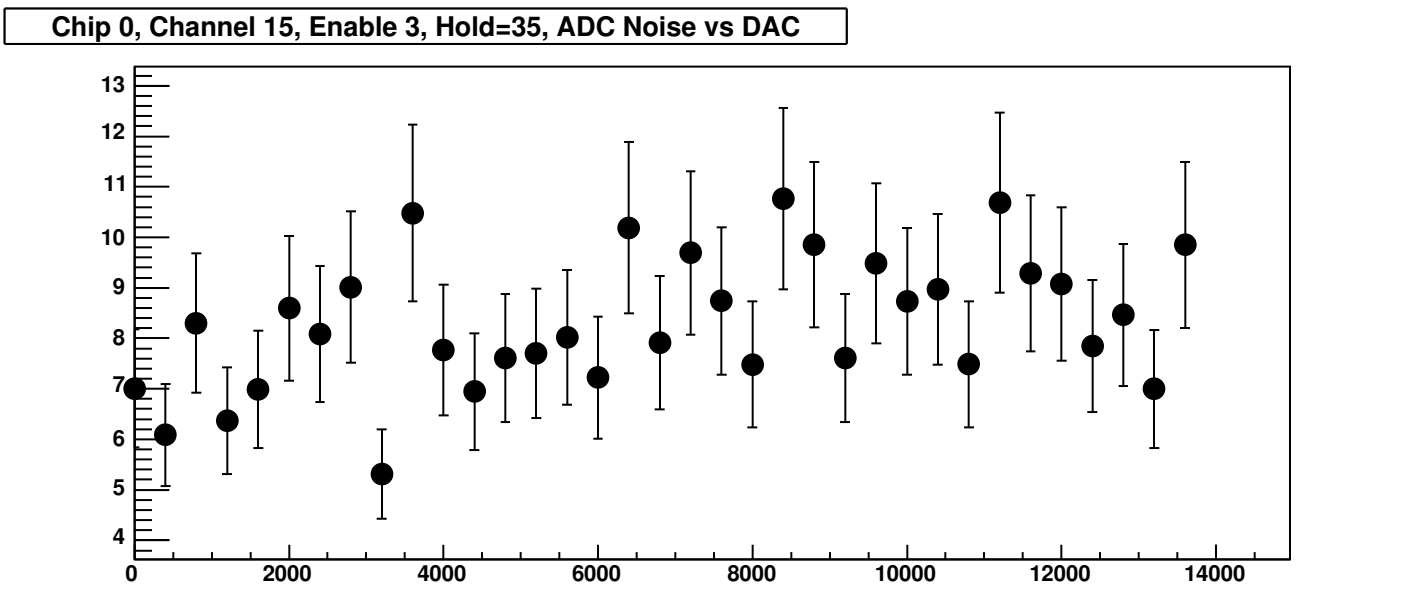

Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

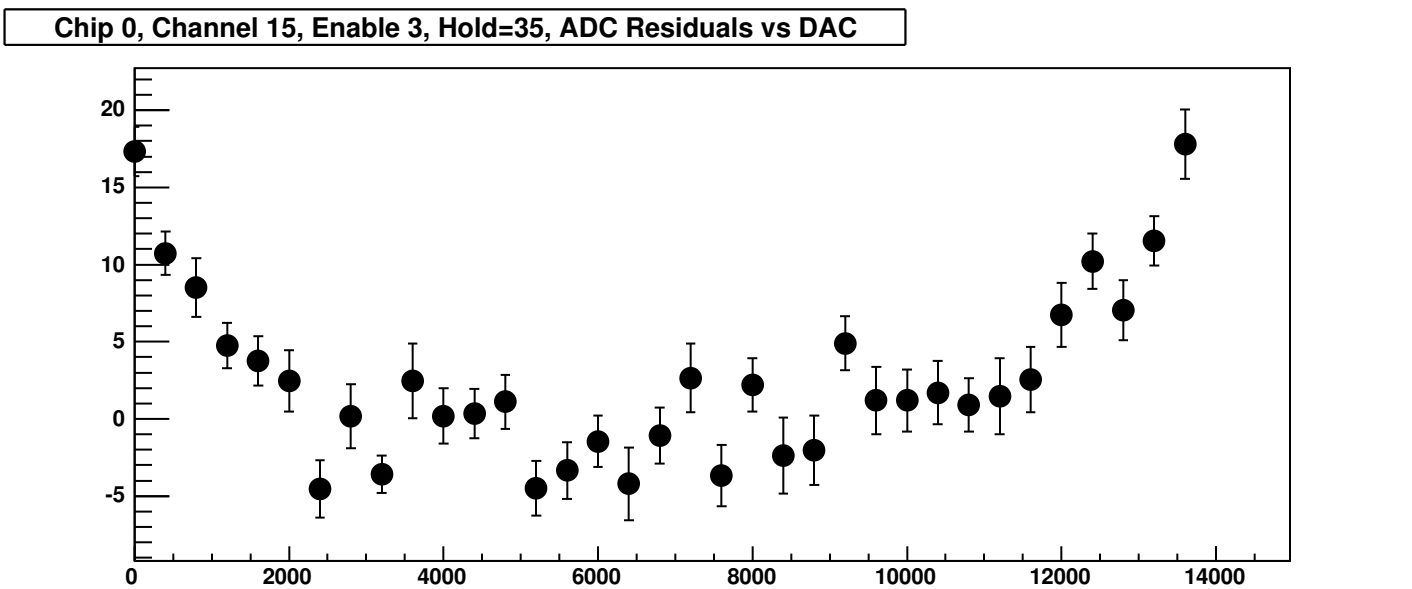

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 541.9 / 23
p0 32.26 ± 1.446
p1 2.197 ± 0.0005704
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

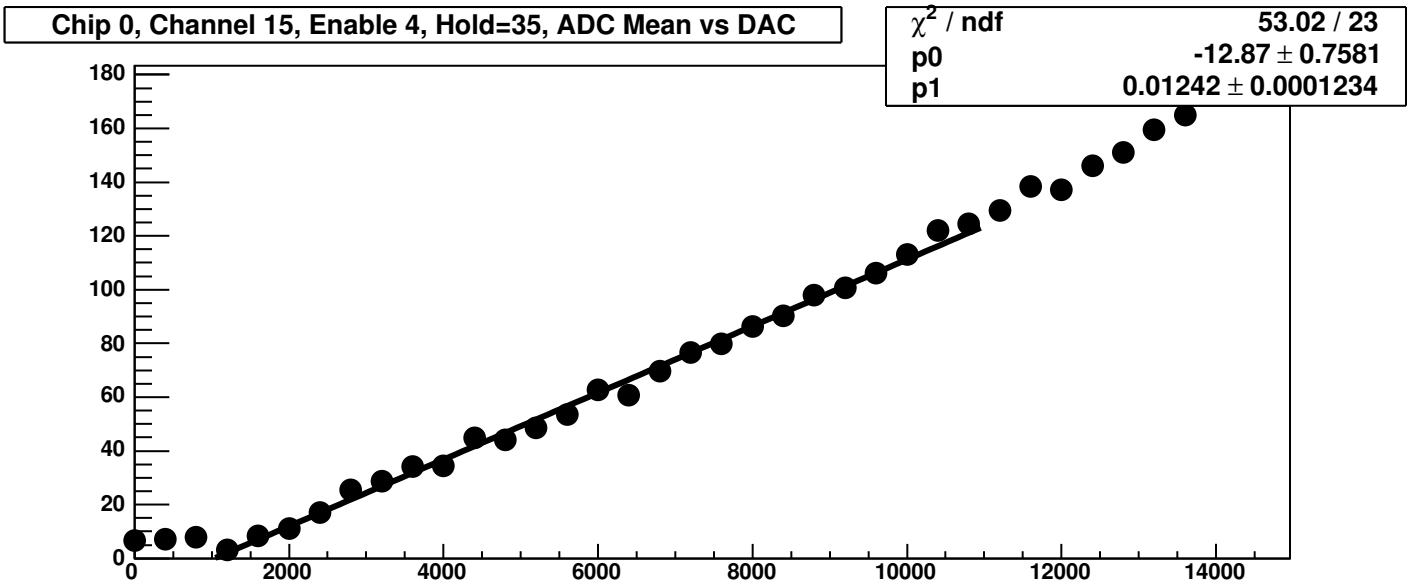

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

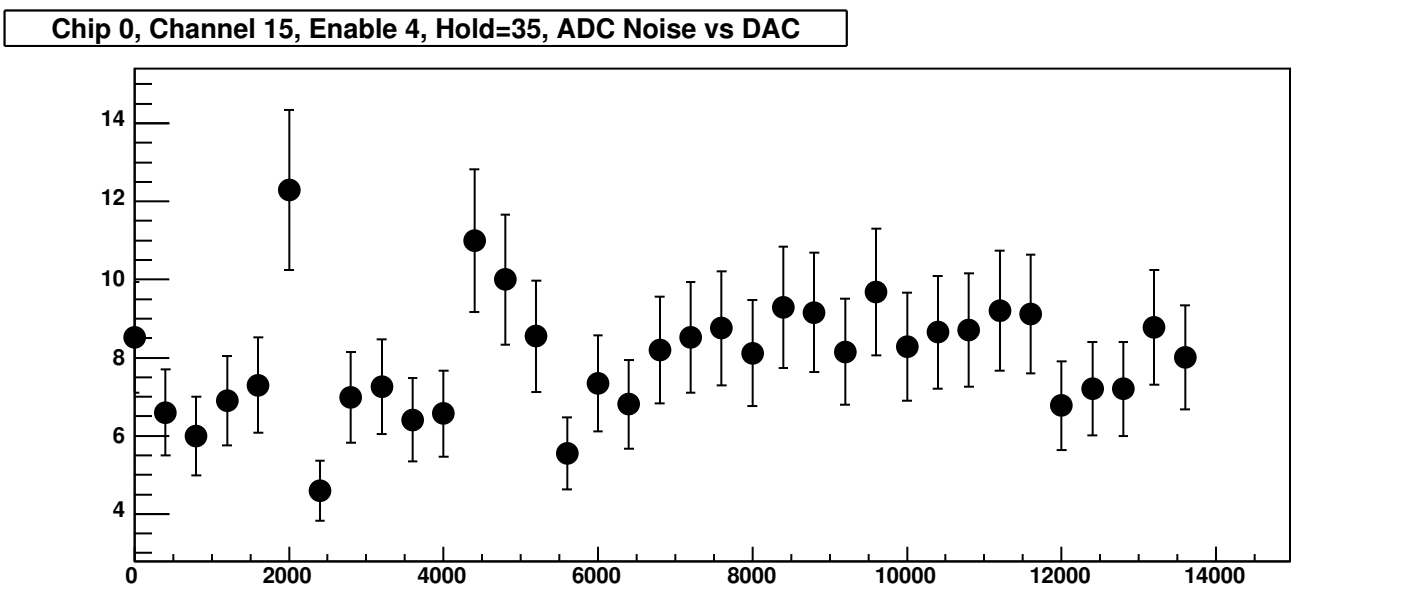

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

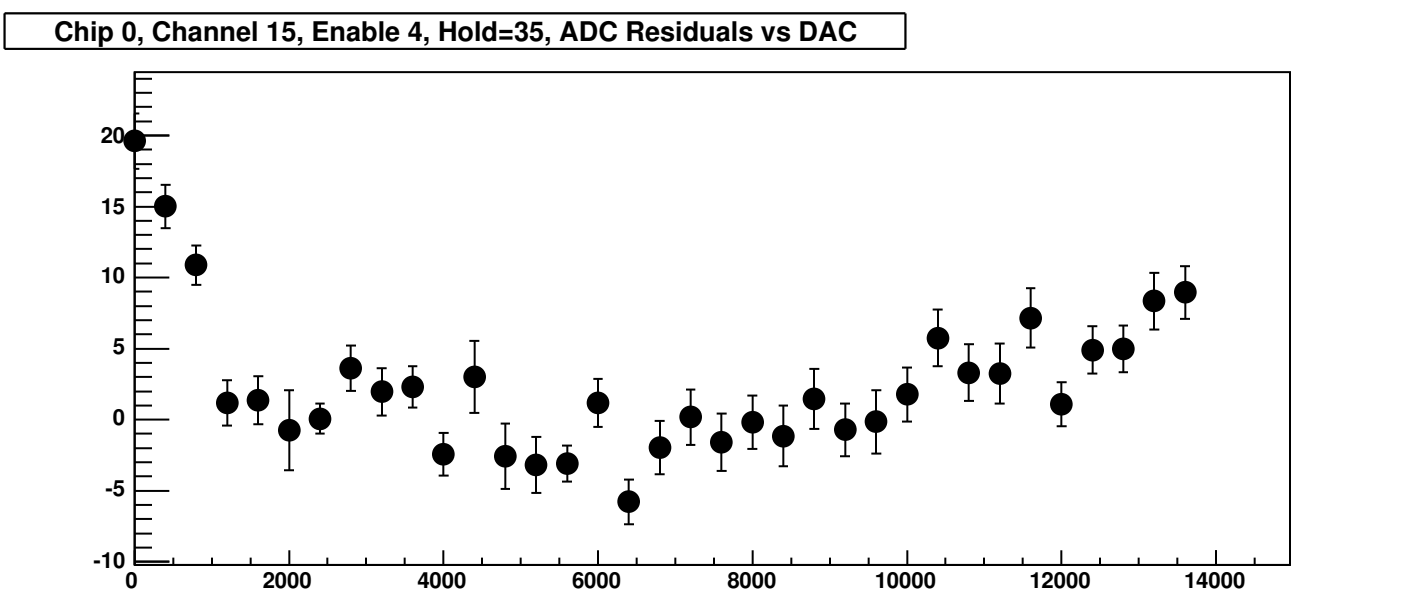

Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 65.53 / 23
p0 -11.11 ± 0.7883
p1 0.01135 ± 0.0001249
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

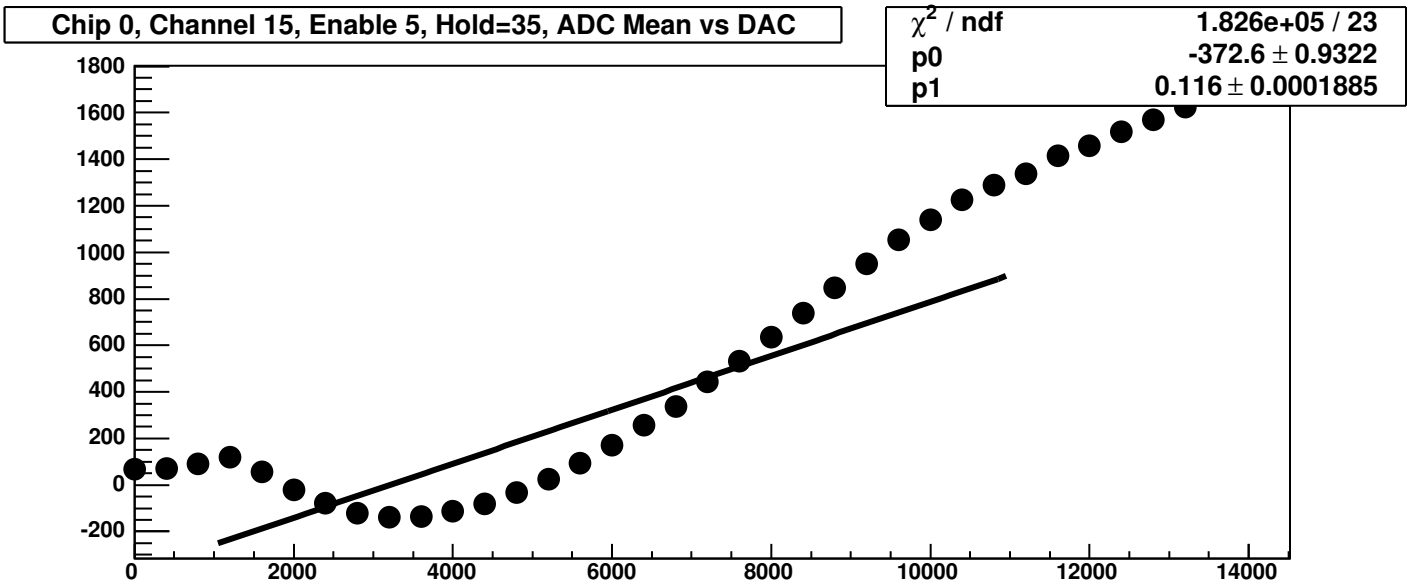

Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

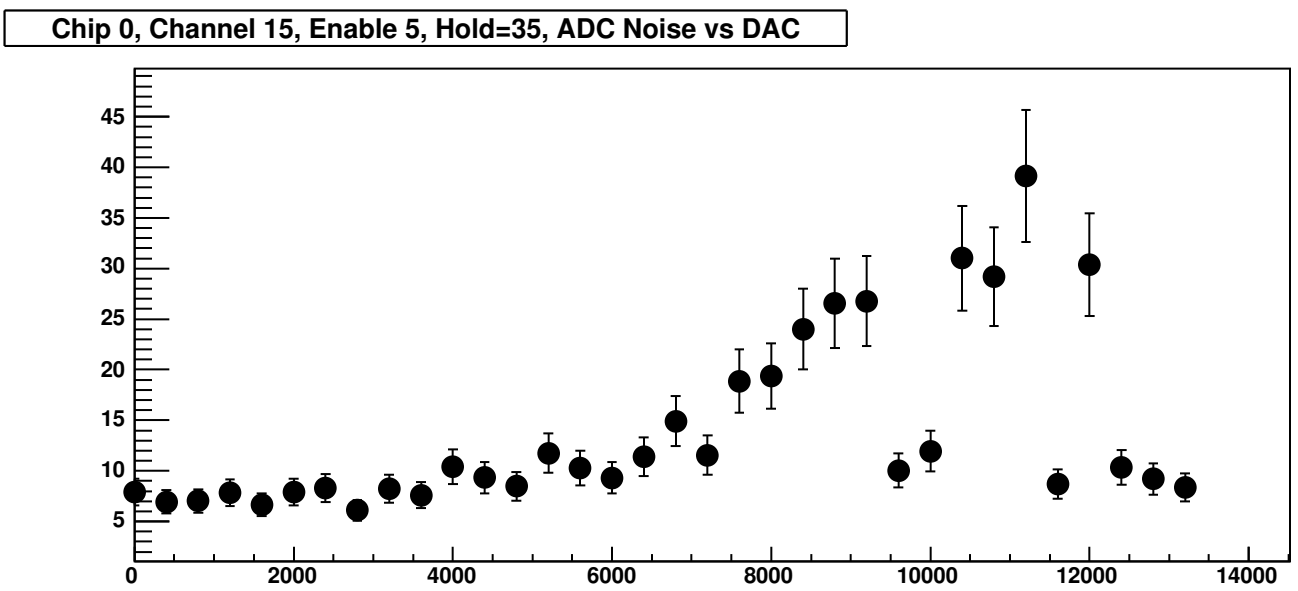

Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

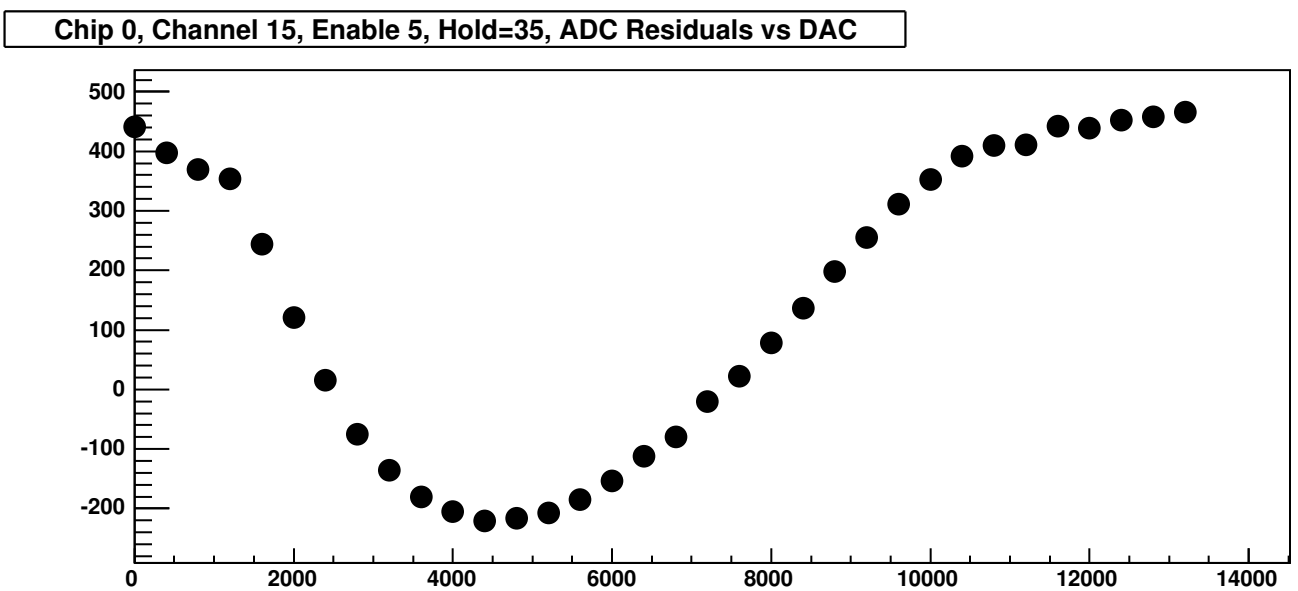

Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 53.02 / 23
p0 -12.87 ± 0.7581
p1 0.01242 ± 0.0001234
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

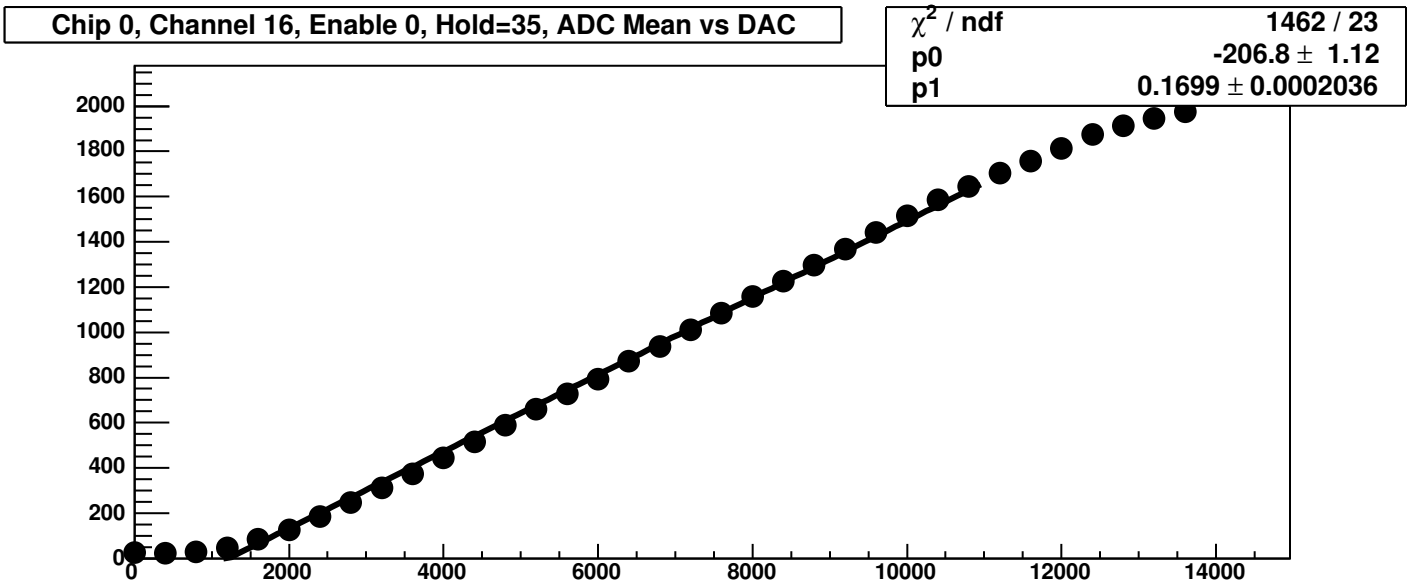

Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

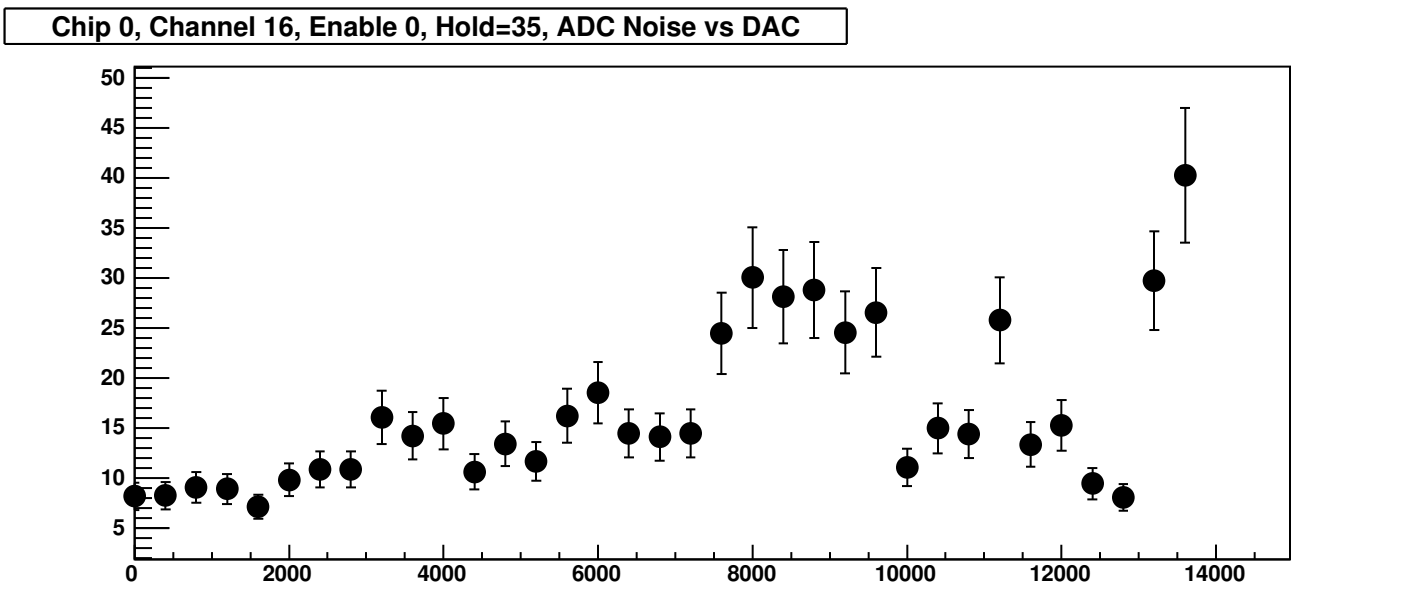

Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

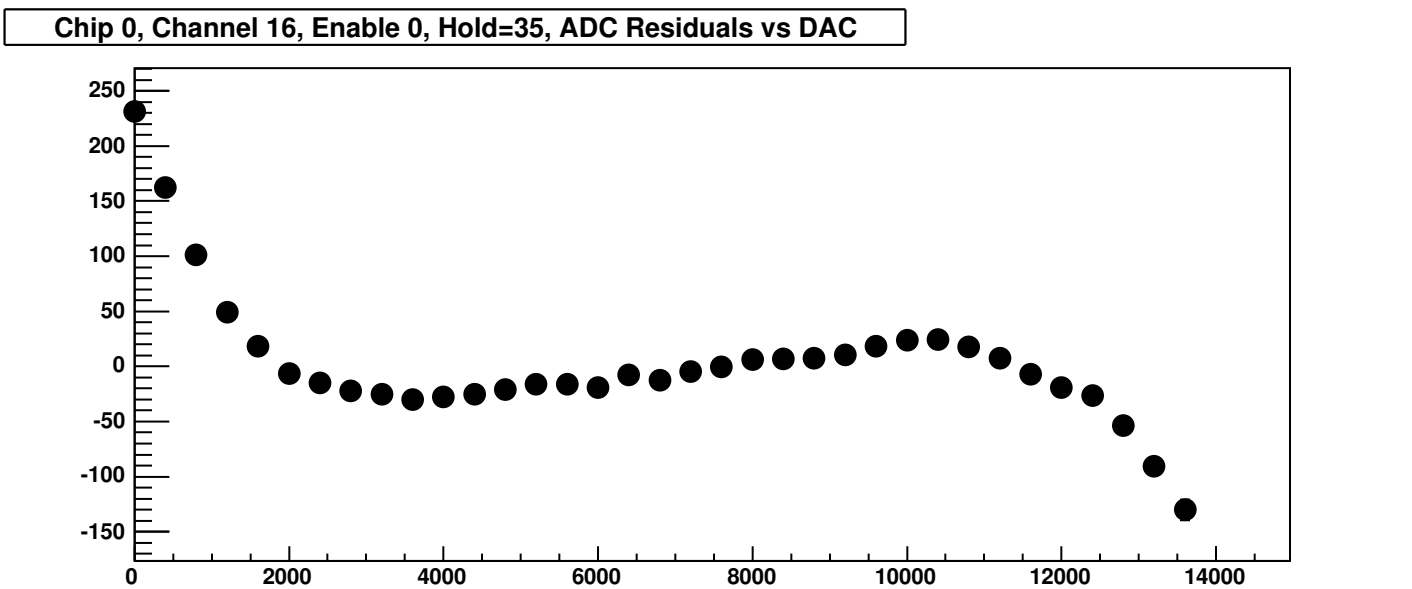

Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.826e+05 / 23
p0 -372.6 ± 0.9322
p1 0.116 ± 0.0001885
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

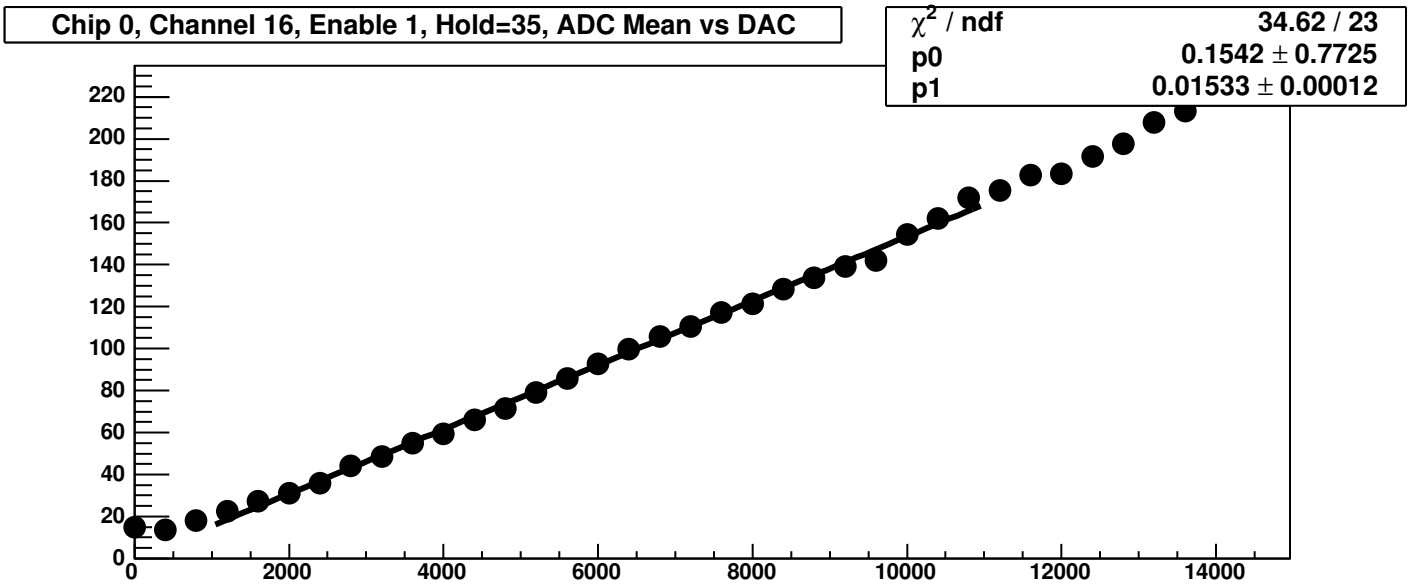

Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

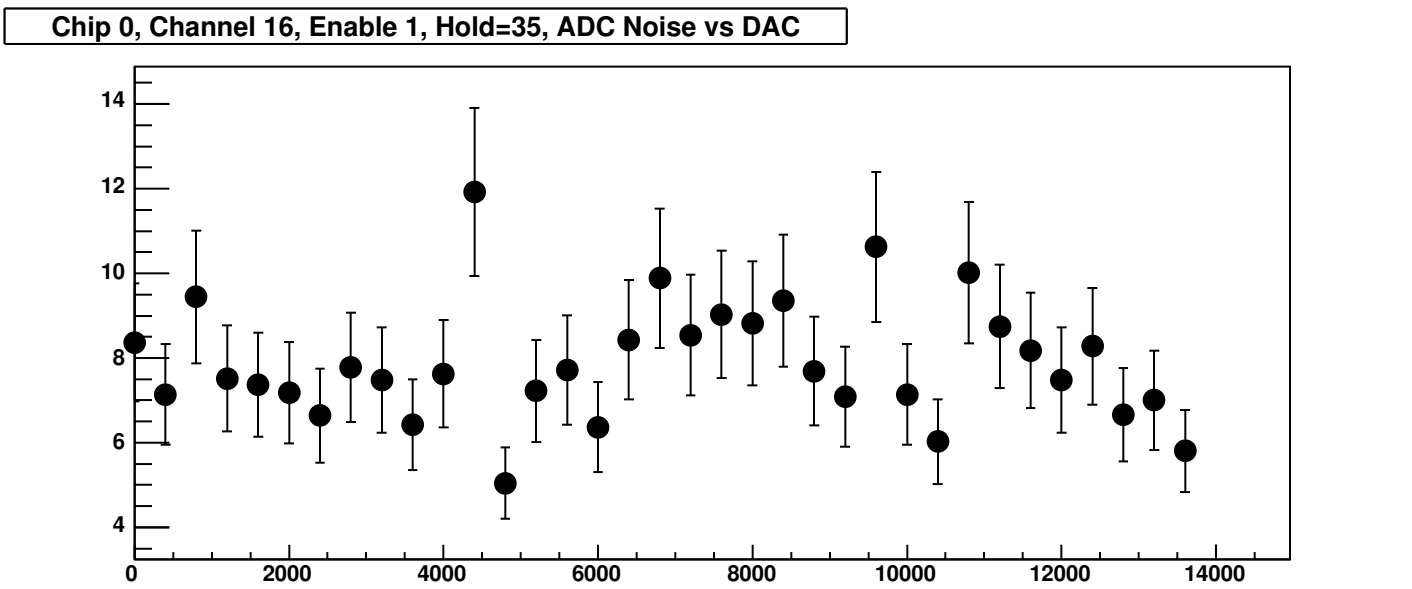

Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

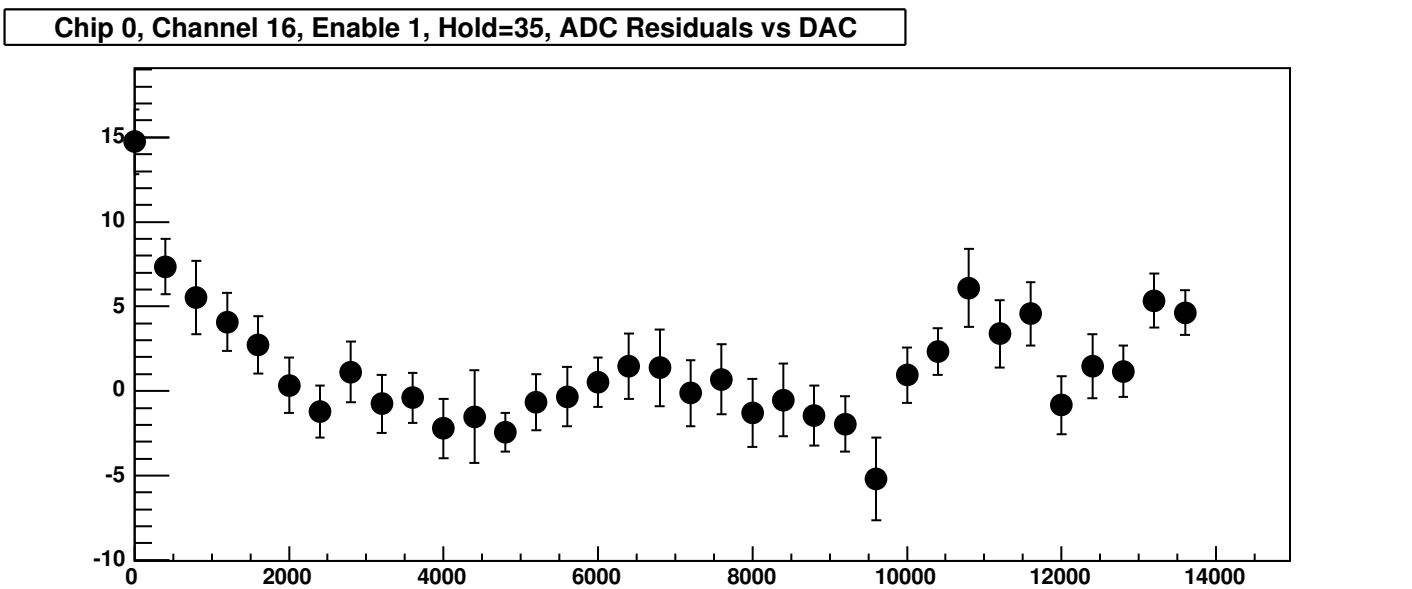

Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1462 / 23
p0 -206.8 ± 1.12
p1 0.1699 ± 0.0002036
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

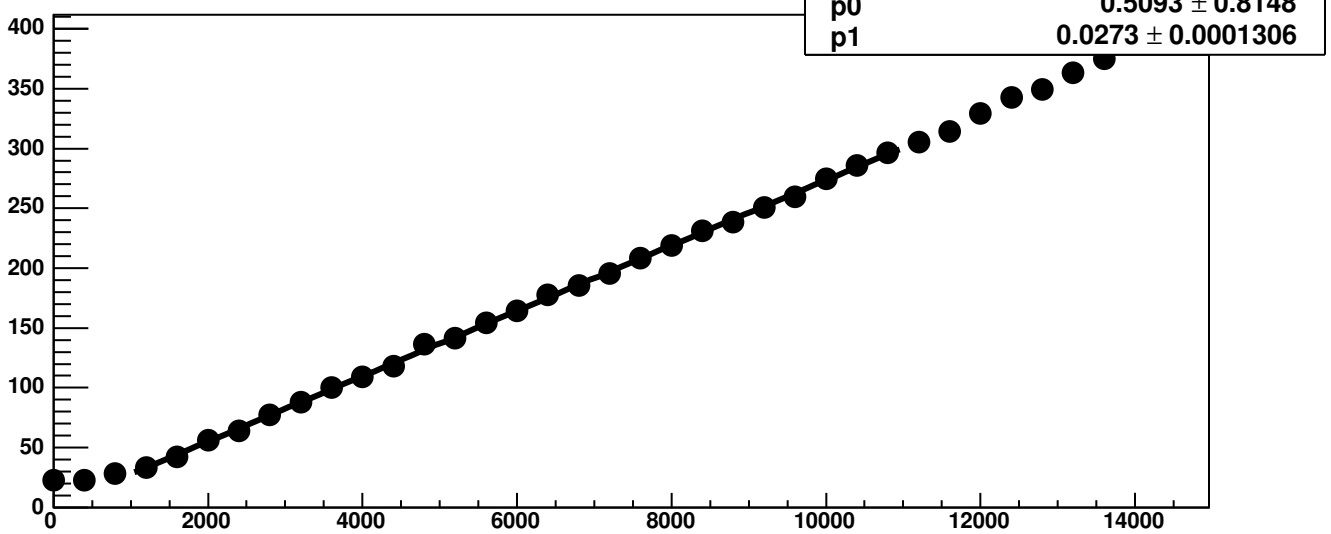

Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.62 / 23
p0 0.1542 ± 0.7725
p1 0.01533 ± 0.00012
0 20 40 60 80 100 120 140 160 180 200 220
0 2000 4000 6000 8000 10000 12000 14000

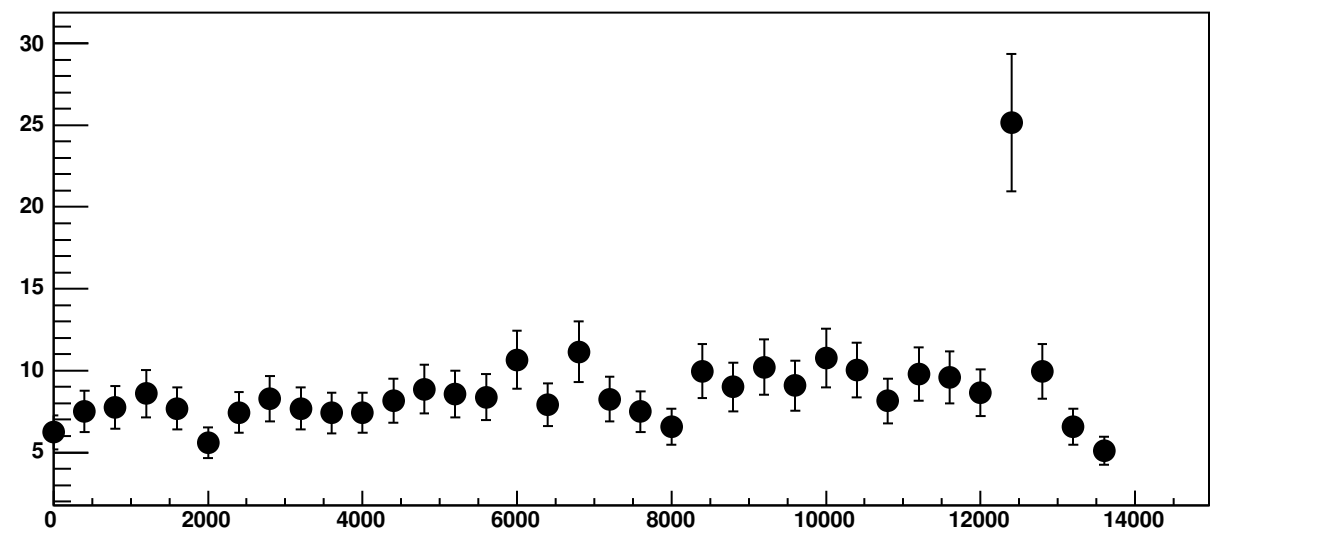

Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
4 6 8 10 12 14
0 2000 4000 6000 8000 10000 12000 14000


Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
-10 -5 0 5 10 15
0 2000 4000 6000 8000 10000 12000 14000

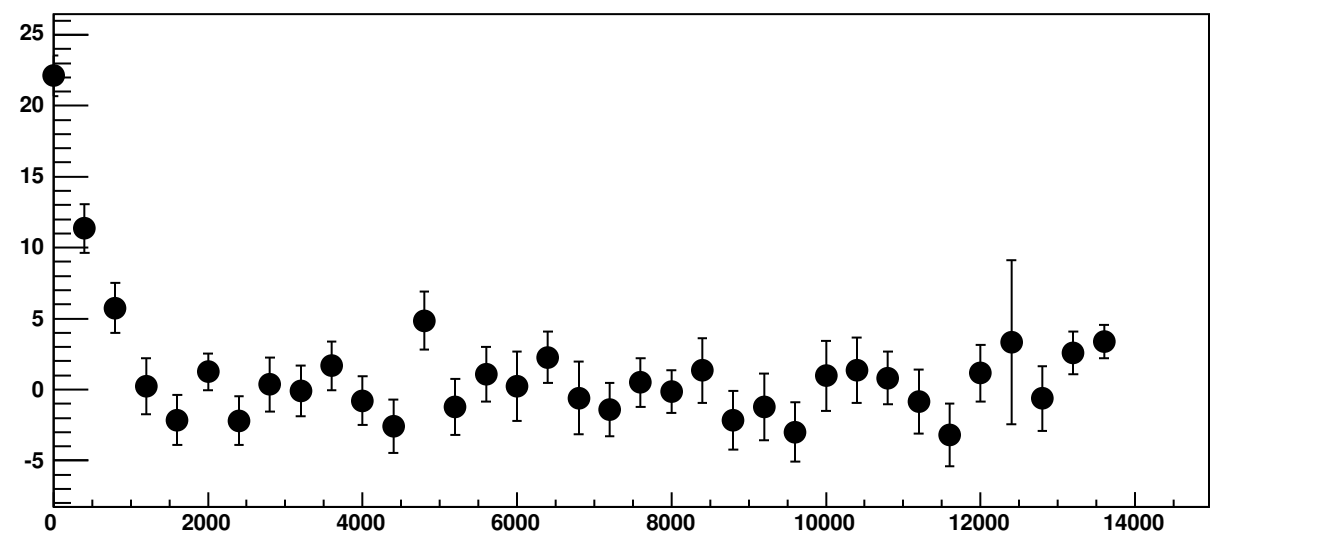
Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

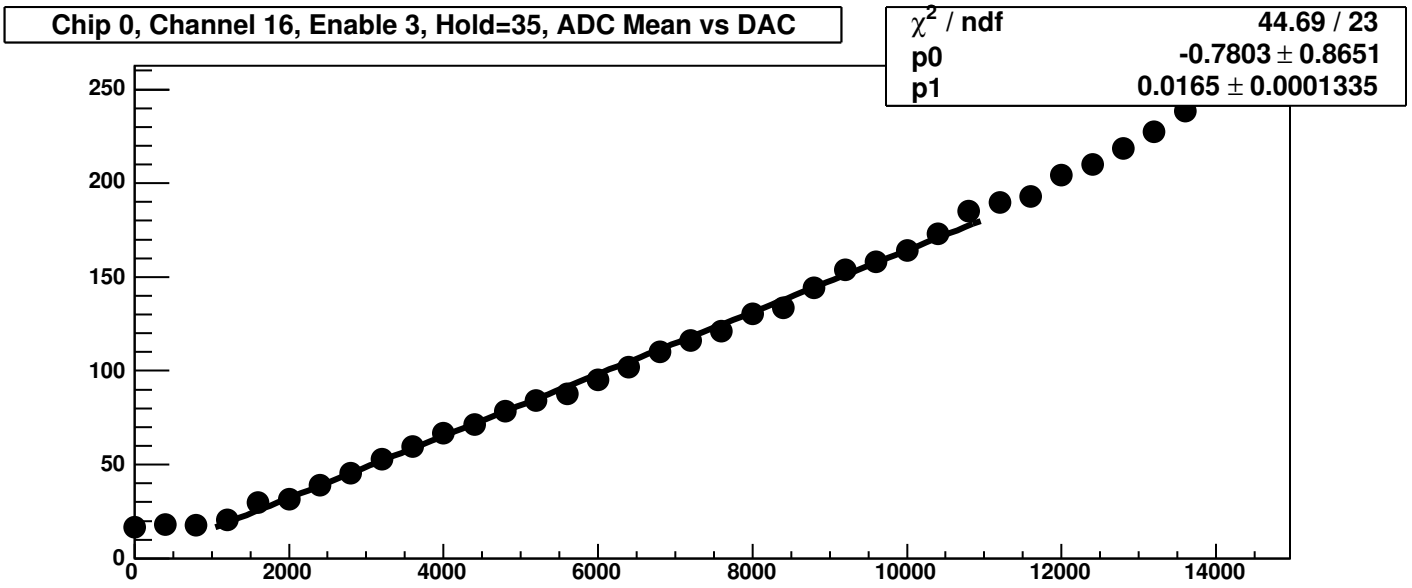


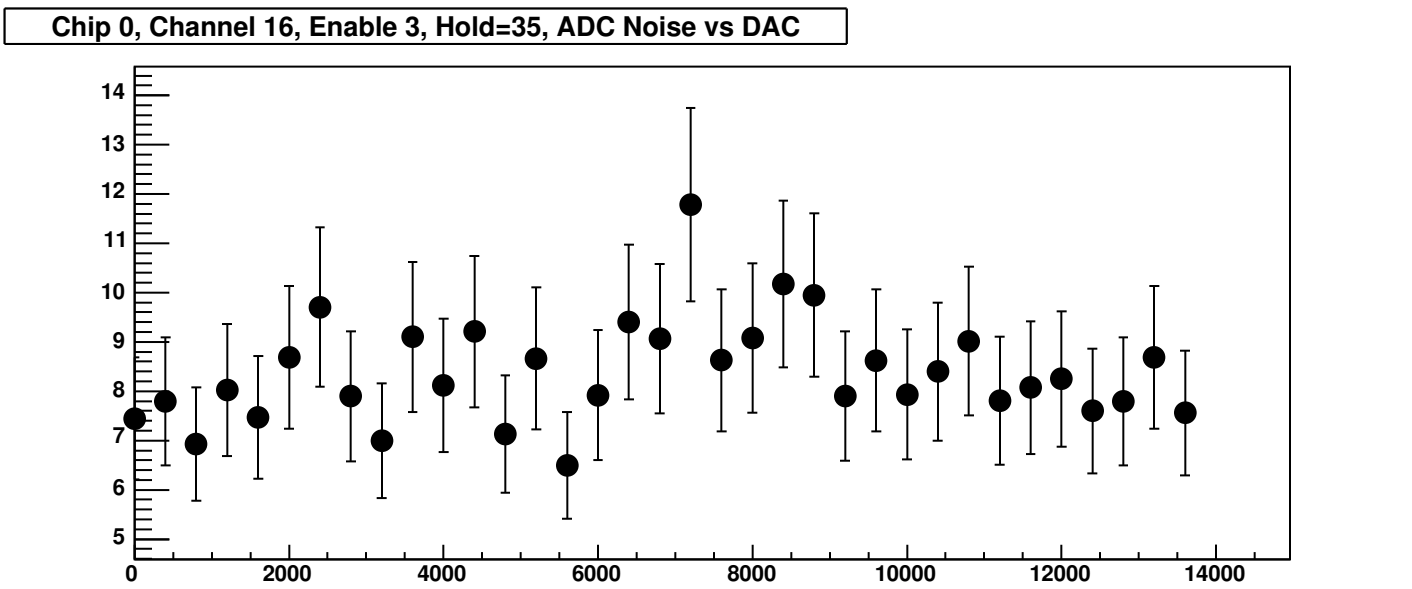
Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

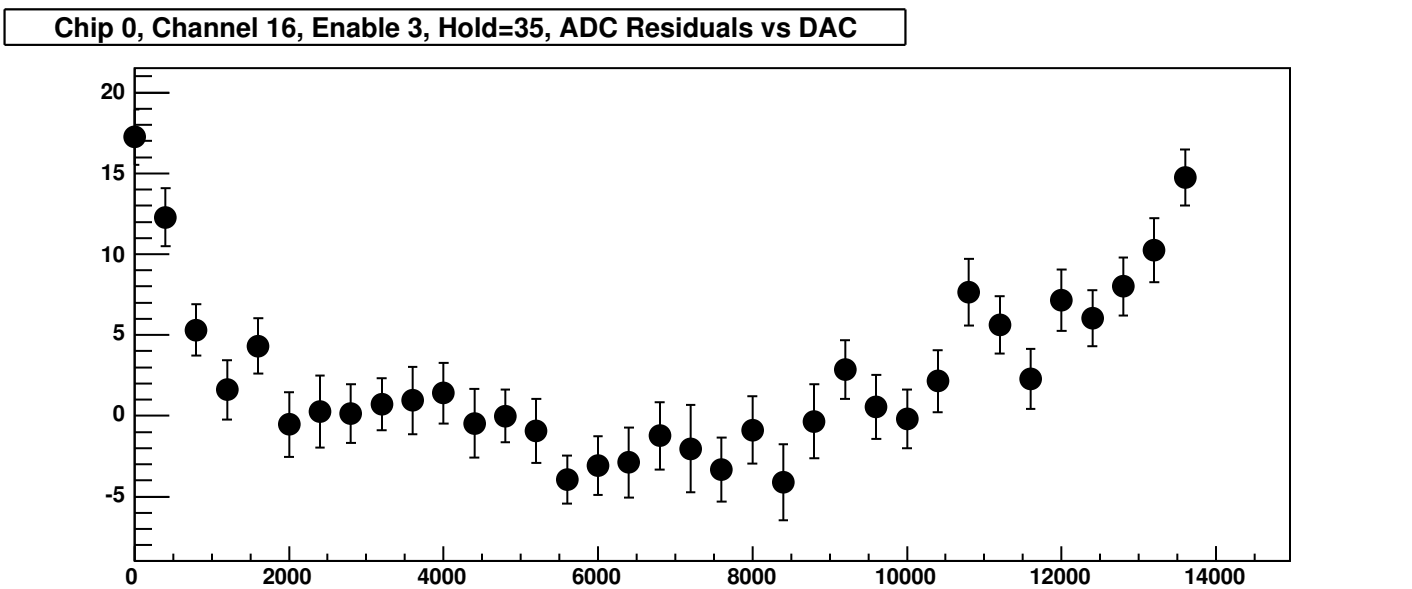


Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

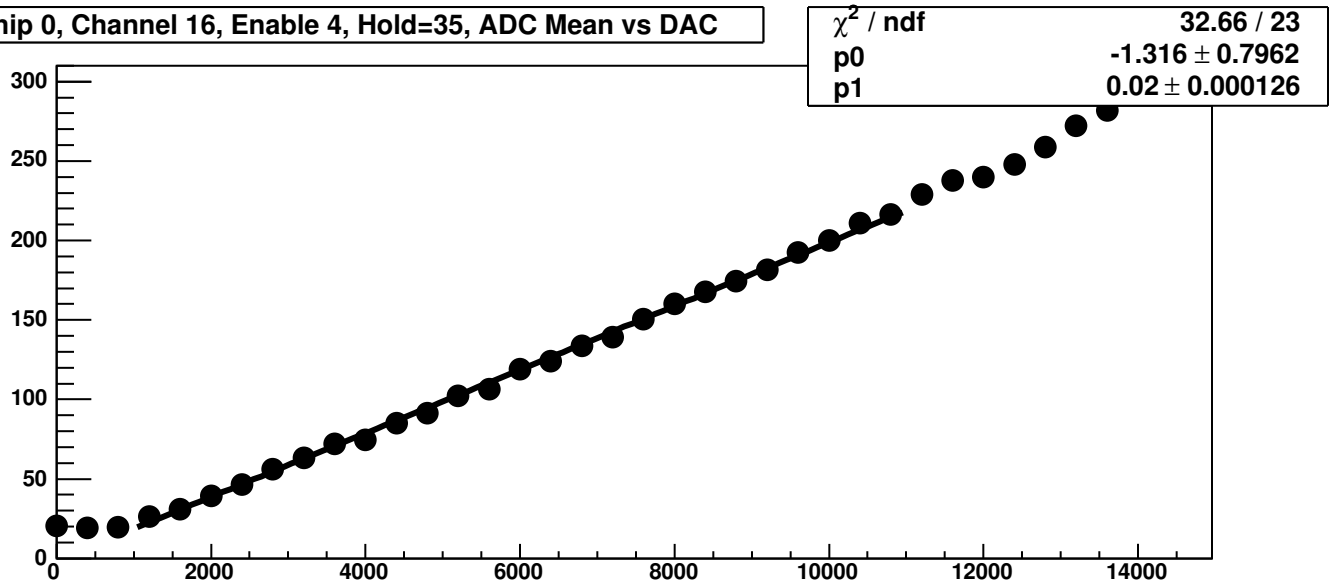



Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.69 / 23
p0 -0.7803 ± 0.8651
p1 0.0165 ± 0.0001335
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

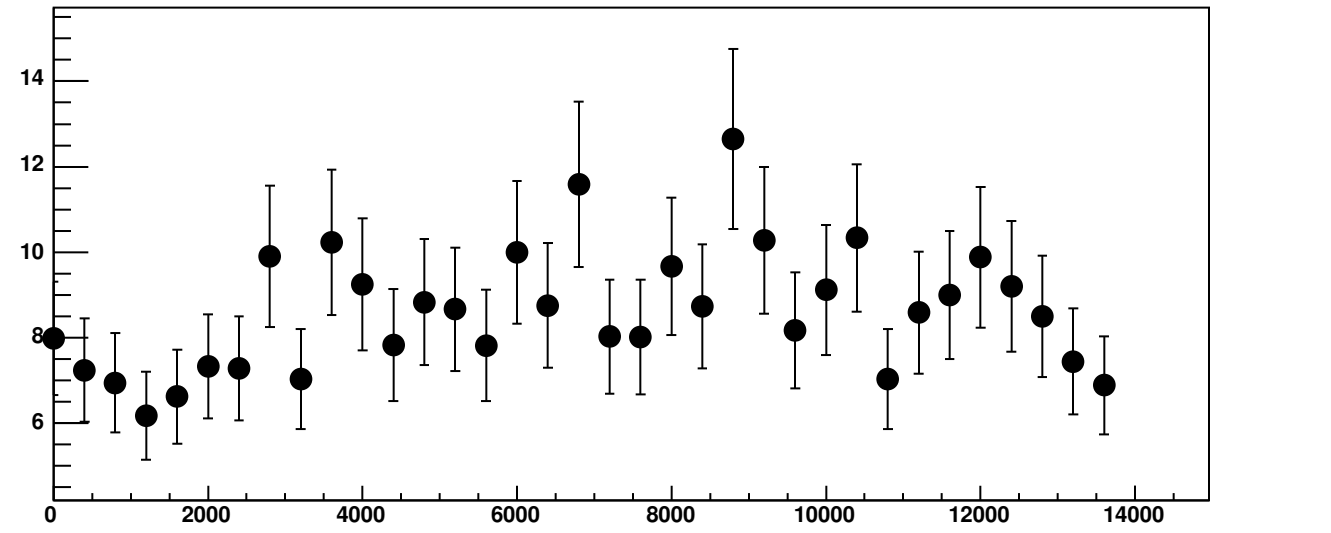

Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

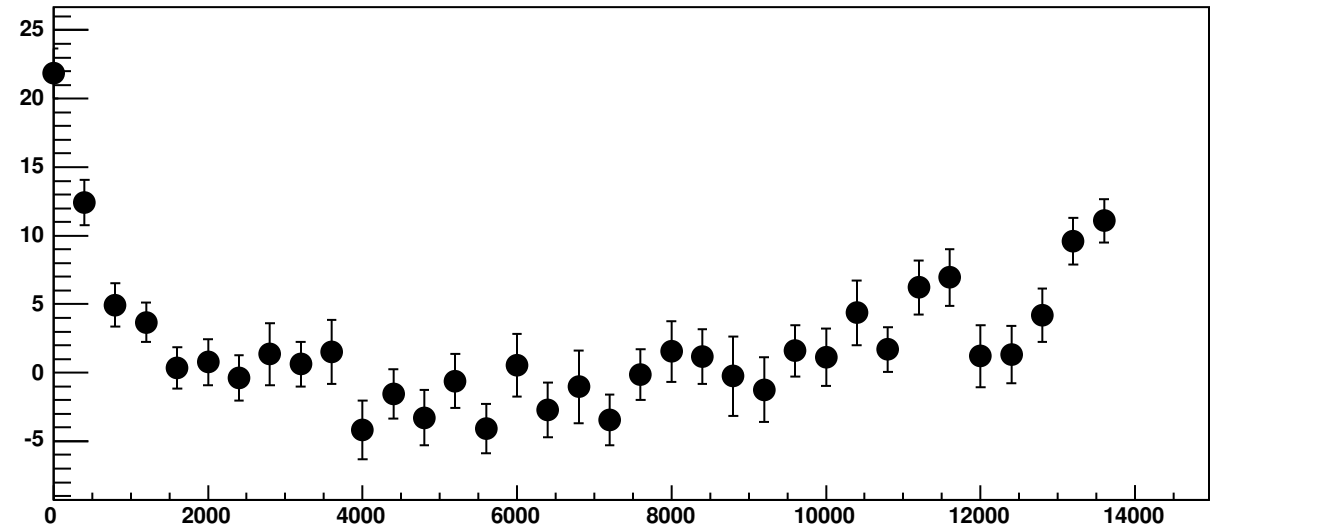
Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

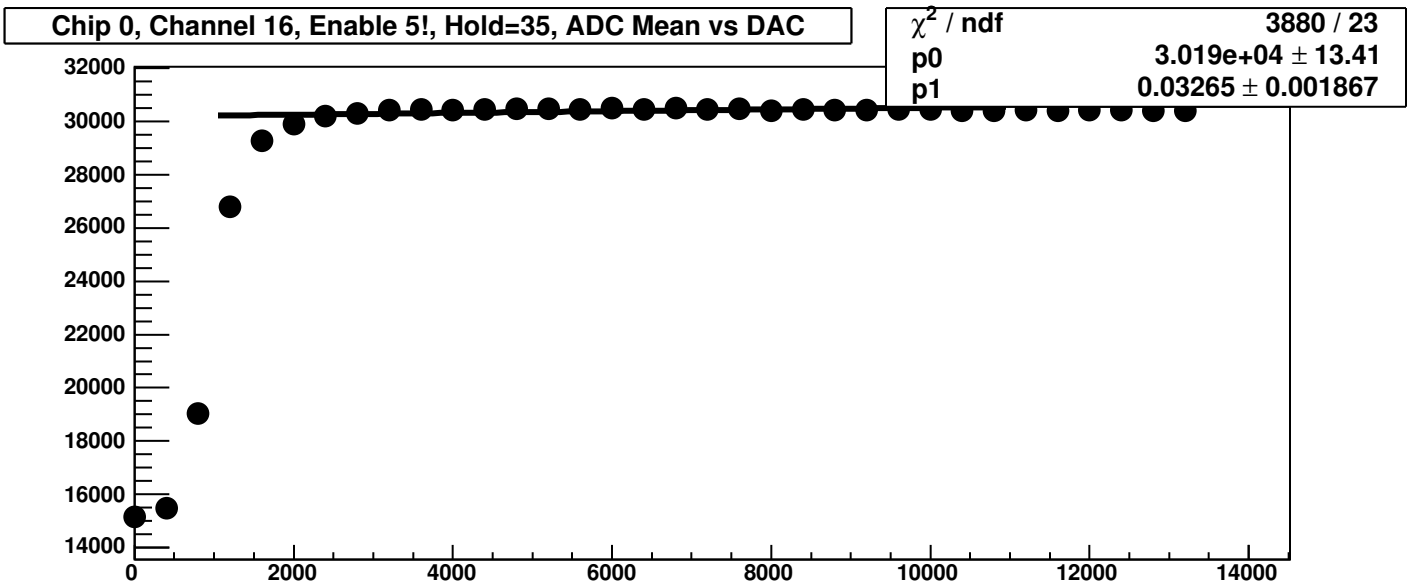


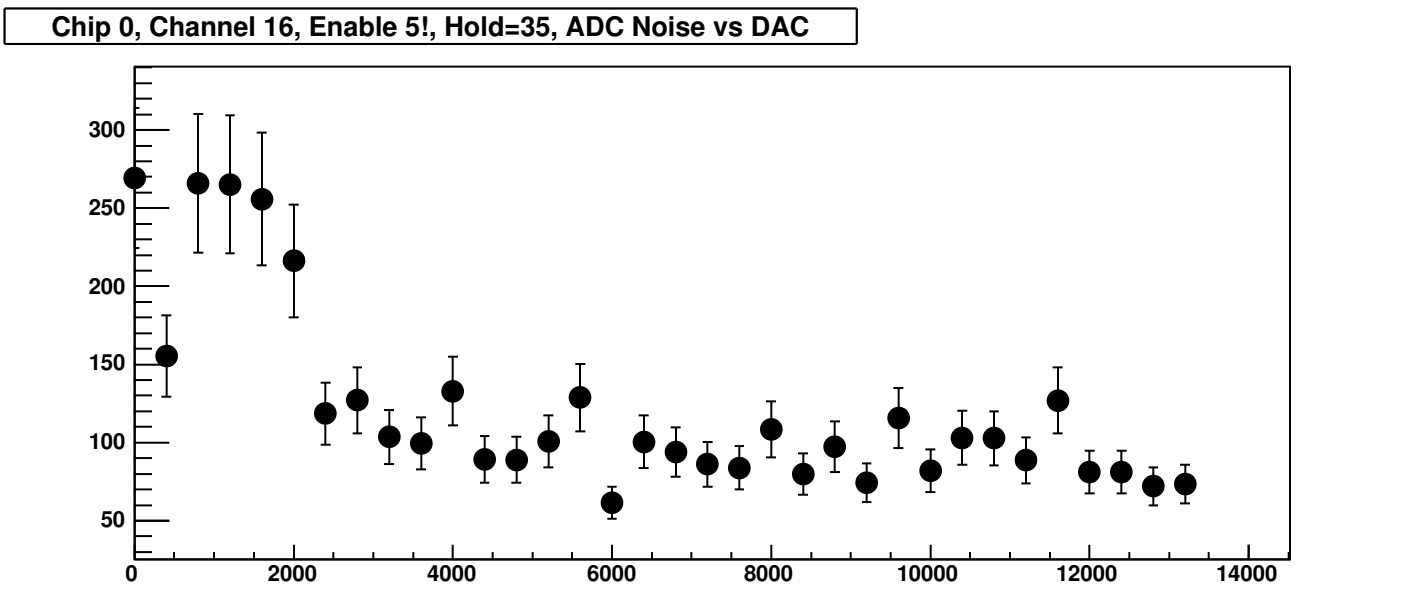
Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

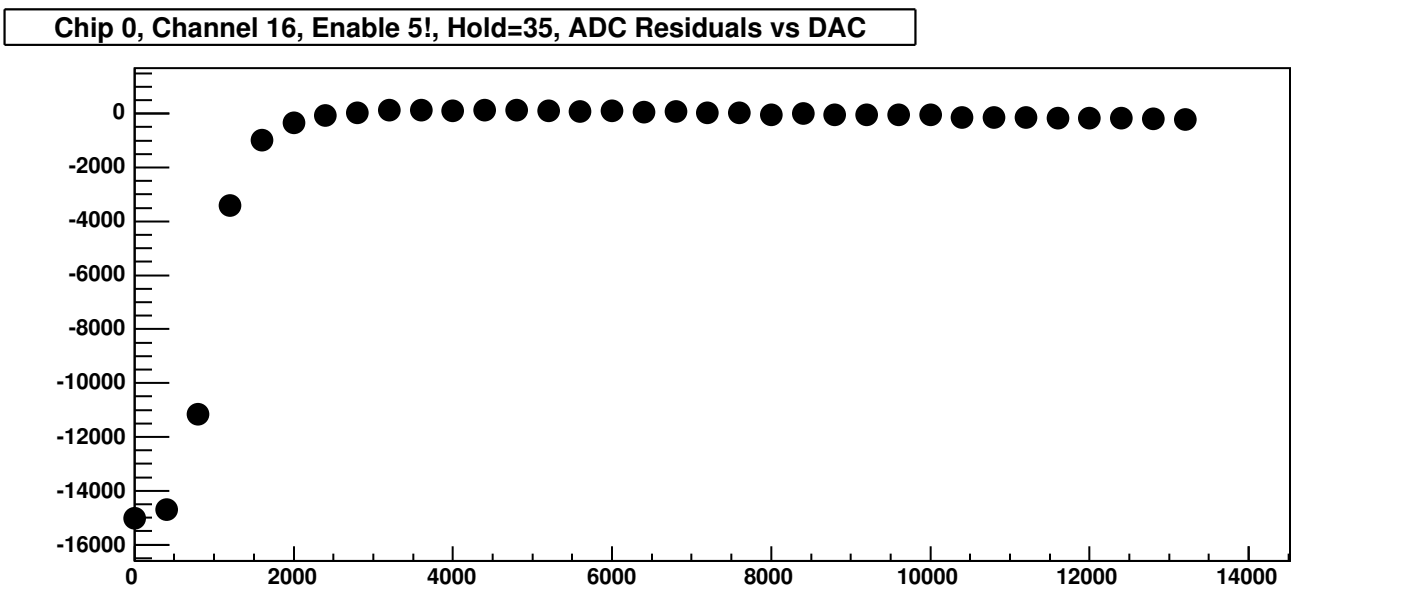


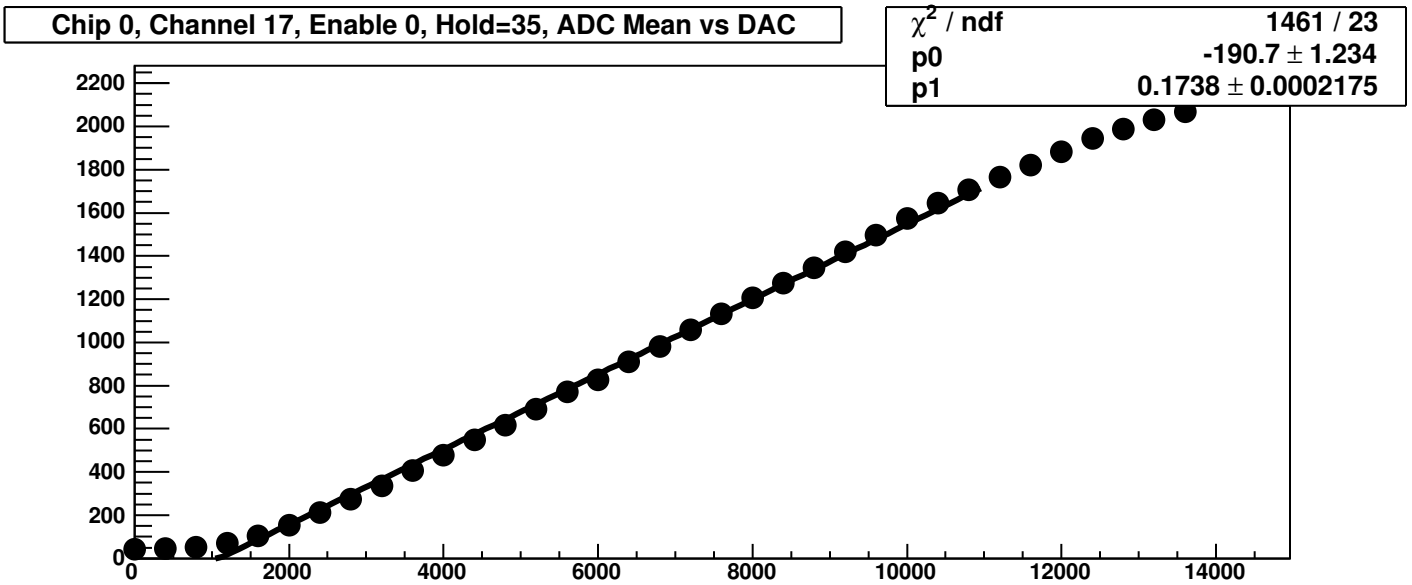
Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

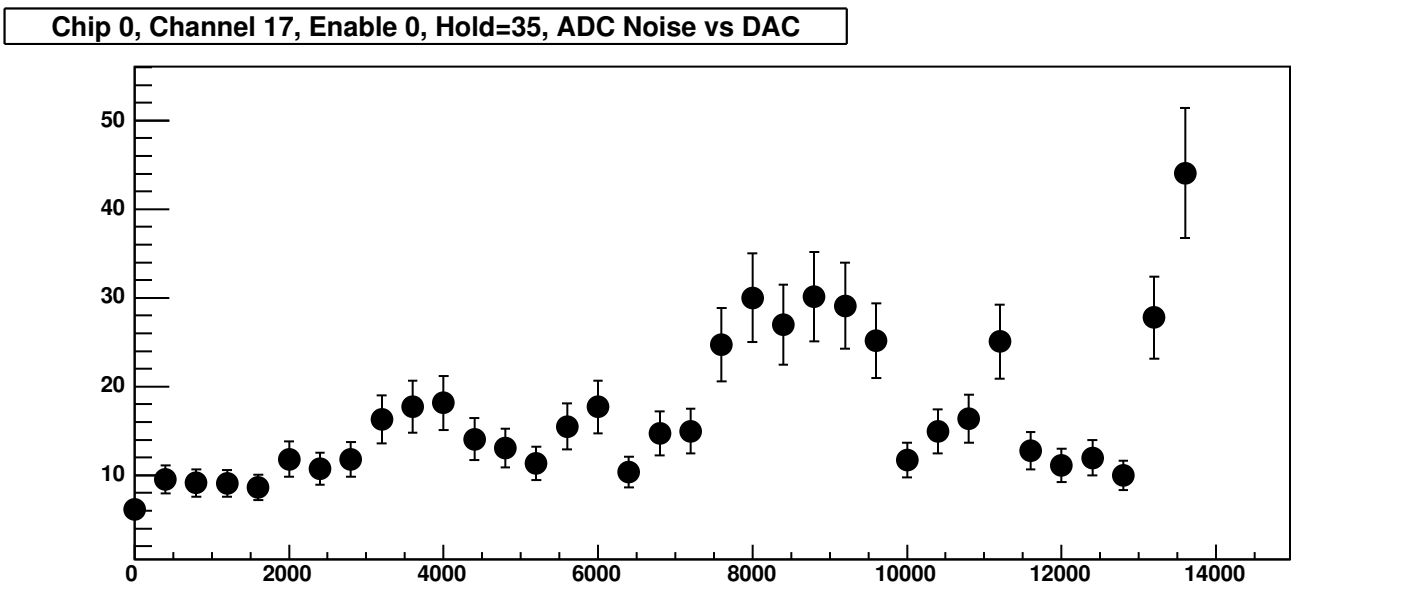


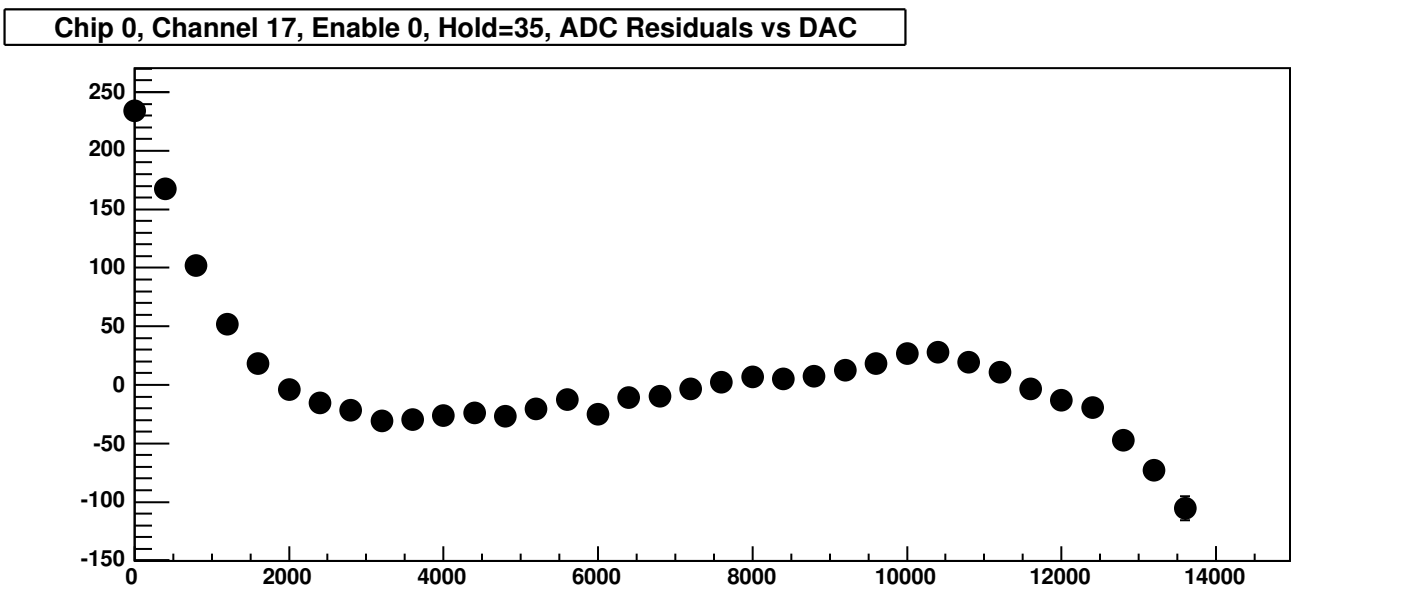

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3880 / 23
p0 3.019e+04 ± 13.41
p1 0.03265 ± 0.001867
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

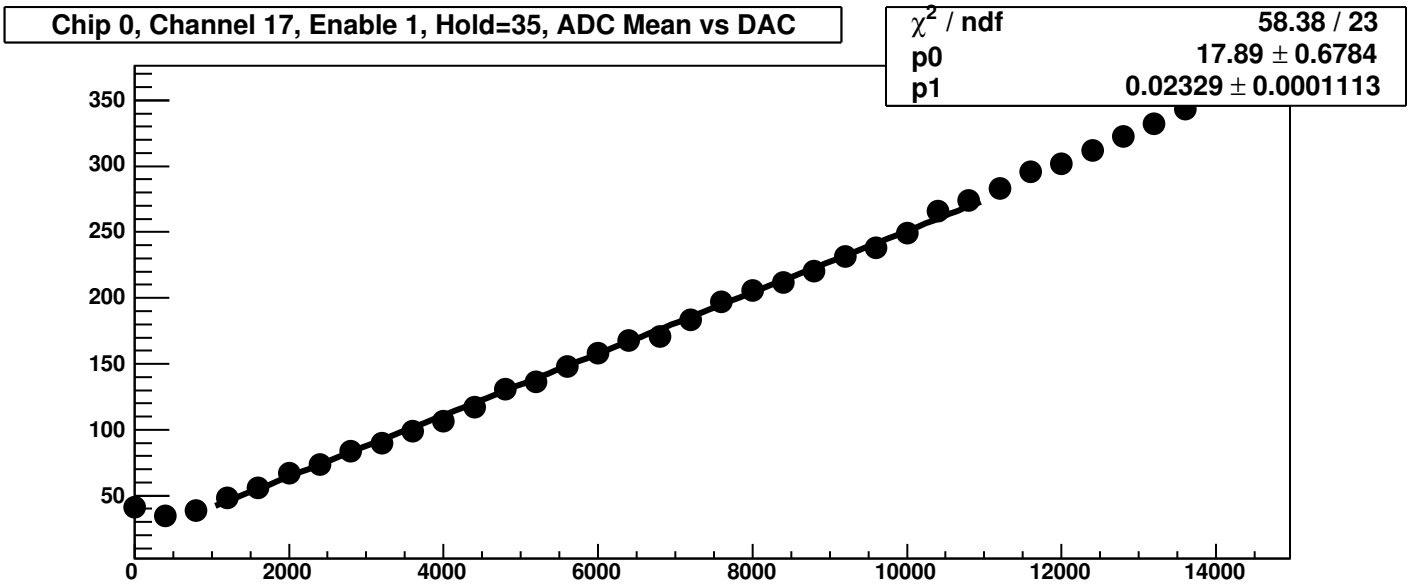

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

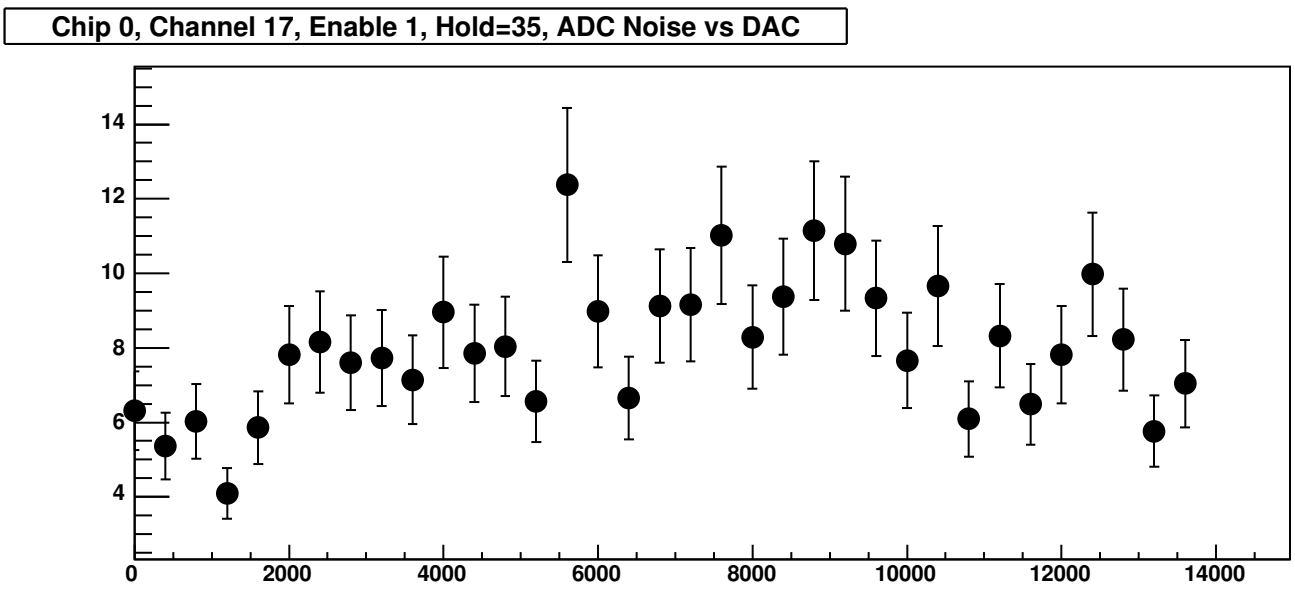

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

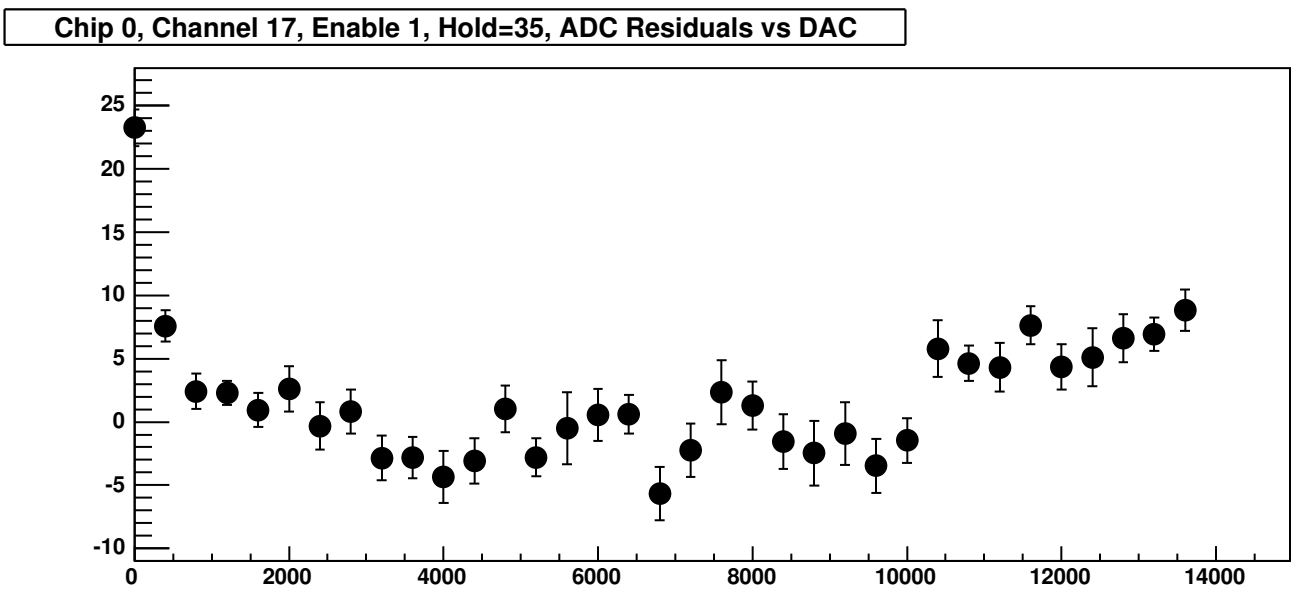

Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1461 / 23
p0 -190.7 ± 1.234
p1 0.1738 ± 0.0002175
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

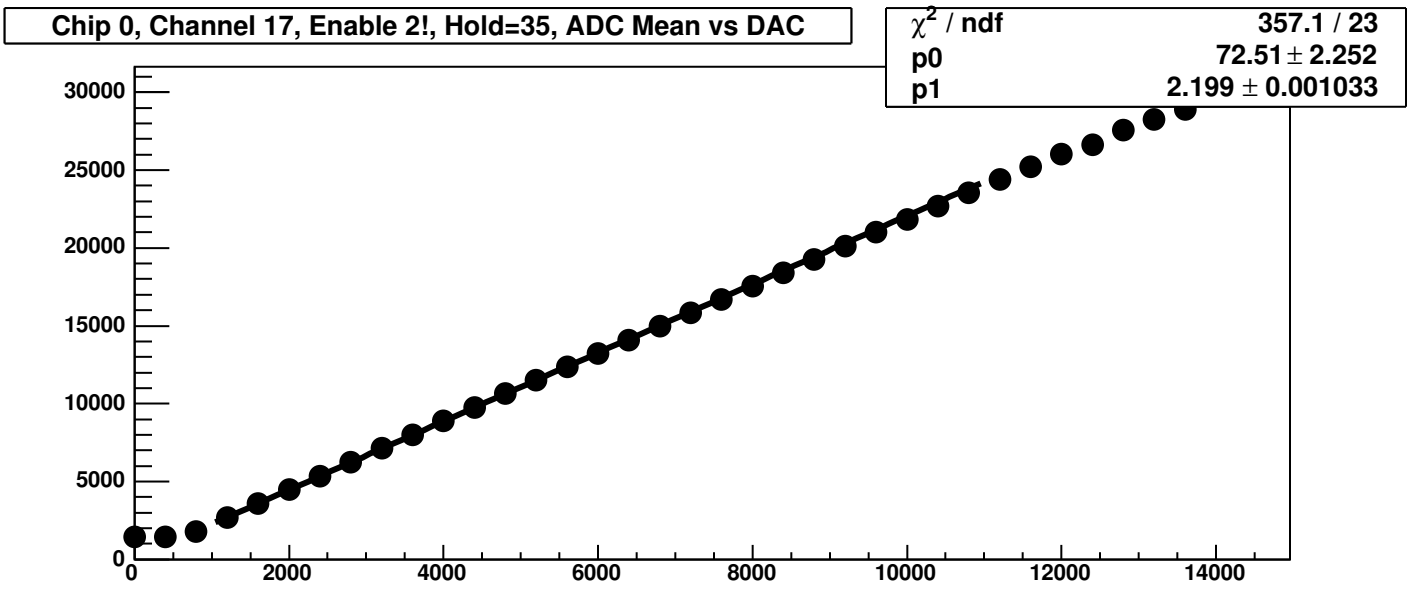

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

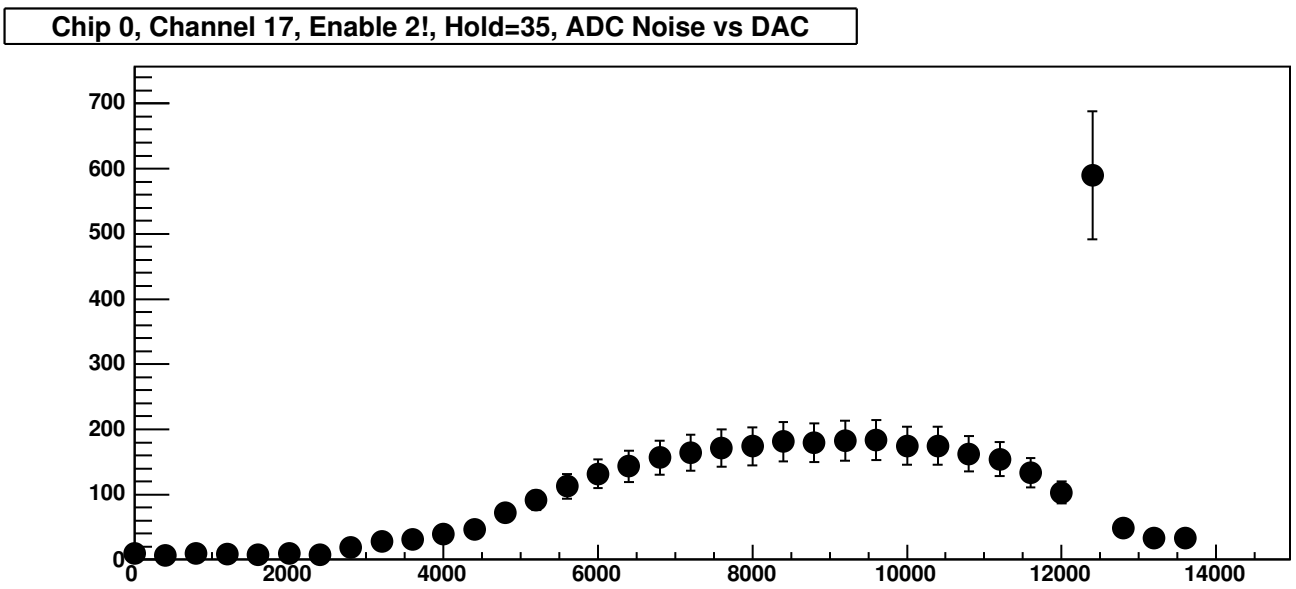

Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

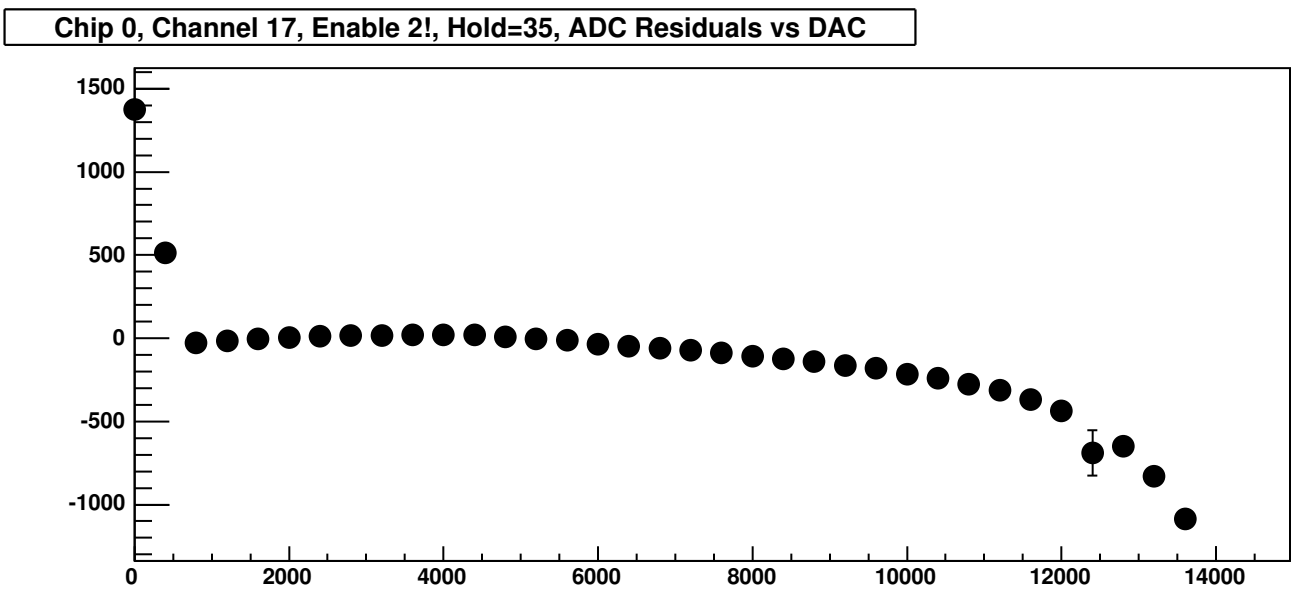

Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 58.38 / 23
p0 17.89 ± 0.6784
p1 0.02329 ± 0.0001113
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

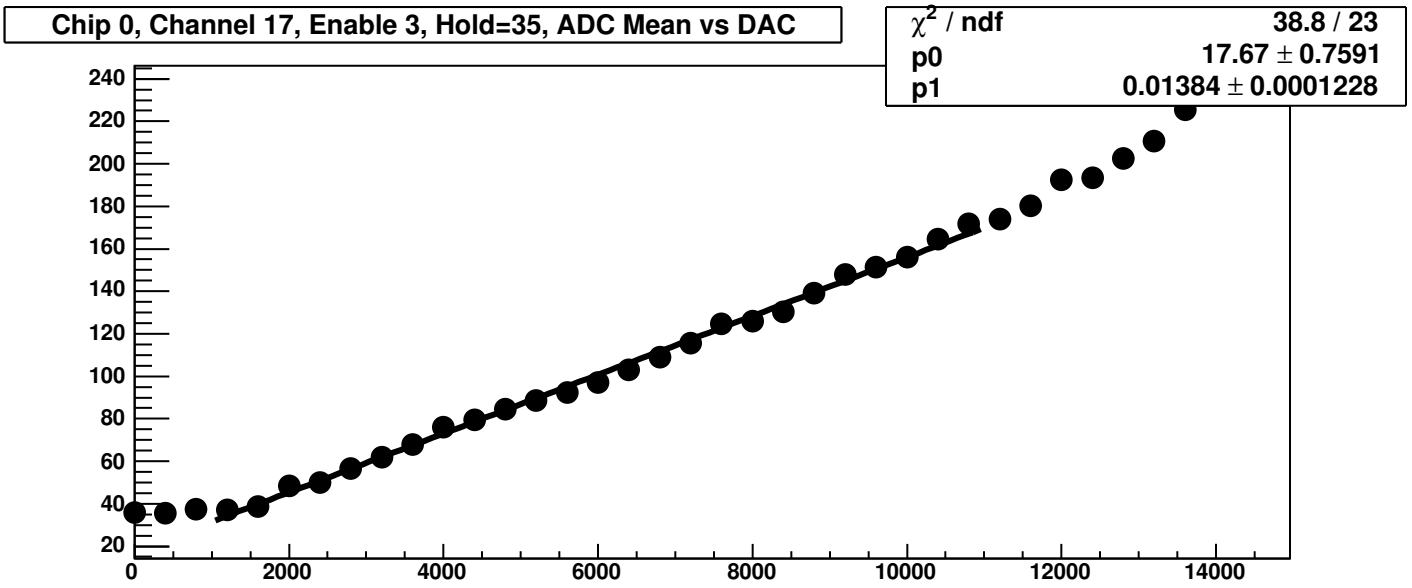

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

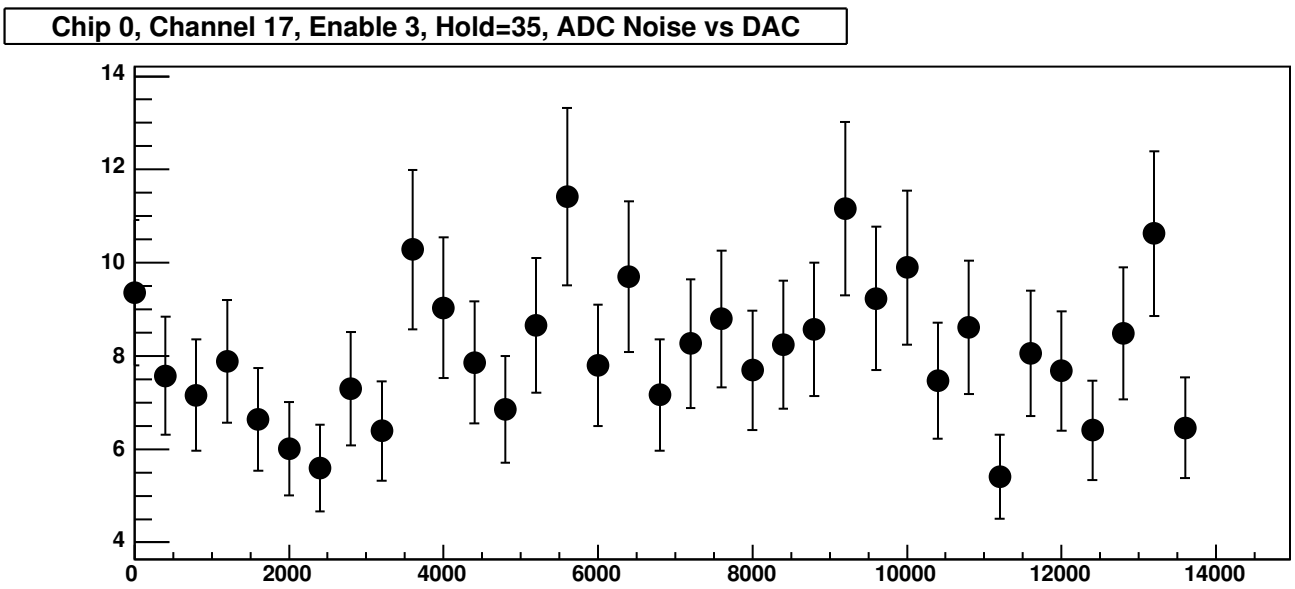

Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

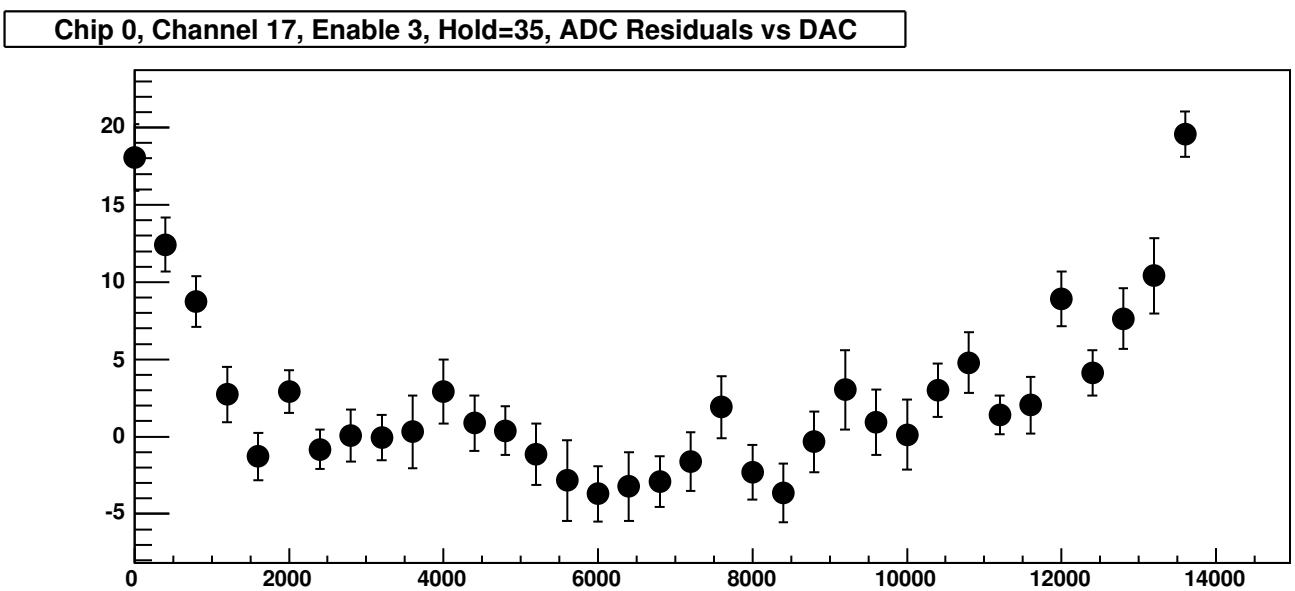

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 357.1 / 23
p0 72.51 ± 2.252
p1 2.199 ± 0.001033
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

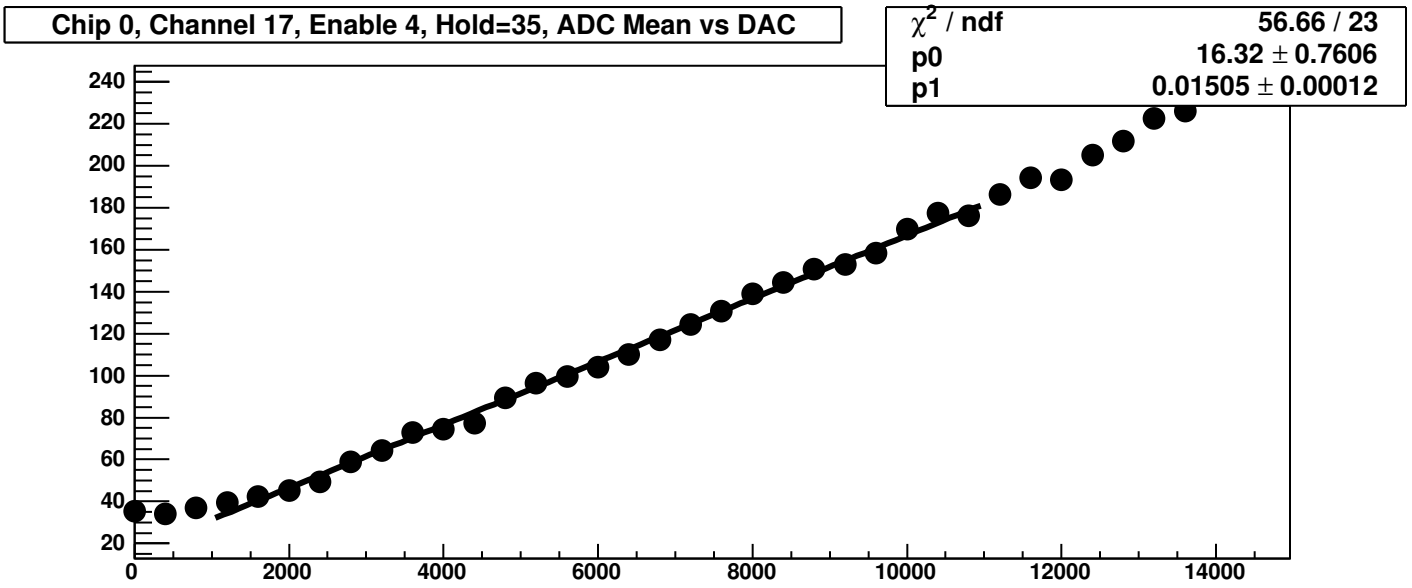

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

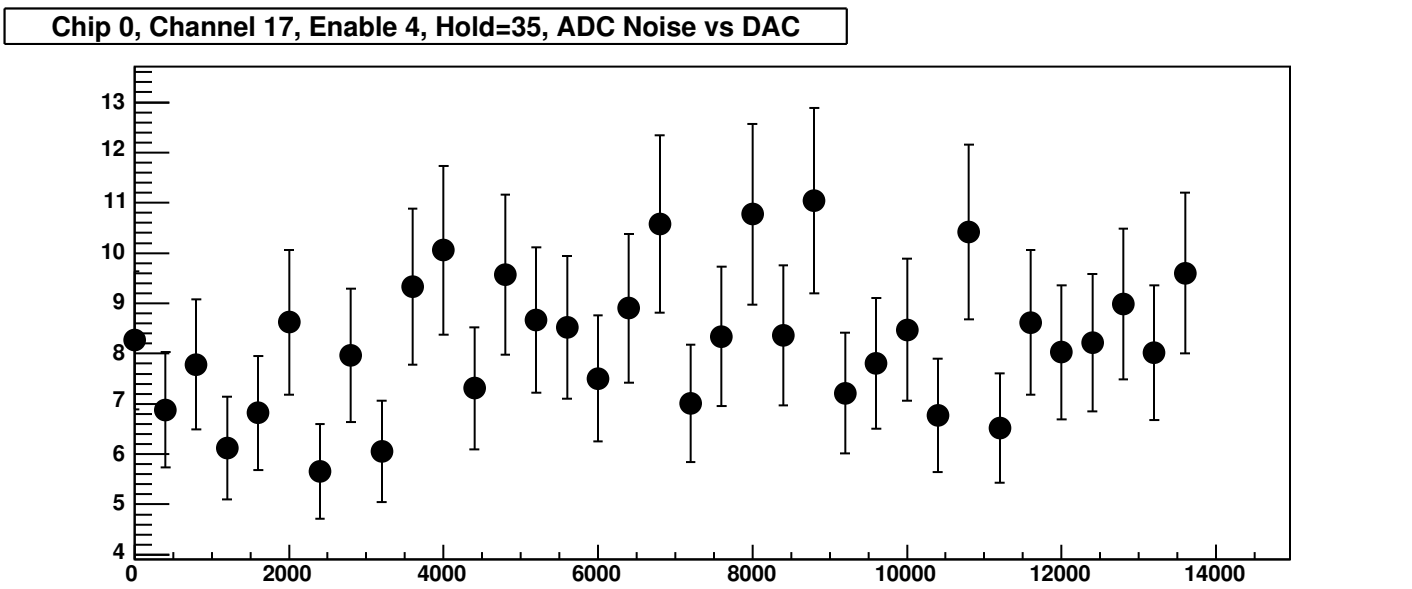

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

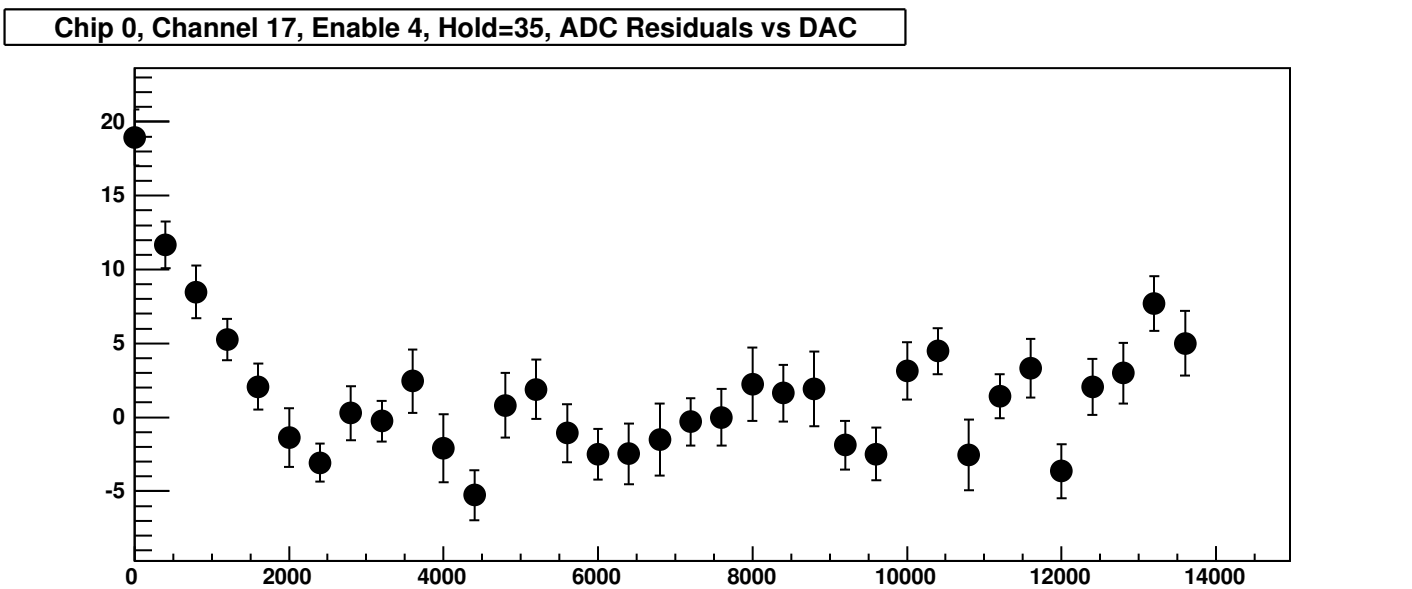

Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.8 / 23
p0 17.67 ± 0.7591
p1 0.01384 ± 0.0001228
240
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

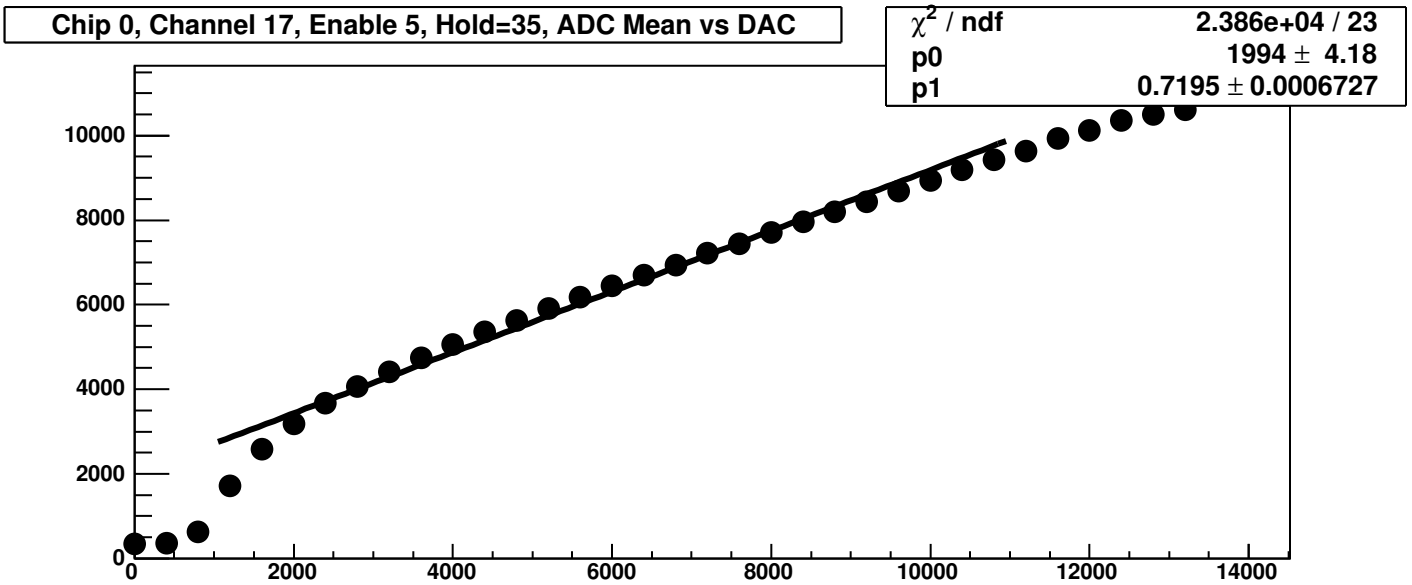

Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

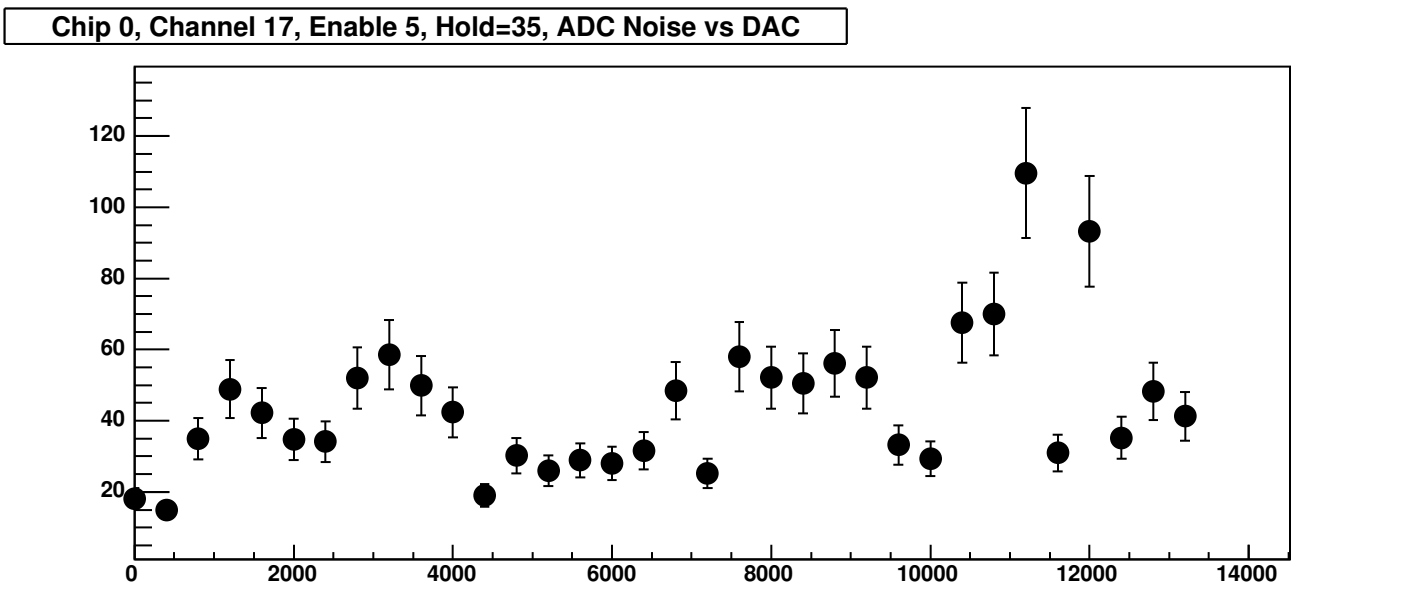

Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

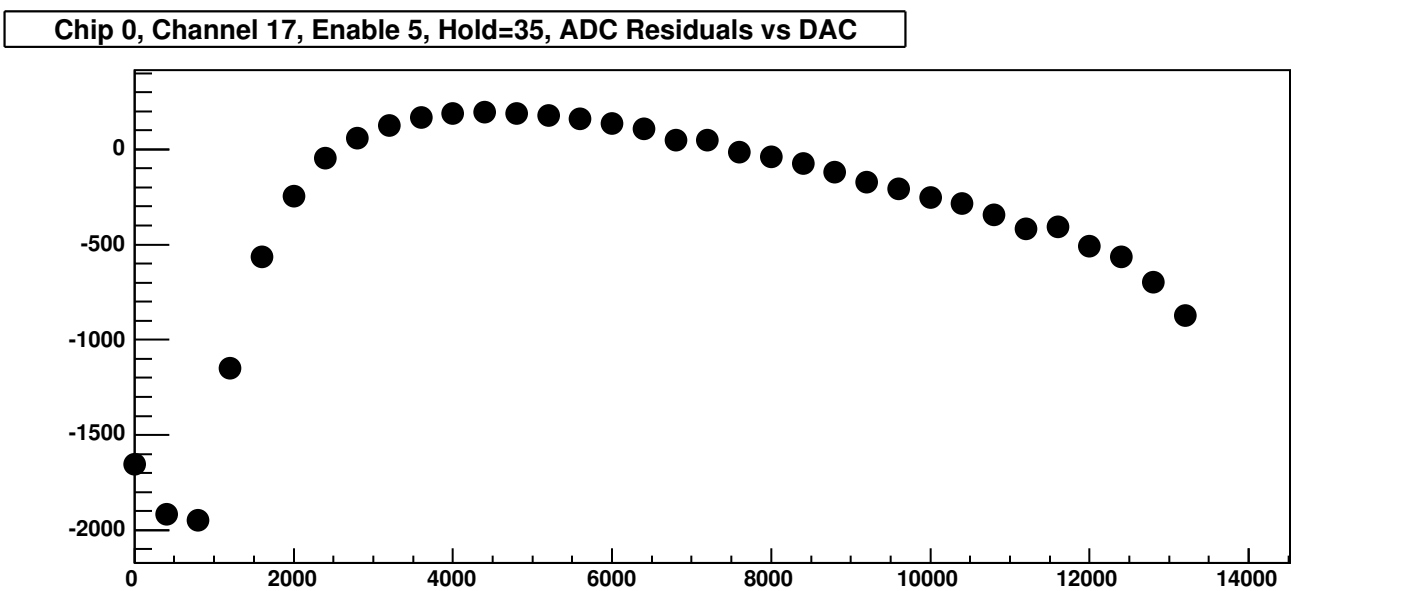

Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 56.66 / 23
p0 16.32 ± 0.7606
p1 0.01505 ± 0.00012
240
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

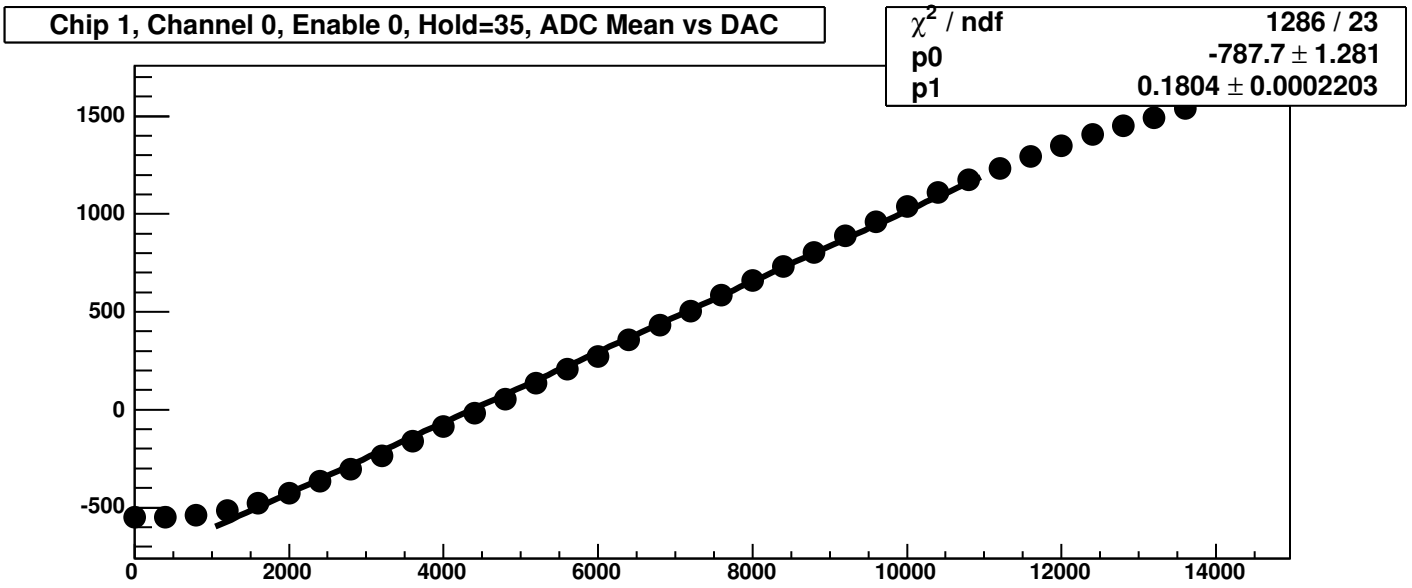

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

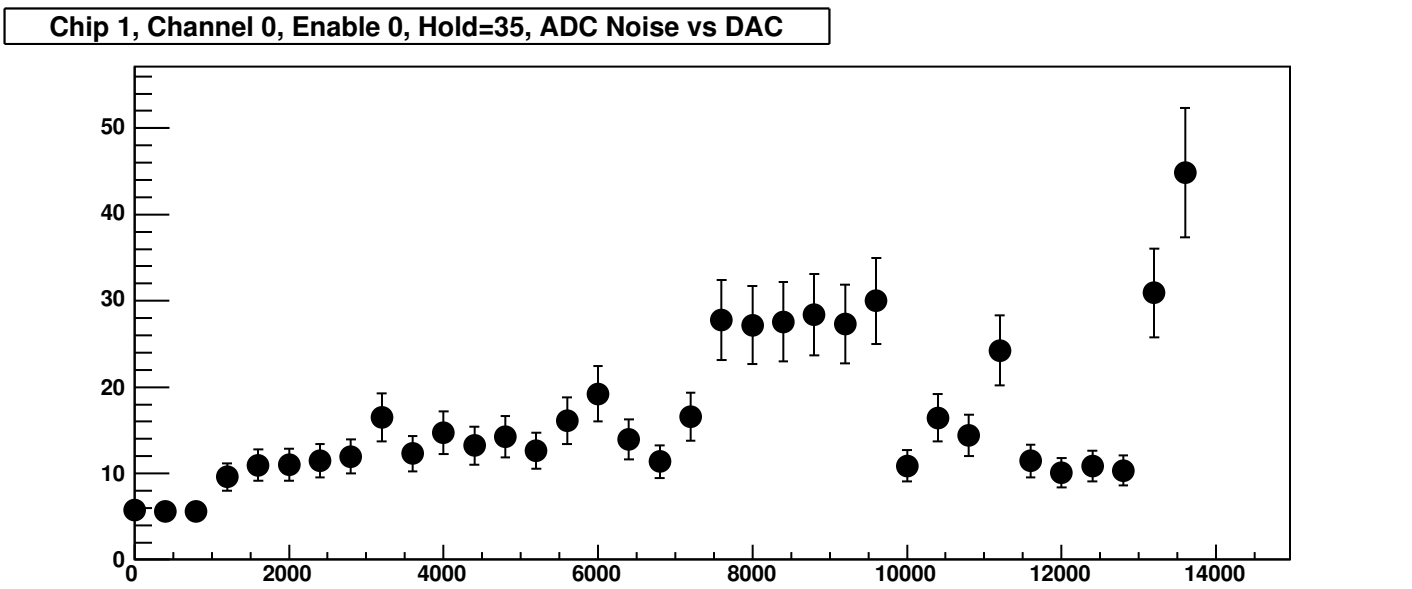

Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

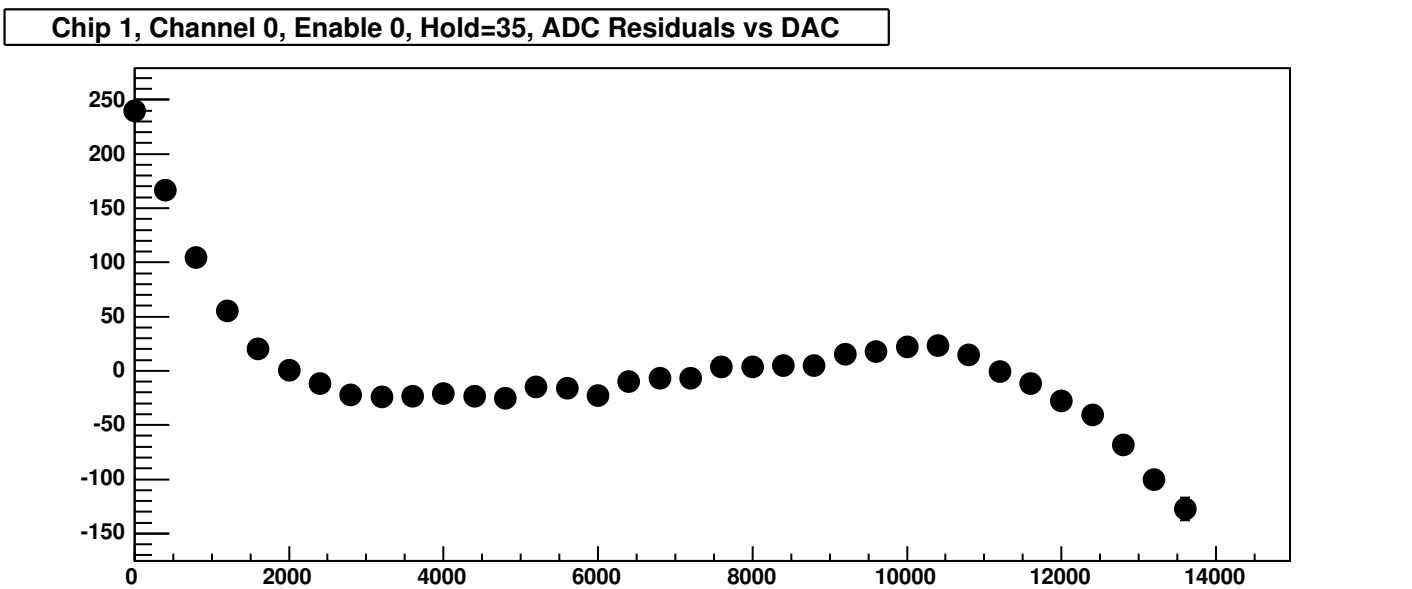

Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.386e+04 / 23
p0 1994 ± 4.18
p1 0.7195 ± 0.0006727
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

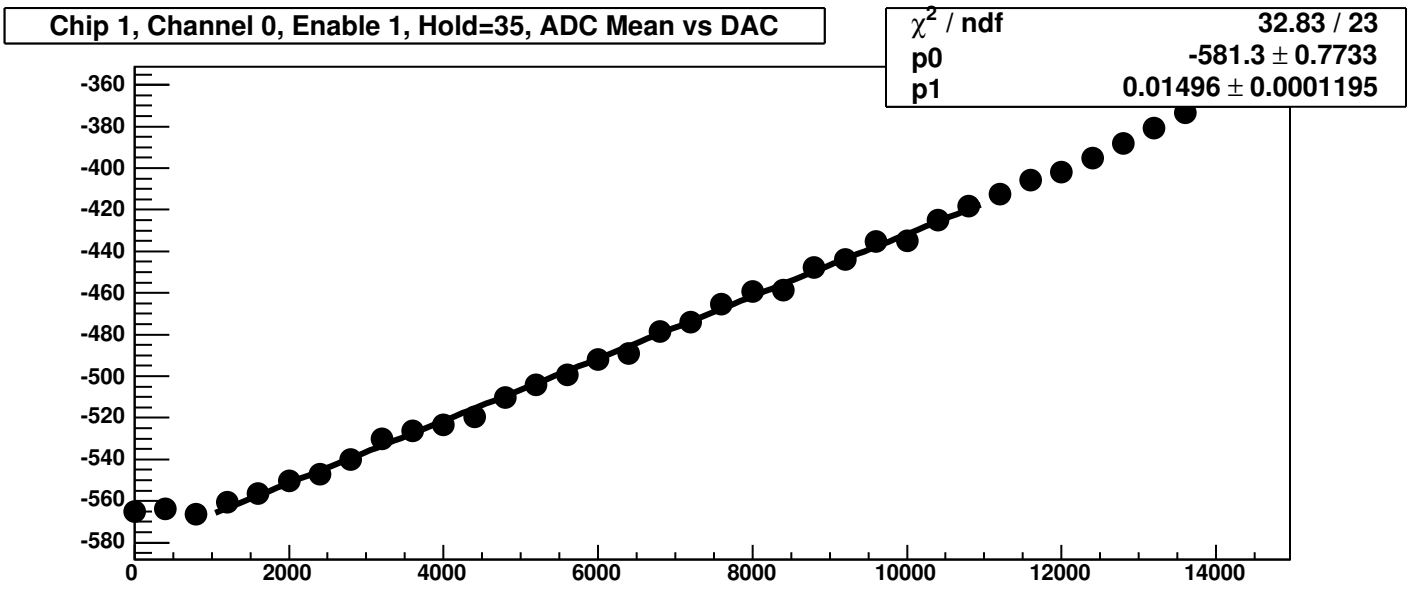

Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

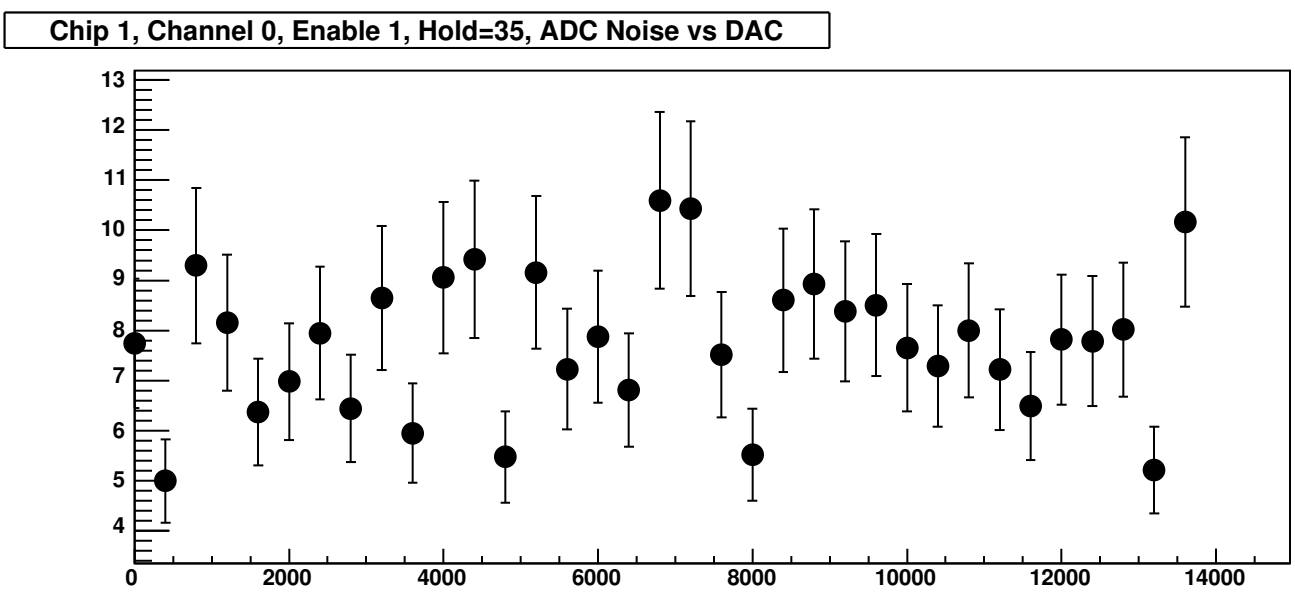

Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

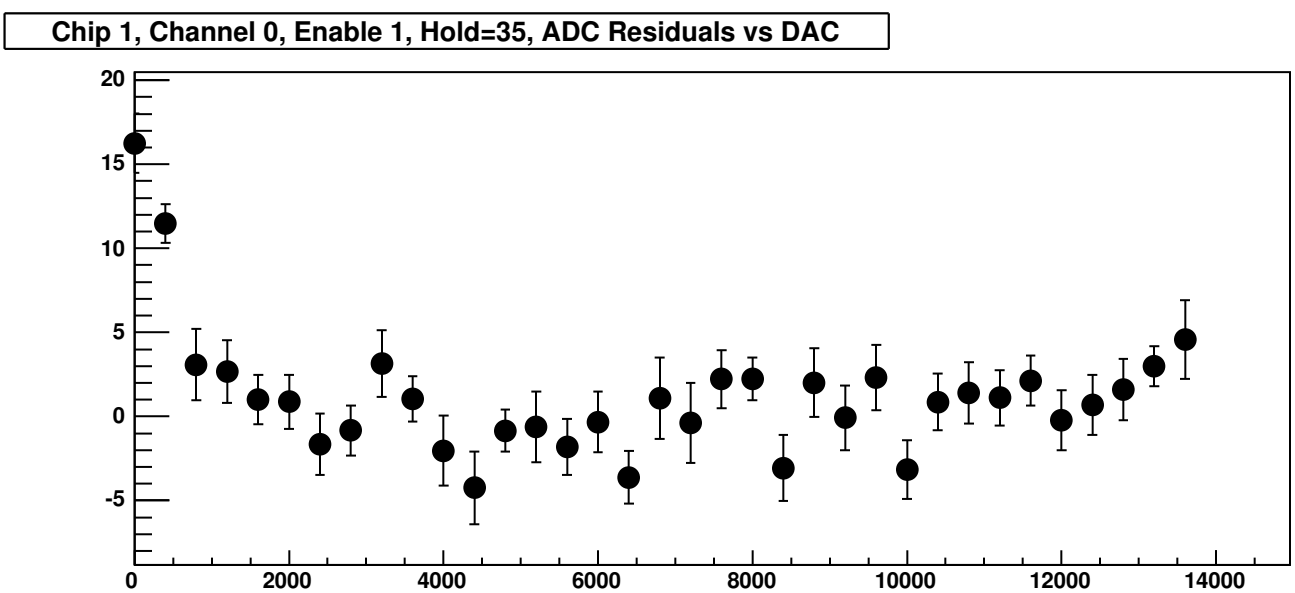

Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1286 / 23
p0 -787.7 ± 1.281
p1 0.1804 ± 0.0002203
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

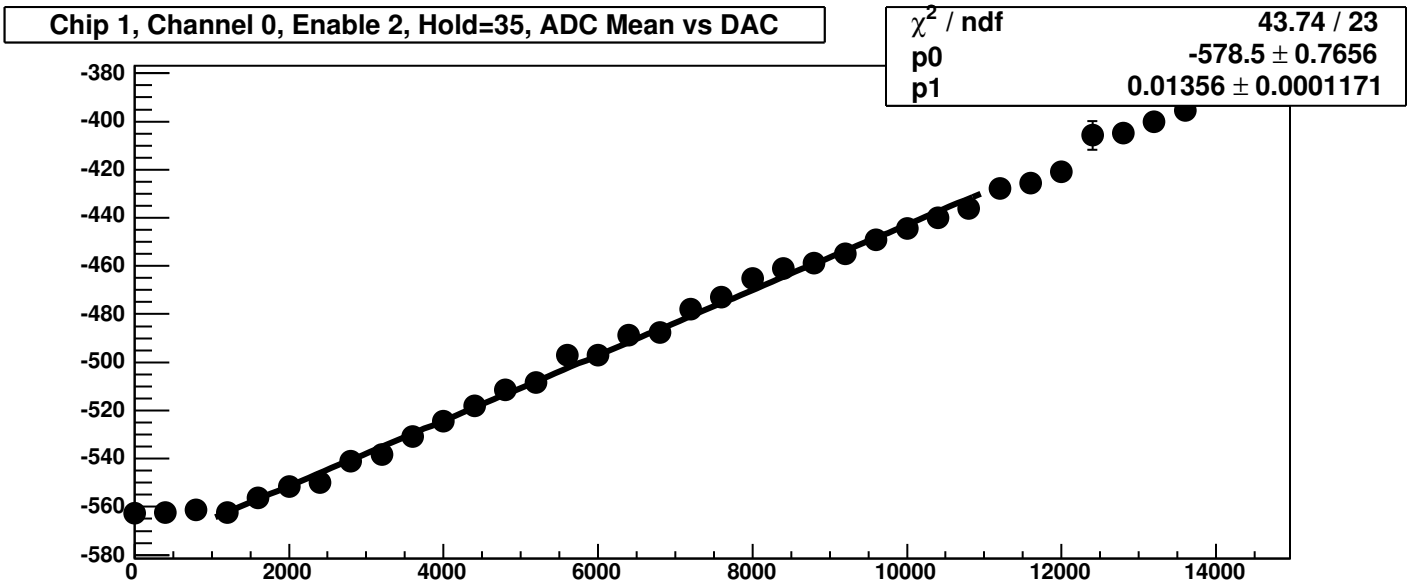

Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

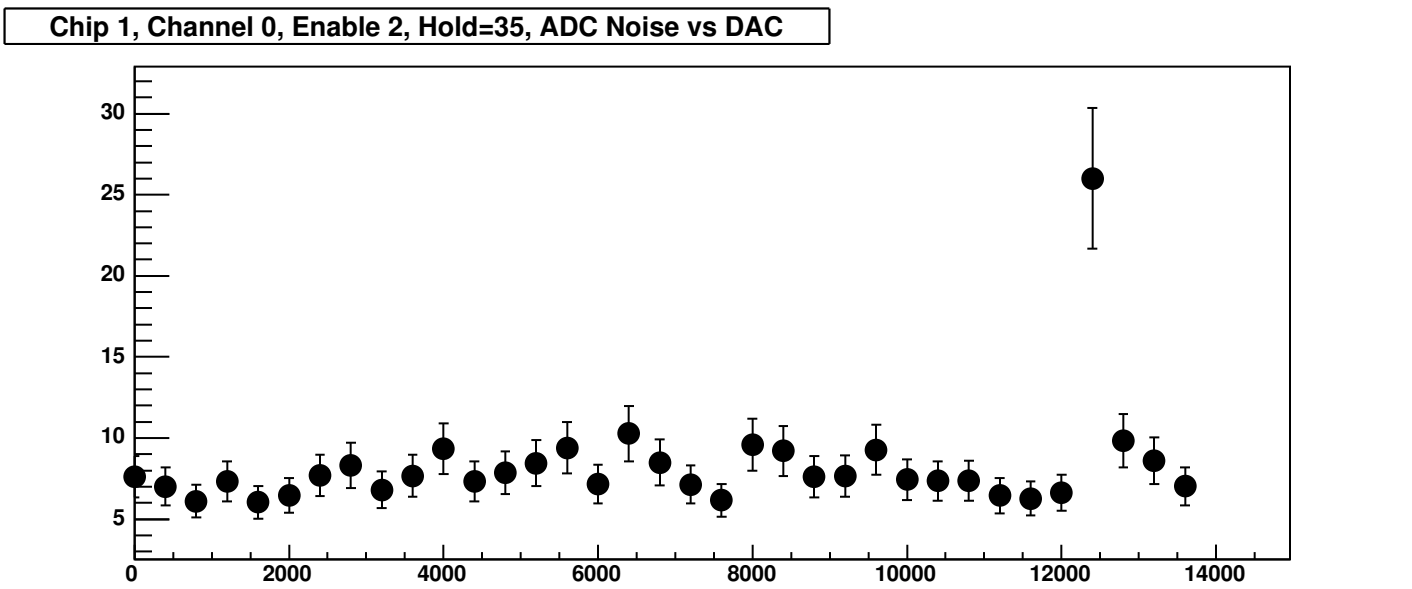

Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

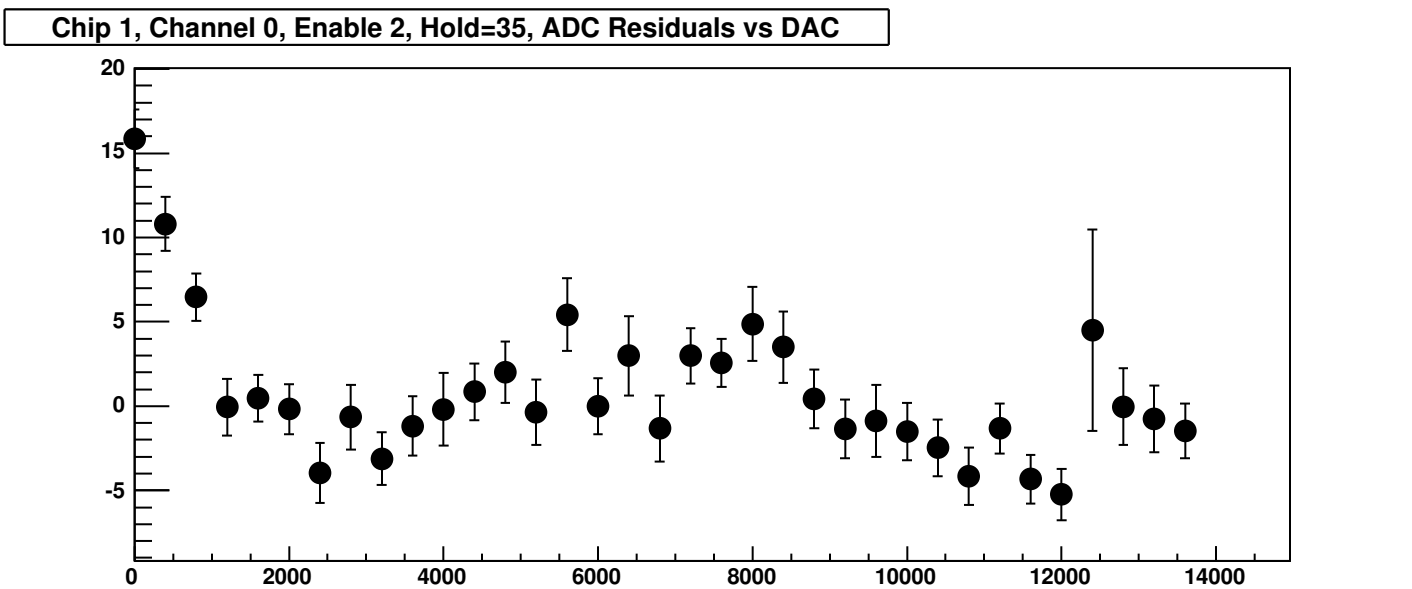

Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.83 / 23
p0 -581.3 ± 0.7733
p1 0.01496 ± 0.0001195
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

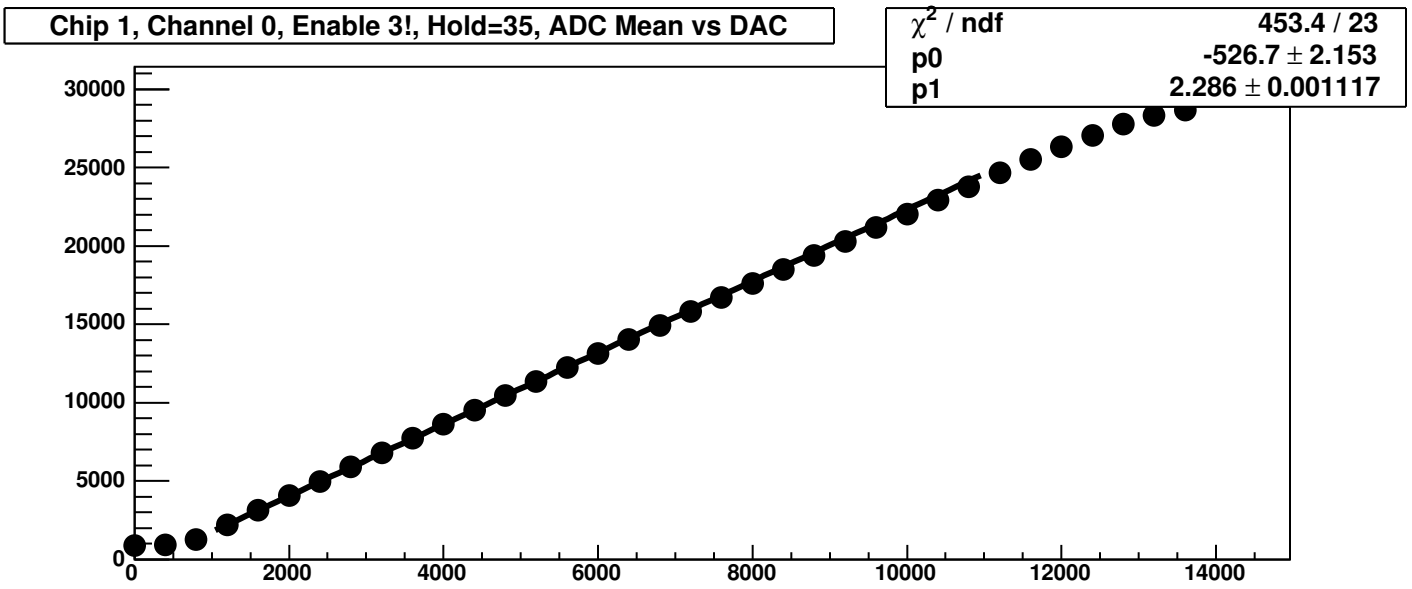

Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

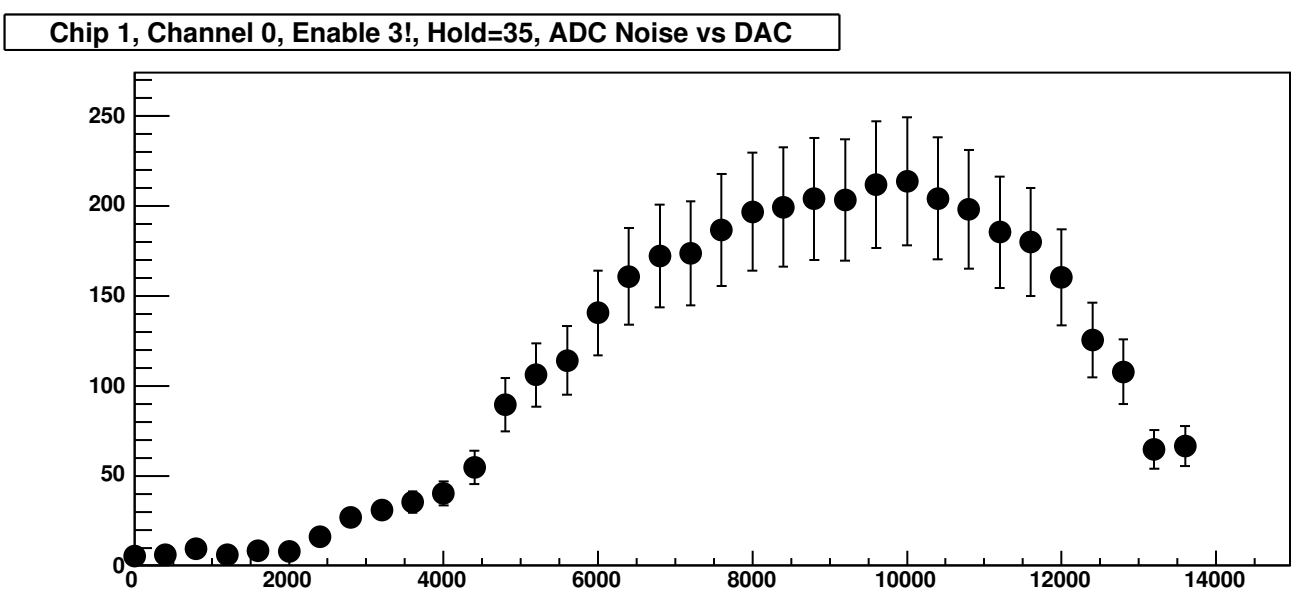

Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

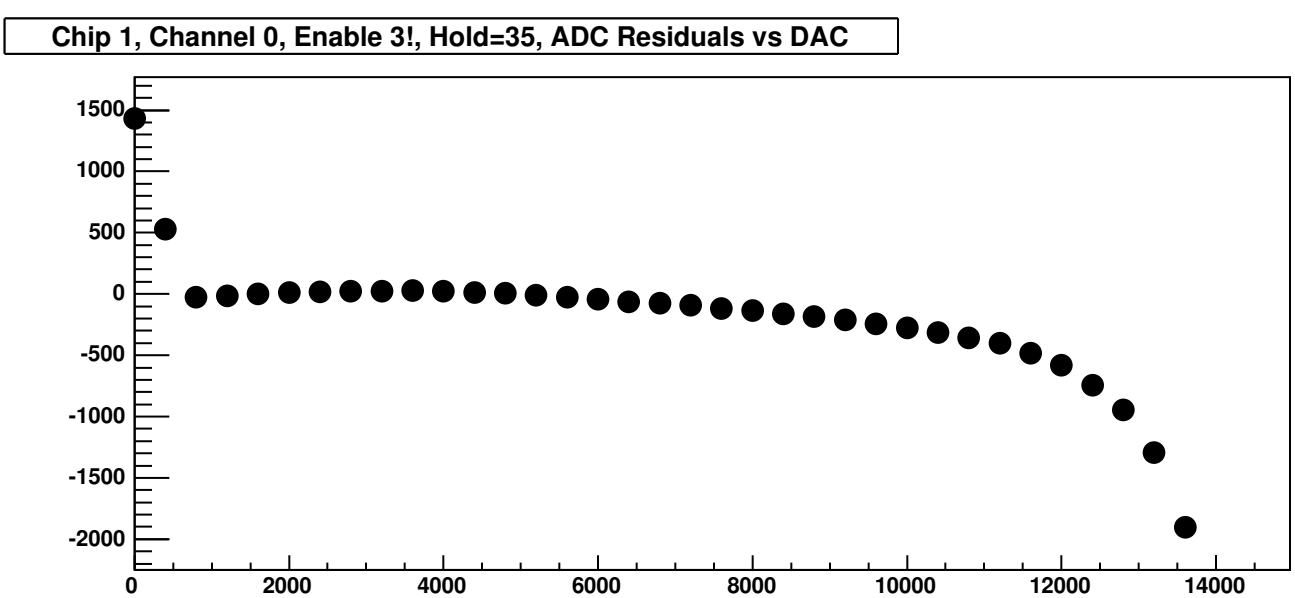

Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.74 / 23
p0 -578.5 ± 0.7656
p1 0.01356 ± 0.0001171
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

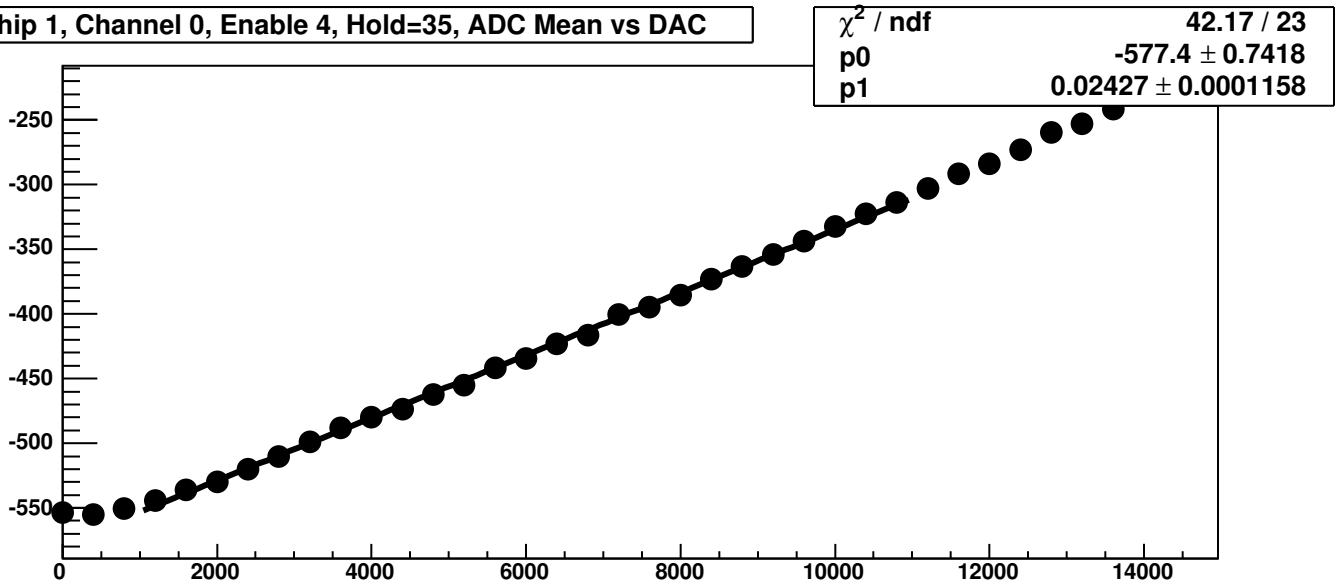

Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 453.4 / 23
p0 -526.7 ± 2.153
p1 2.286 ± 0.001117
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

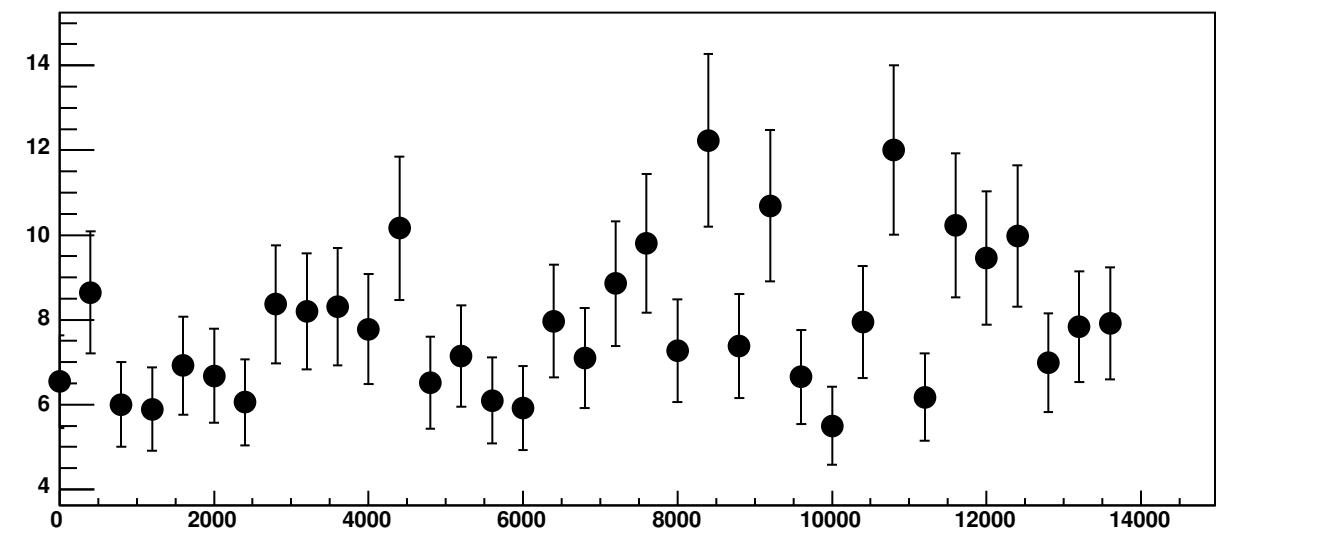

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

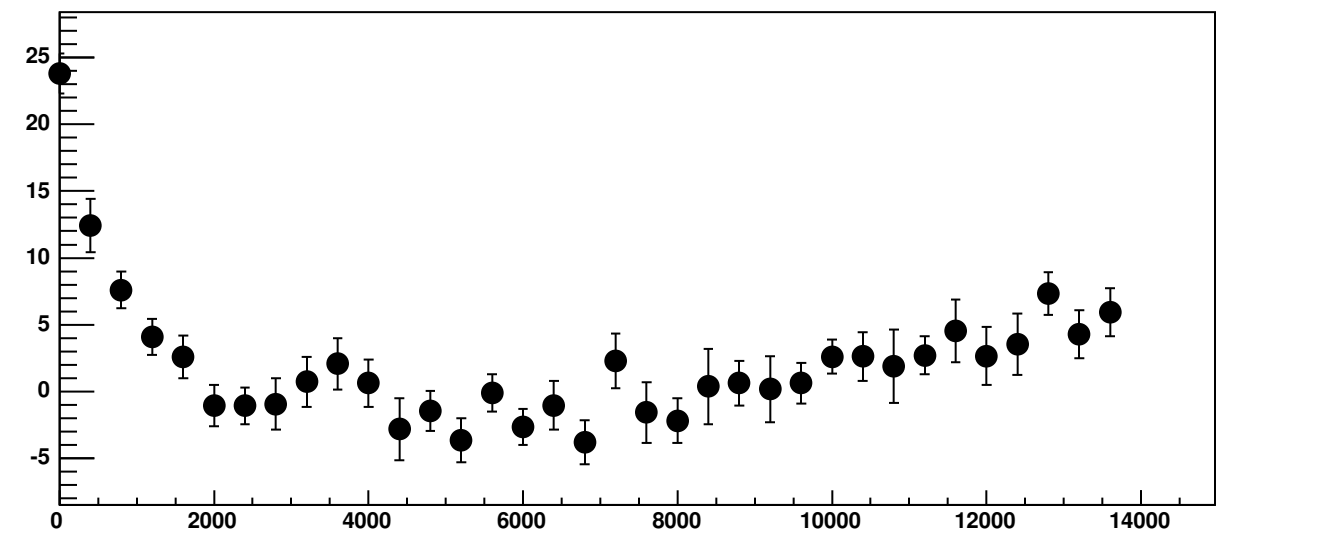
Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



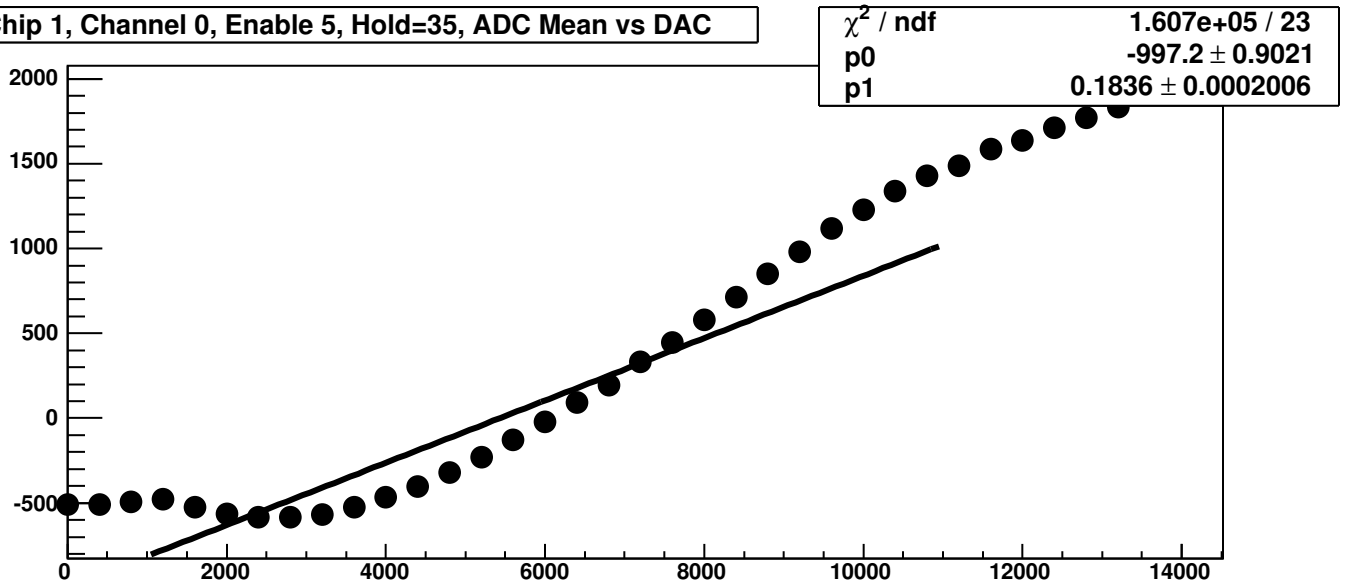
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



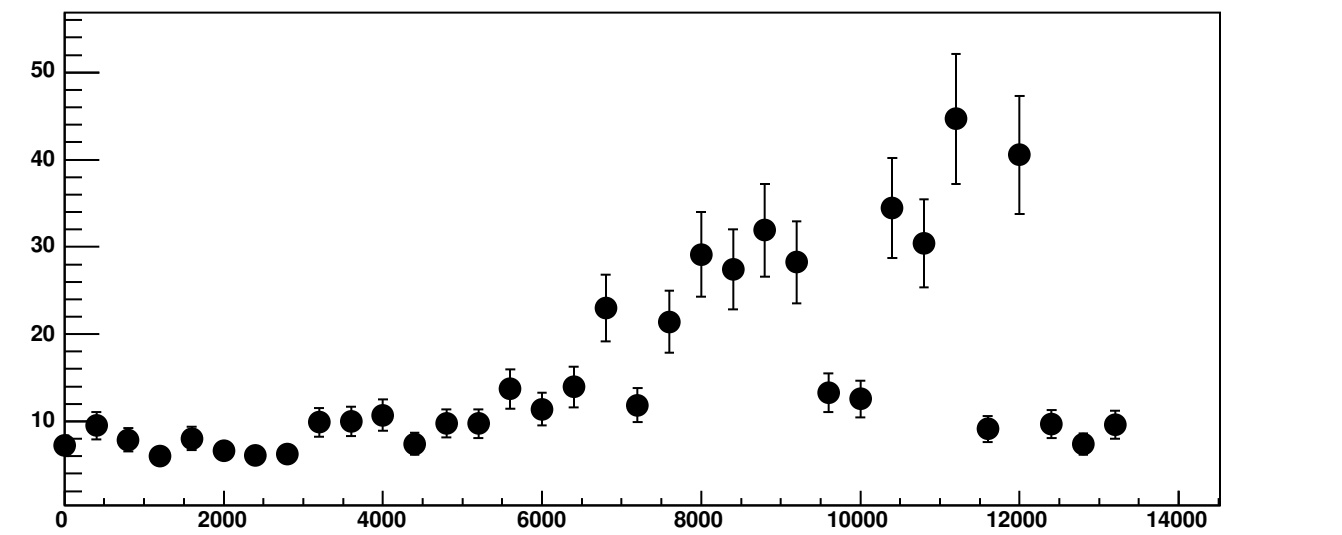
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



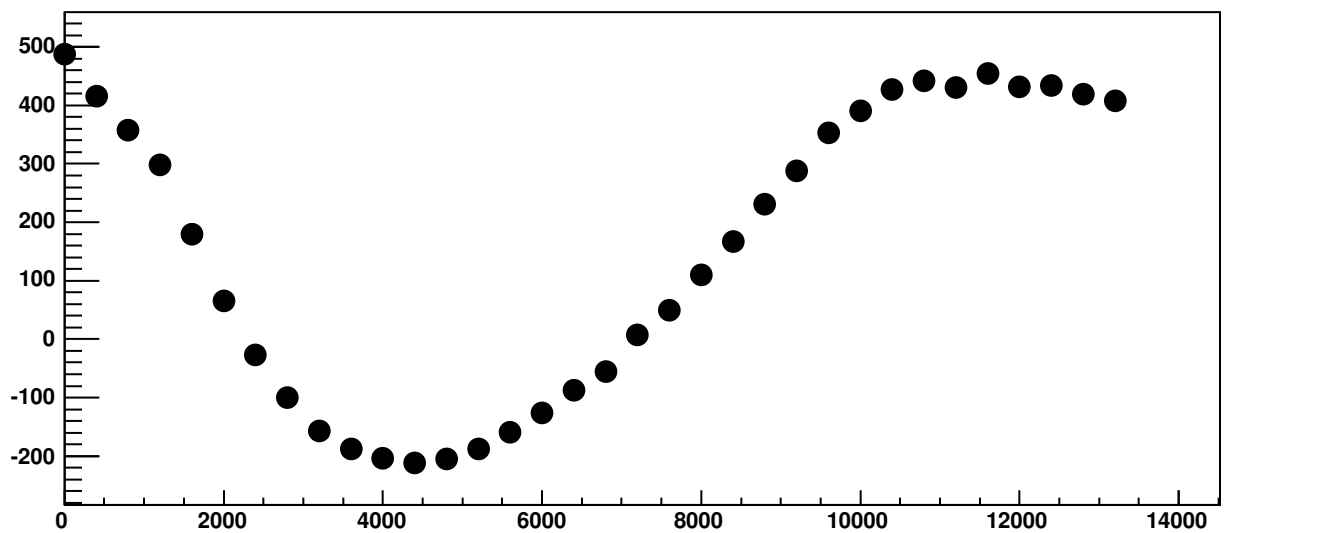
Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

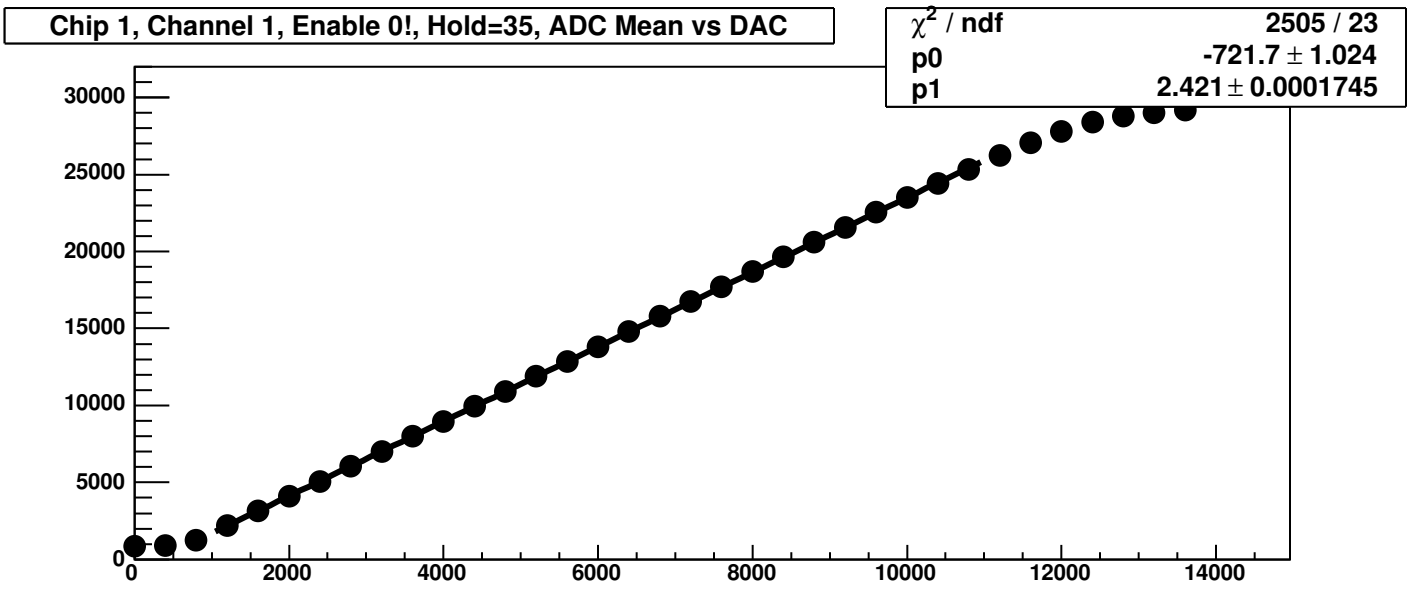


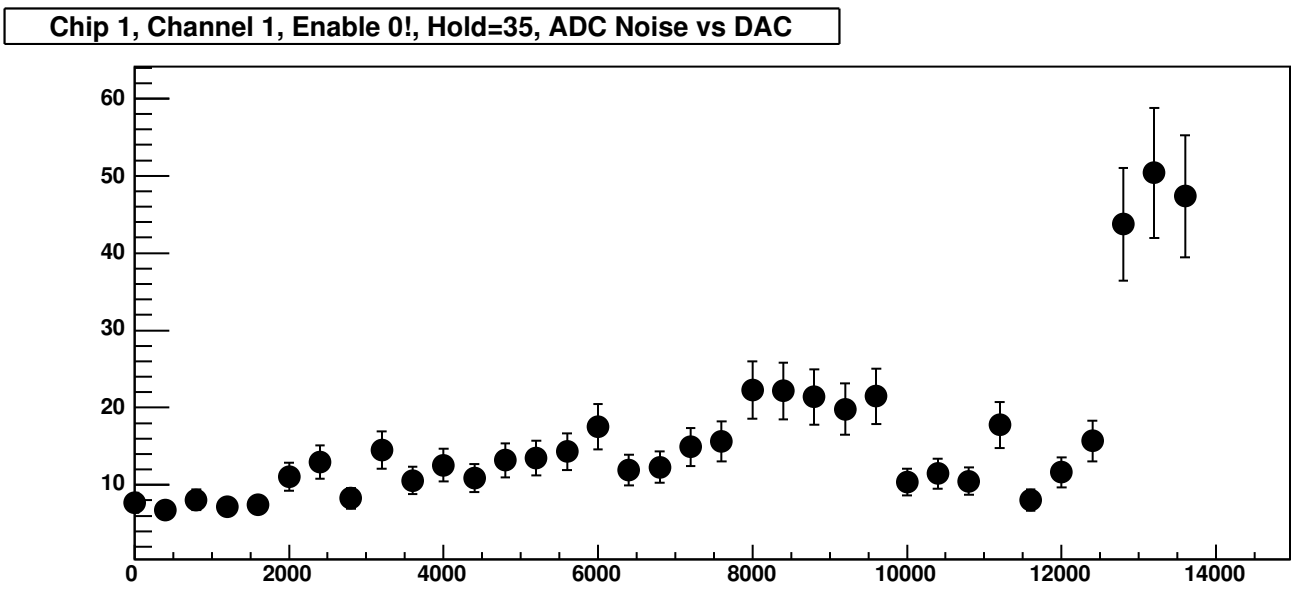
Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

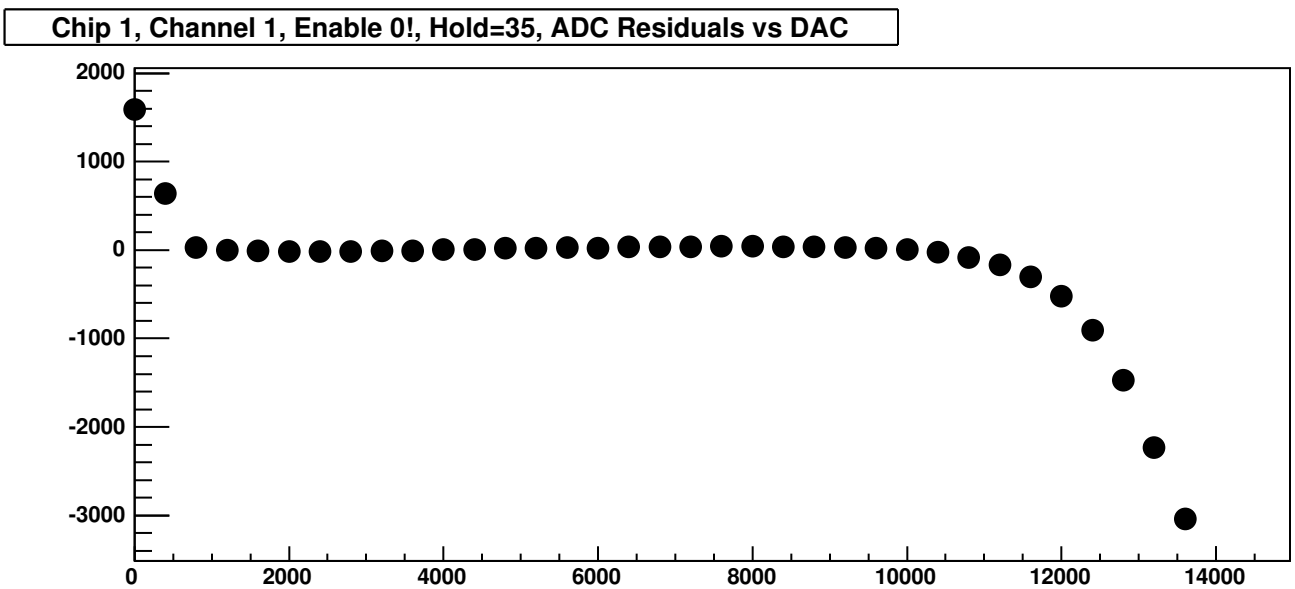


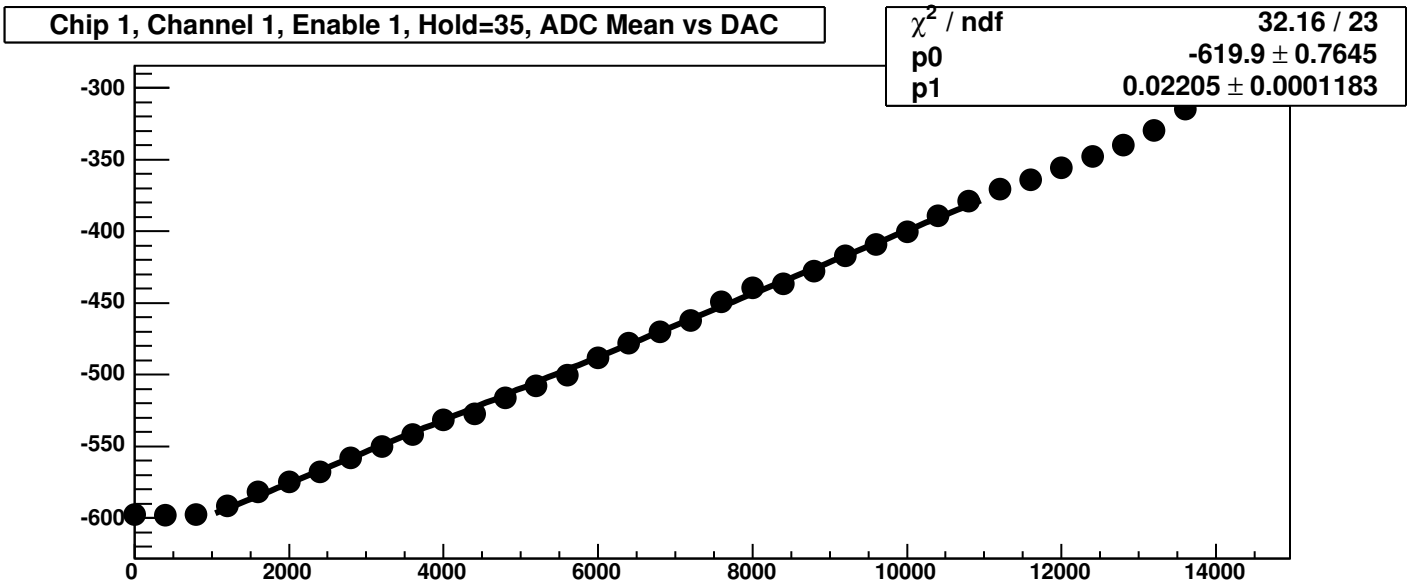
Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

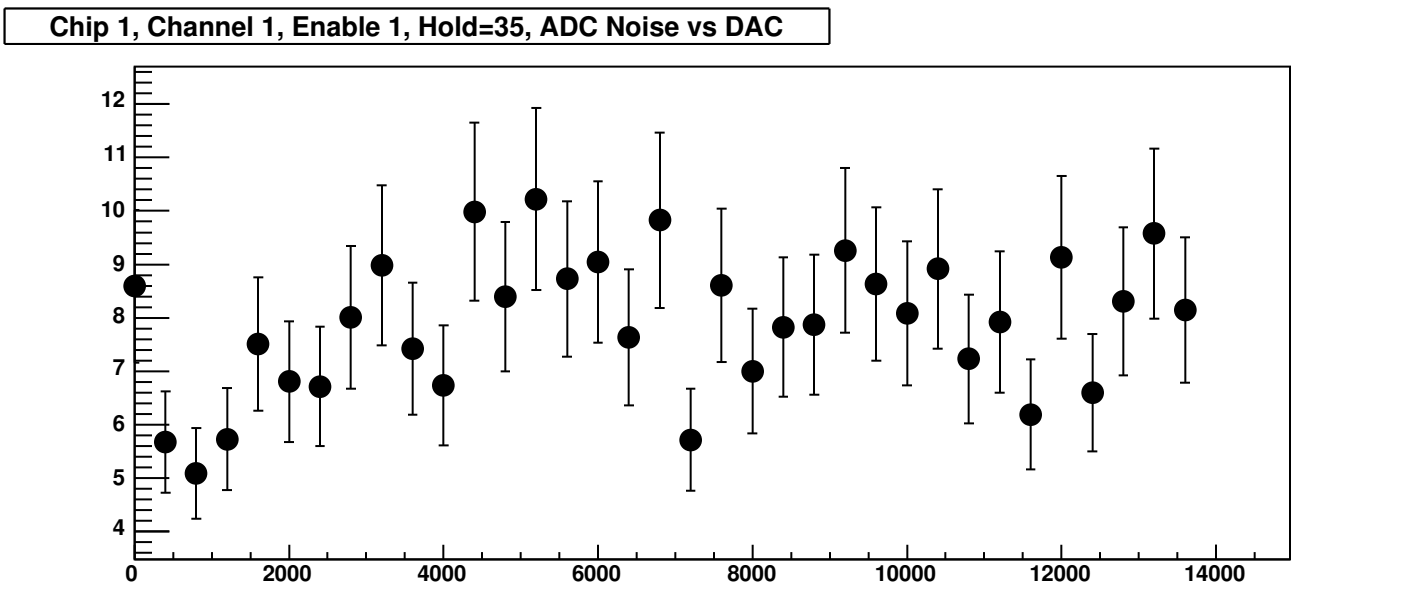


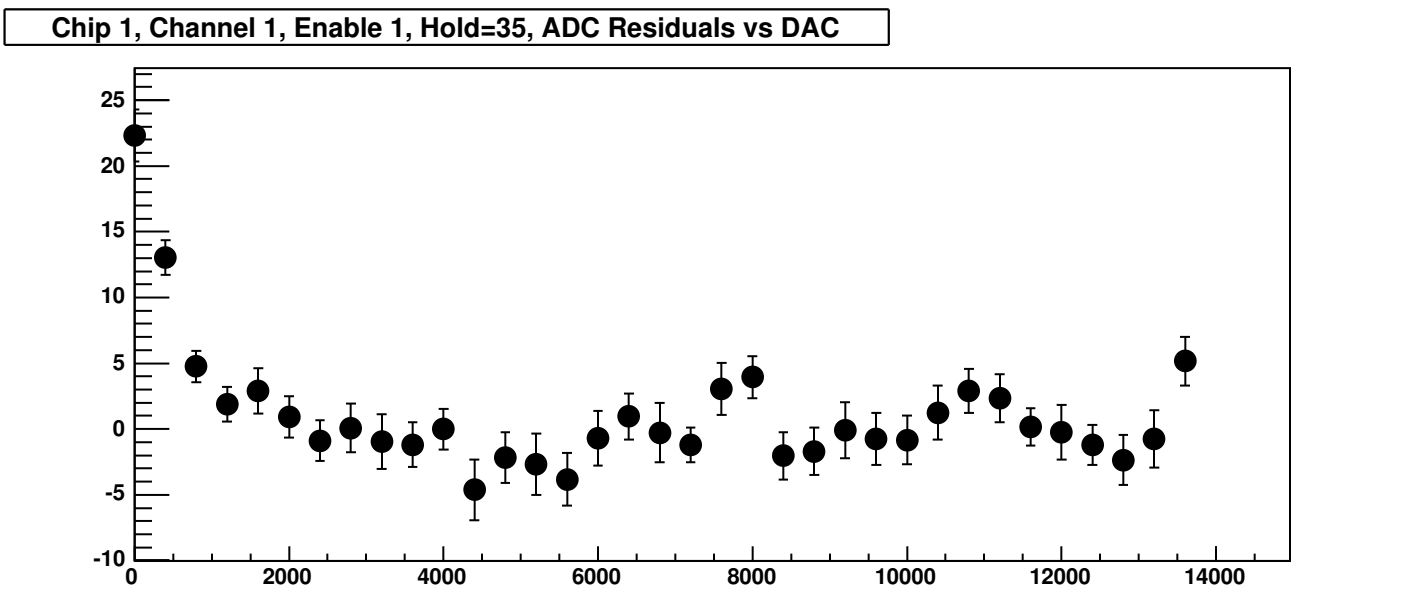

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2505 / 23
p0 -721.7 ± 1.024
p1 2.421 ± 0.0001745
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

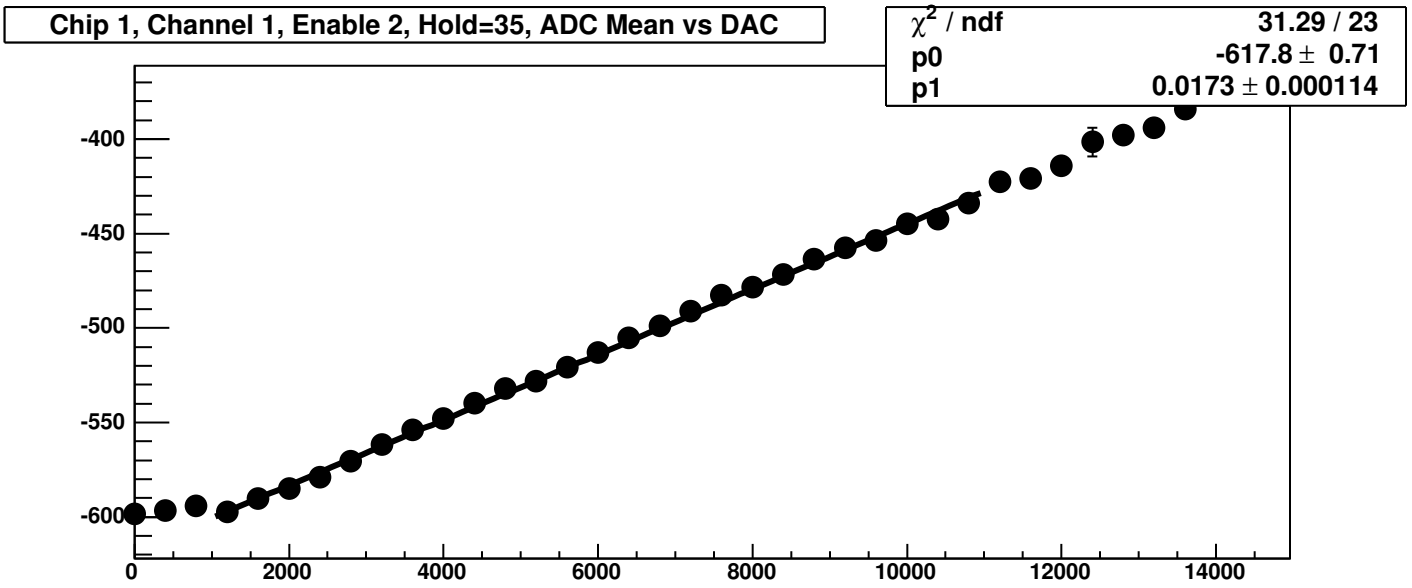

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

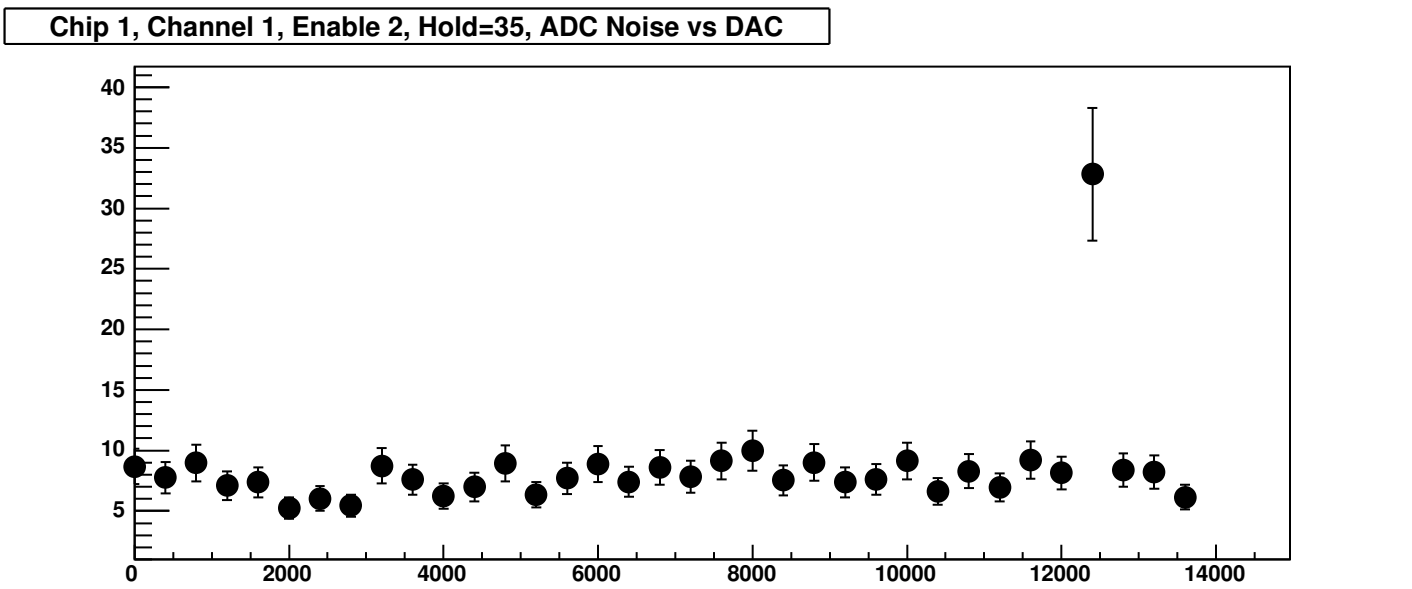

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

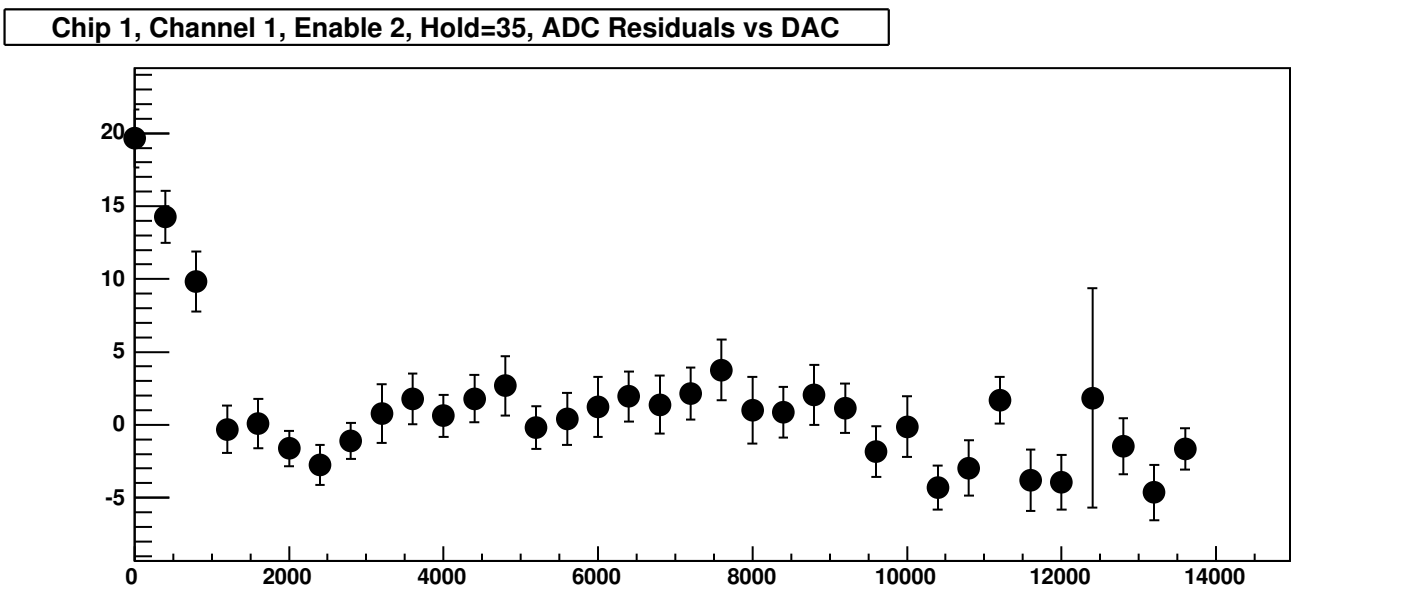

Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.16 / 23
p0 -619.9 ± 0.7645
p1 0.02205 ± 0.0001183
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

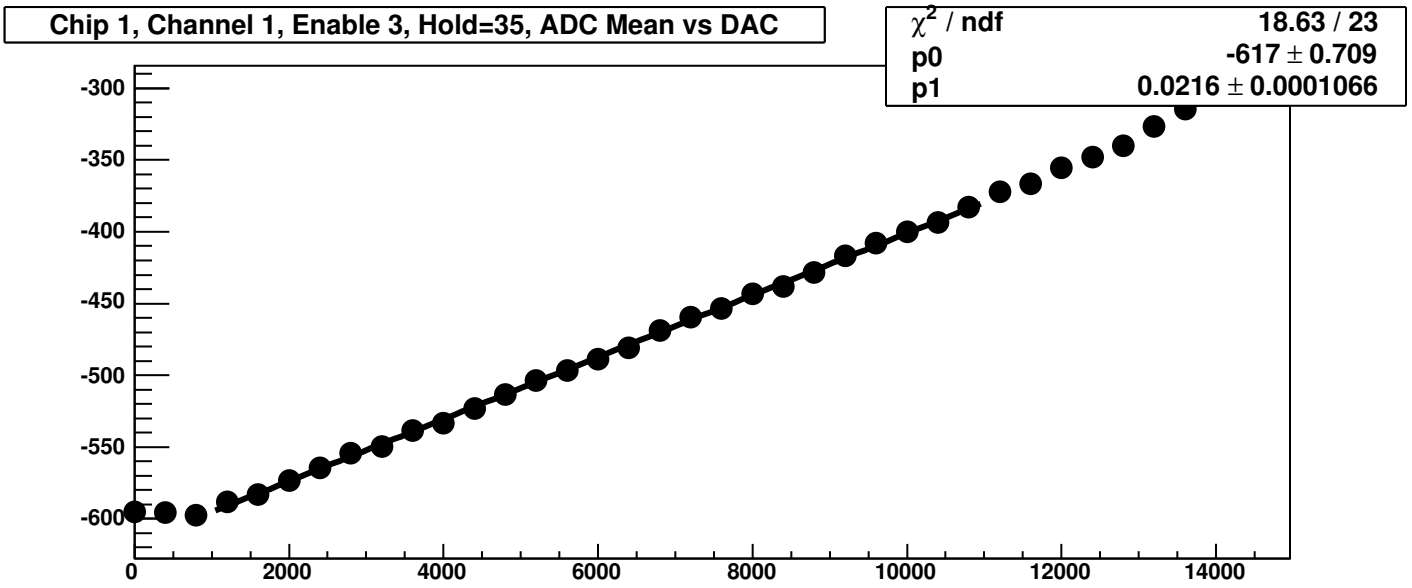

Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

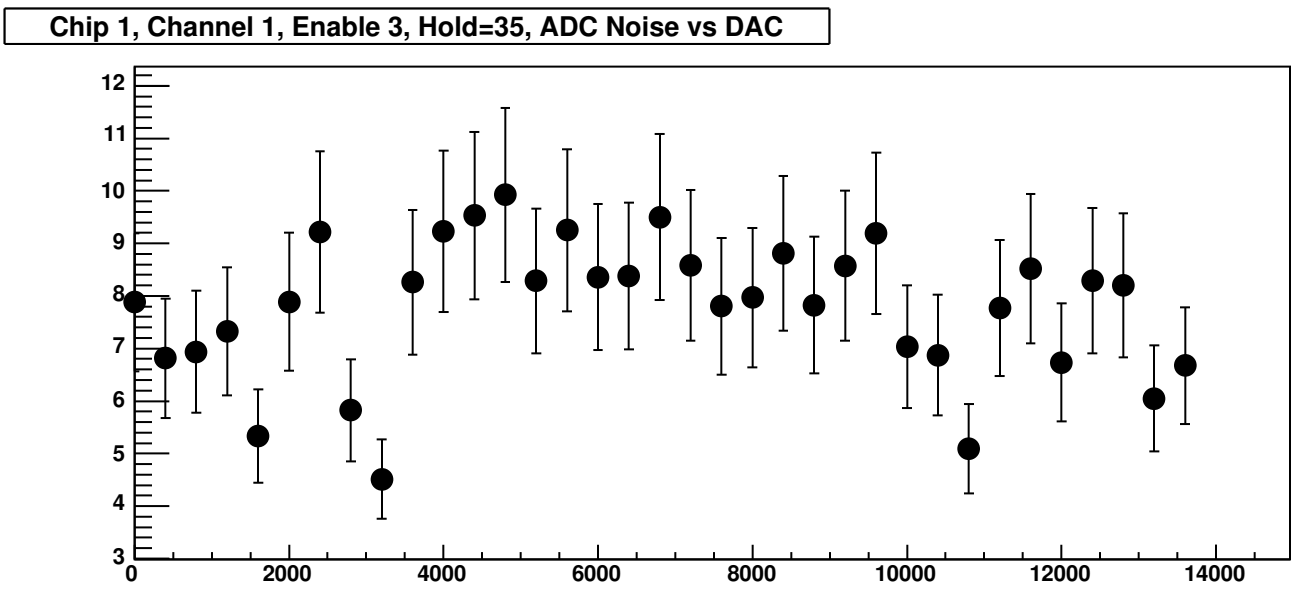

Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

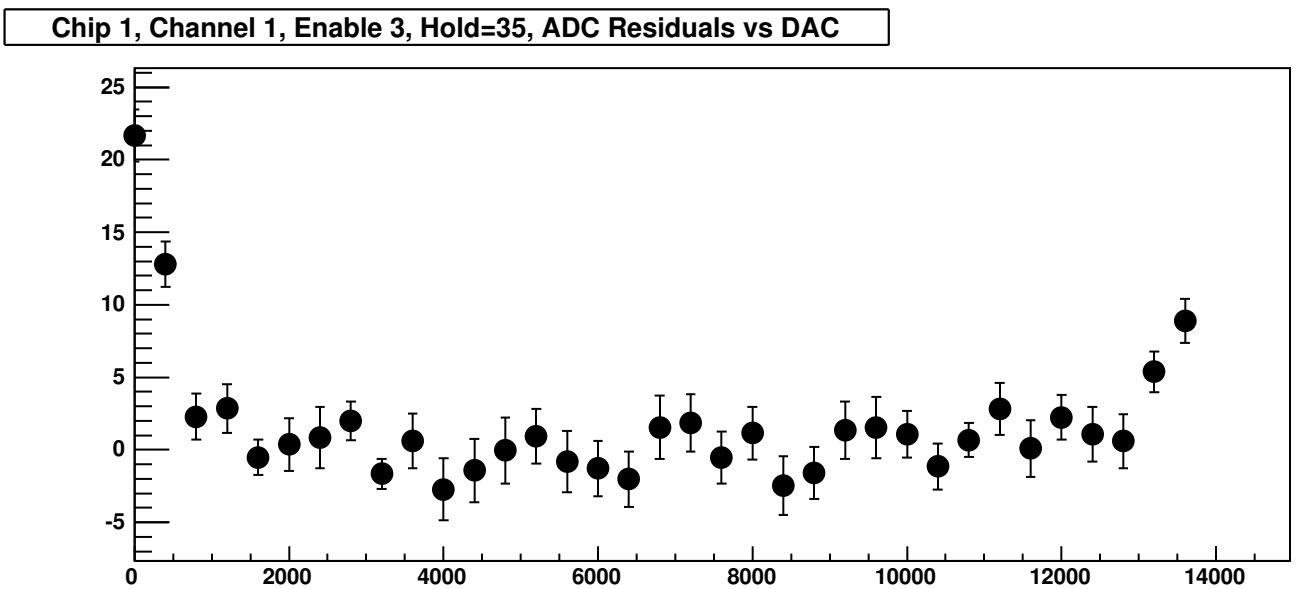

Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.29 / 23
p0 -617.8 ± 0.71
p1 0.0173 ± 0.000114
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

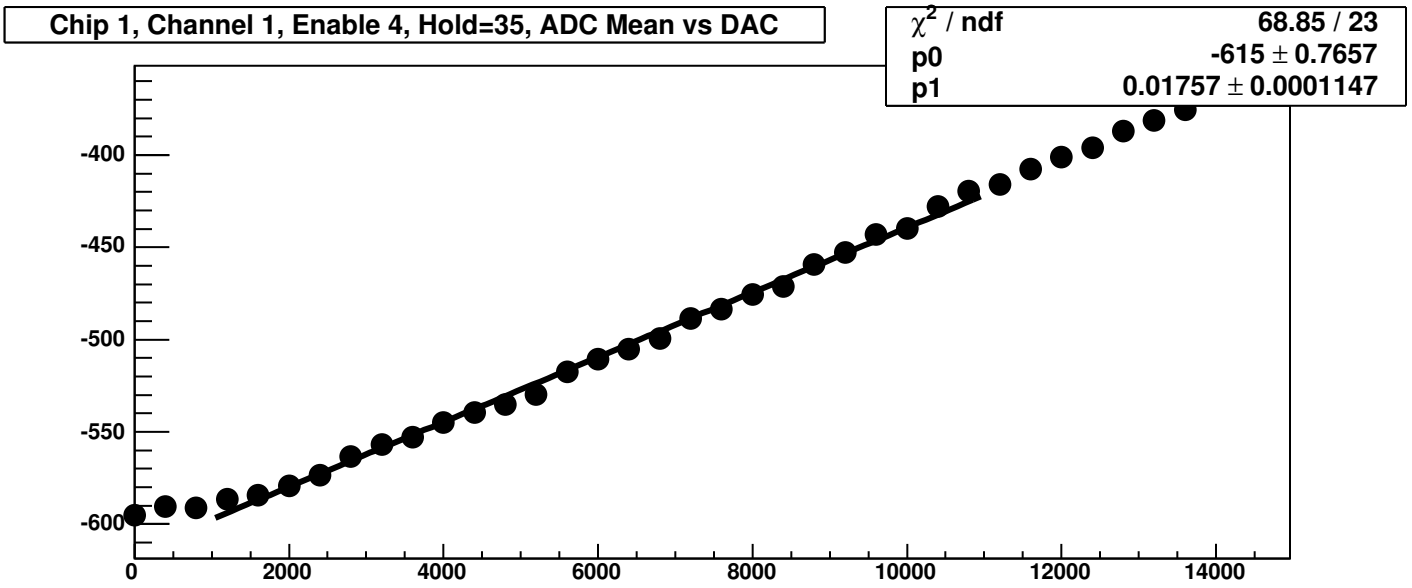

Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

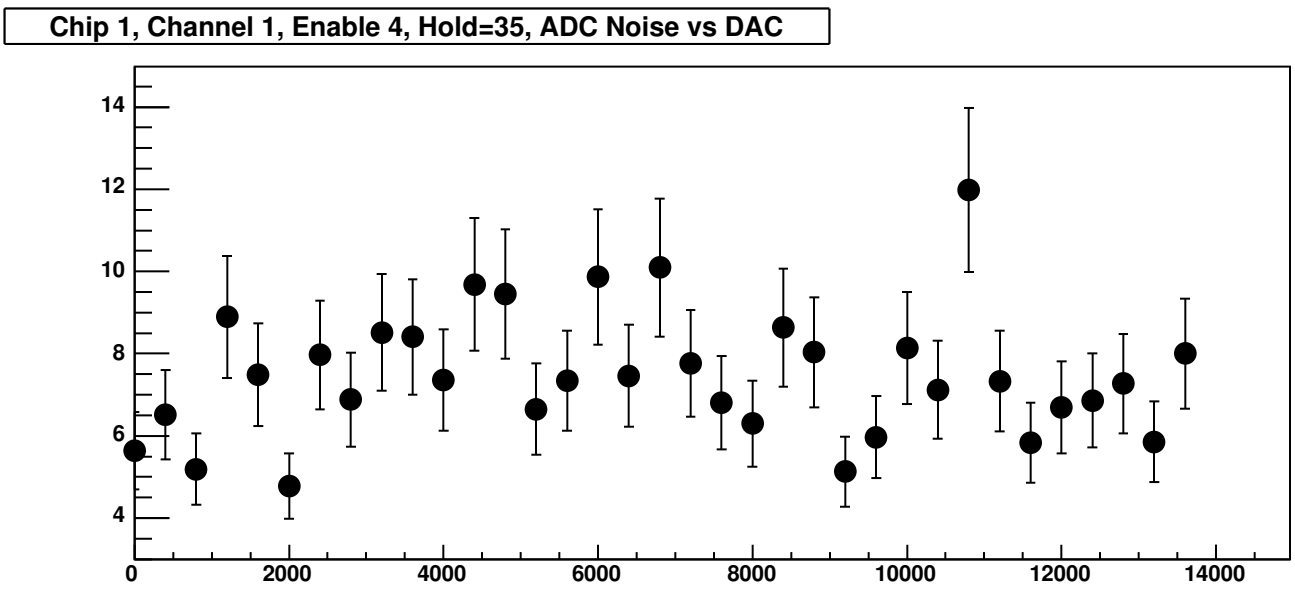

Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

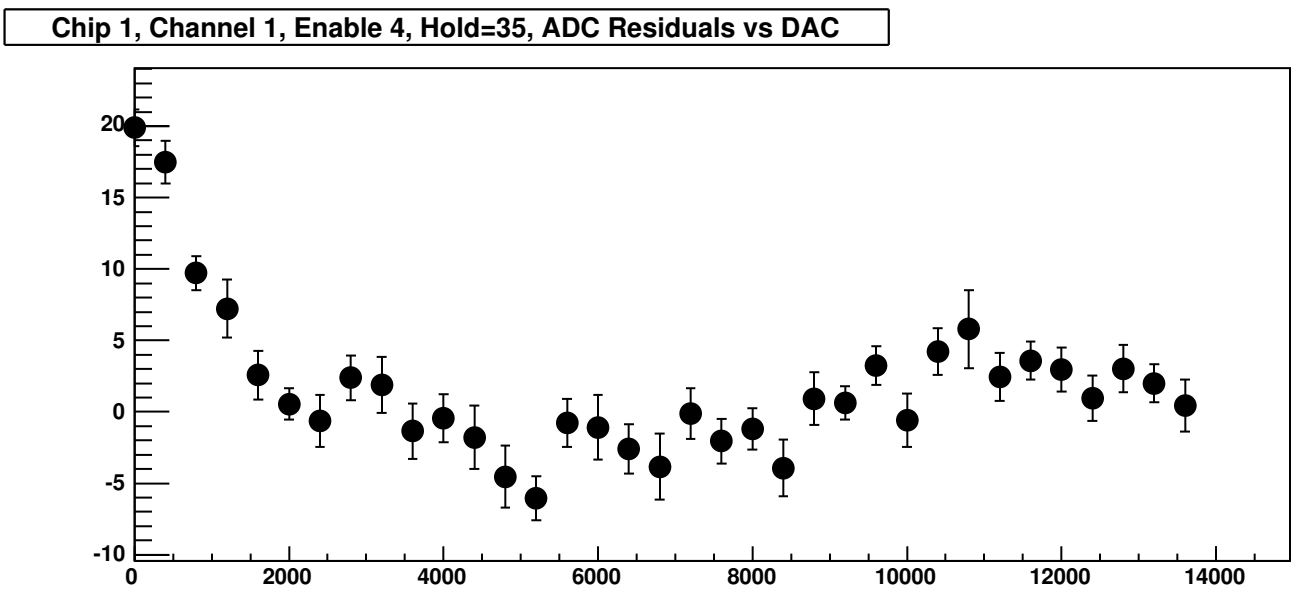

Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.63 / 23
p0 -617 ± 0.709
p1 0.0216 ± 0.0001066
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

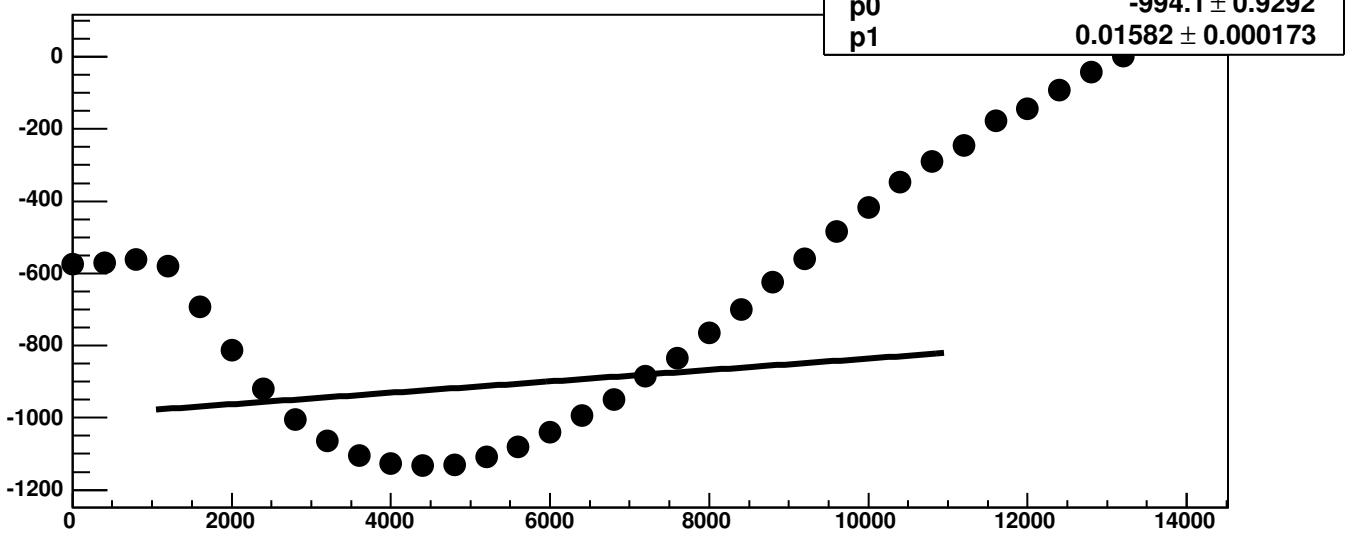

Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 68.85 / 23
p0 -615 ± 0.7657
p1 0.01757 ± 0.0001147
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

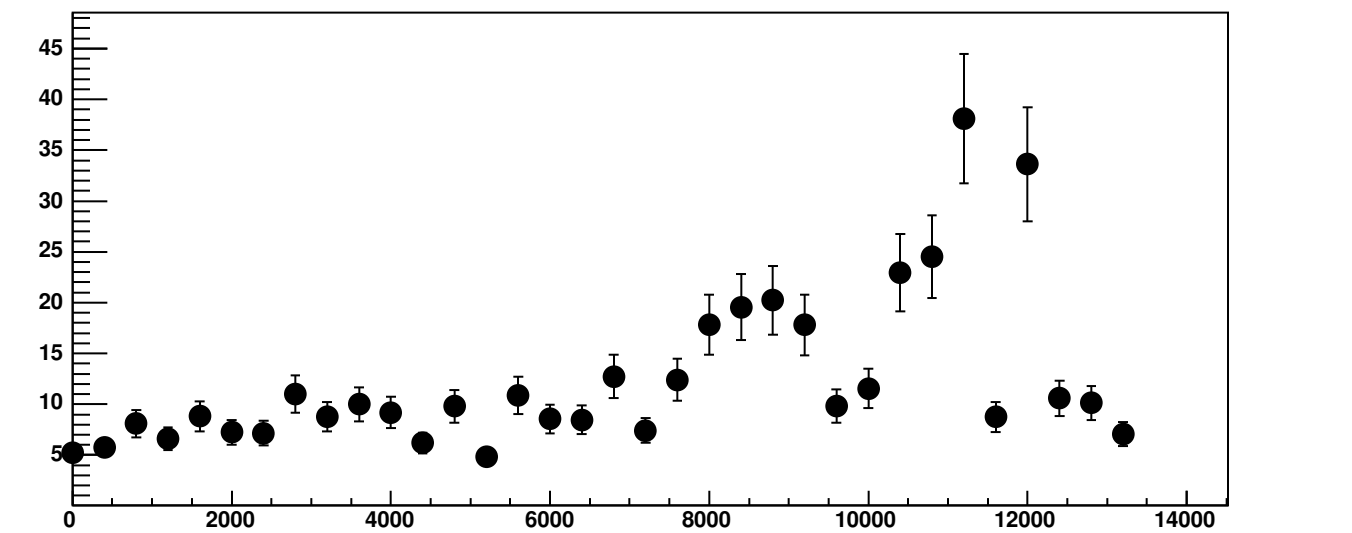

Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

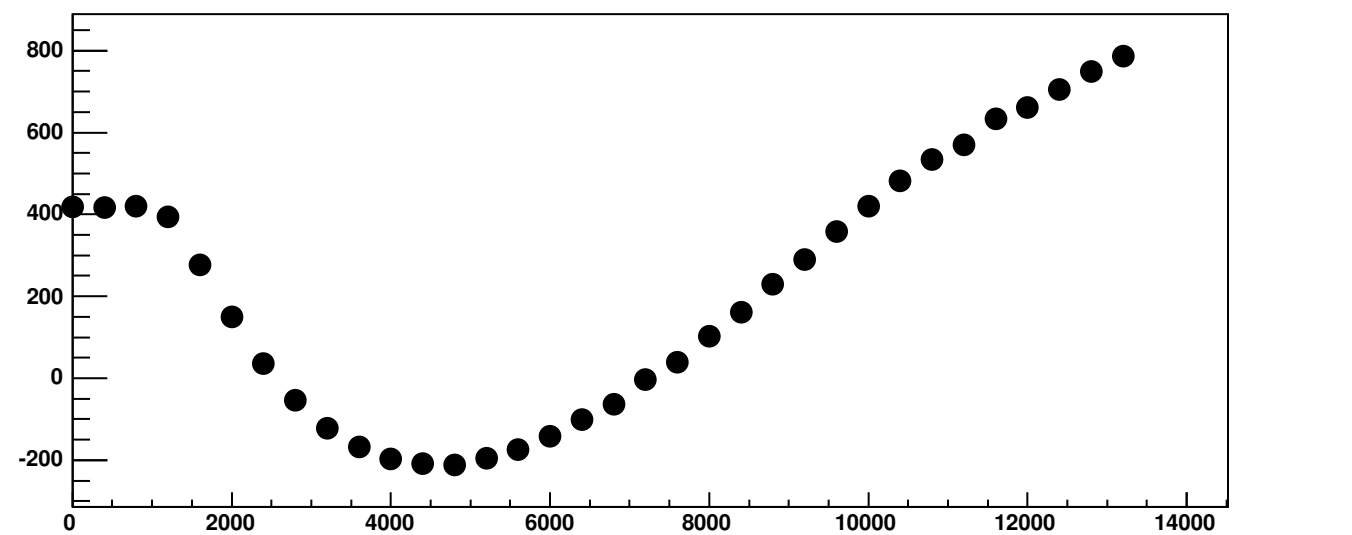
Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

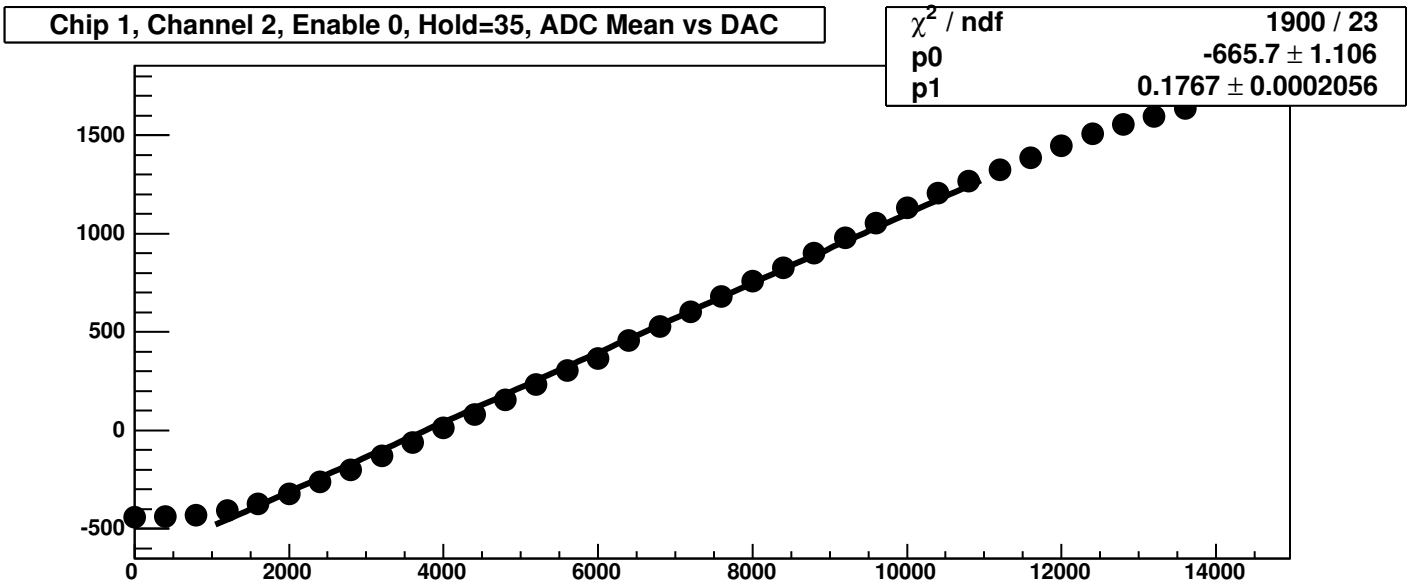


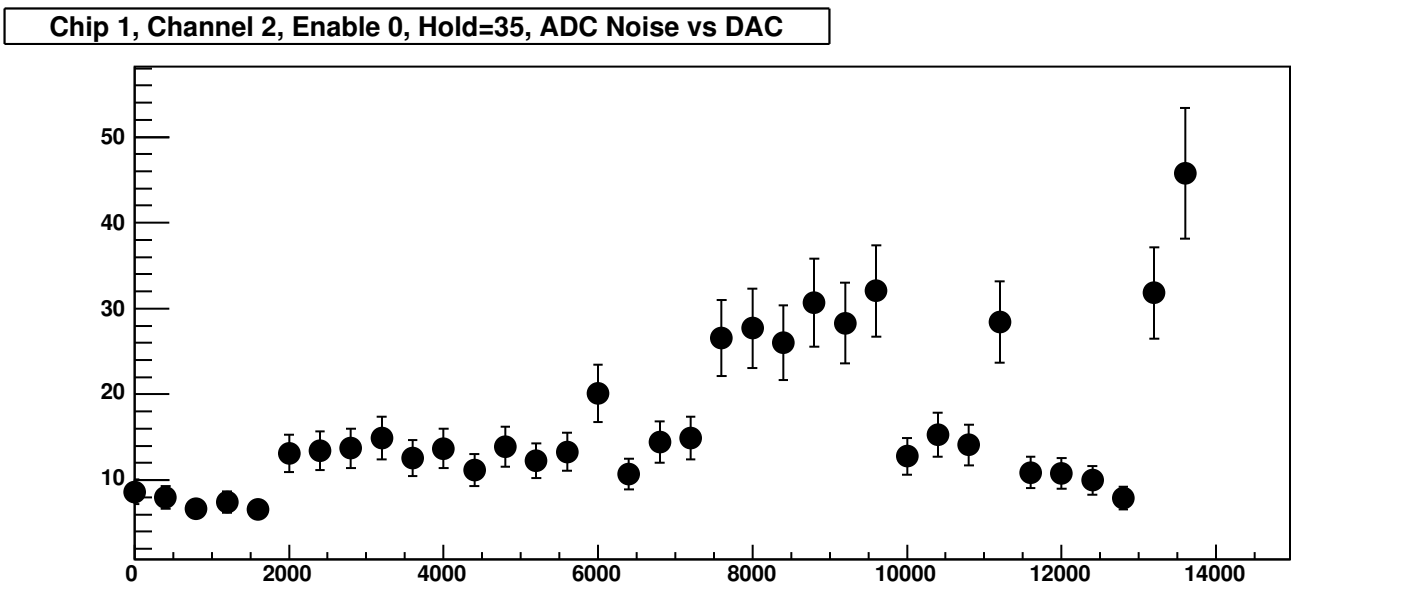
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

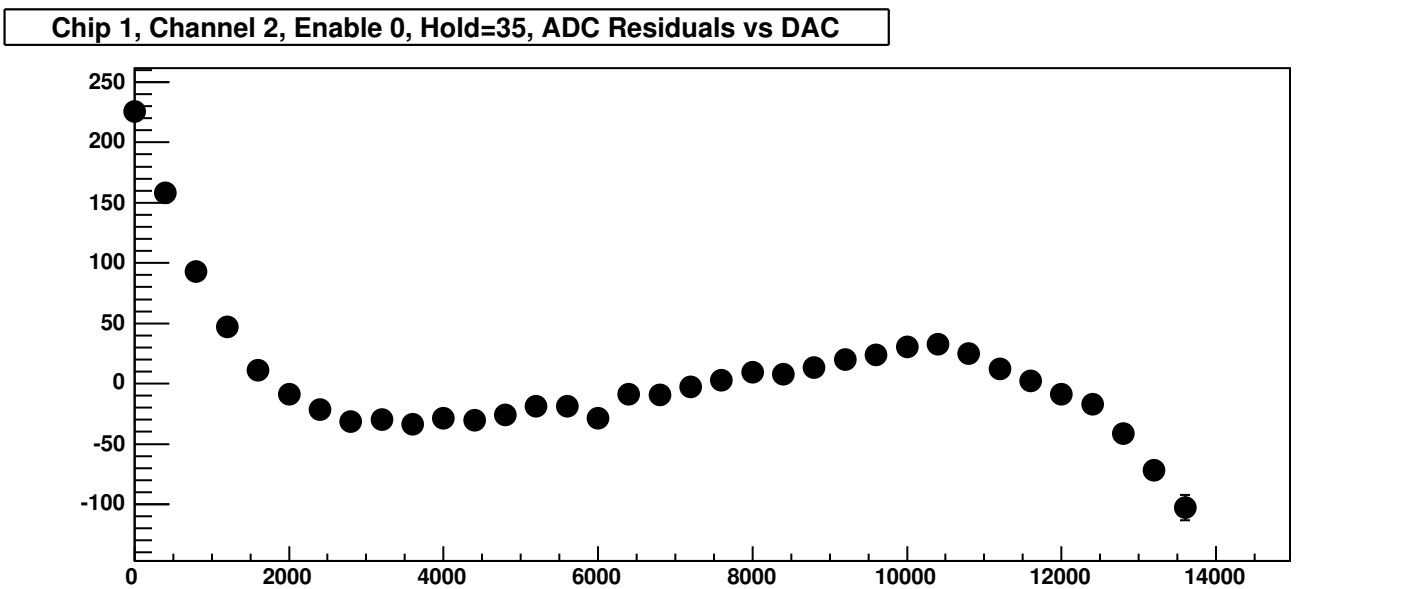


Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

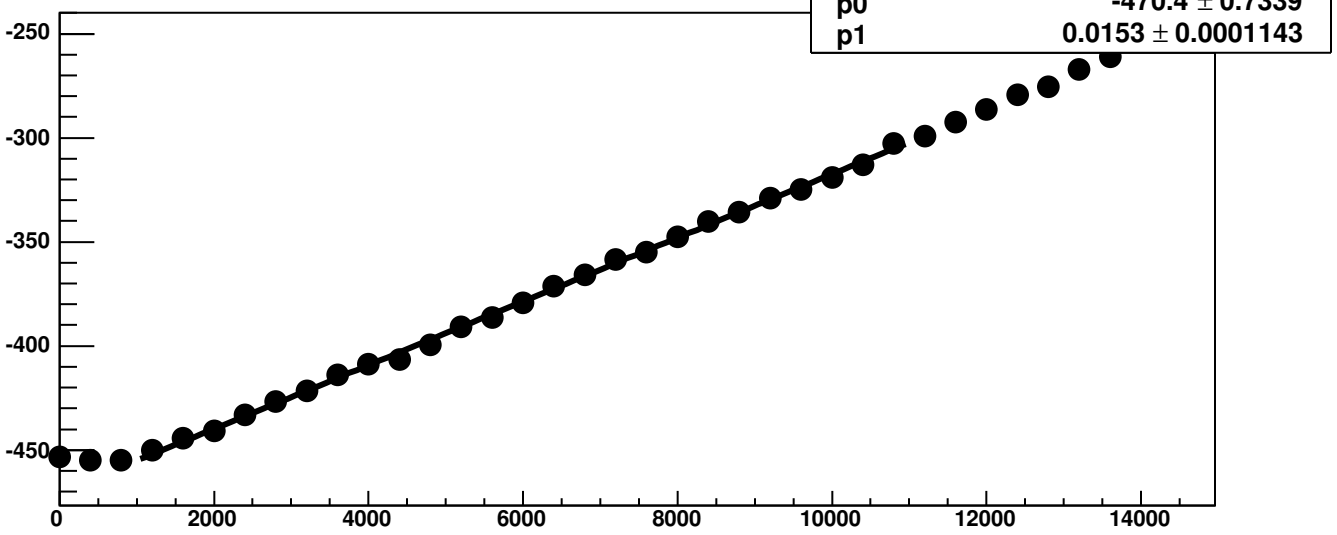



Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1900 / 23
p0 -665.7 ± 1.106
p1 0.1767 ± 0.0002056
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

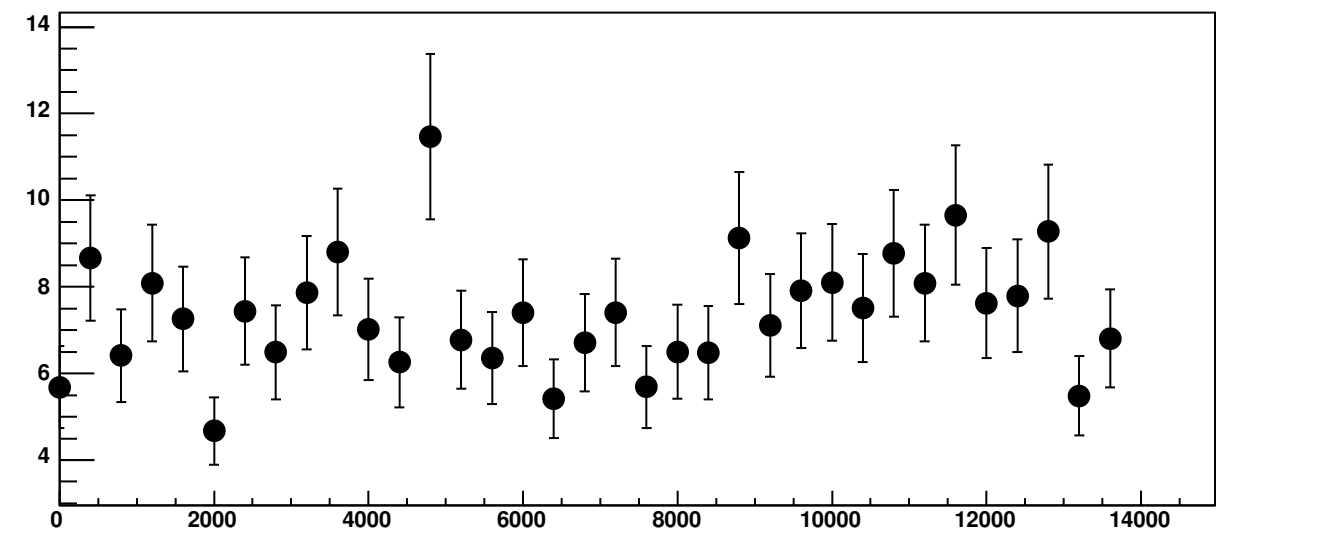

Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

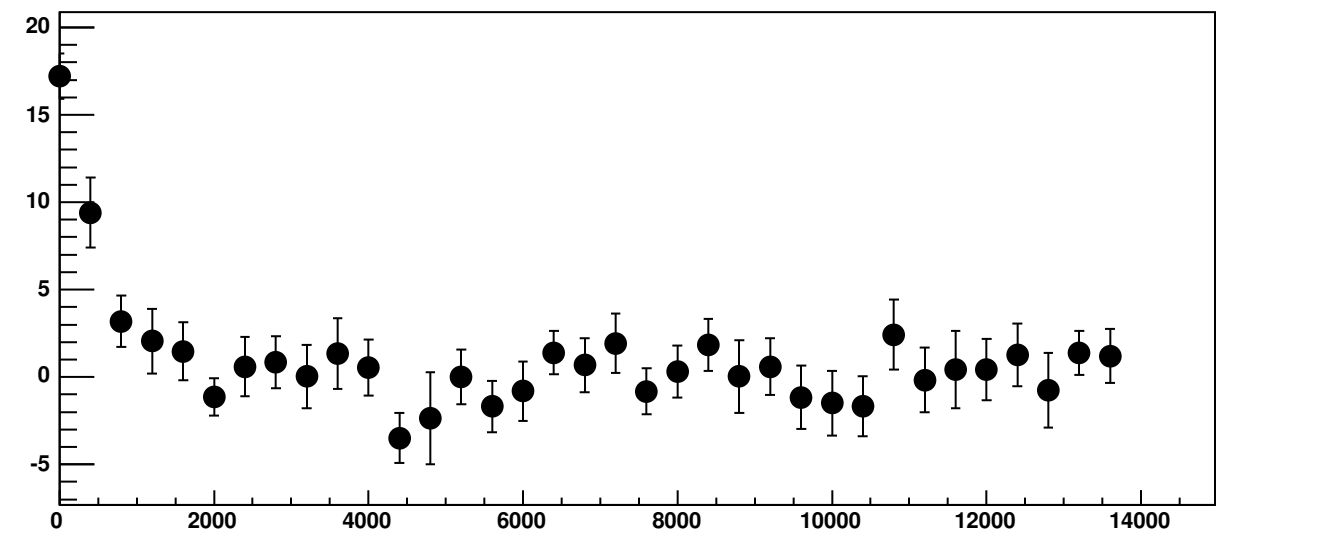
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

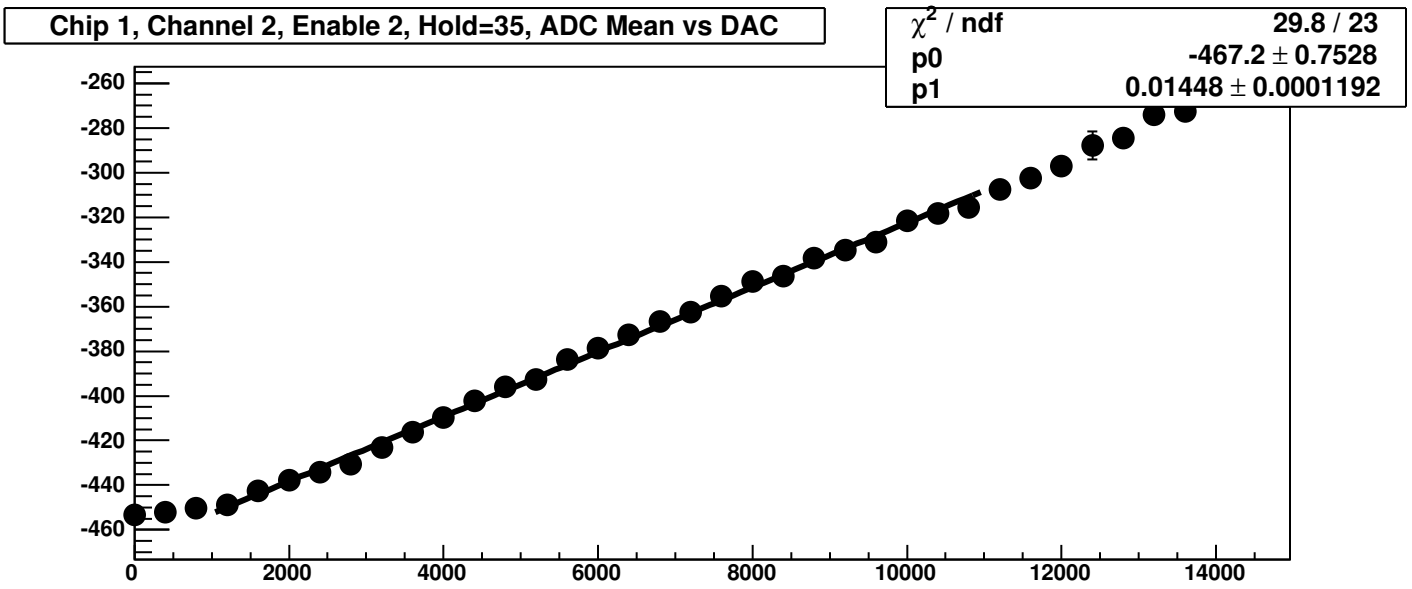


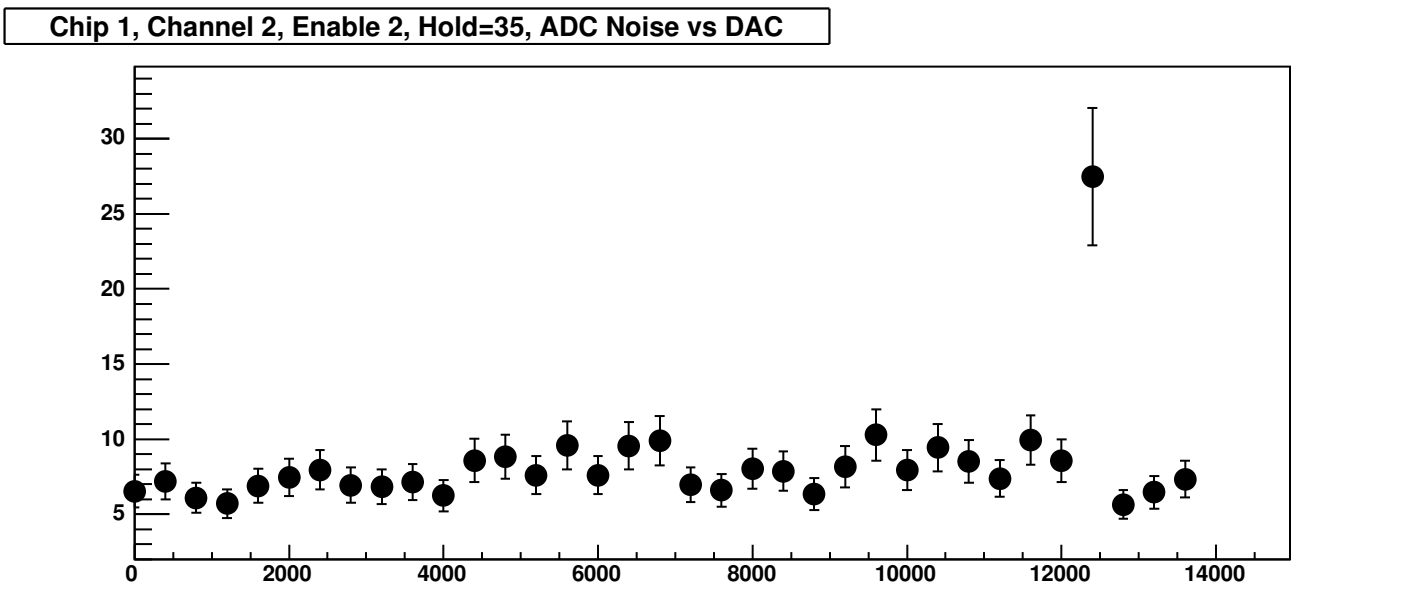
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

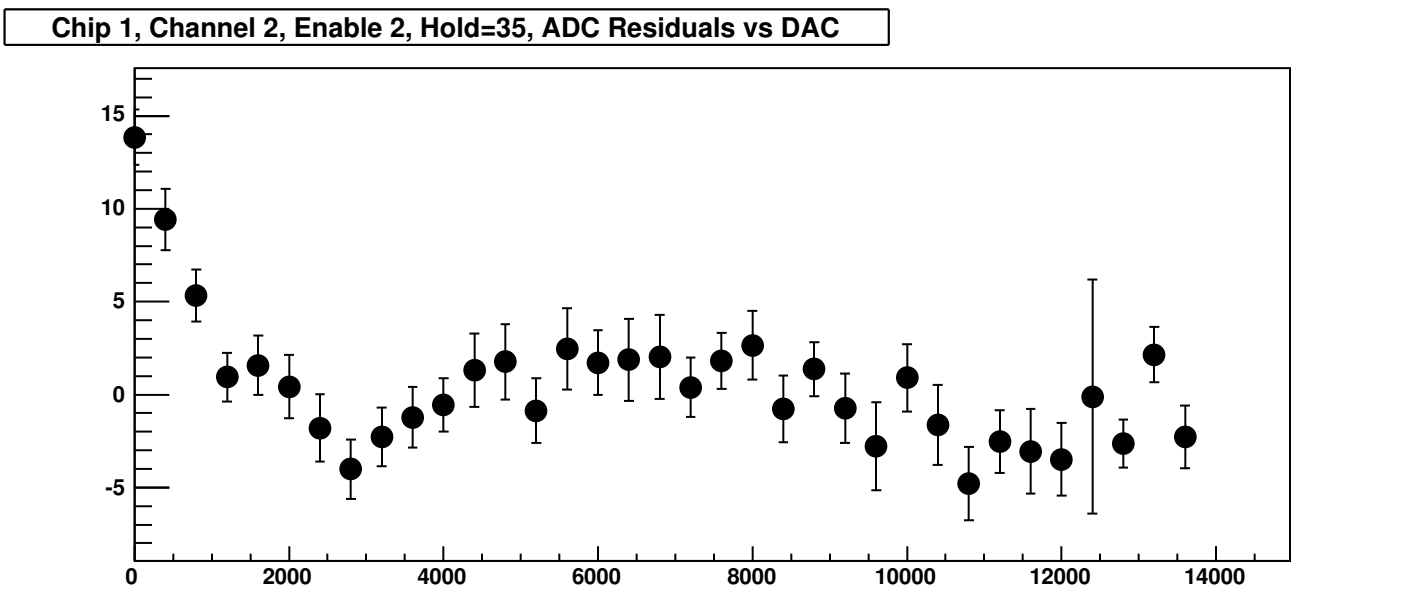


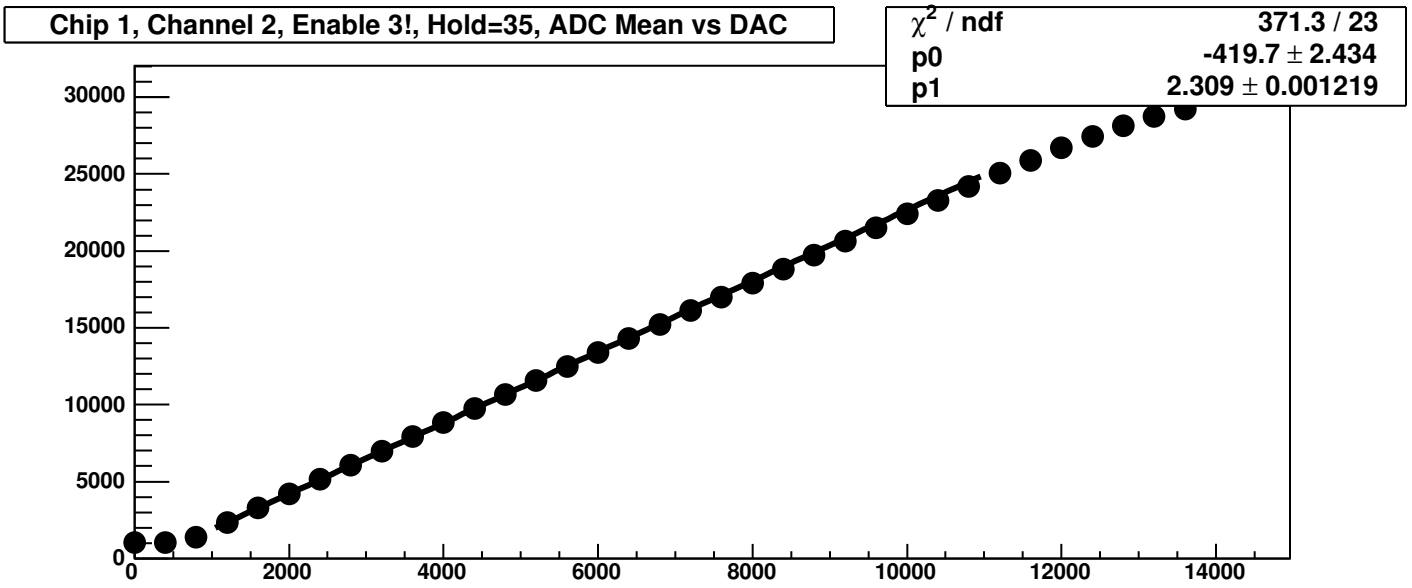
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

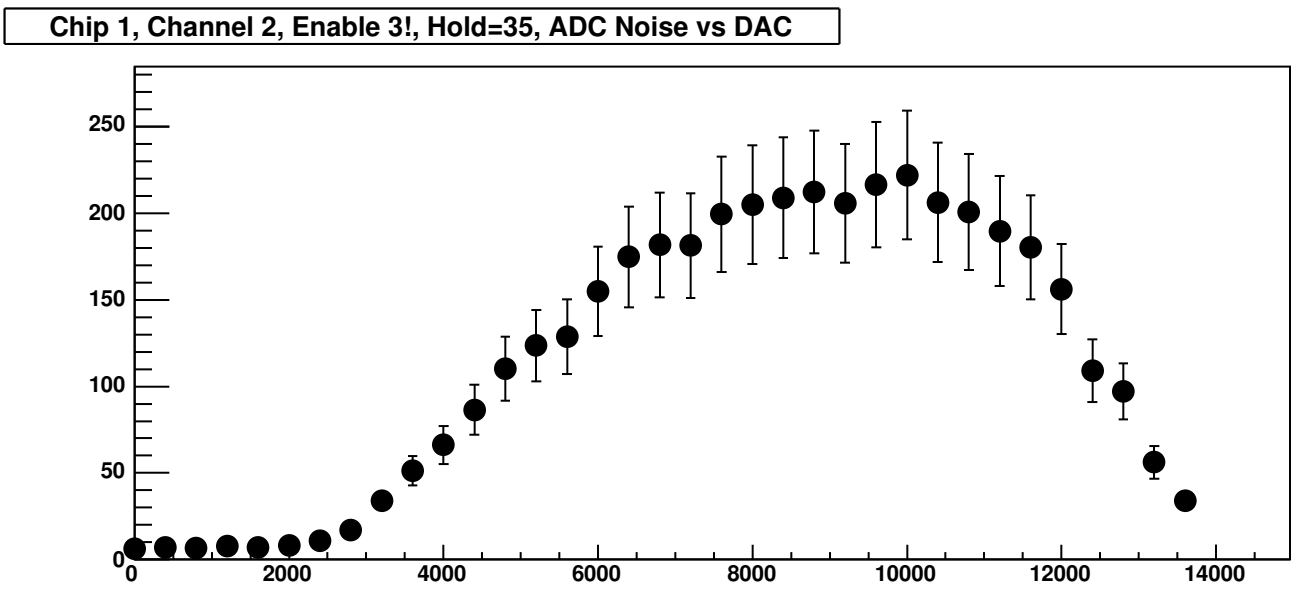


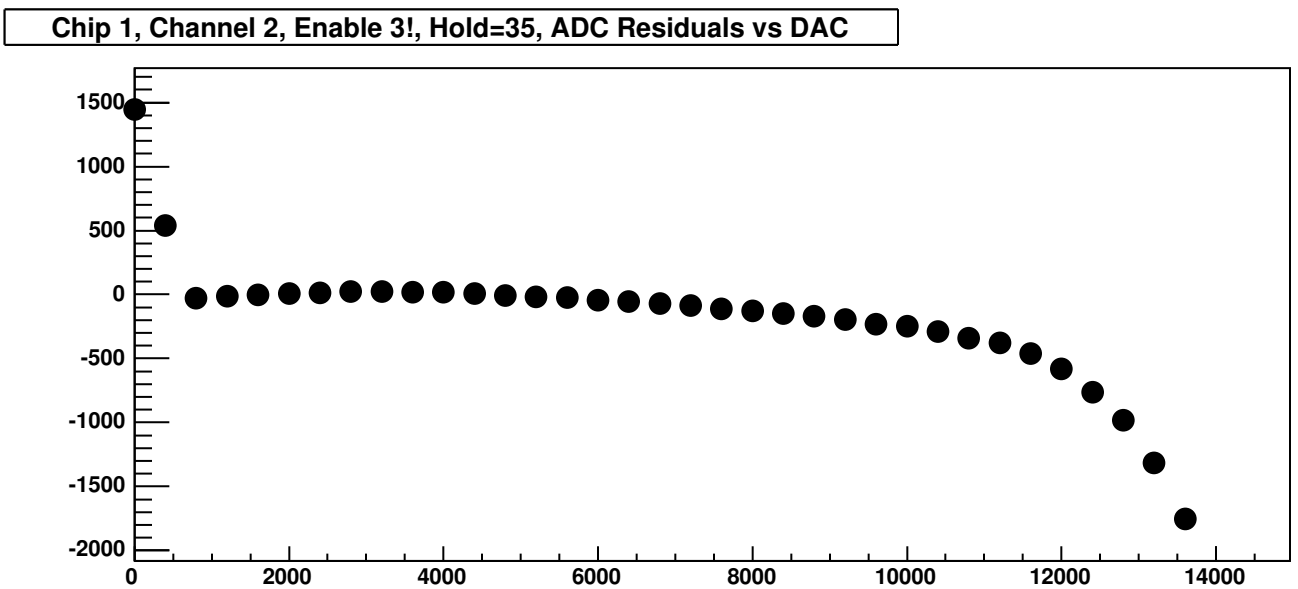

Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.8 / 23
p0 -467.2 ± 0.7528
p1 0.01448 ± 0.0001192
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
2000
4000
6000
8000
10000
12000
14000

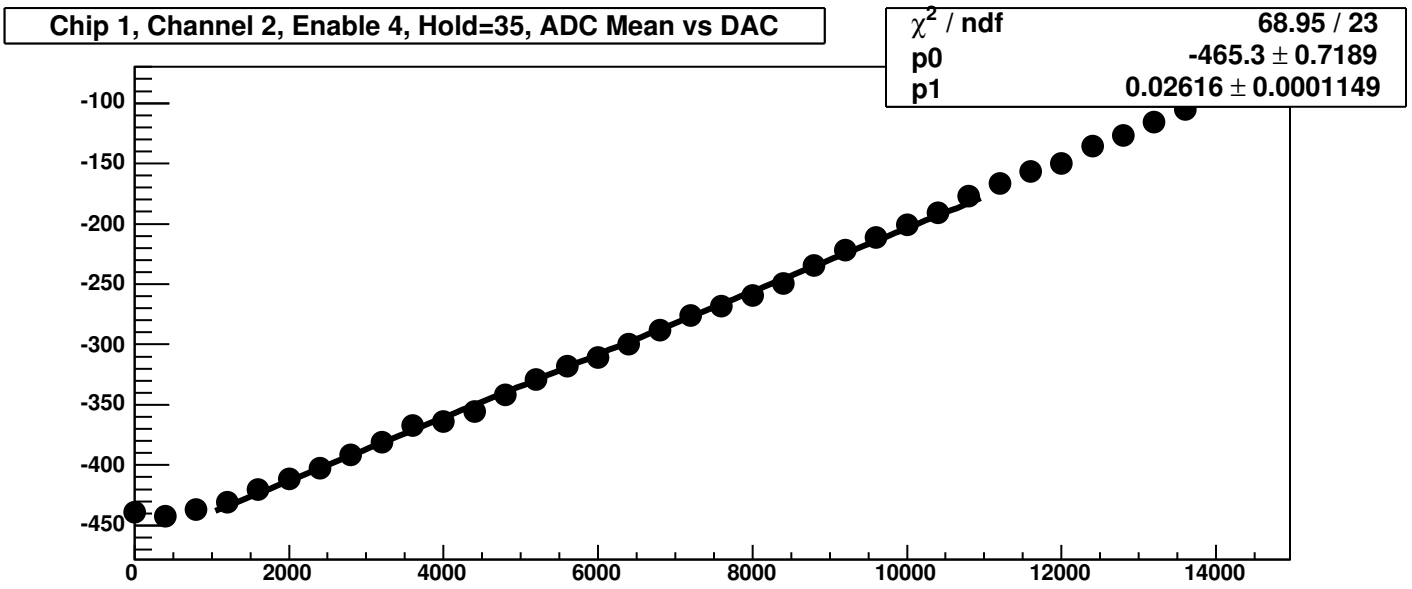

Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

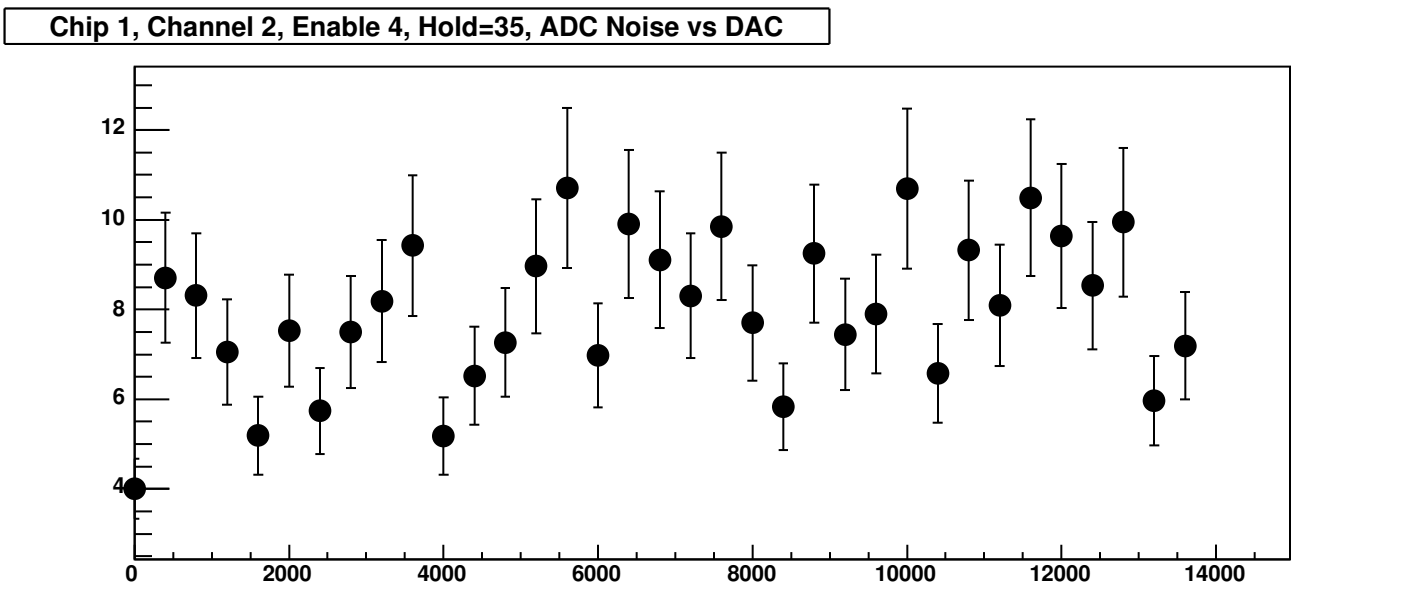

Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

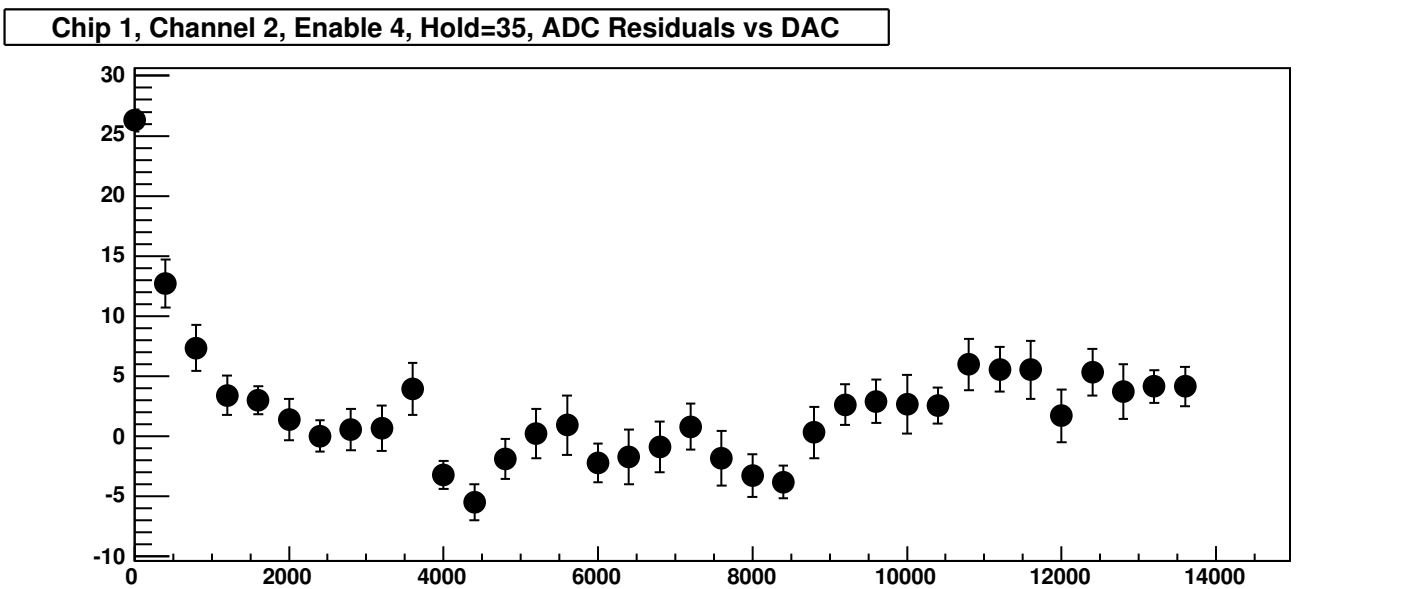

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 371.3 / 23
p0 -419.7 ± 2.434
p1 2.309 ± 0.001219
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

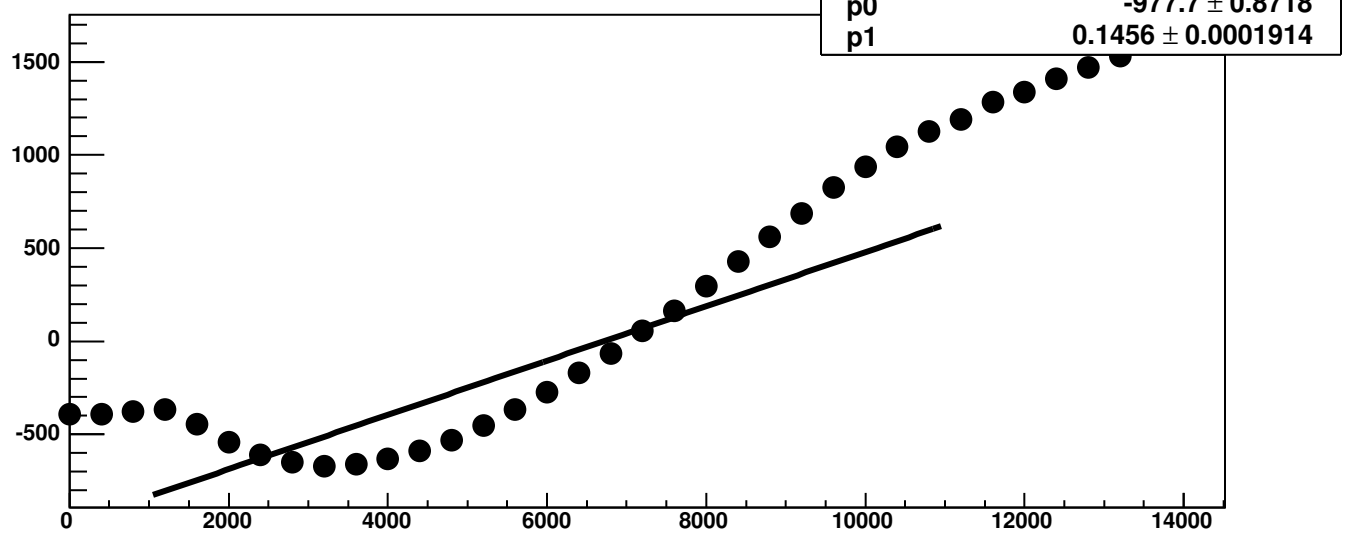

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 68.95 / 23
p0 -465.3 ± 0.7189
p1 0.02616 ± 0.0001149
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

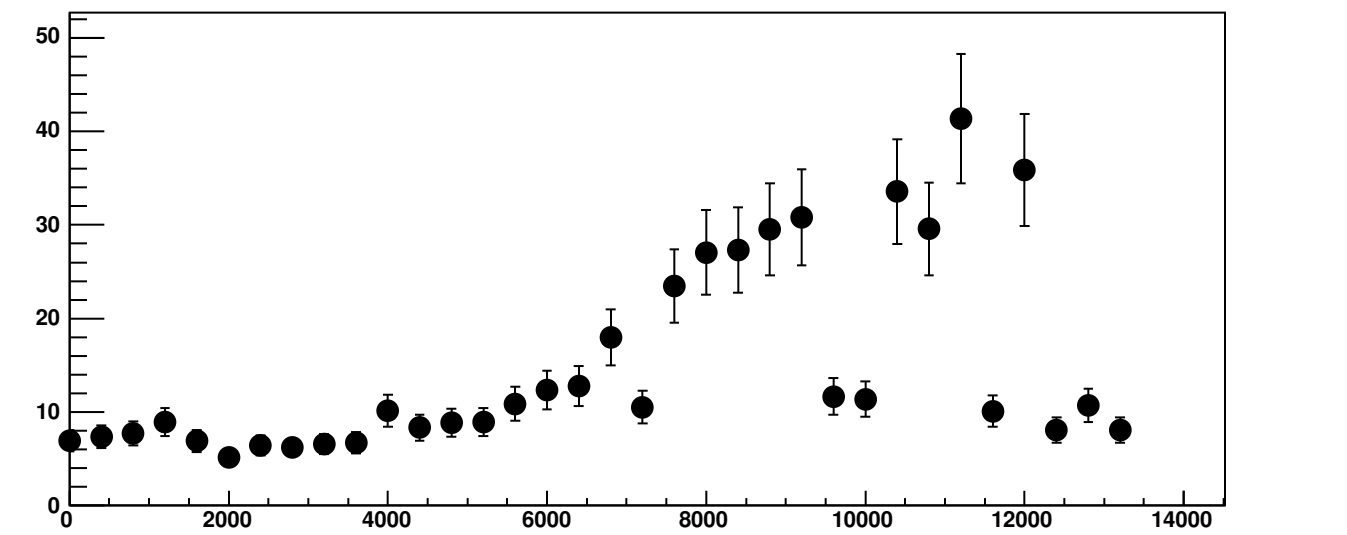

Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

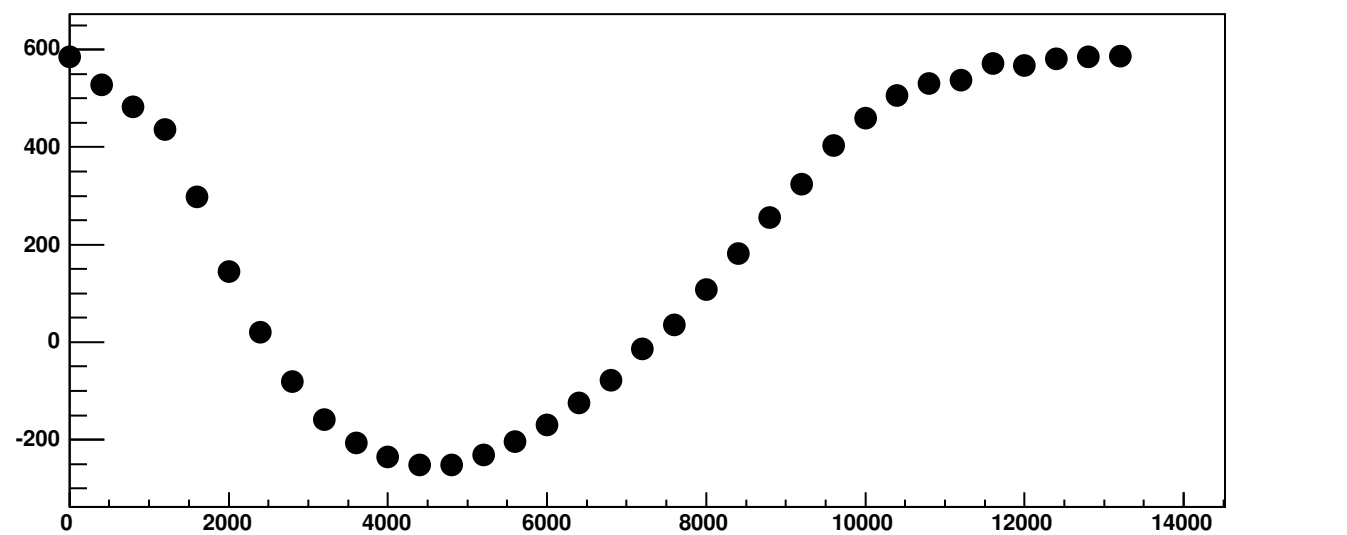
Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

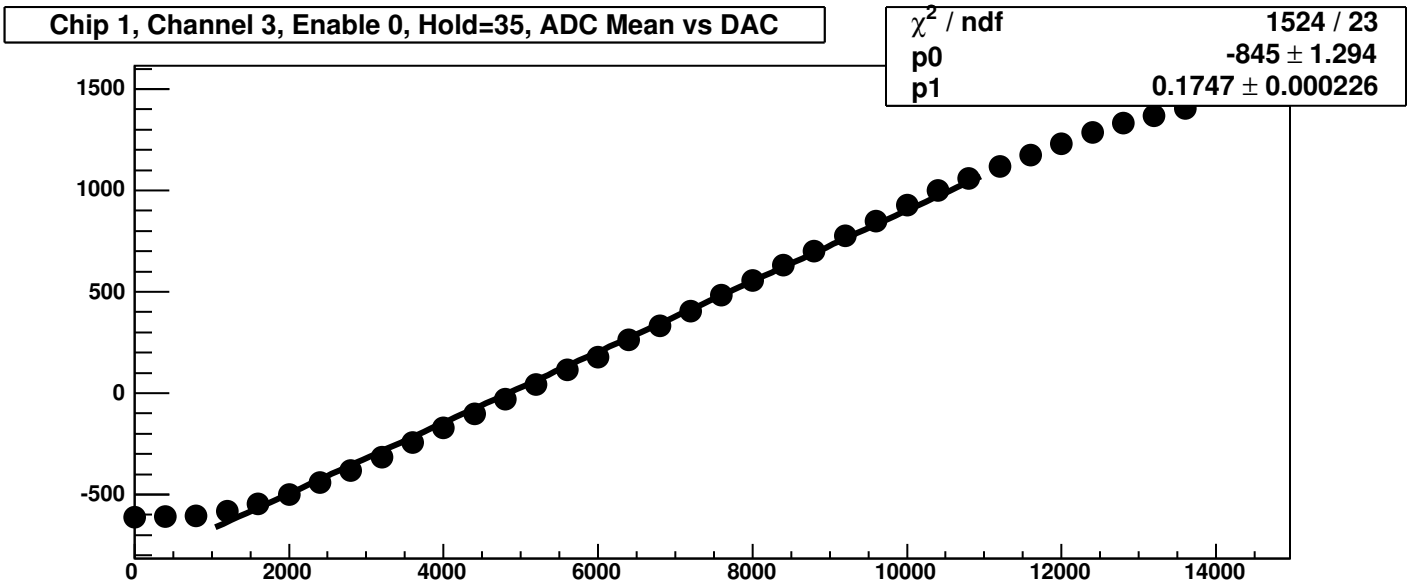


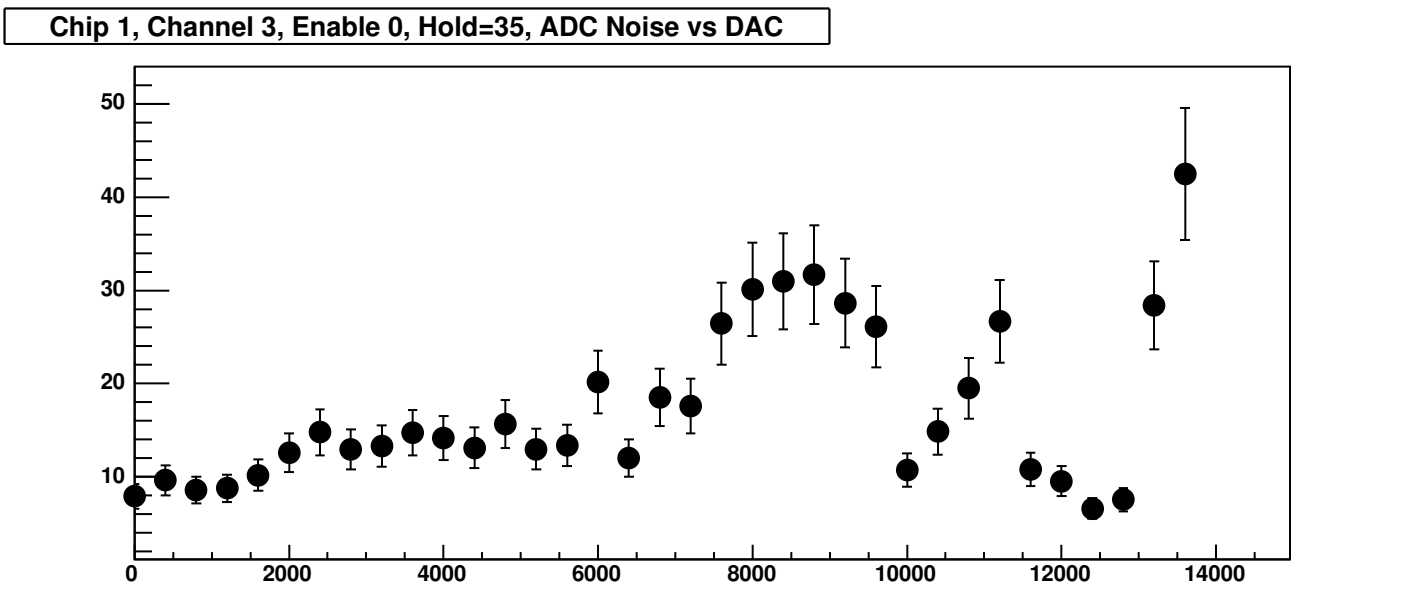
Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

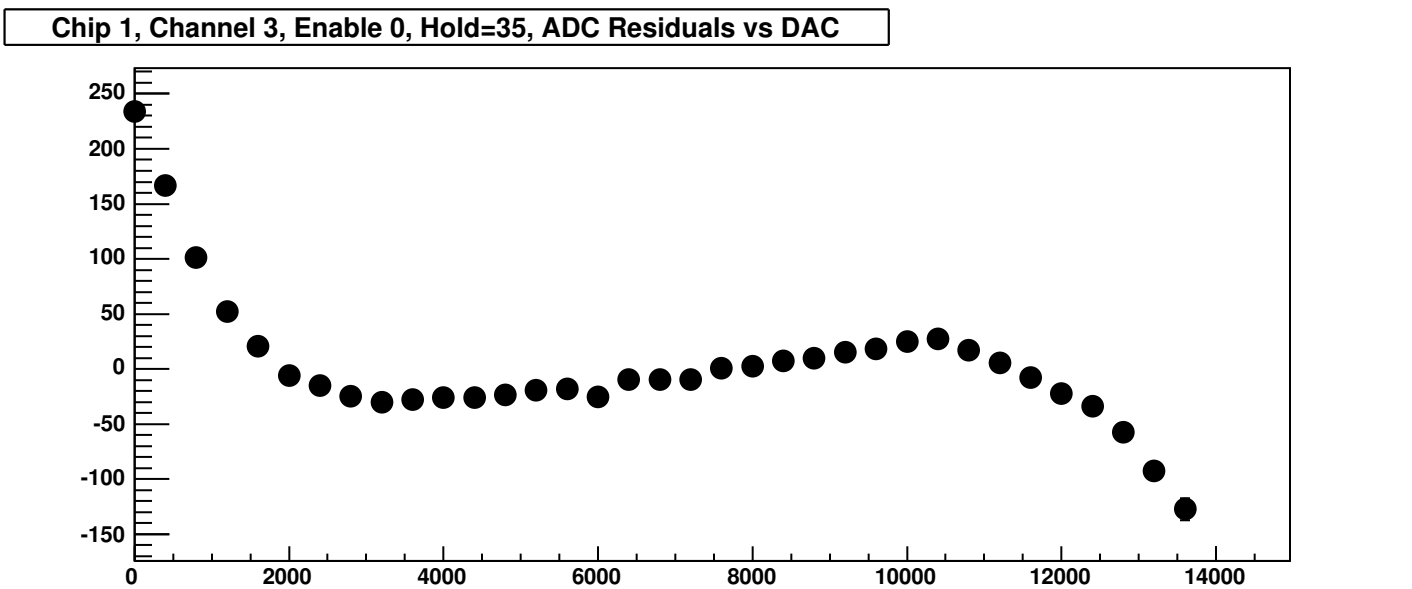


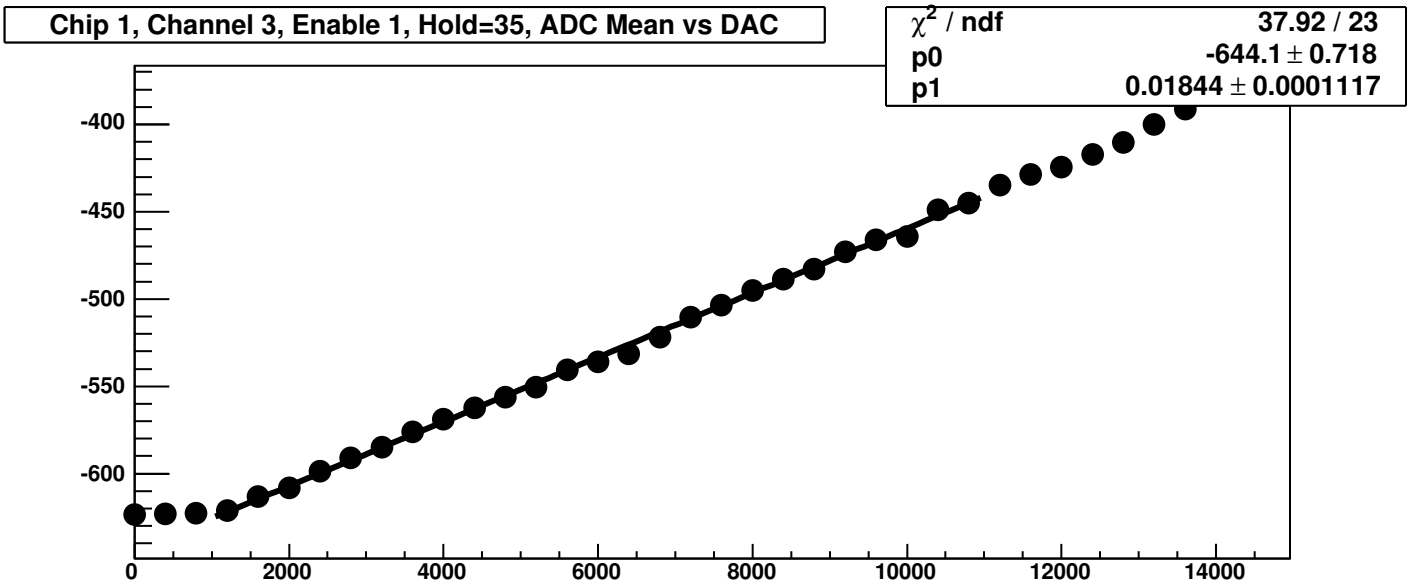
Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

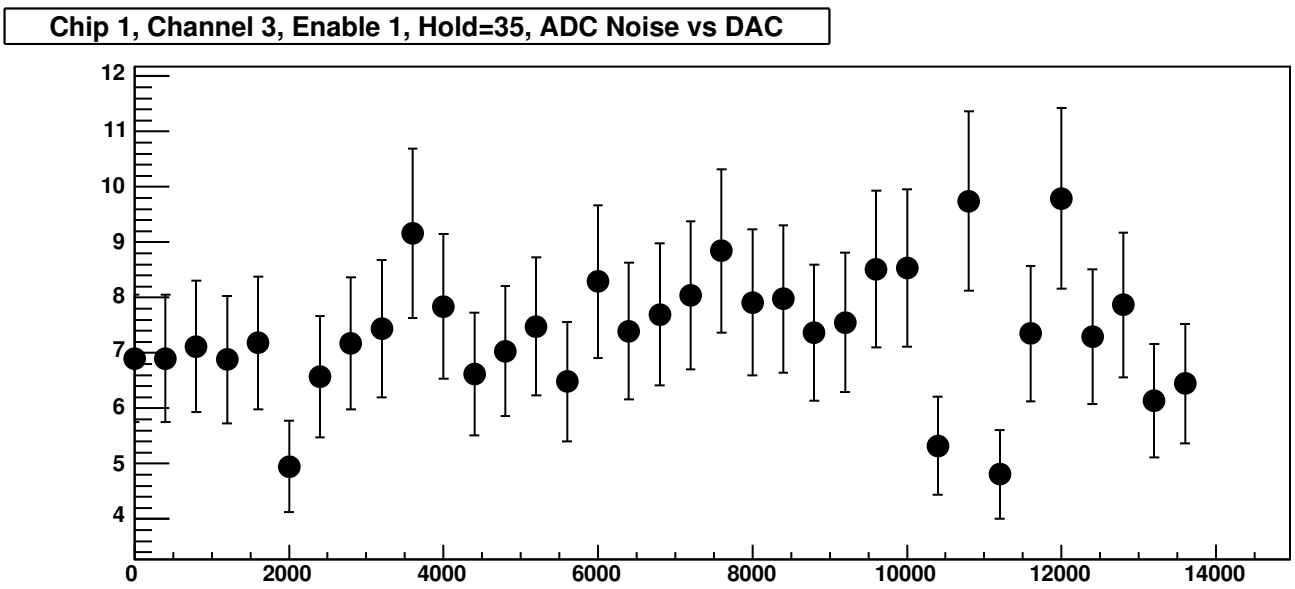


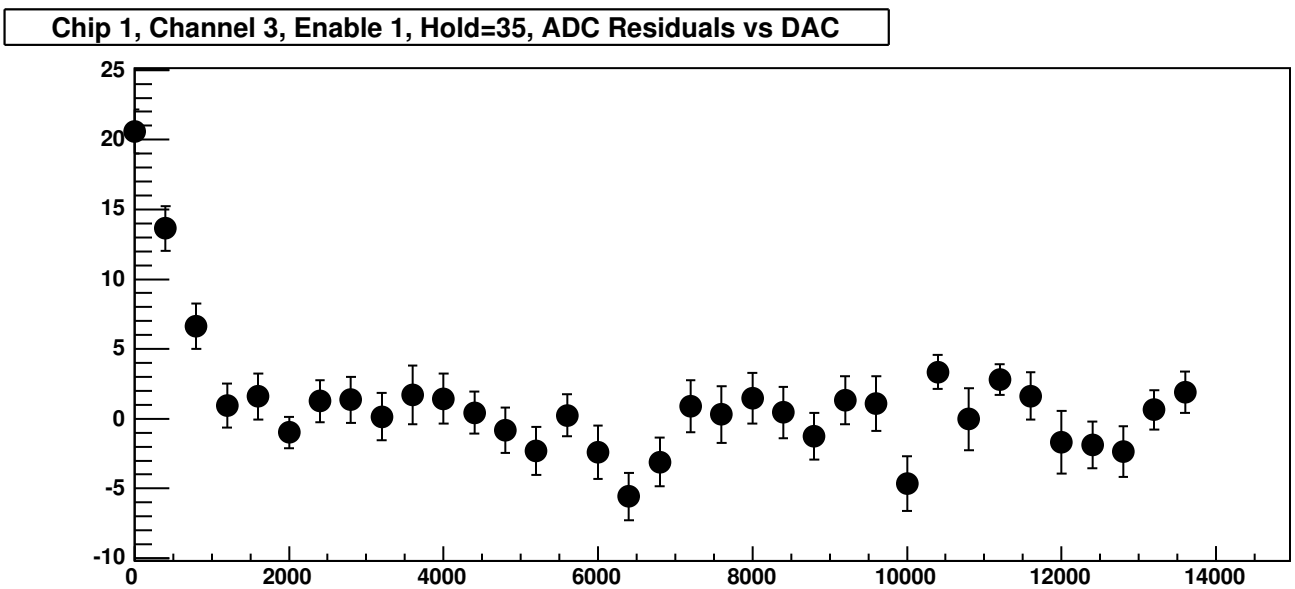

Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1524 / 23
p0 -845 ± 1.294
p1 0.1747 ± 0.000226
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

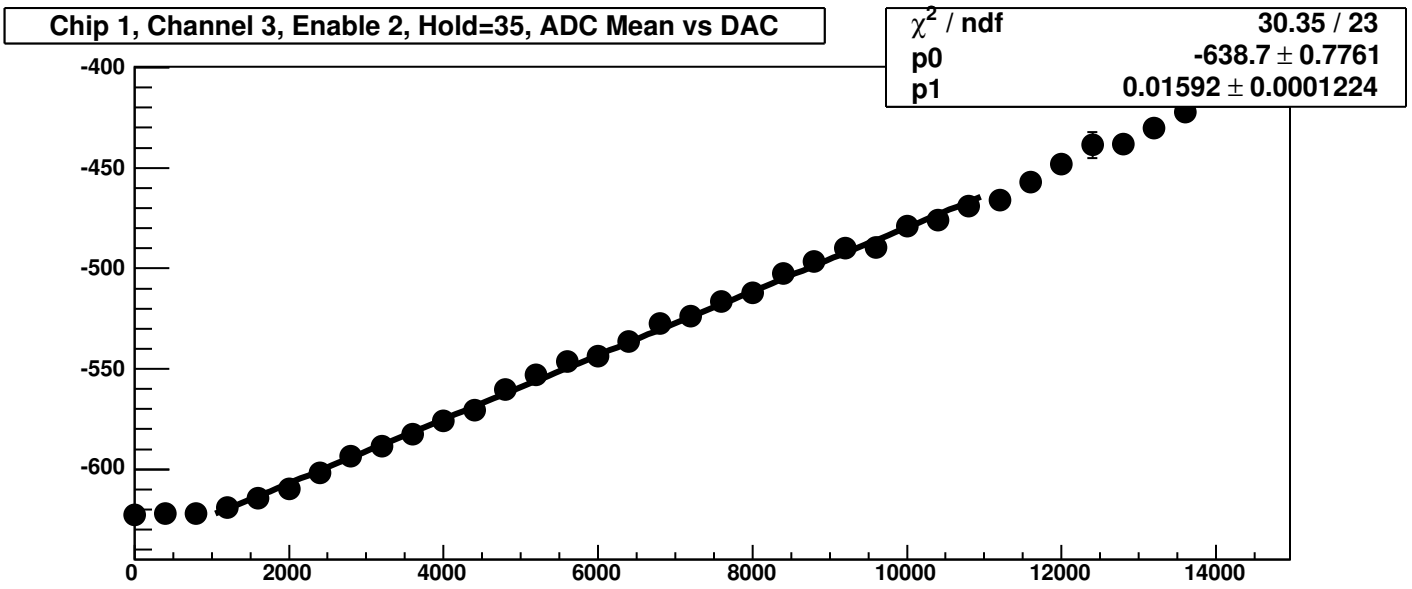

Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

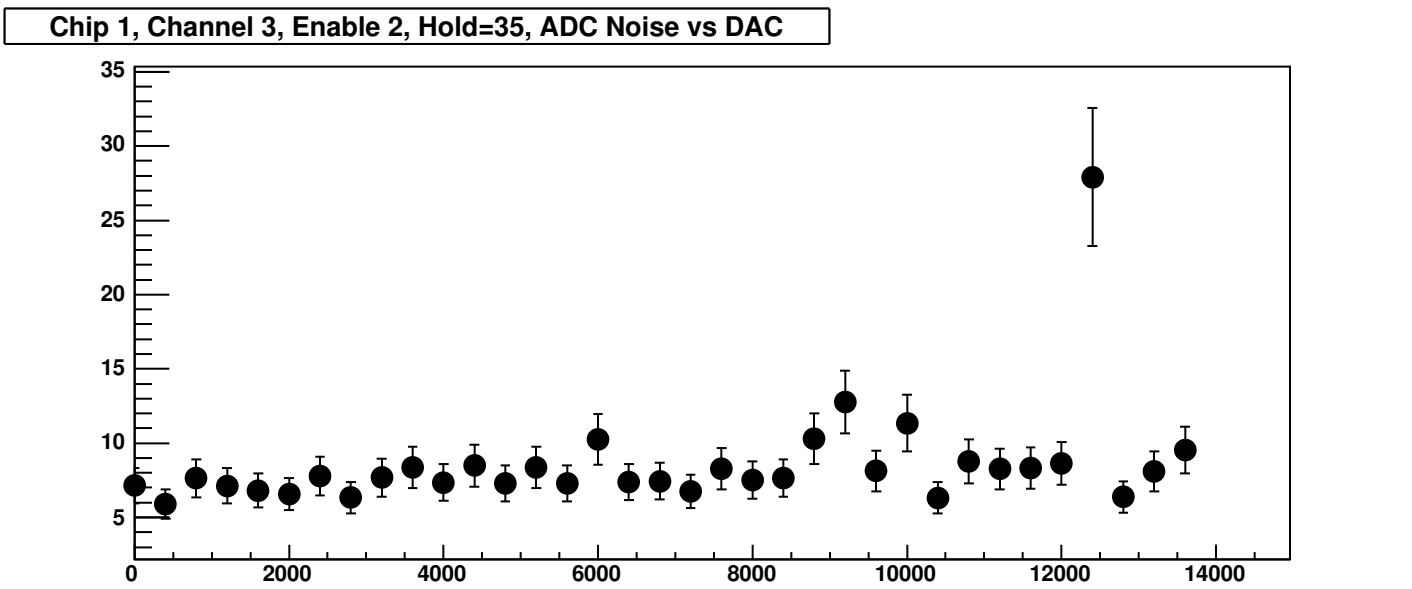

Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

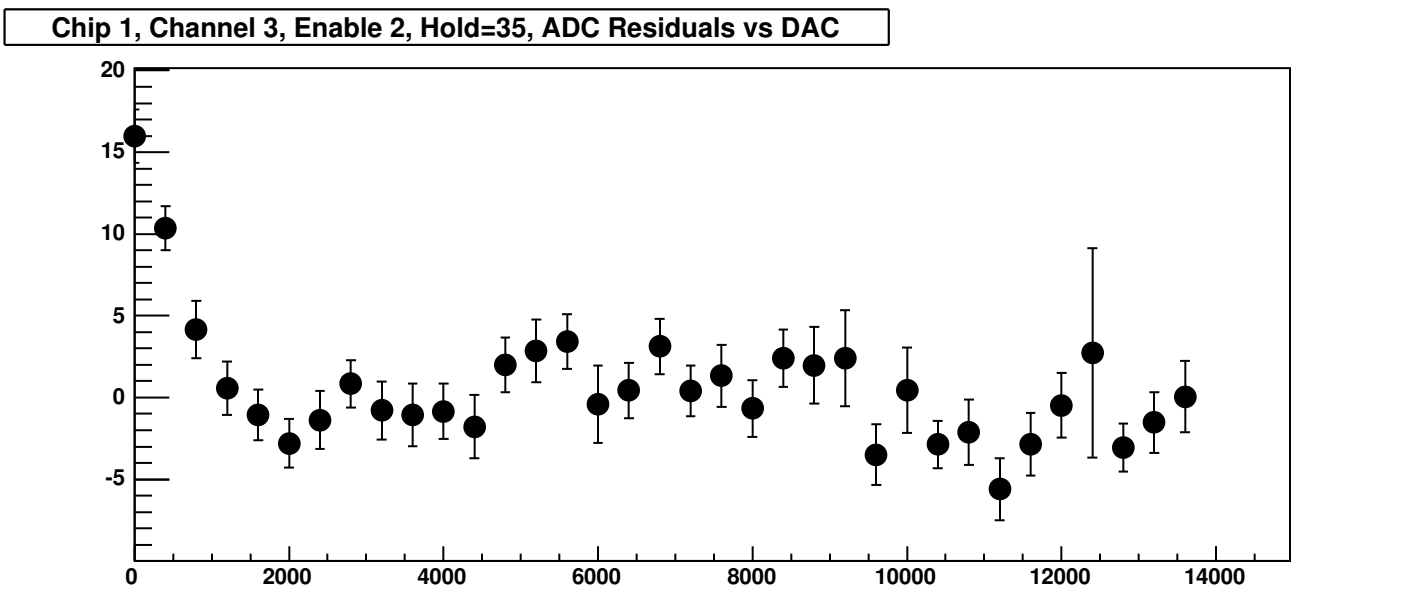

Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.92 / 23
p0 -644.1 ± 0.718
p1 0.01844 ± 0.0001117
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

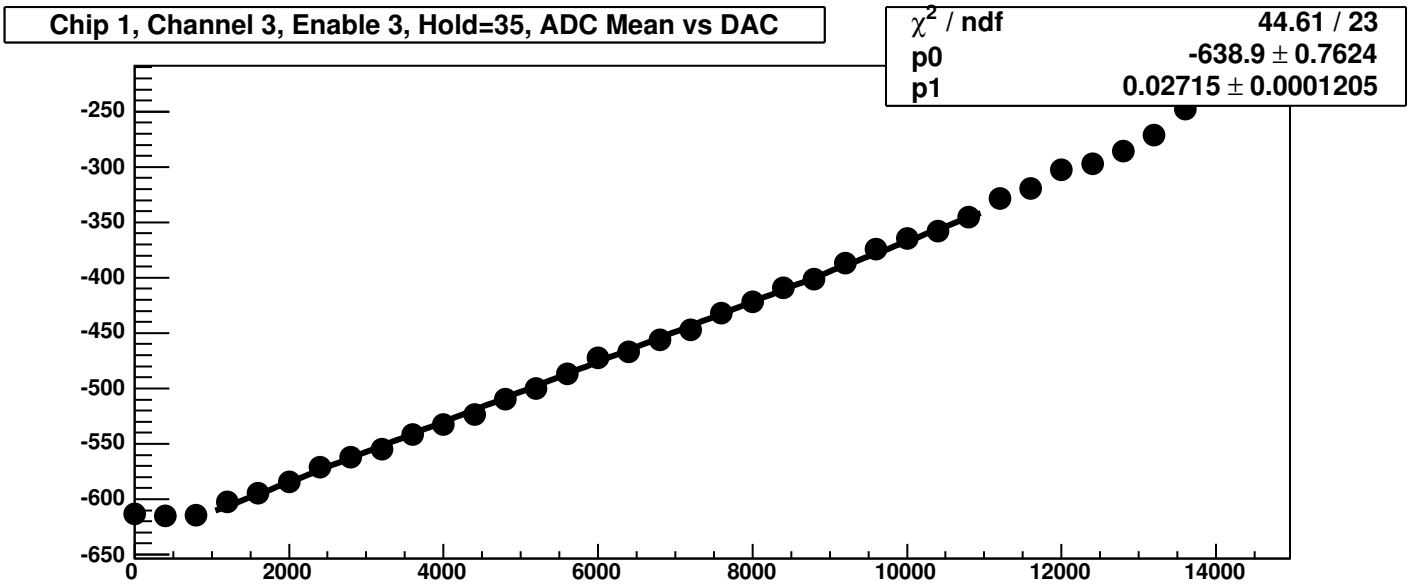

Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

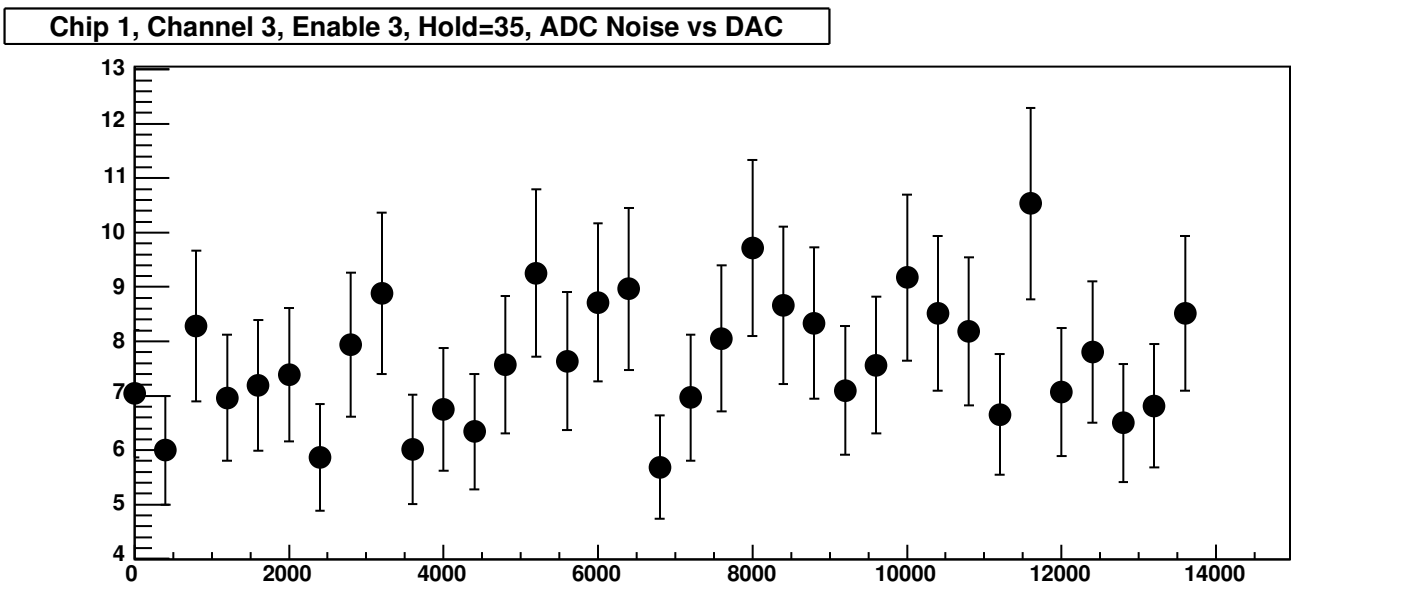

Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

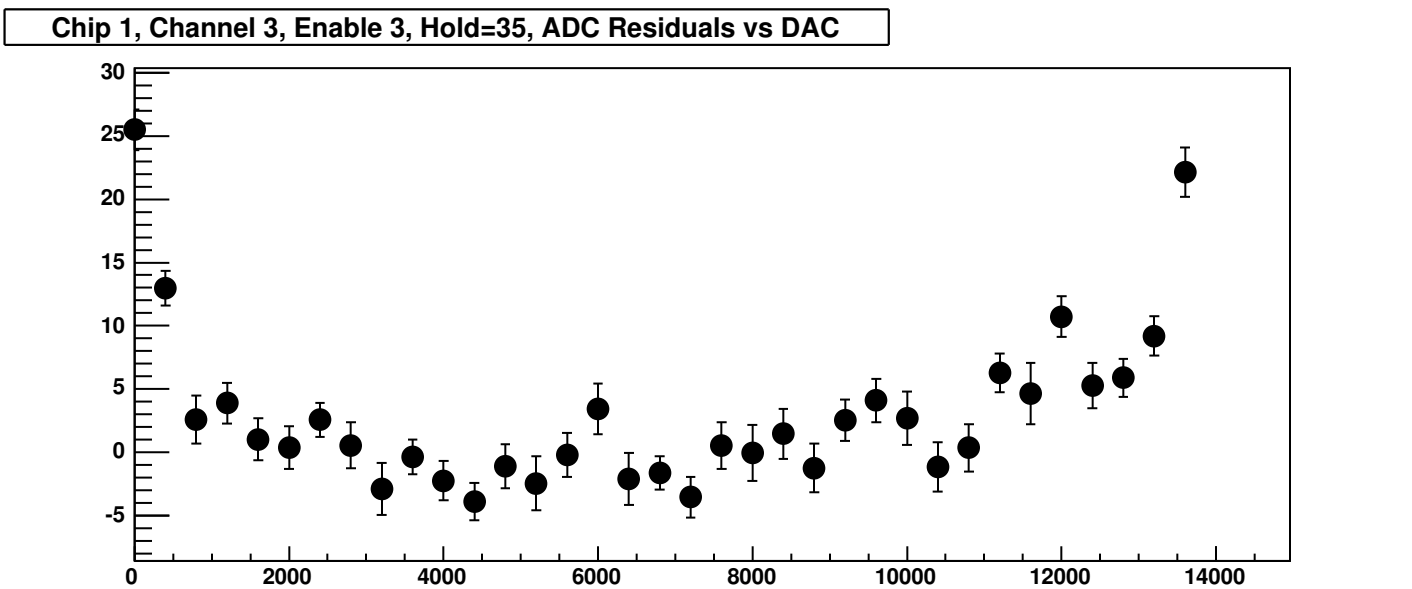

Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.35 / 23
p0 -638.7 ± 0.7761
p1 0.01592 ± 0.0001224
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

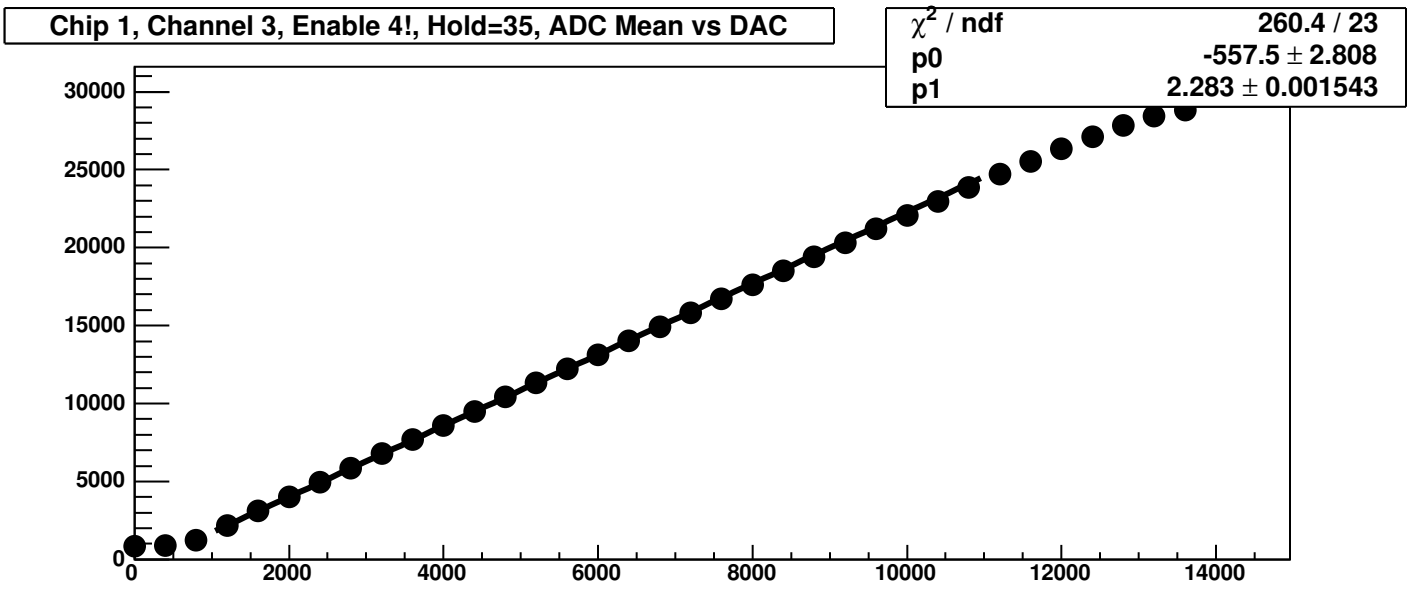

Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

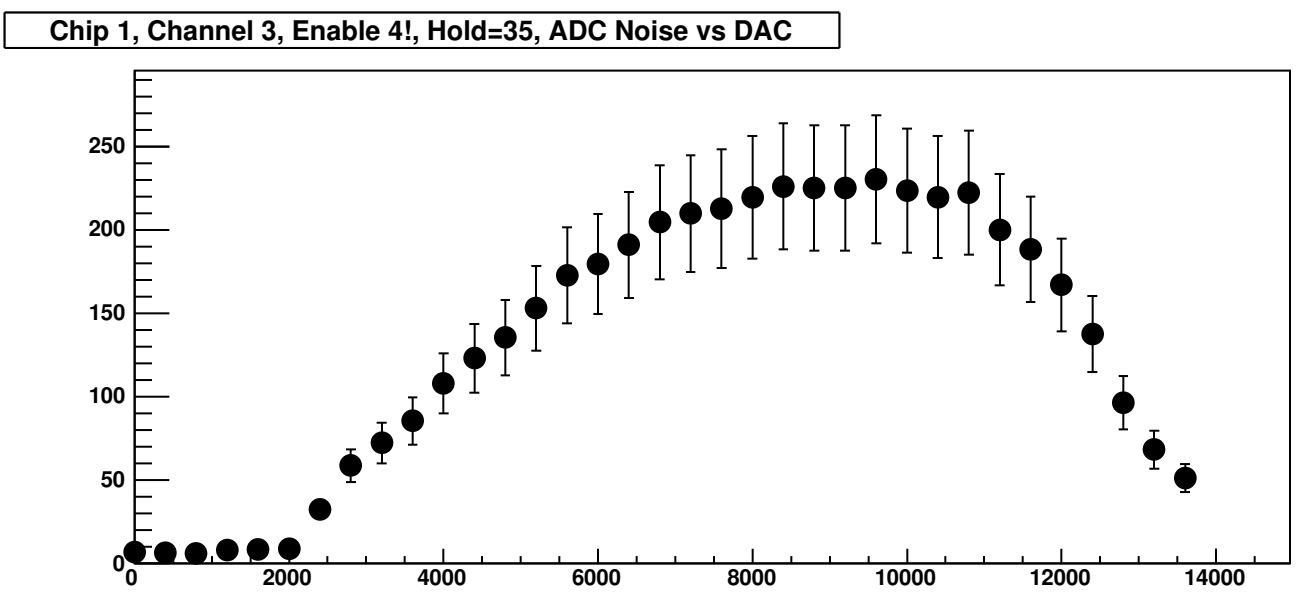

Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

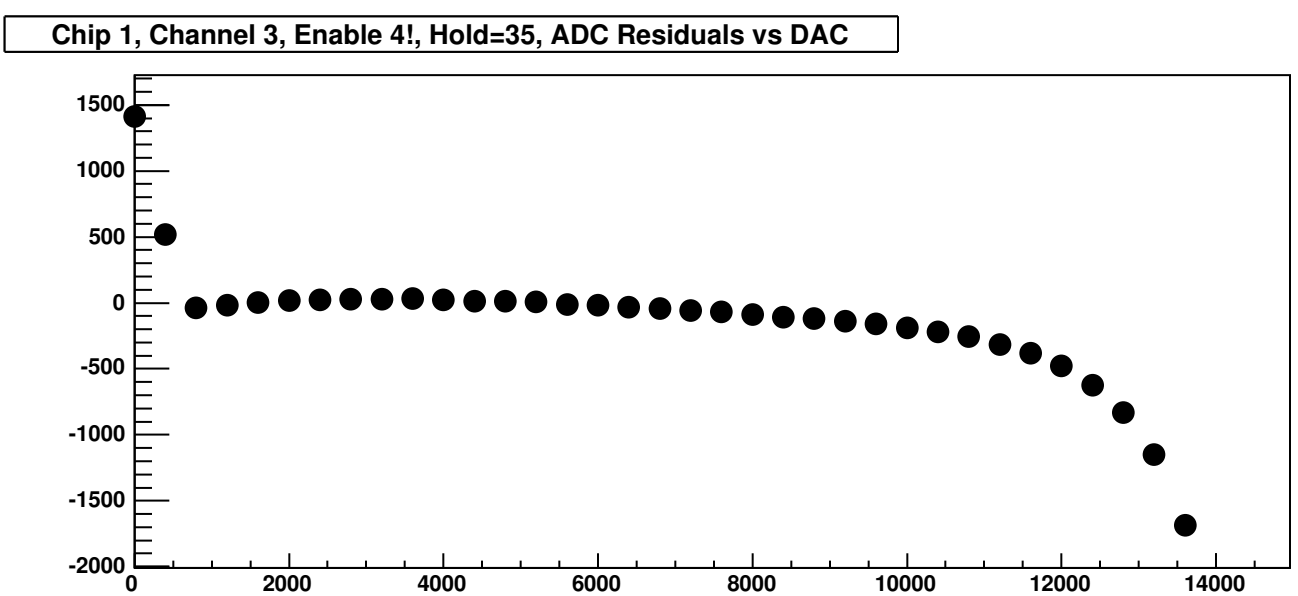

Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.61 / 23
p0 -638.9 ± 0.7624
p1 0.02715 ± 0.0001205
-250
-300
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

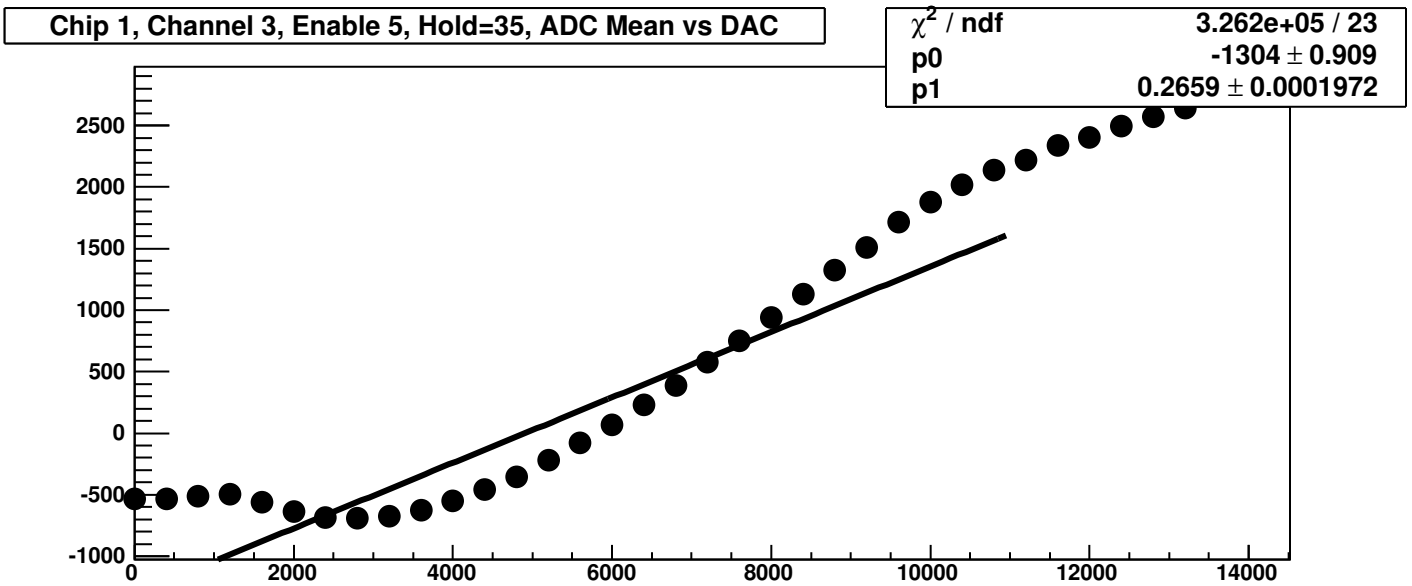

Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

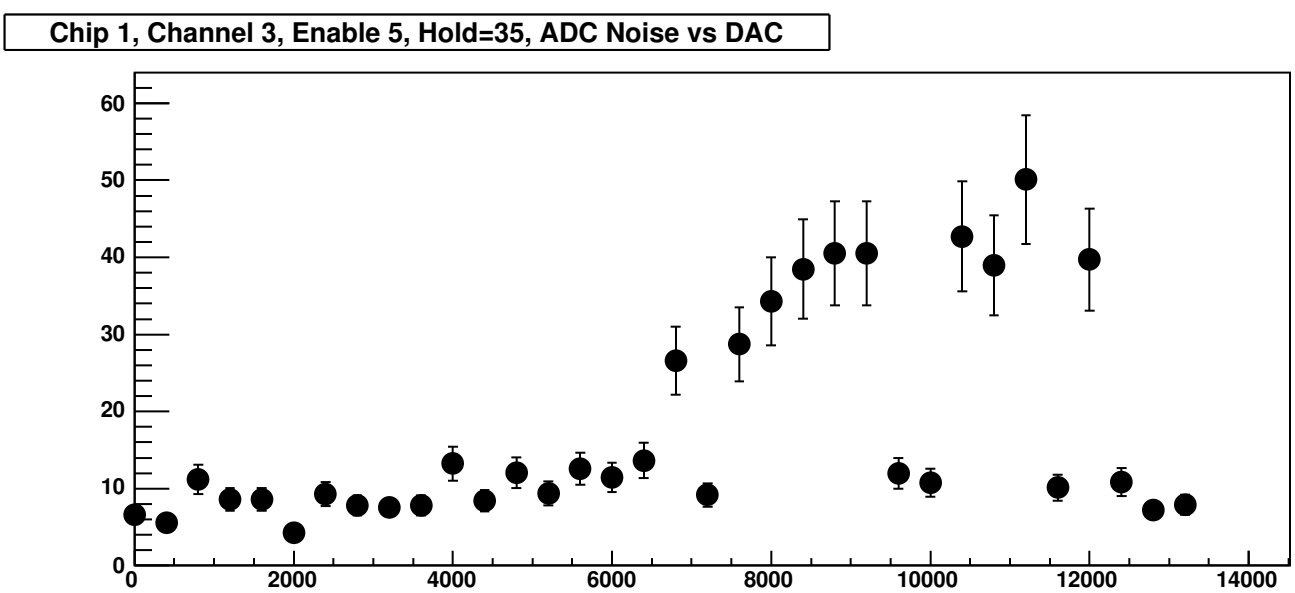

Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

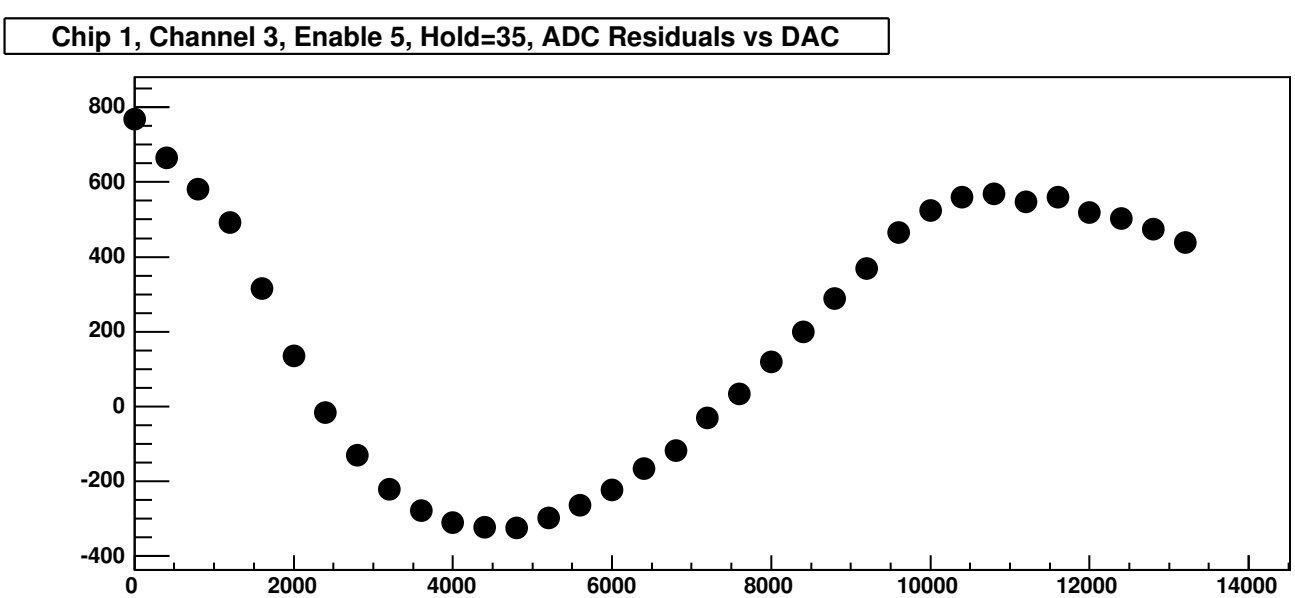

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 260.4 / 23
p0 -557.5 ± 2.808
p1 2.283 ± 0.001543
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

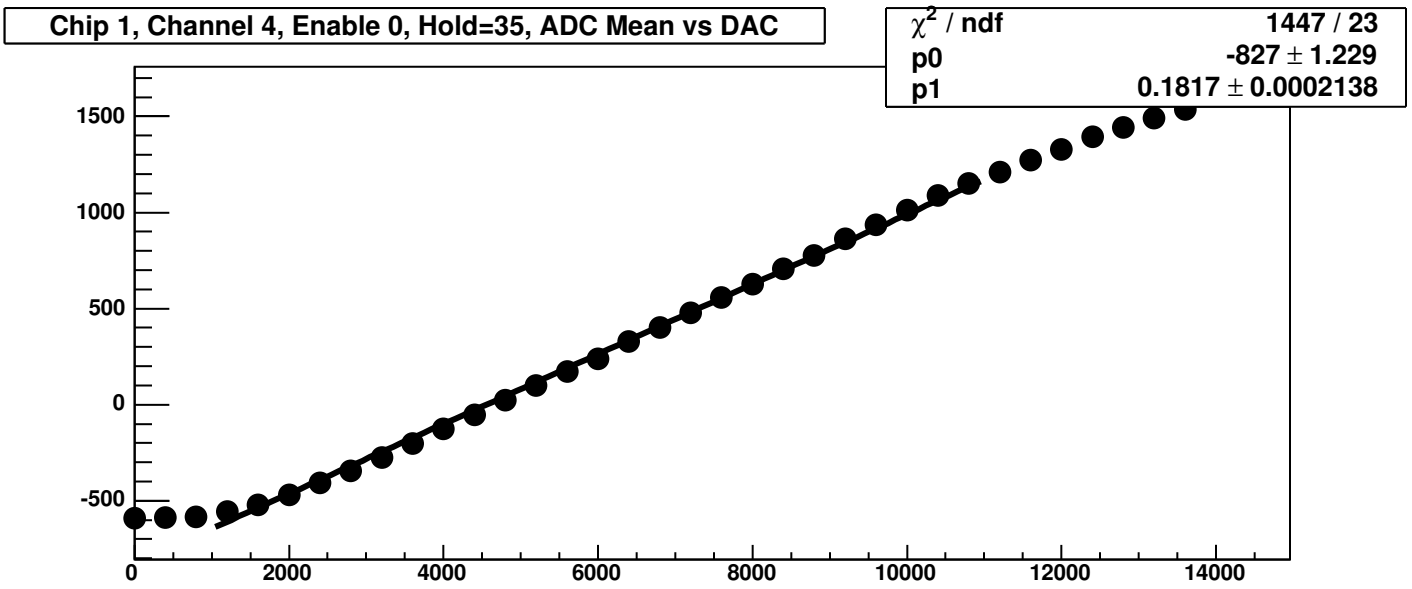

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

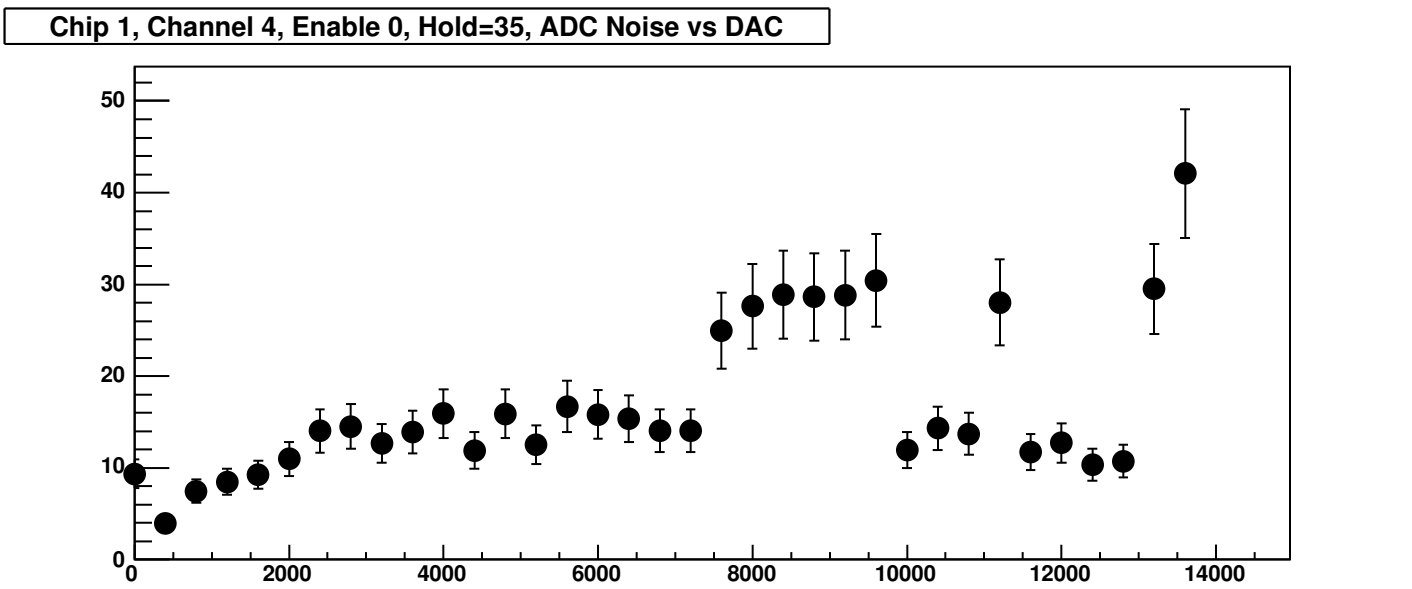

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

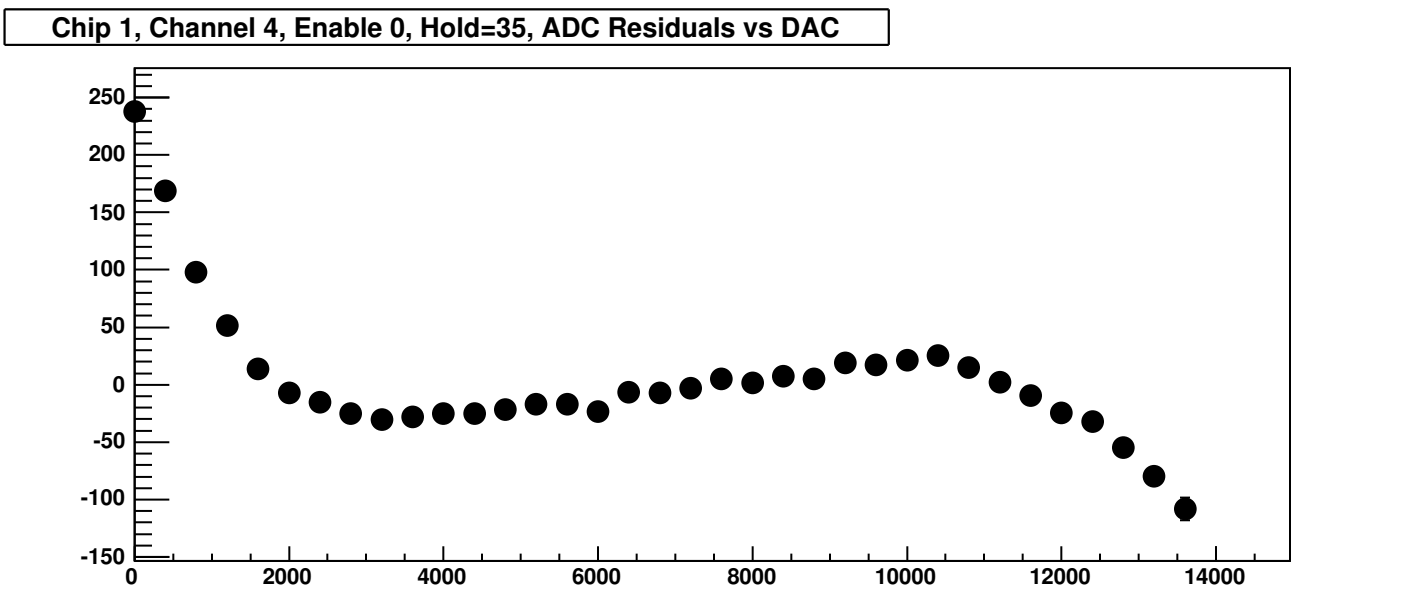

Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.262e+05 / 23
p0 -1304 ± 0.909
p1 0.2659 ± 0.0001972
2500
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

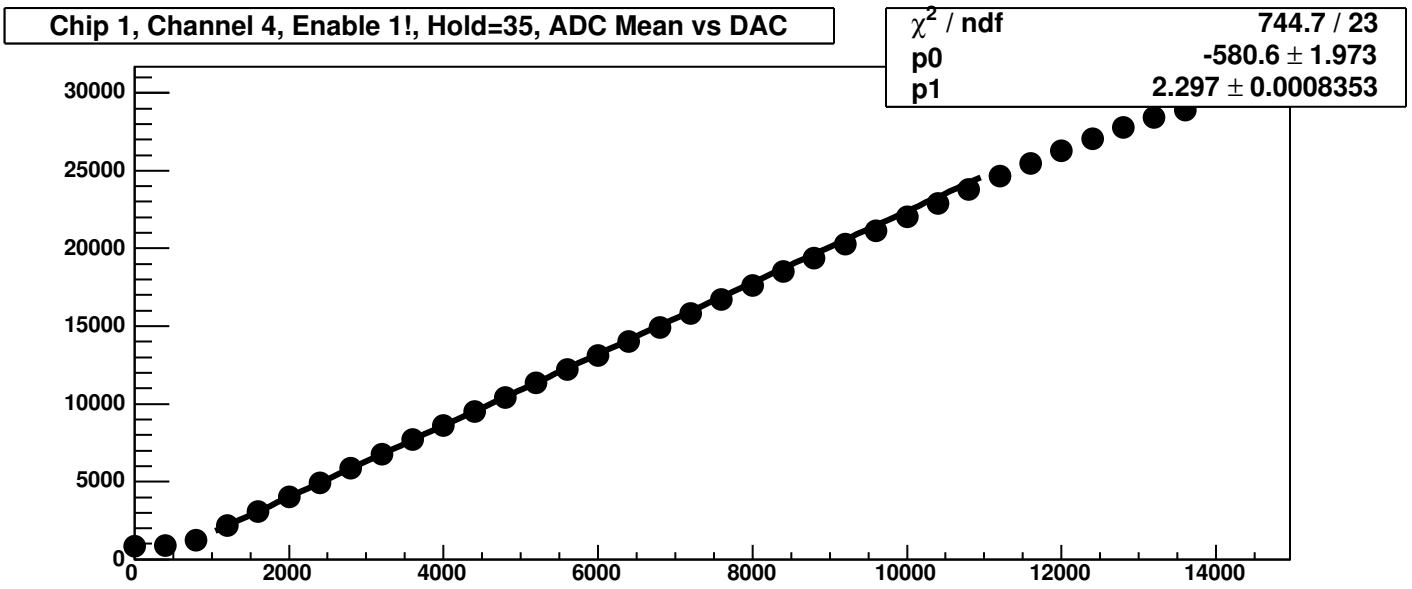

Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

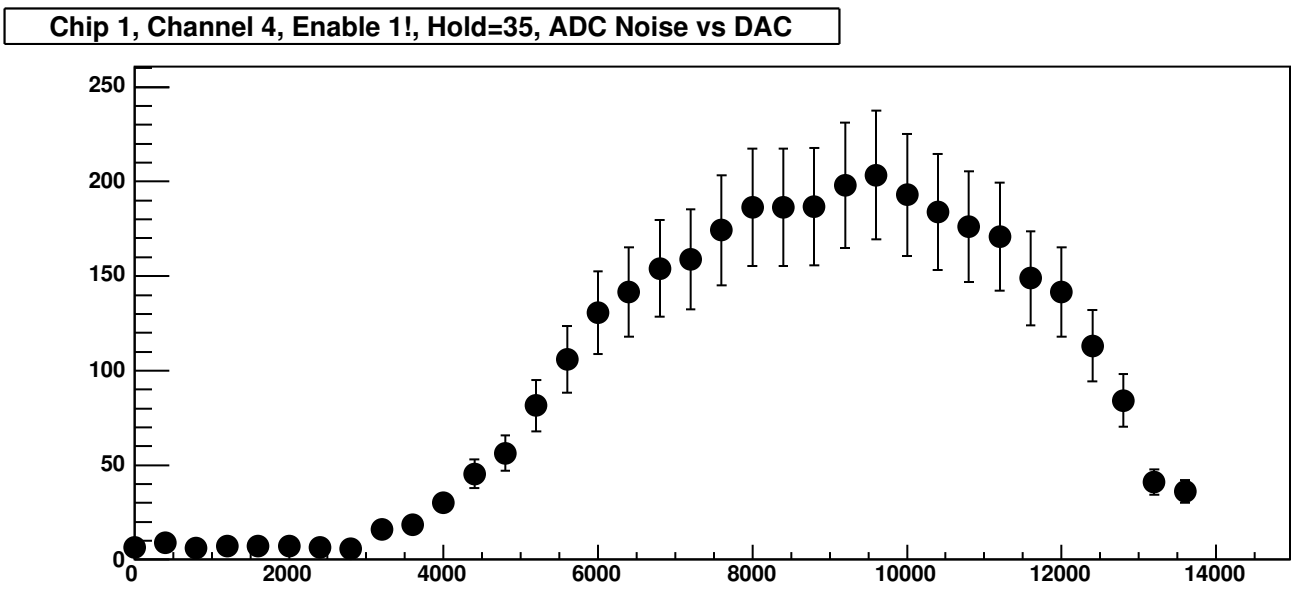

Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

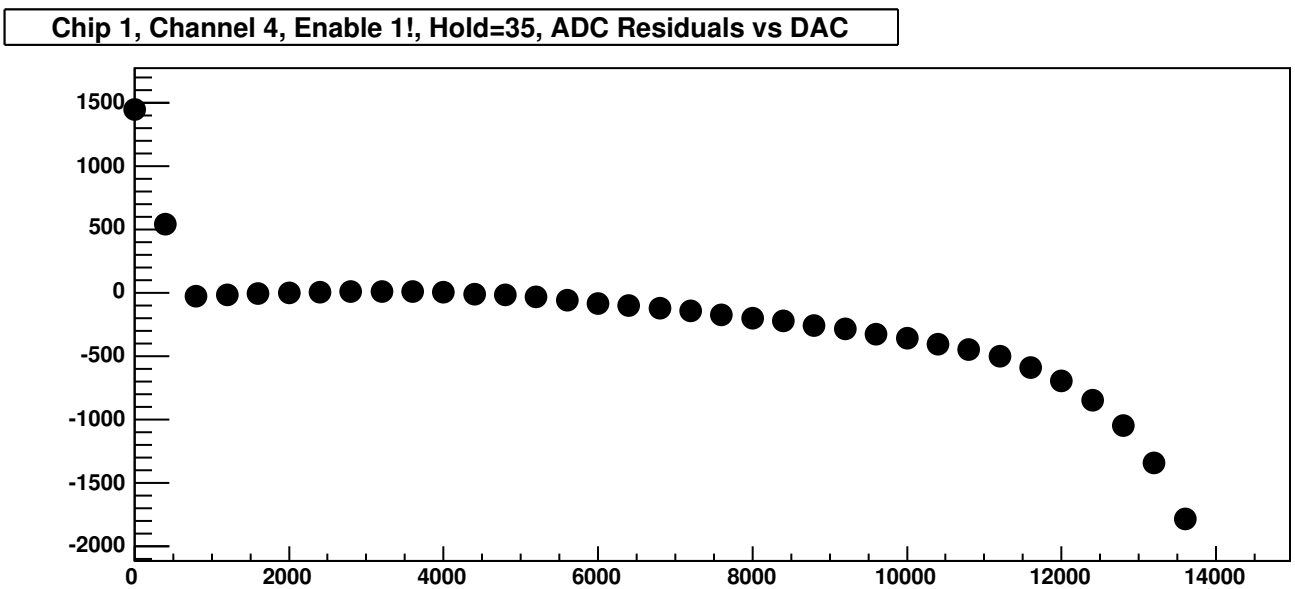

Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1447 / 23
p0 -827 ± 1.229
p1 0.1817 ± 0.0002138
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

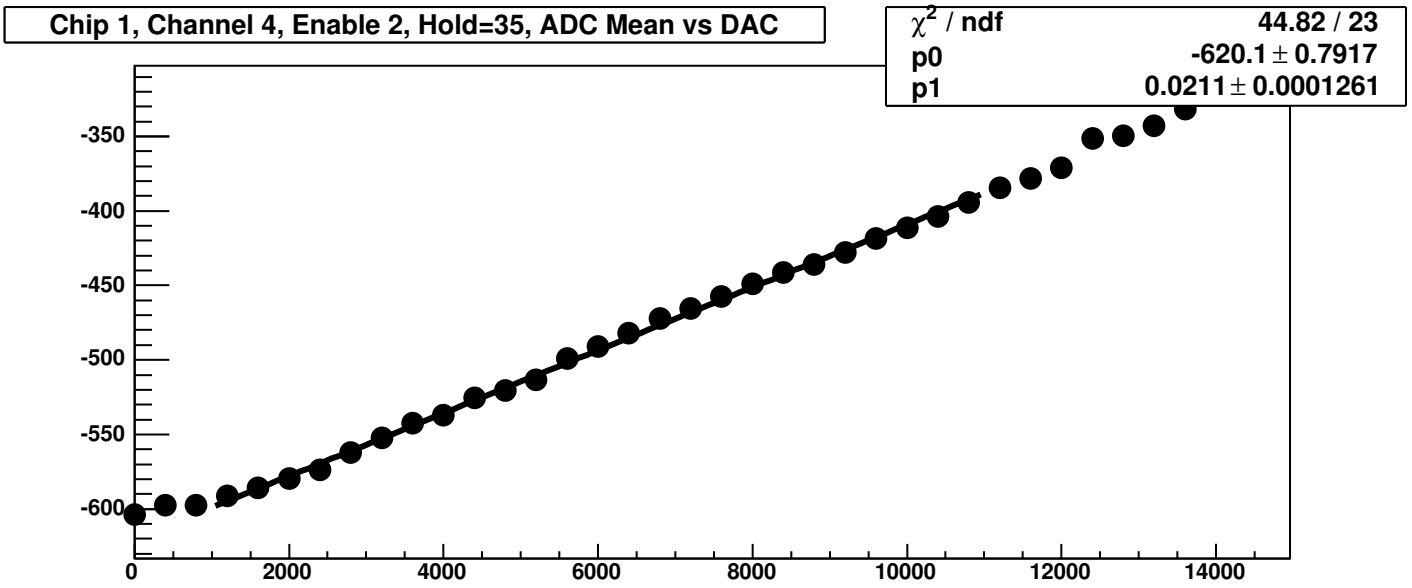

Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

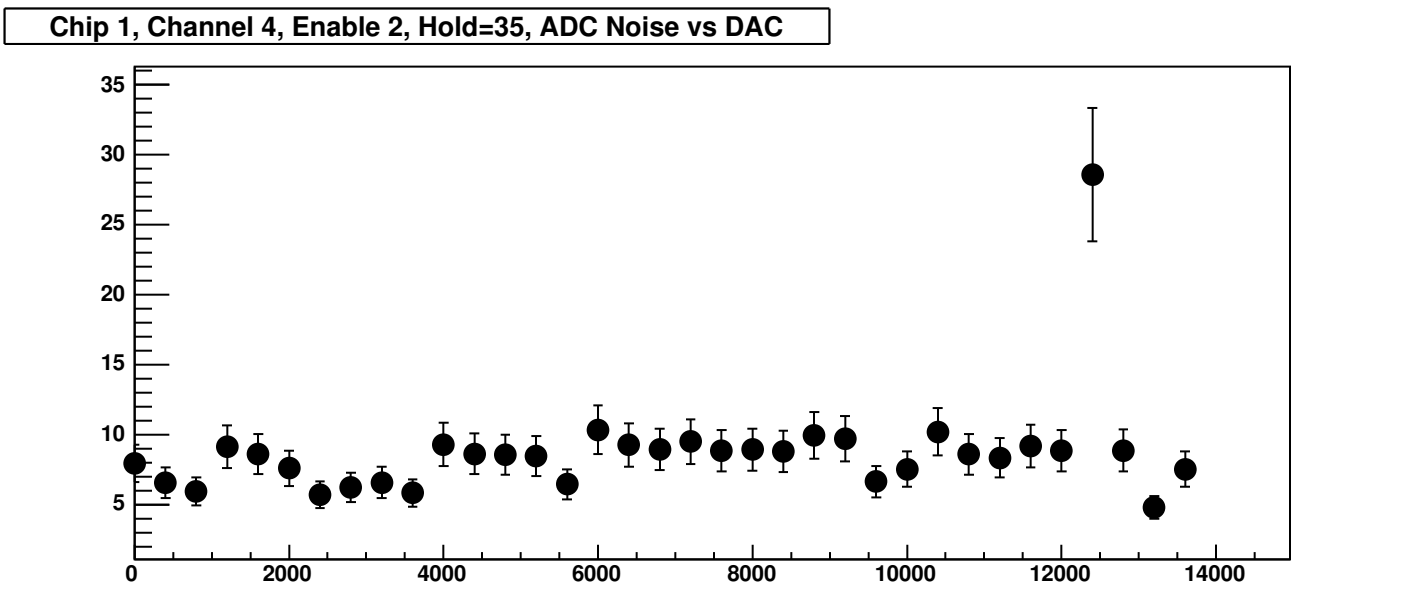

Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

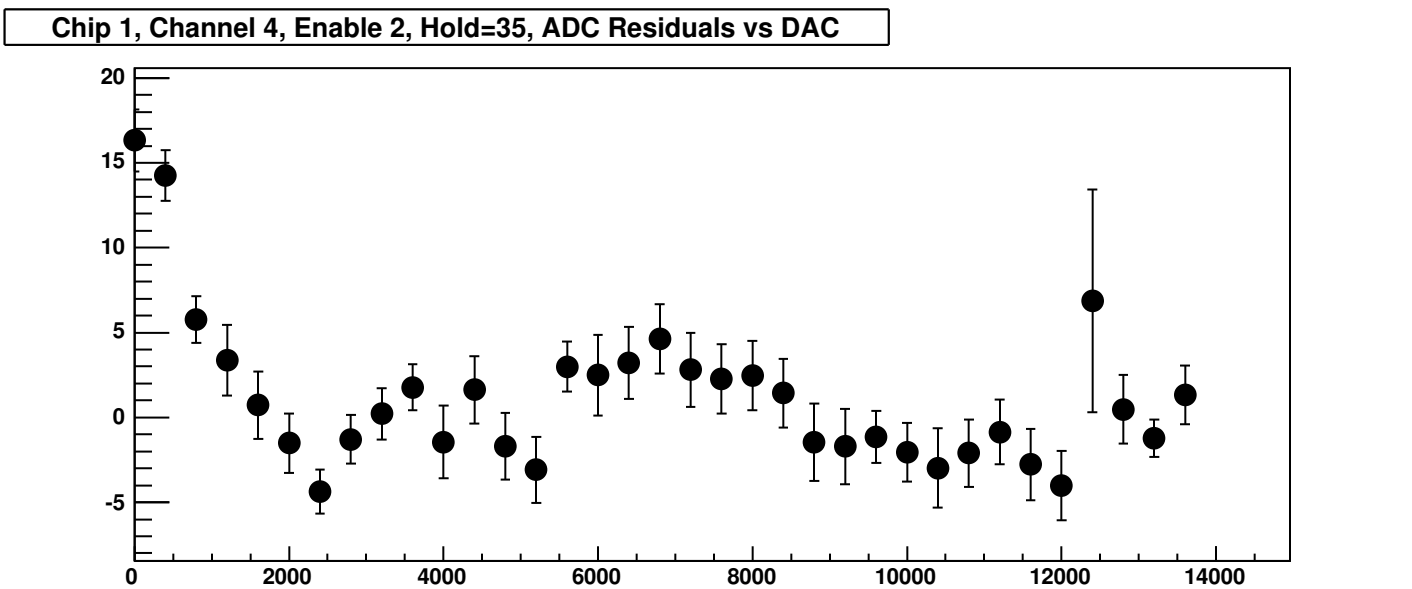

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 744.7 / 23
p0 -580.6 ± 1.973
p1 2.297 ± 0.0008353
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

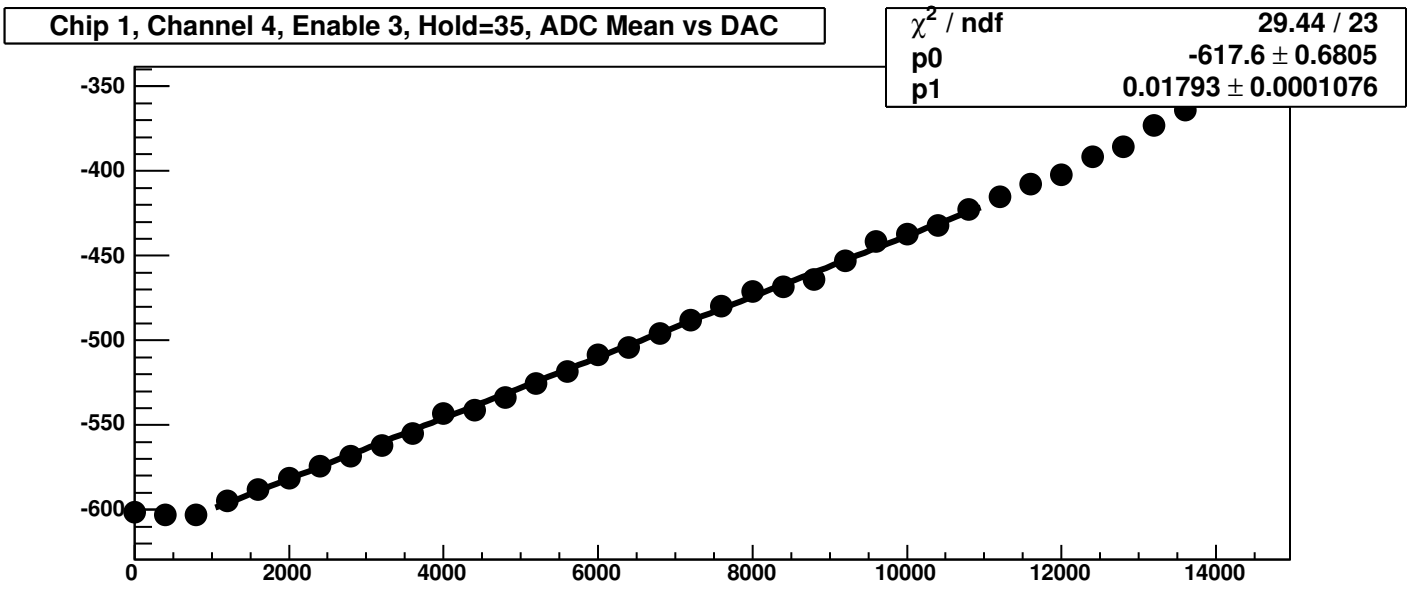

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

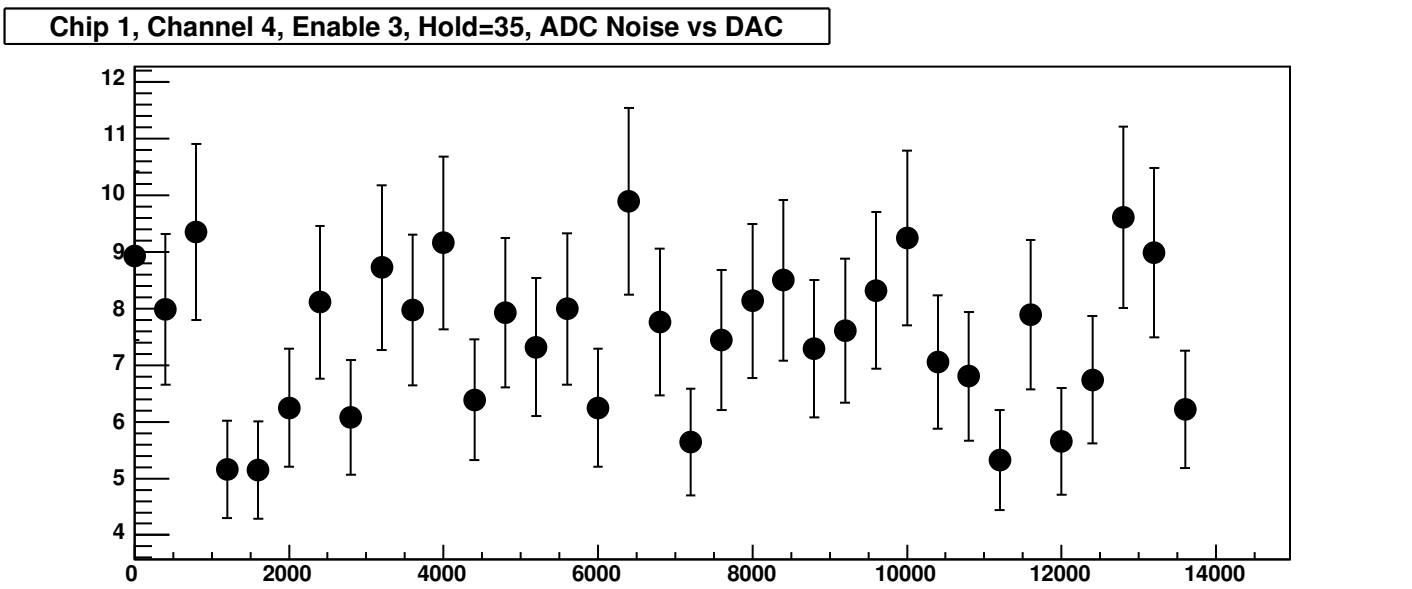

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

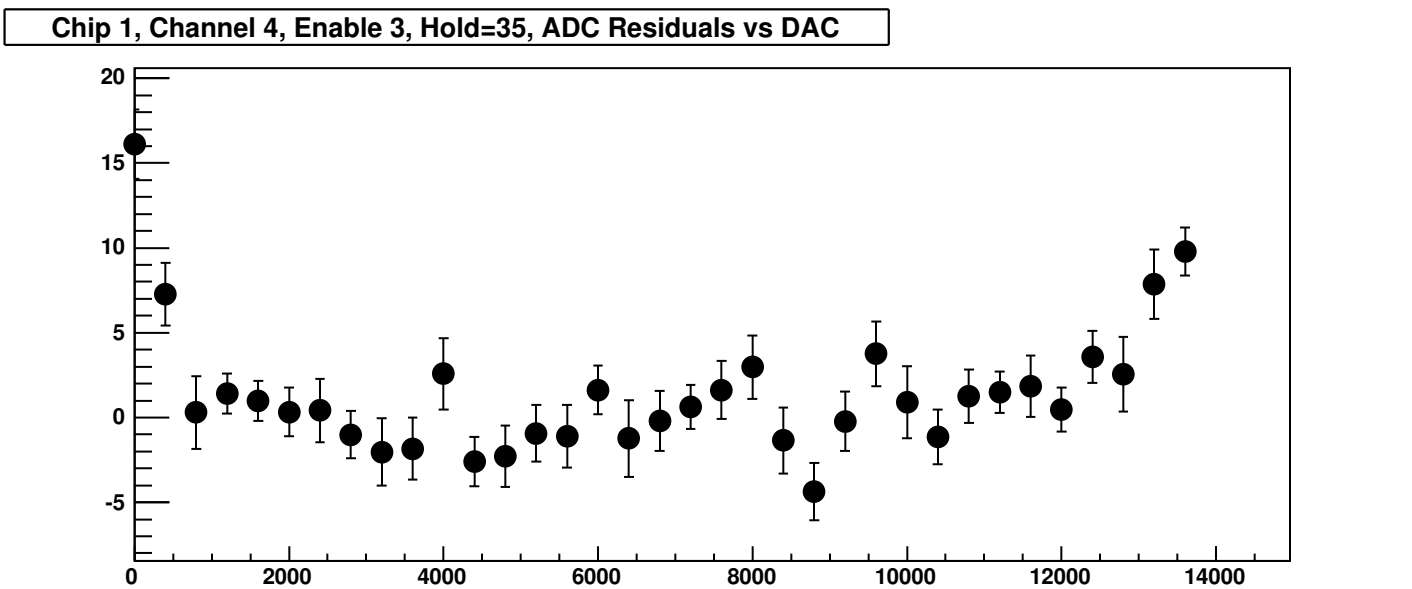

Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 44.82 / 23
p0 -620.1 ± 0.7917
p1 0.0211 ± 0.0001261
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

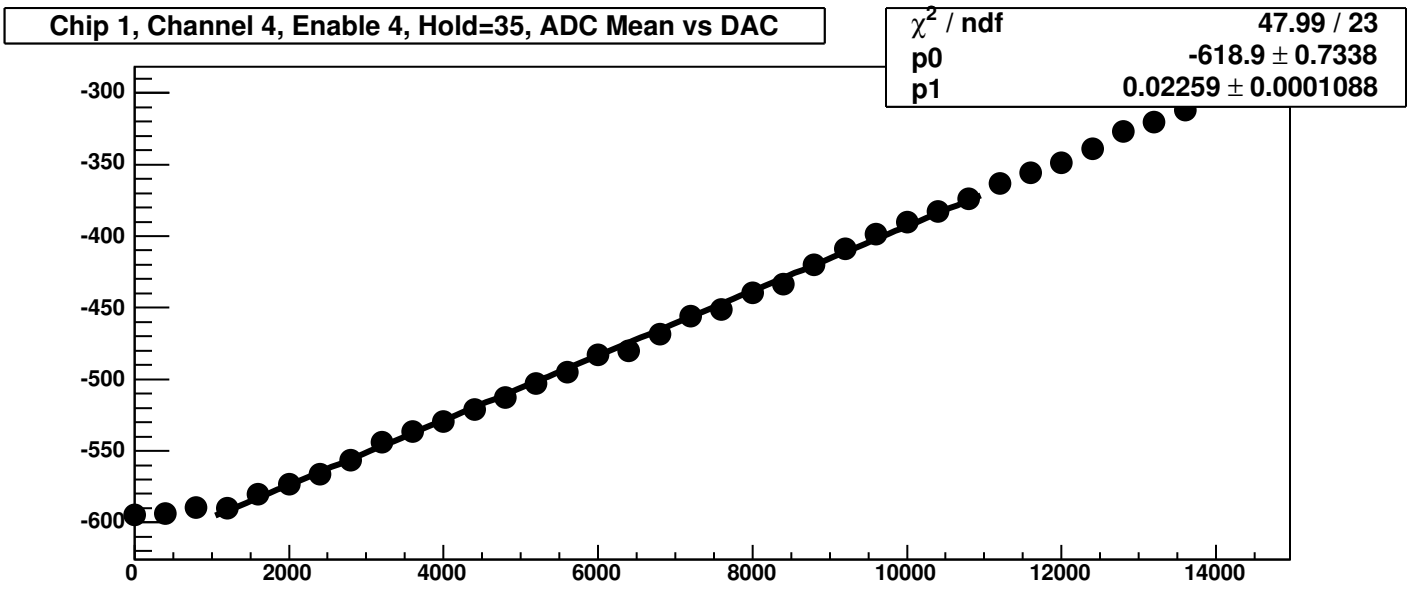

Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

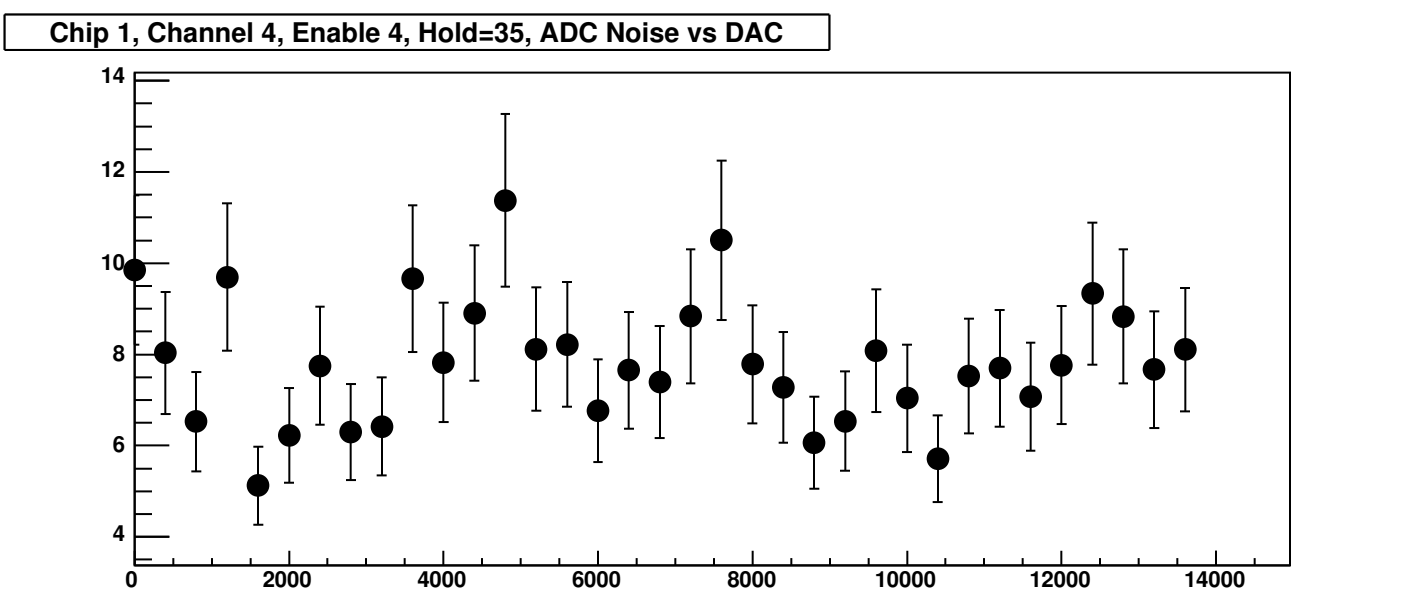

Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

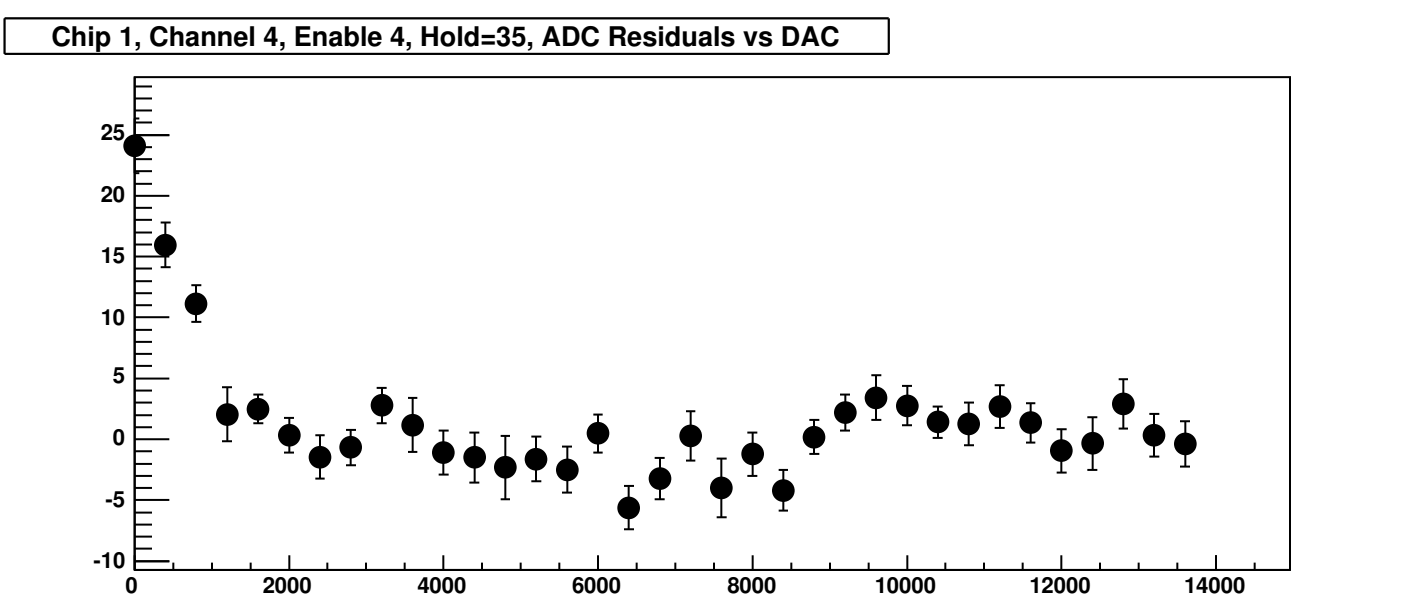

Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.44 / 23
p0 -617.6 ± 0.6805
p1 0.01793 ± 0.0001076
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

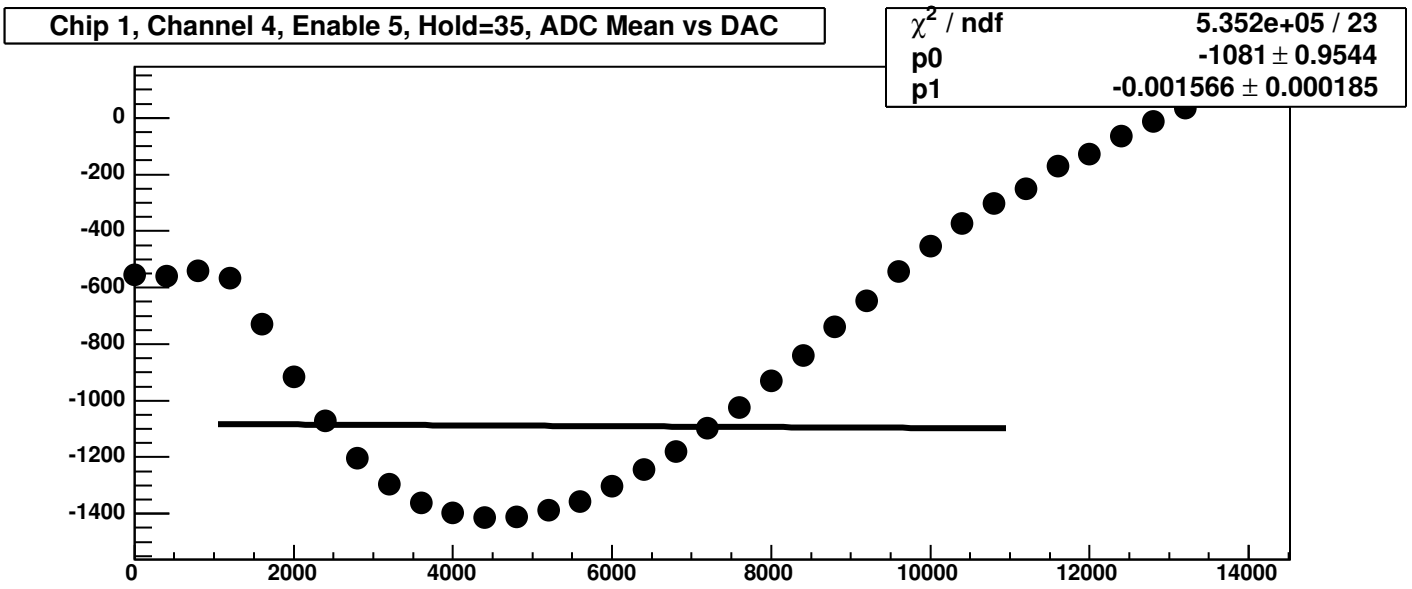

Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

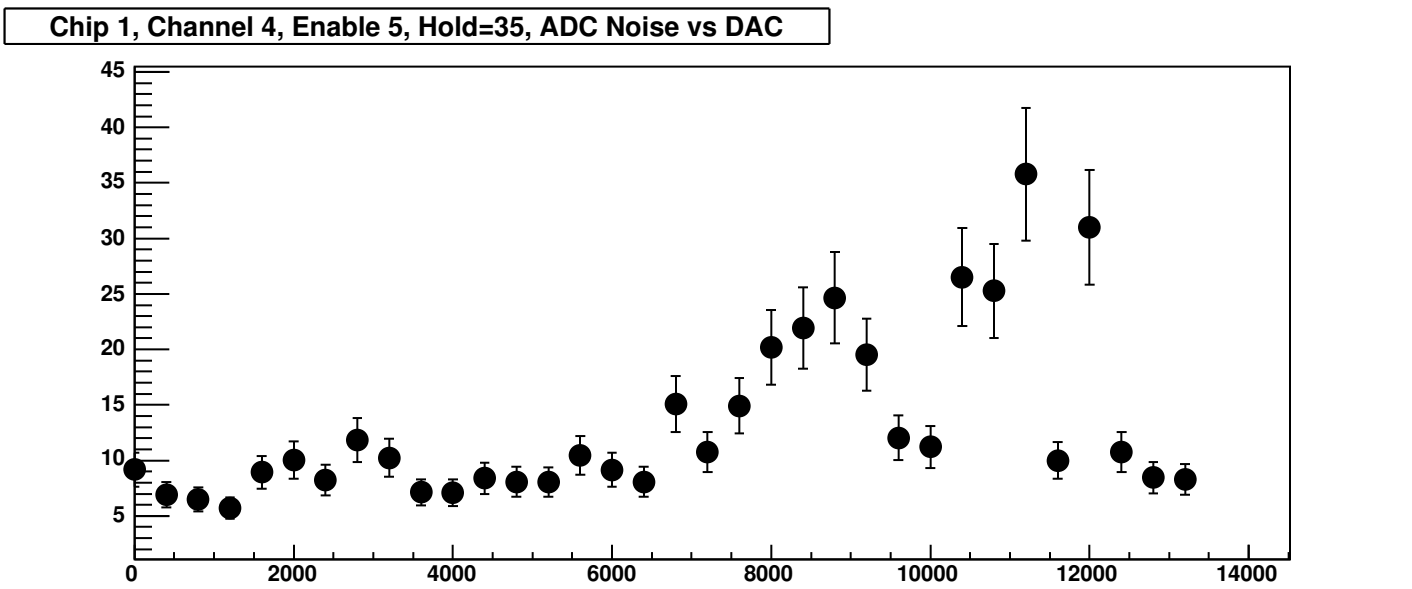

Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

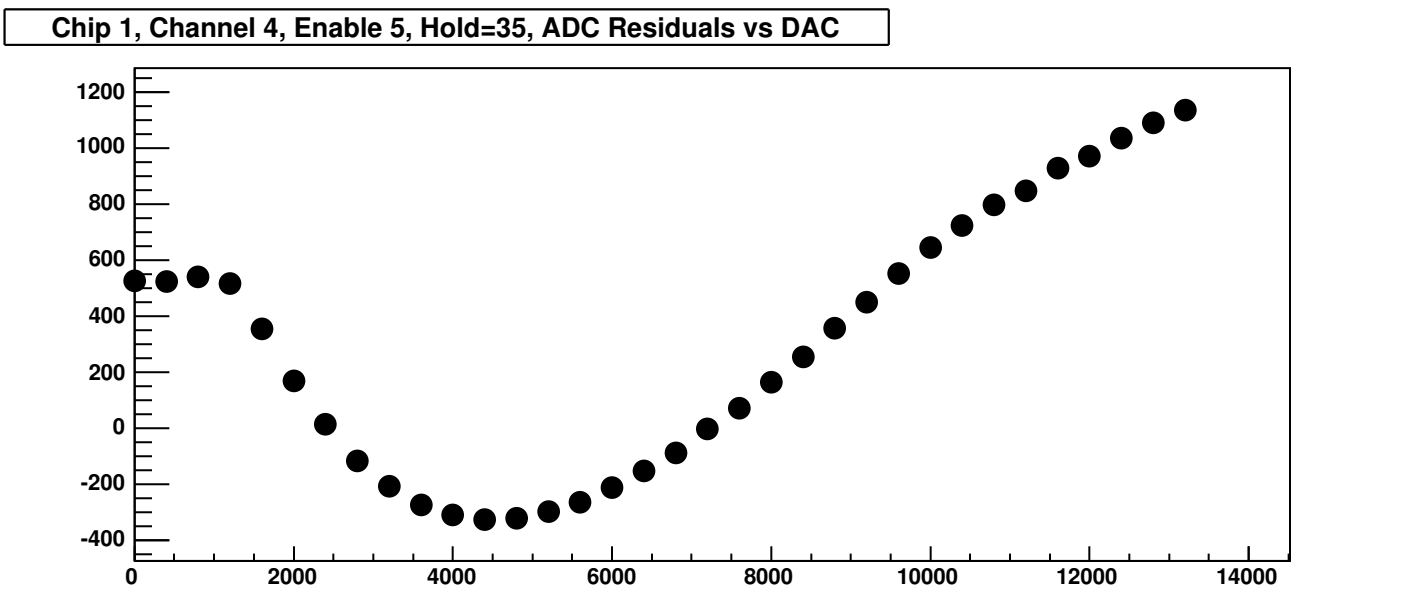

Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.99 / 23
p0 -618.9 ± 0.7338
p1 0.02259 ± 0.0001088
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

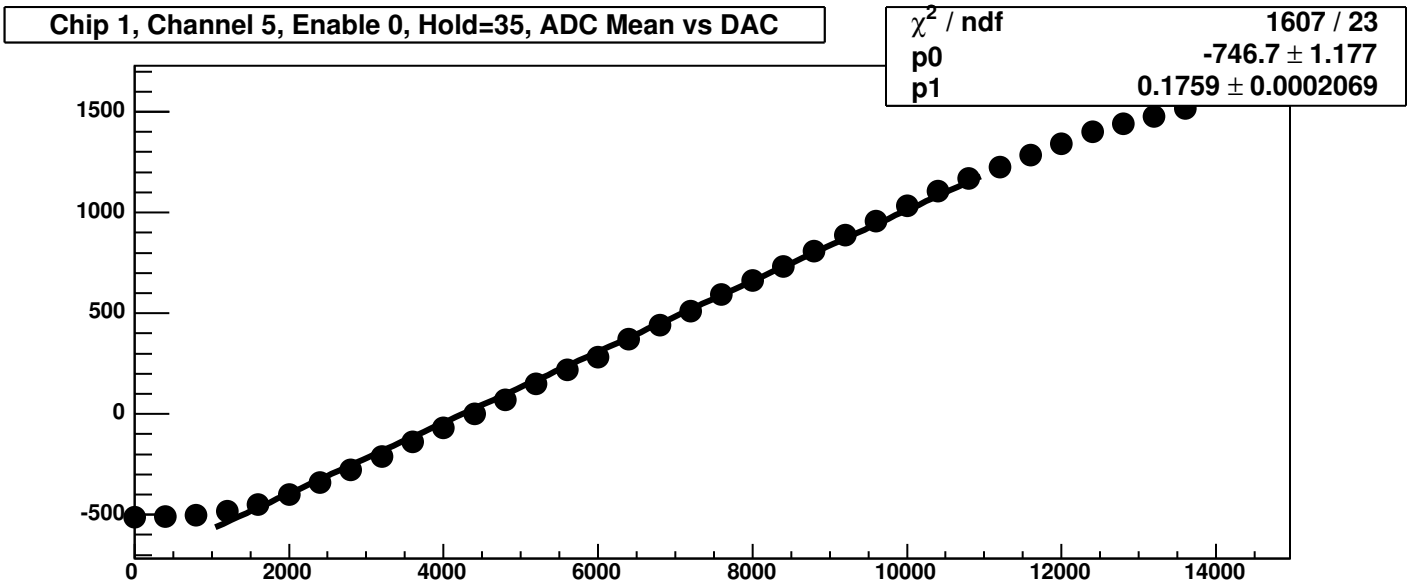

Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

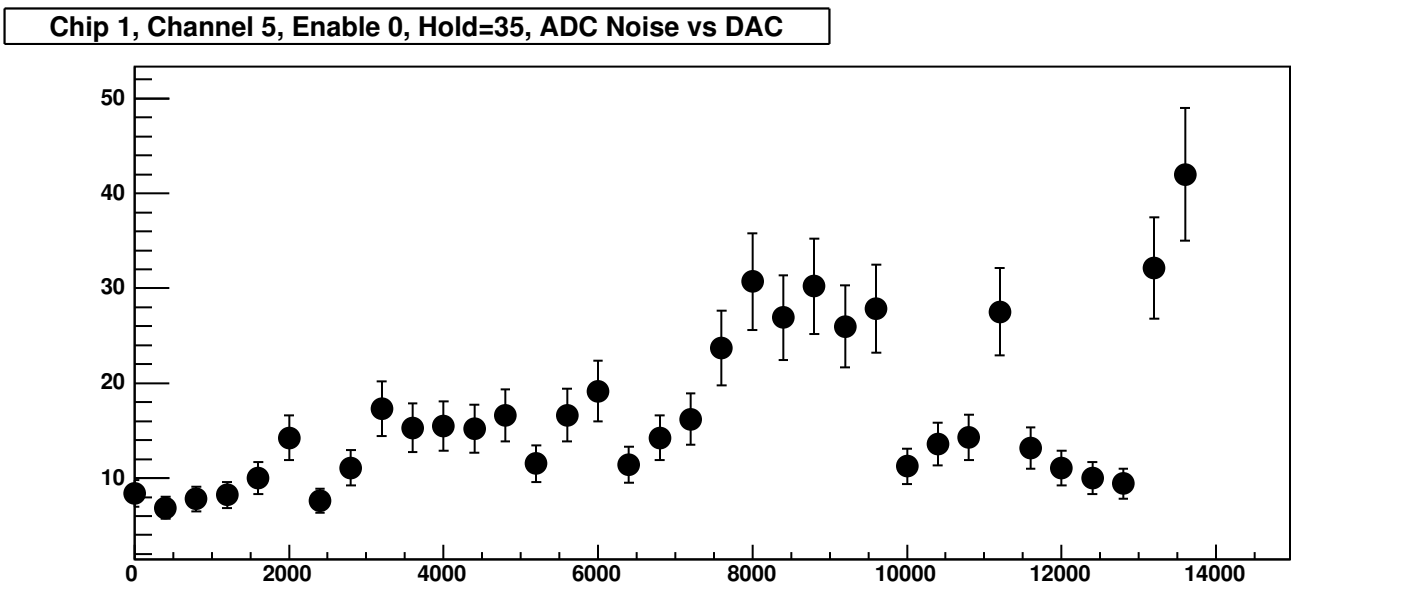

Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

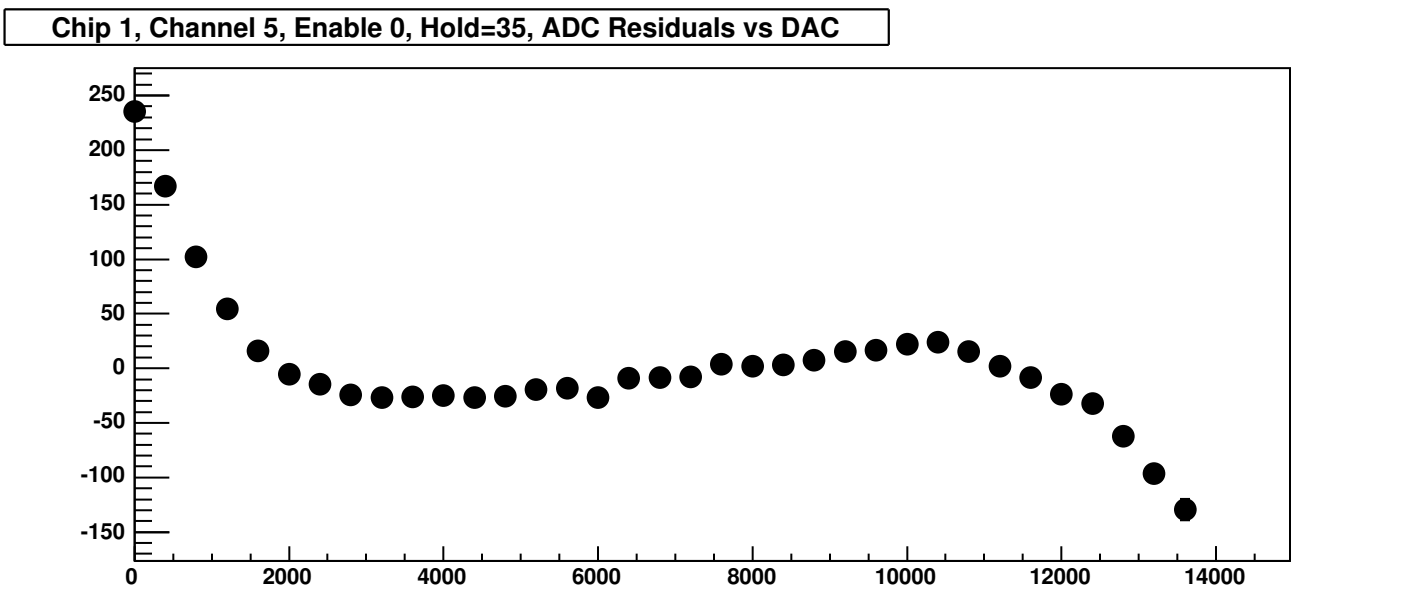

Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.352e+05 / 23
p0 -1081 ± 0.9544
p1 -0.001566 ± 0.000185
0
-200
-400
-600
-800
-1000
-1200
-1400
0
2000
4000
6000
8000
10000
12000
14000

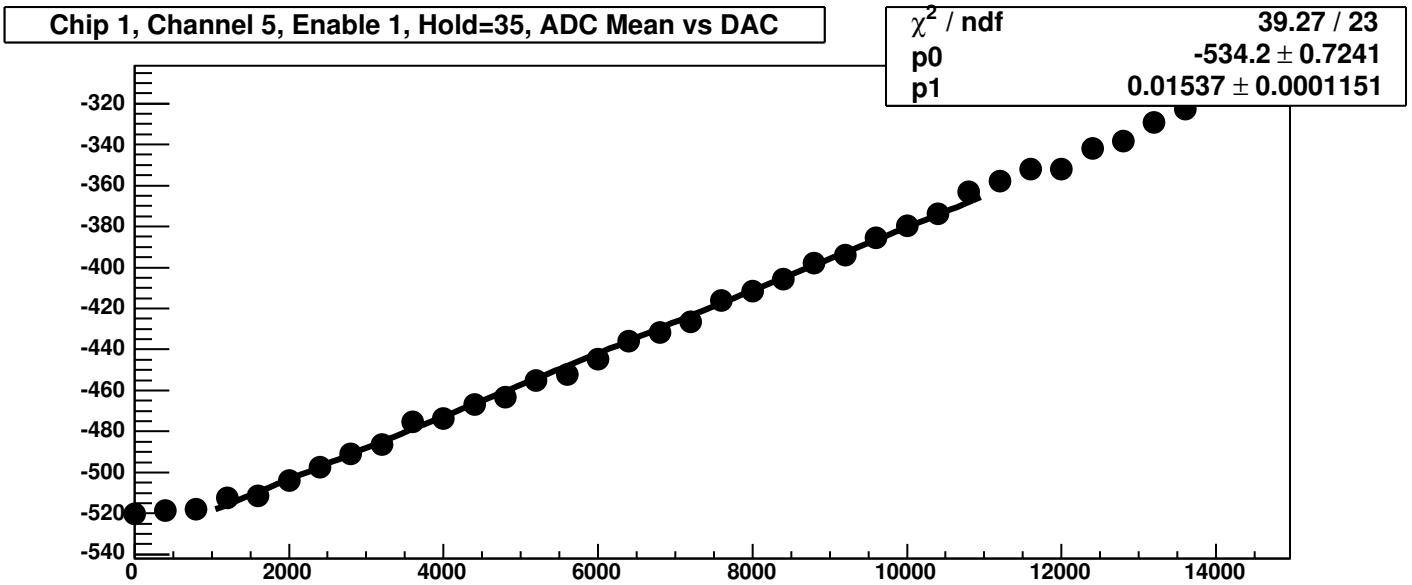

Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

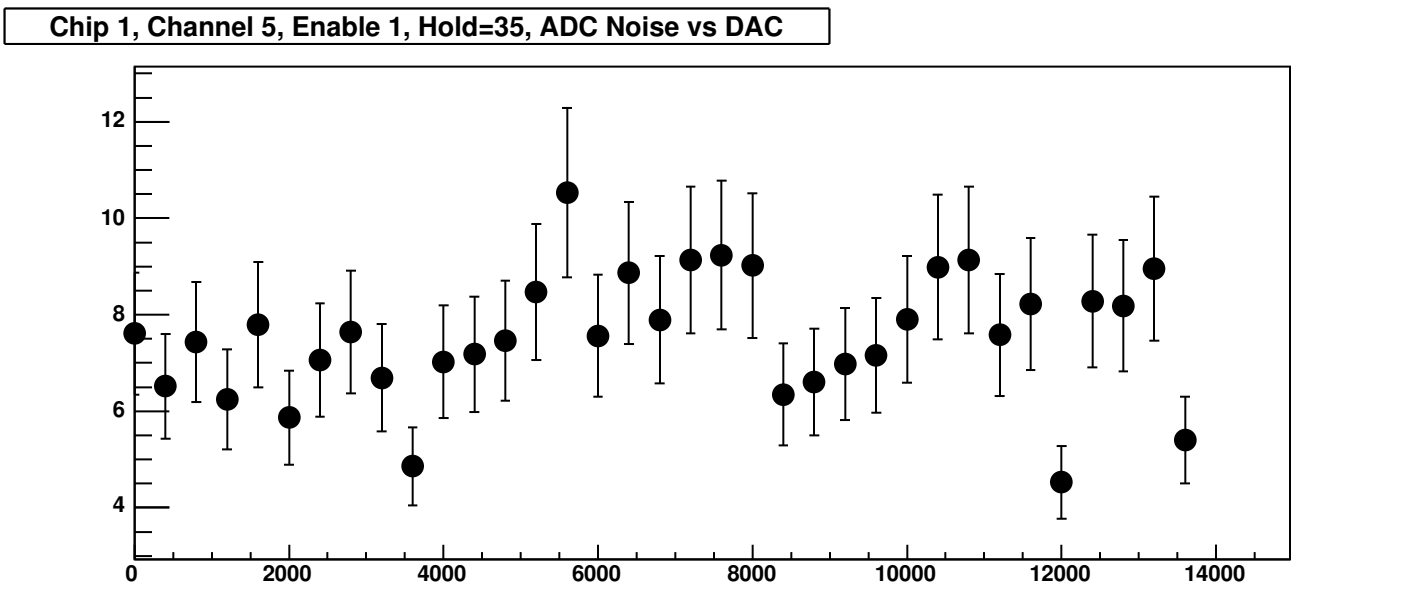

Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

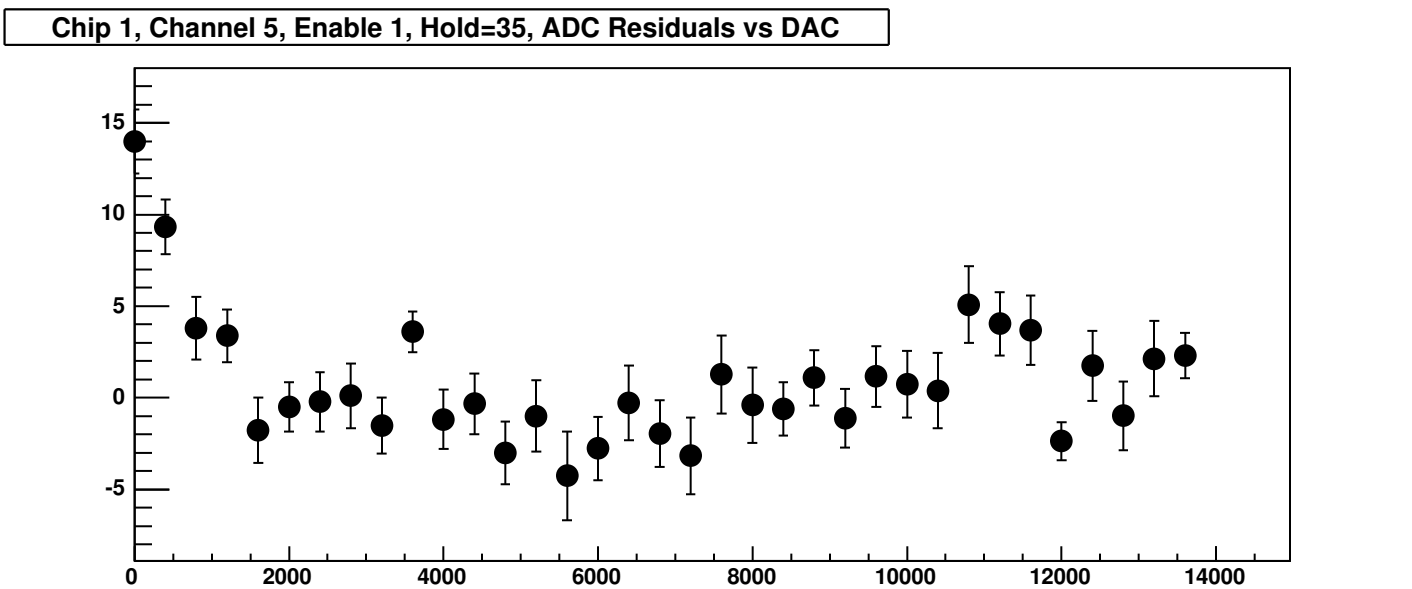

Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1607 / 23
p0 -746.7 ± 1.177
p1 0.1759 ± 0.0002069
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

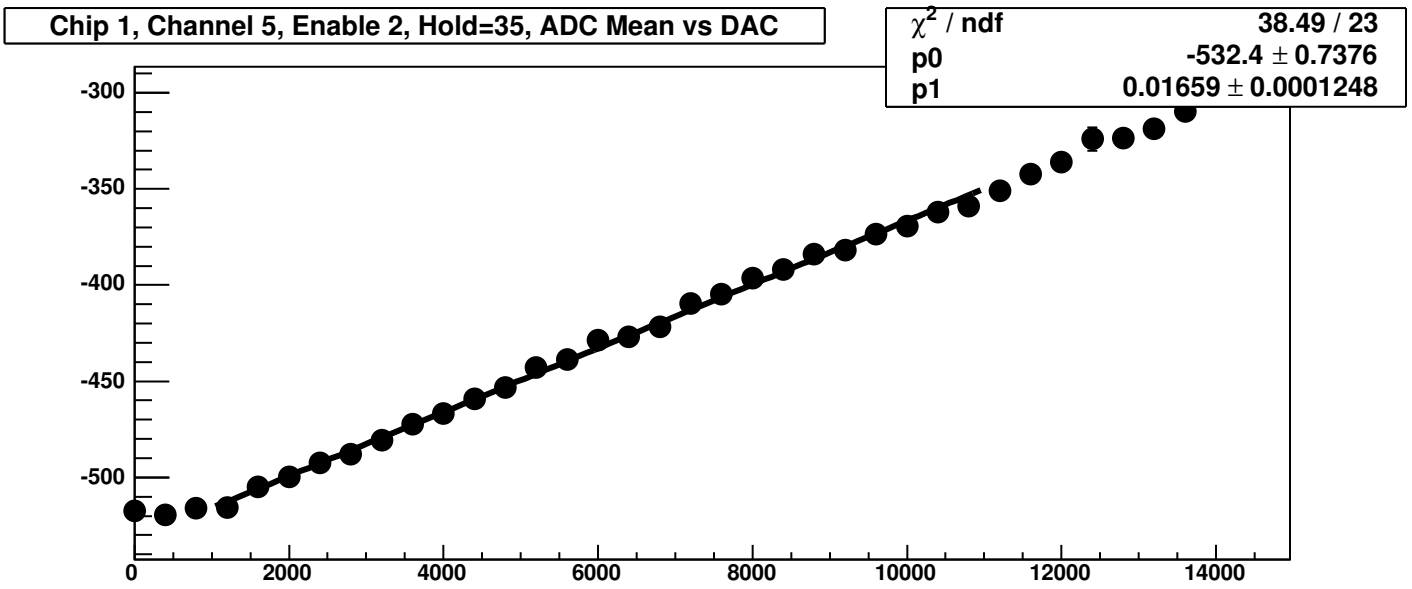

Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

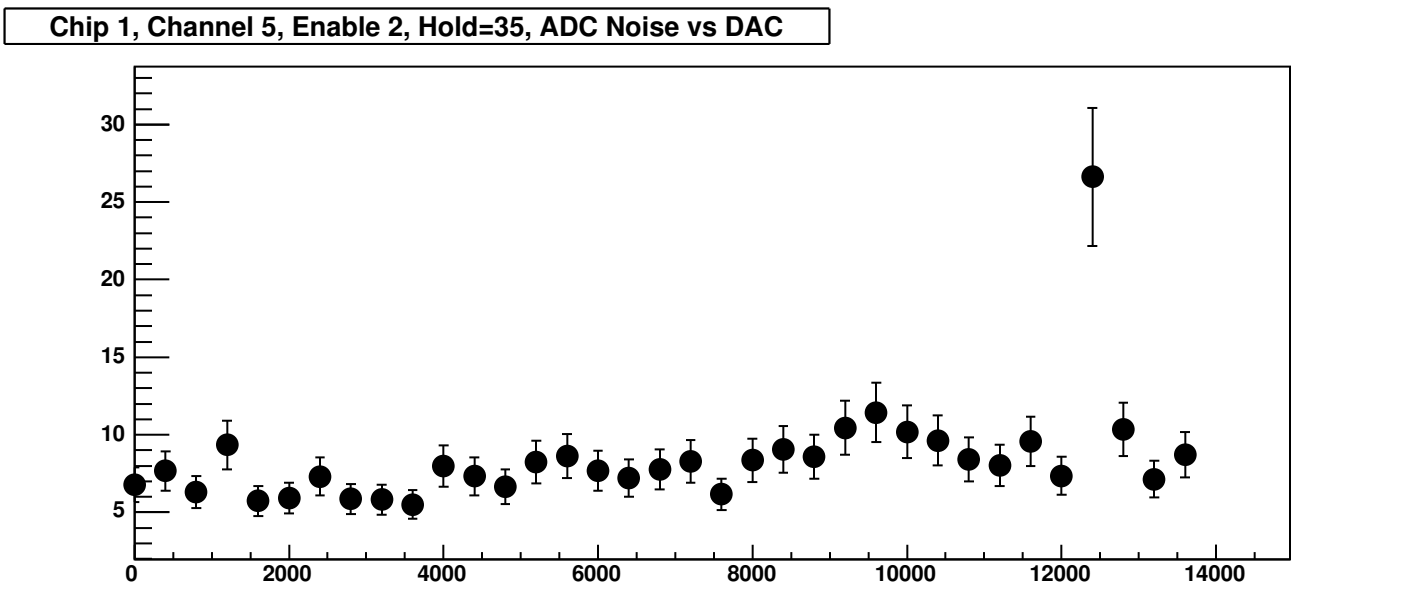

Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

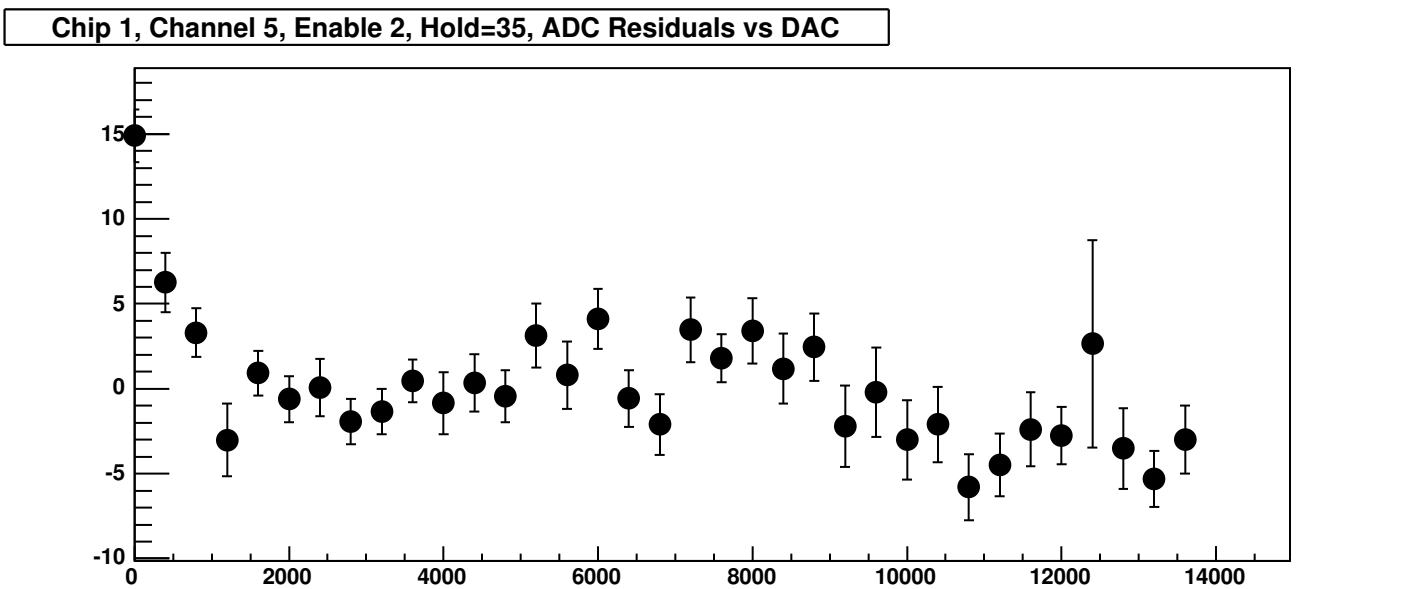

Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.27 / 23
p0 -534.2 ± 0.7241
p1 0.01537 ± 0.0001151
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

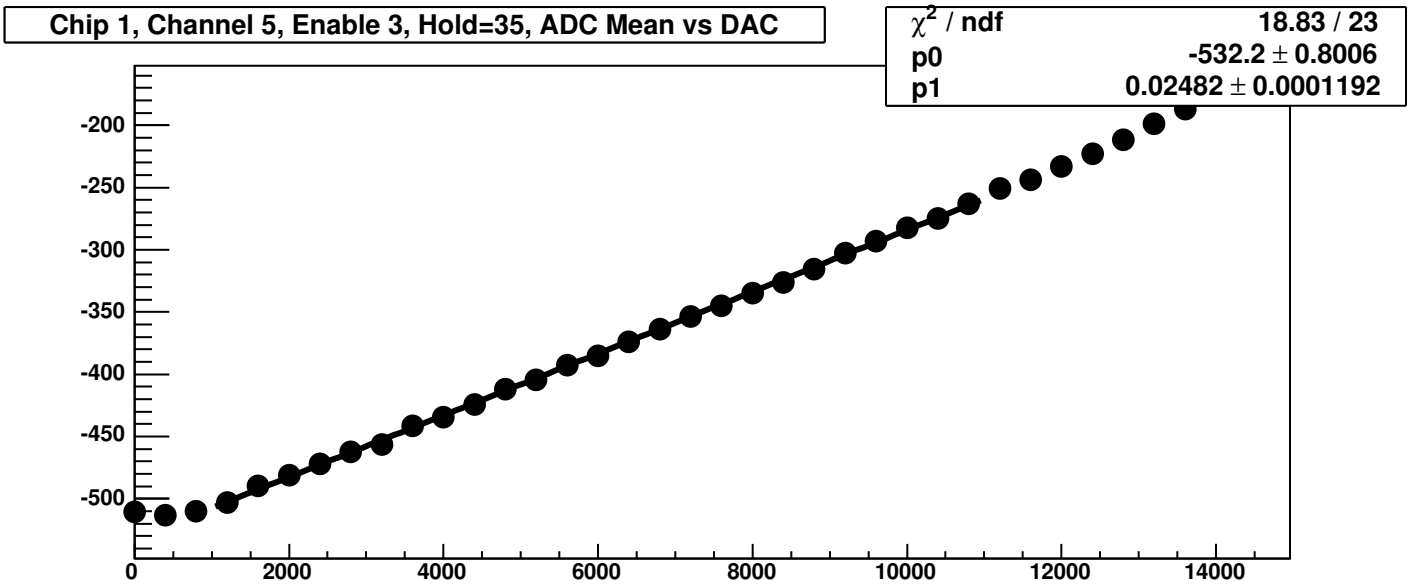

Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

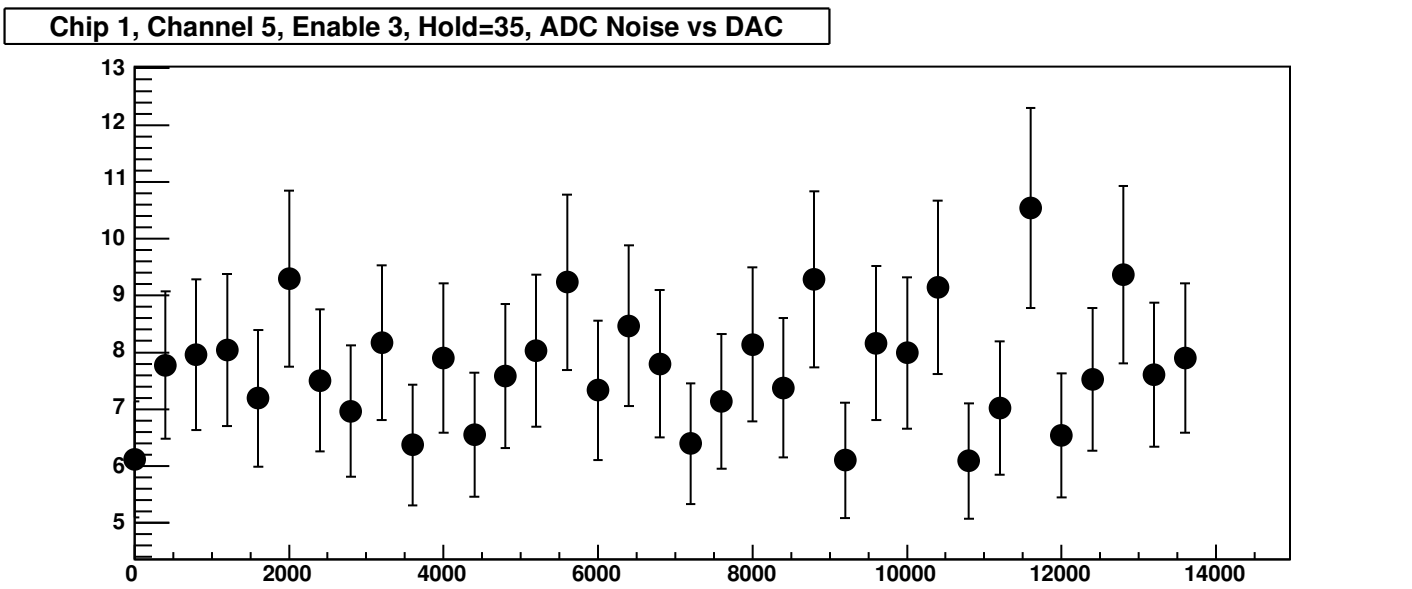

Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

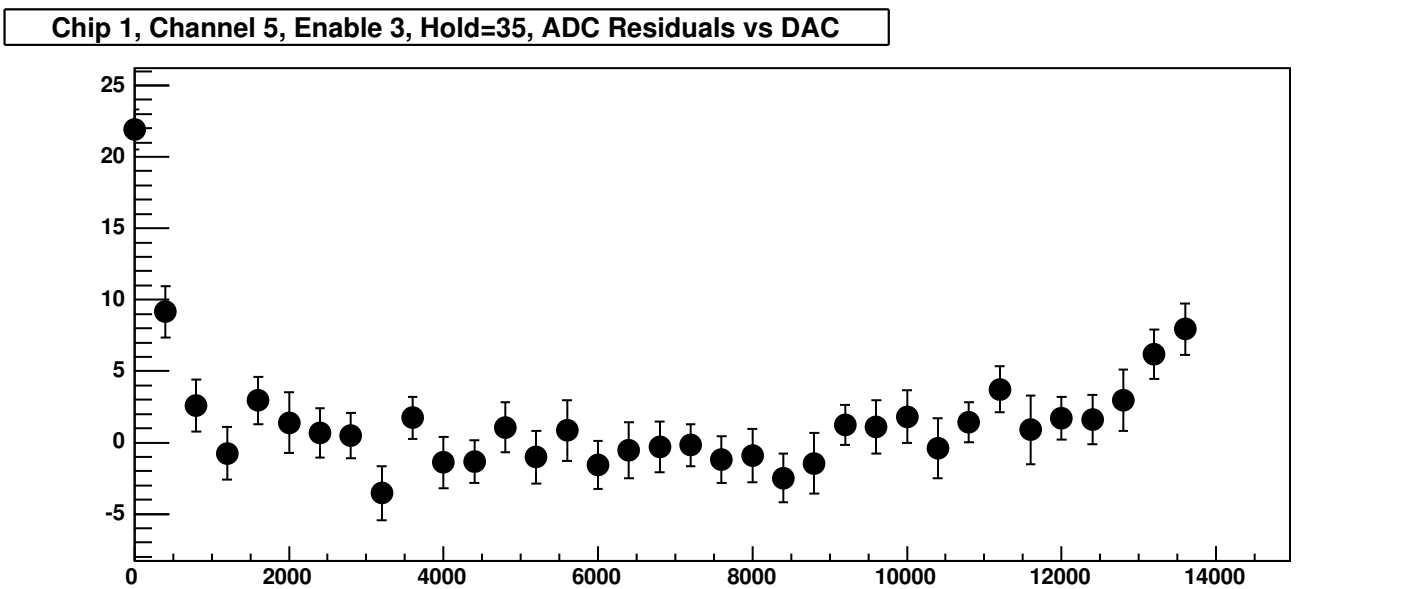

Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.49 / 23
p0 -532.4 ± 0.7376
p1 0.01659 ± 0.0001248
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

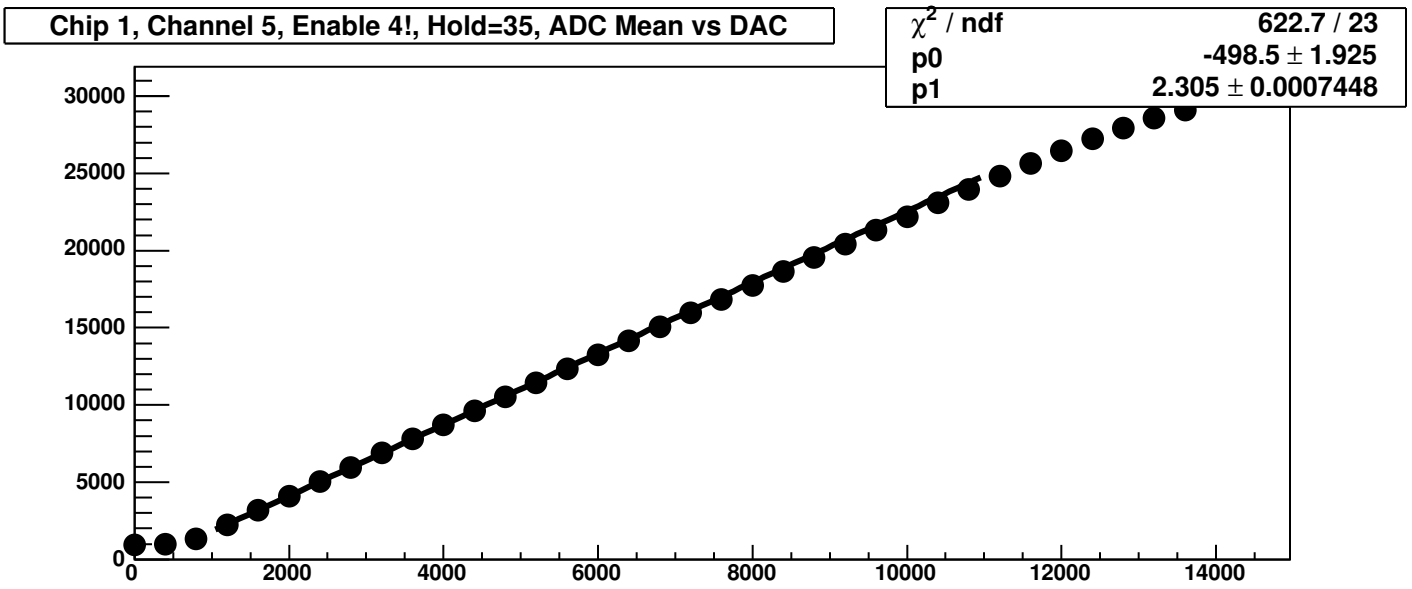

Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

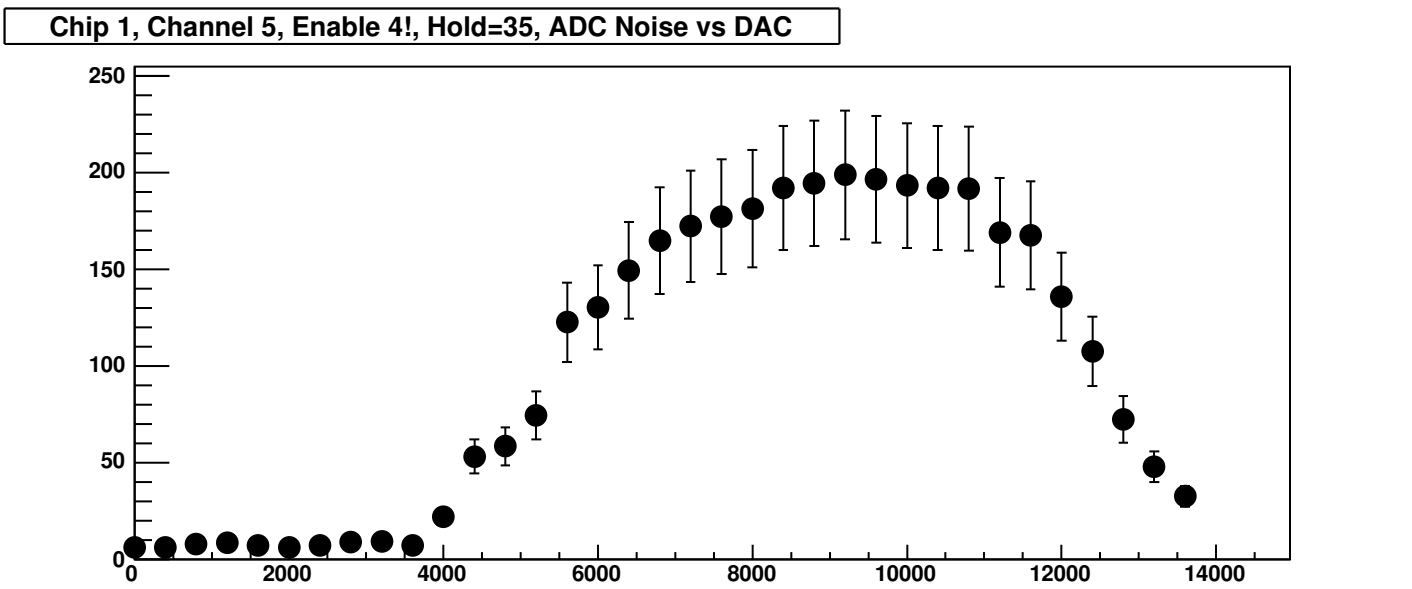

Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

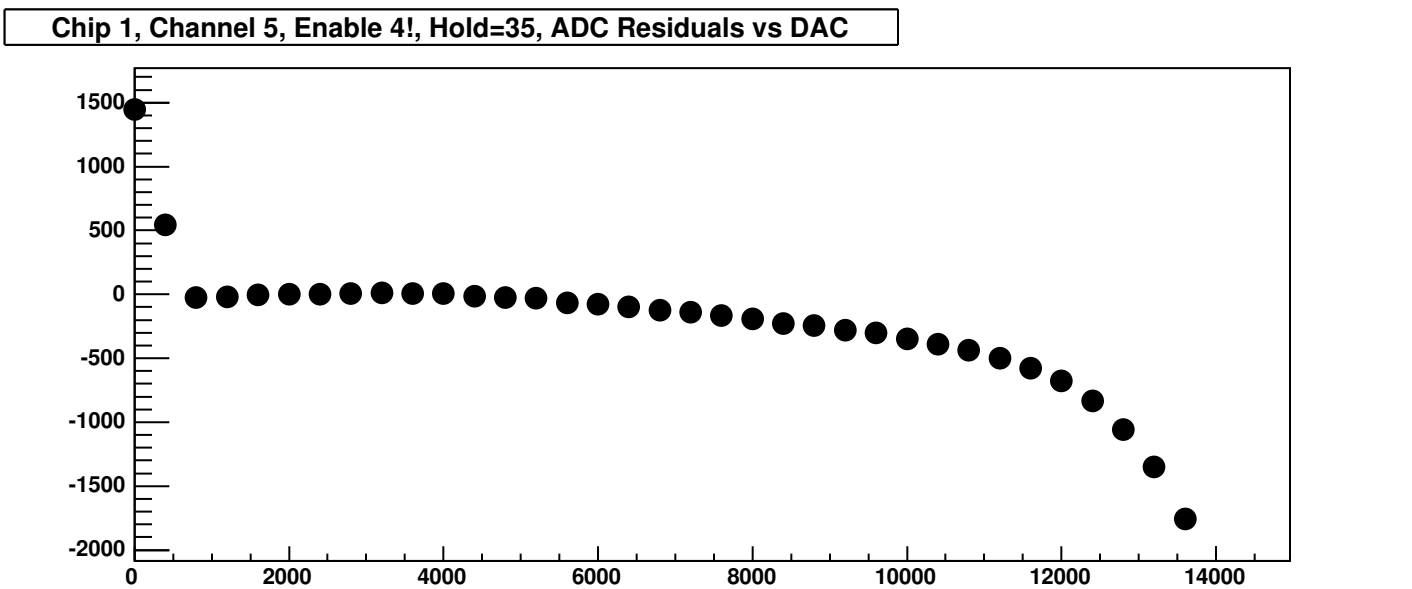

Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.83 / 23
p0 -532.2 ± 0.8006
p1 0.02482 ± 0.0001192
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

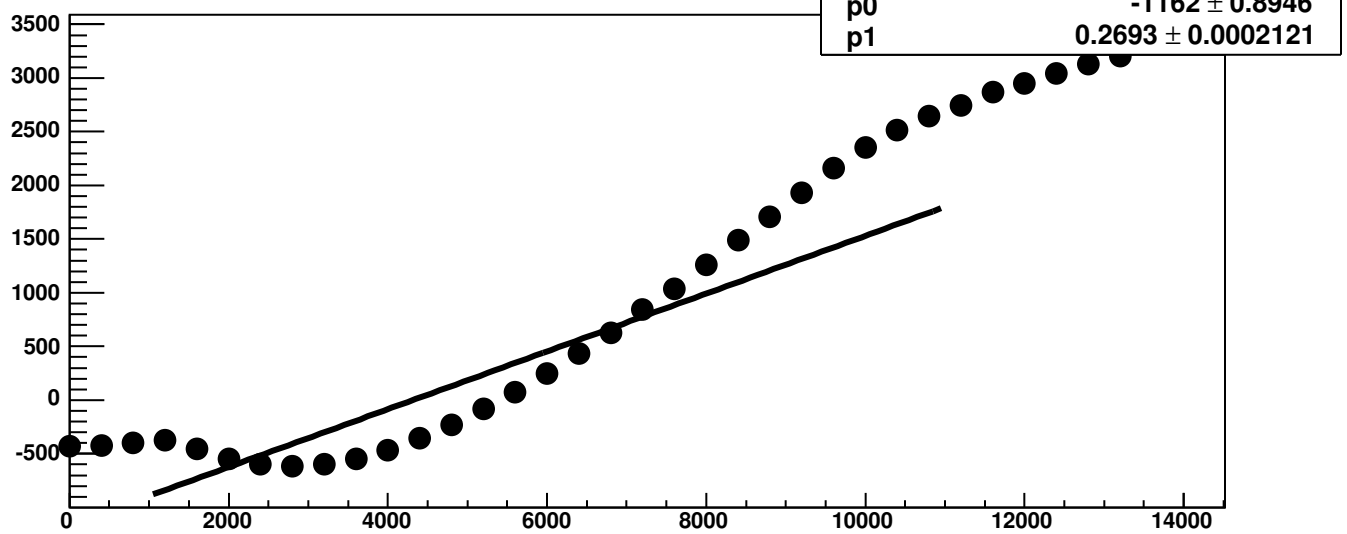

Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 622.7 / 23
p0 -498.5 ± 1.925
p1 2.305 ± 0.0007448
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

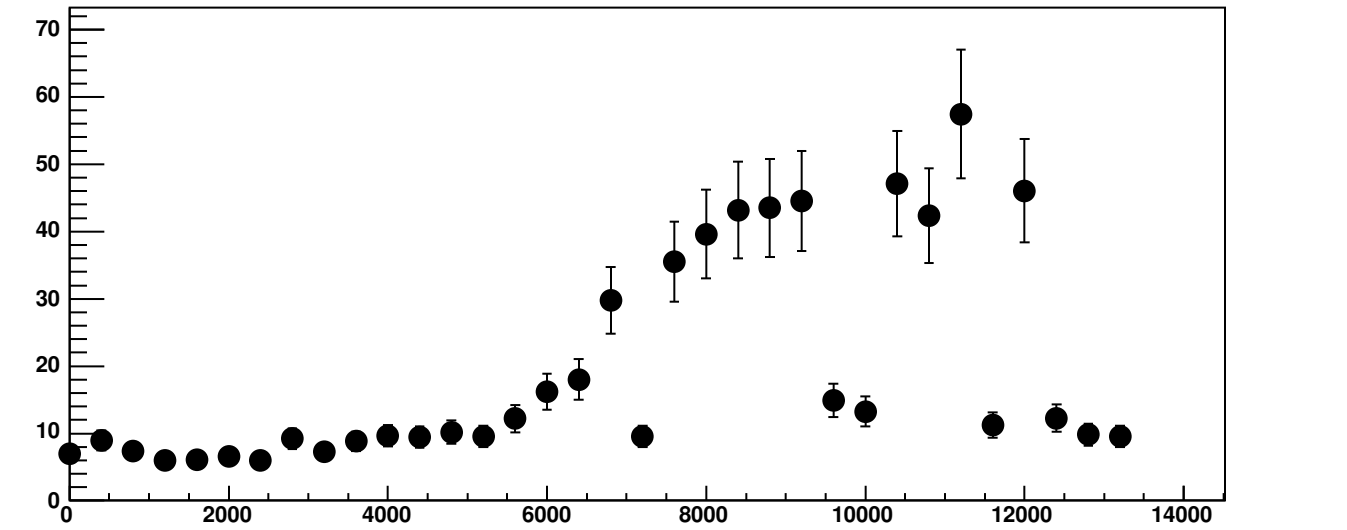

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

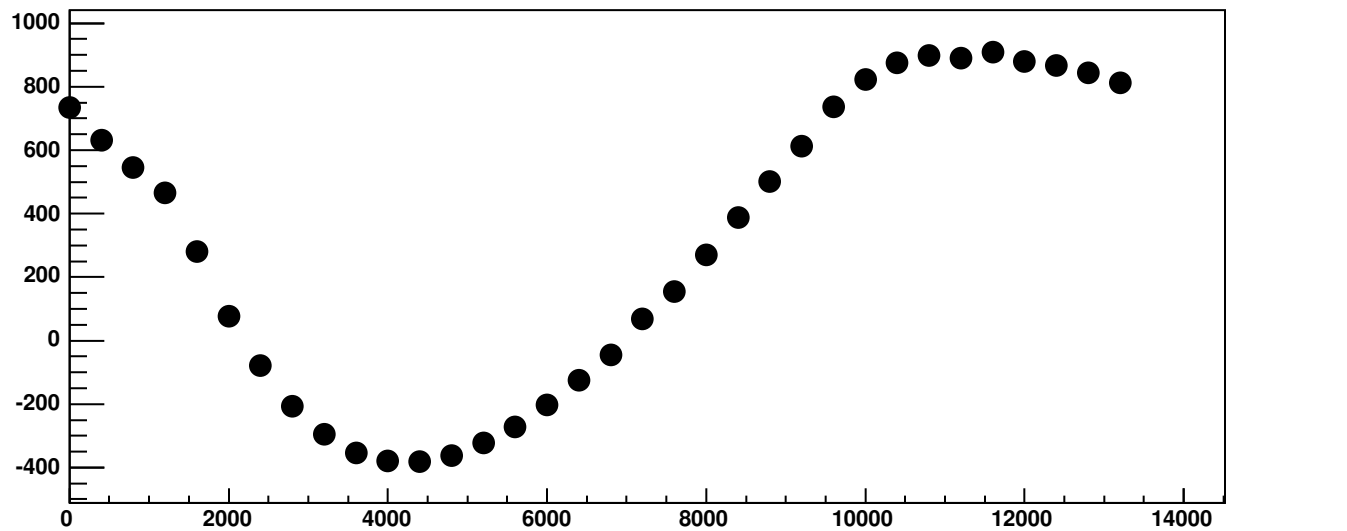
Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

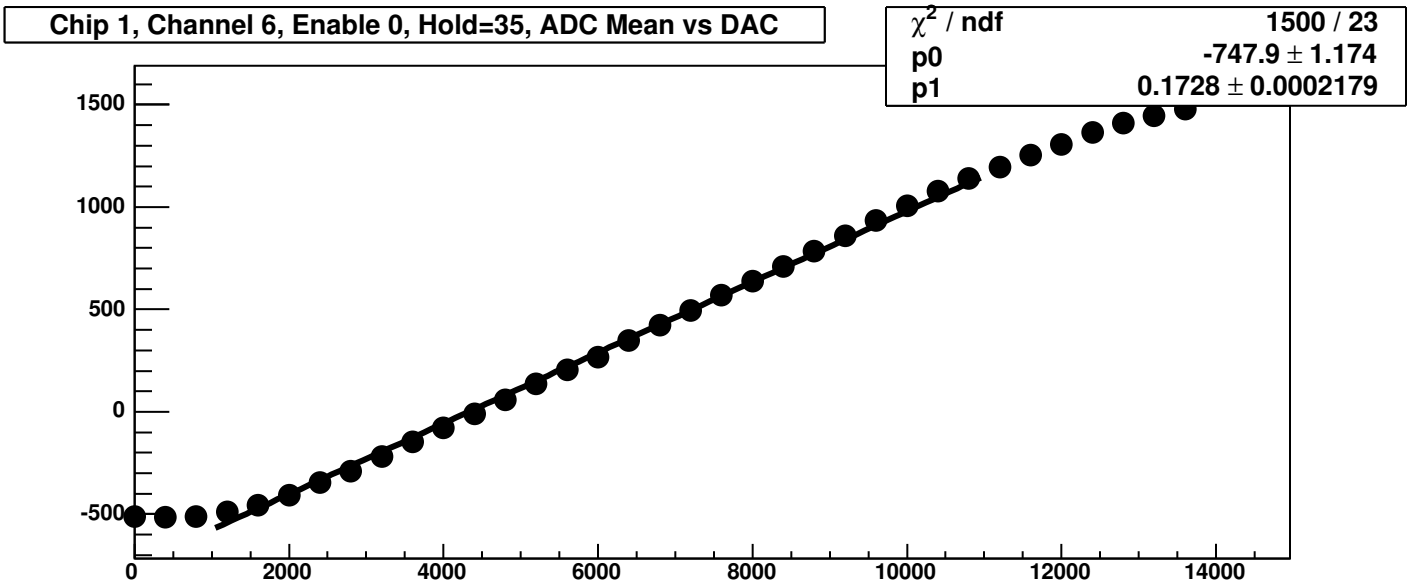


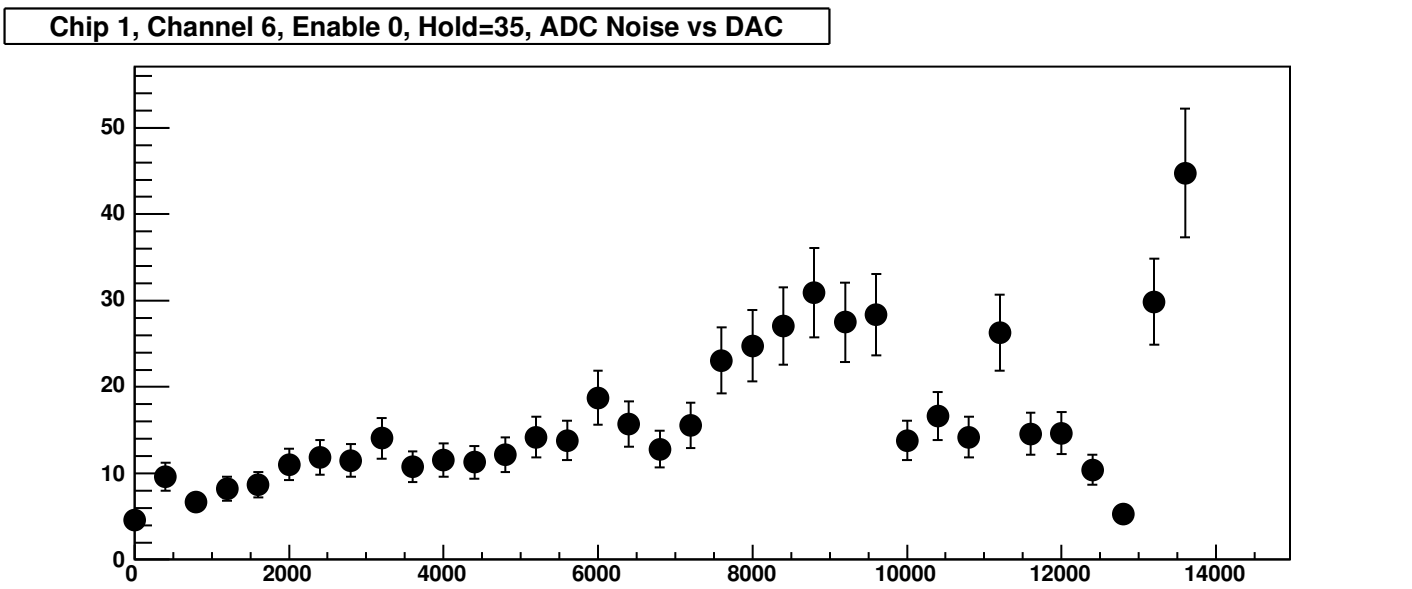
Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

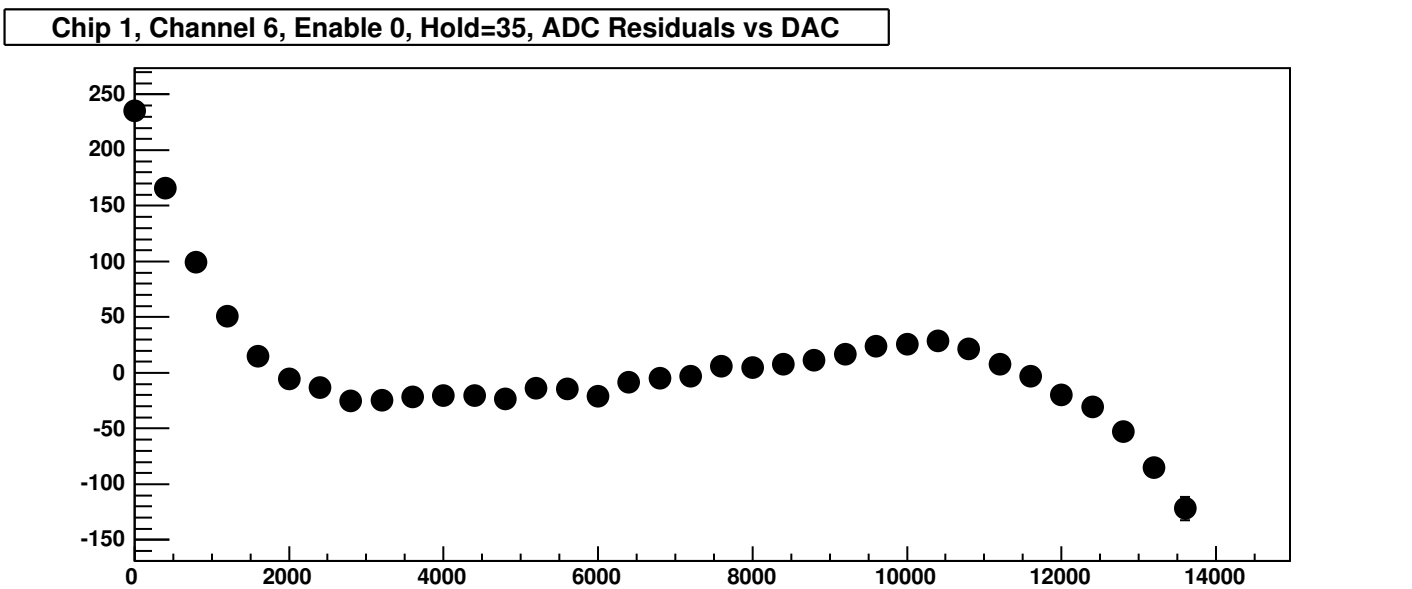


Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

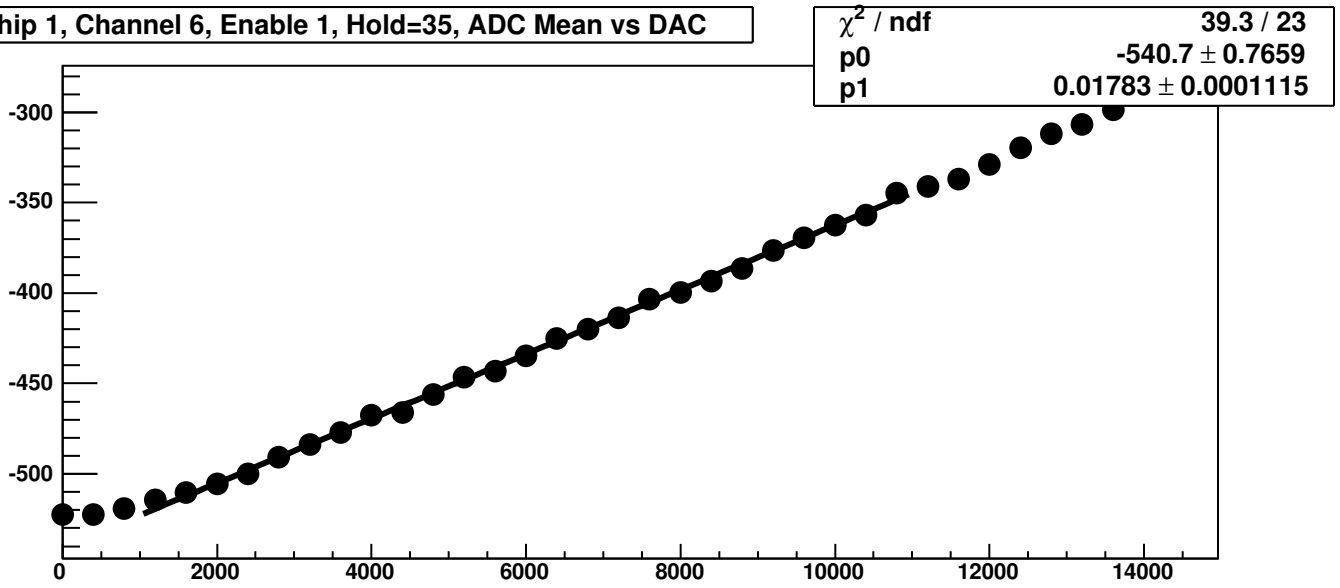



Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1500 / 23
p0 -747.9 ± 1.174
p1 0.1728 ± 0.0002179
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

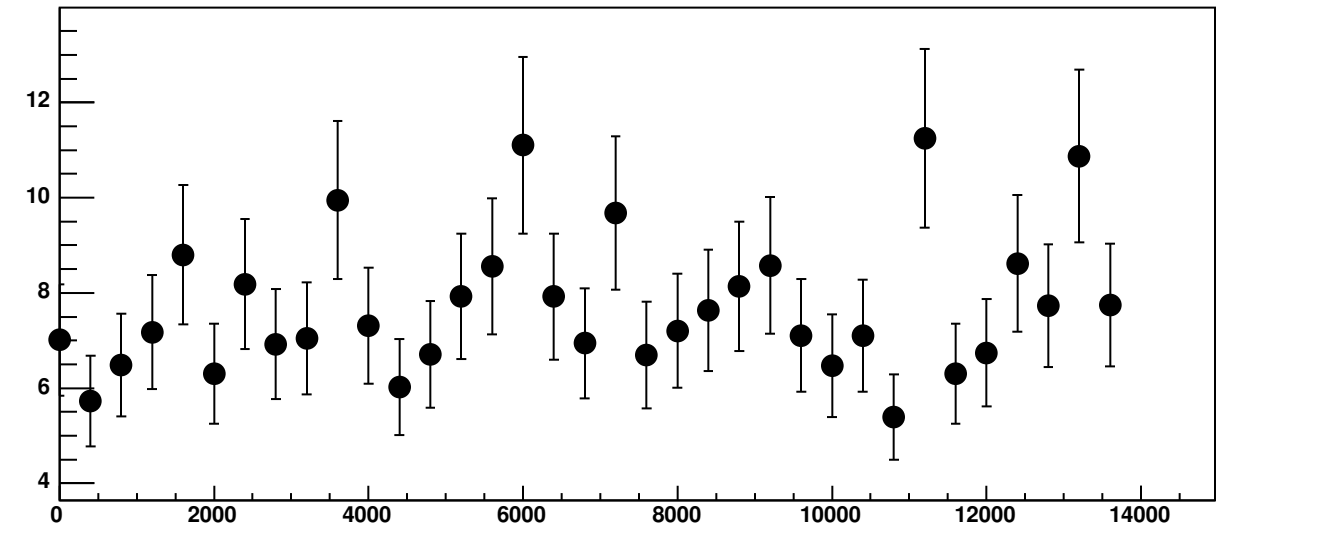

Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

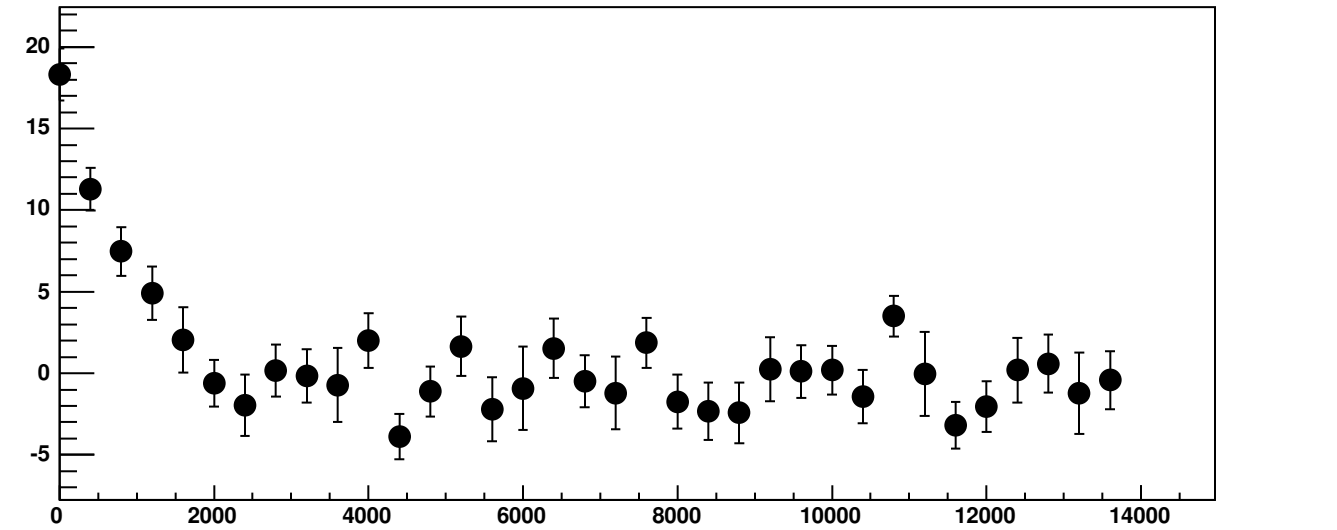
Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

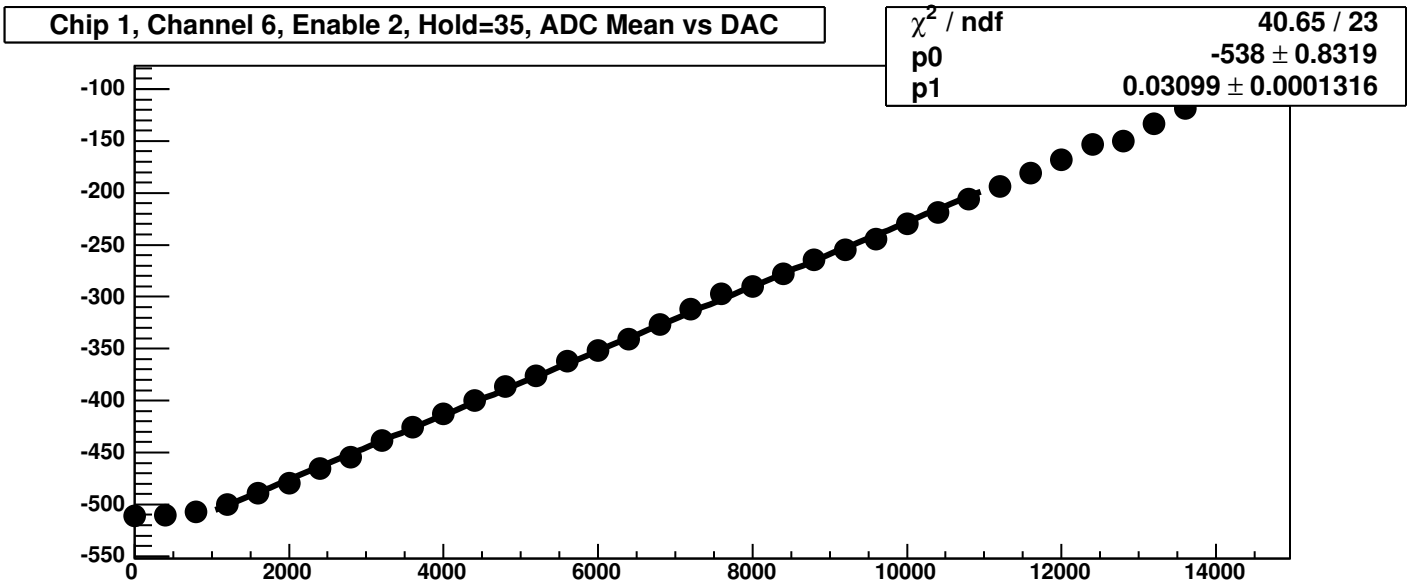


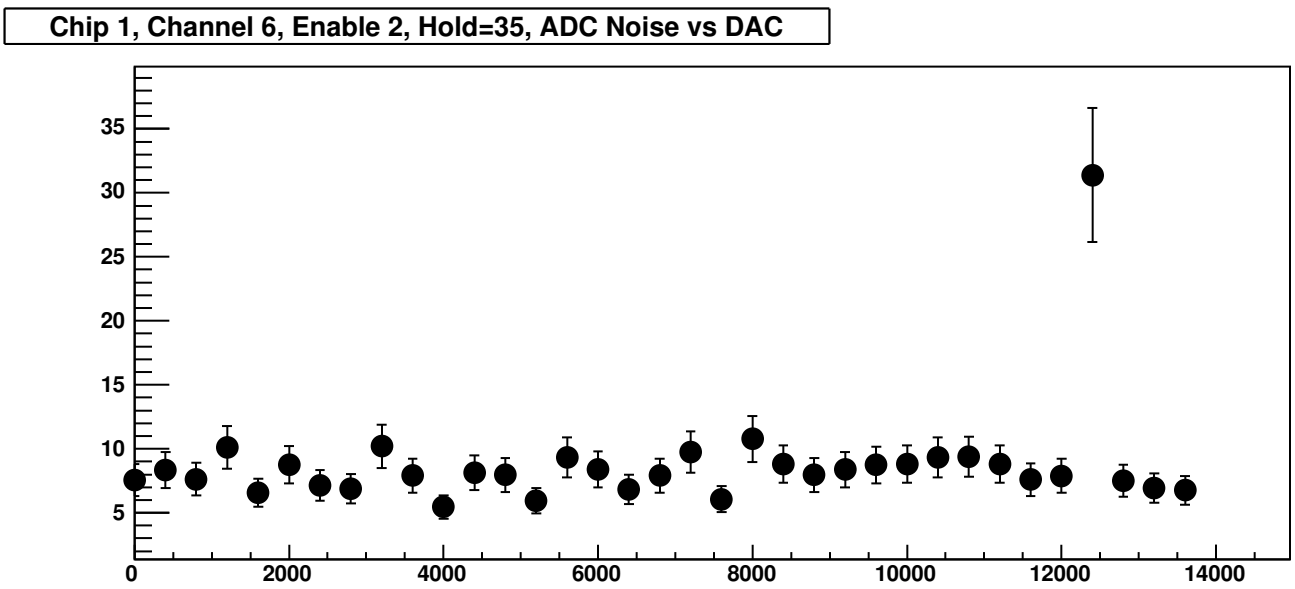
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

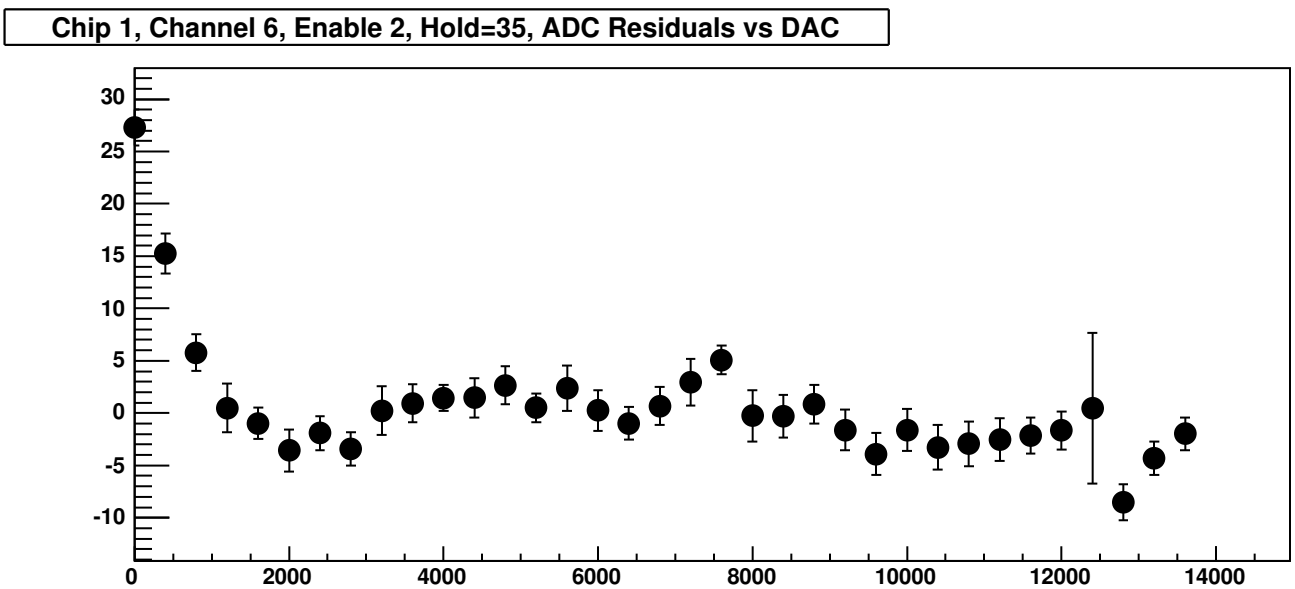


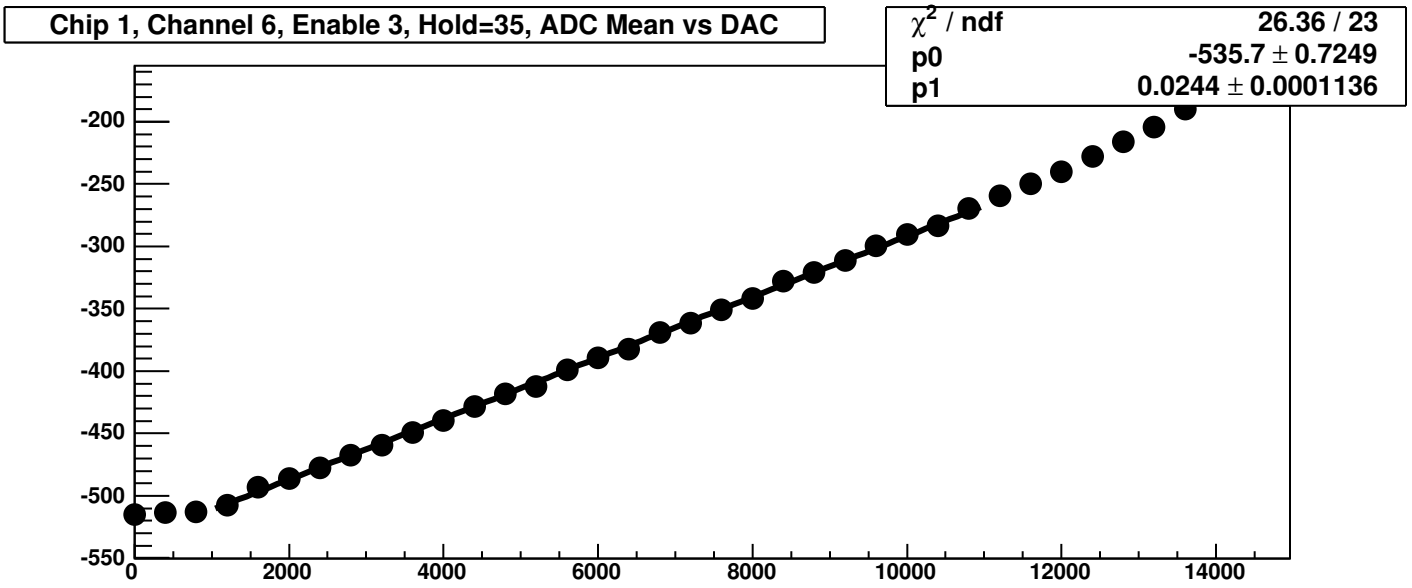
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

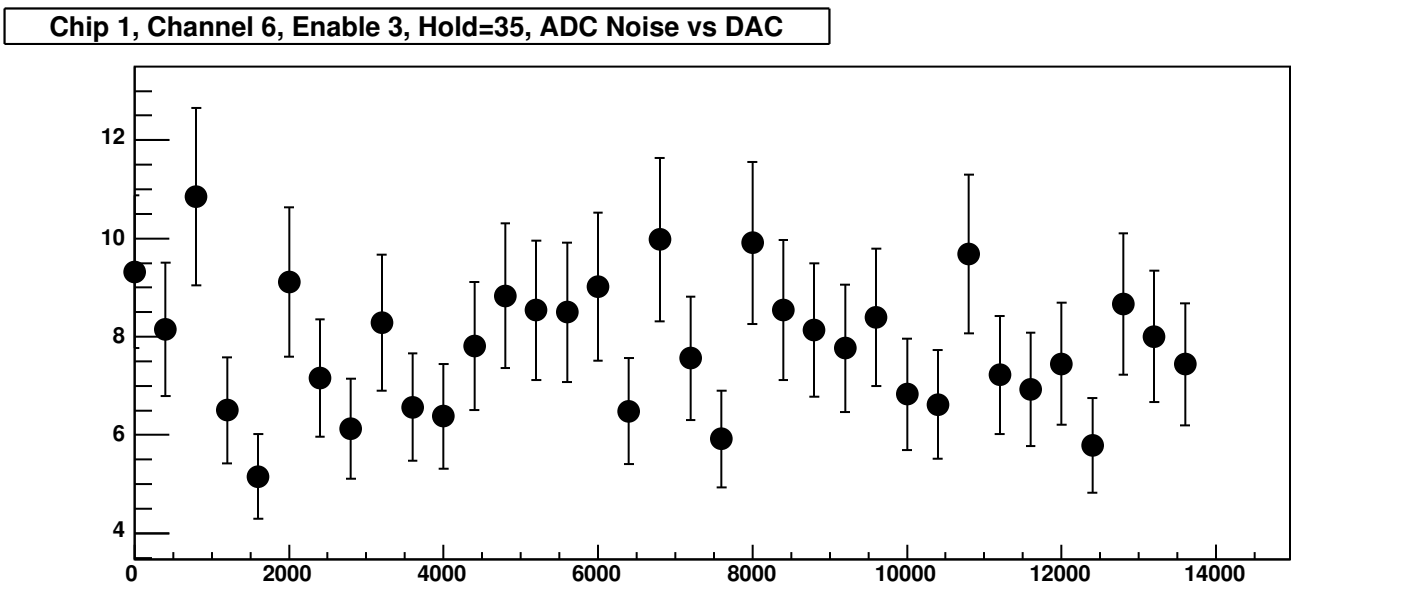


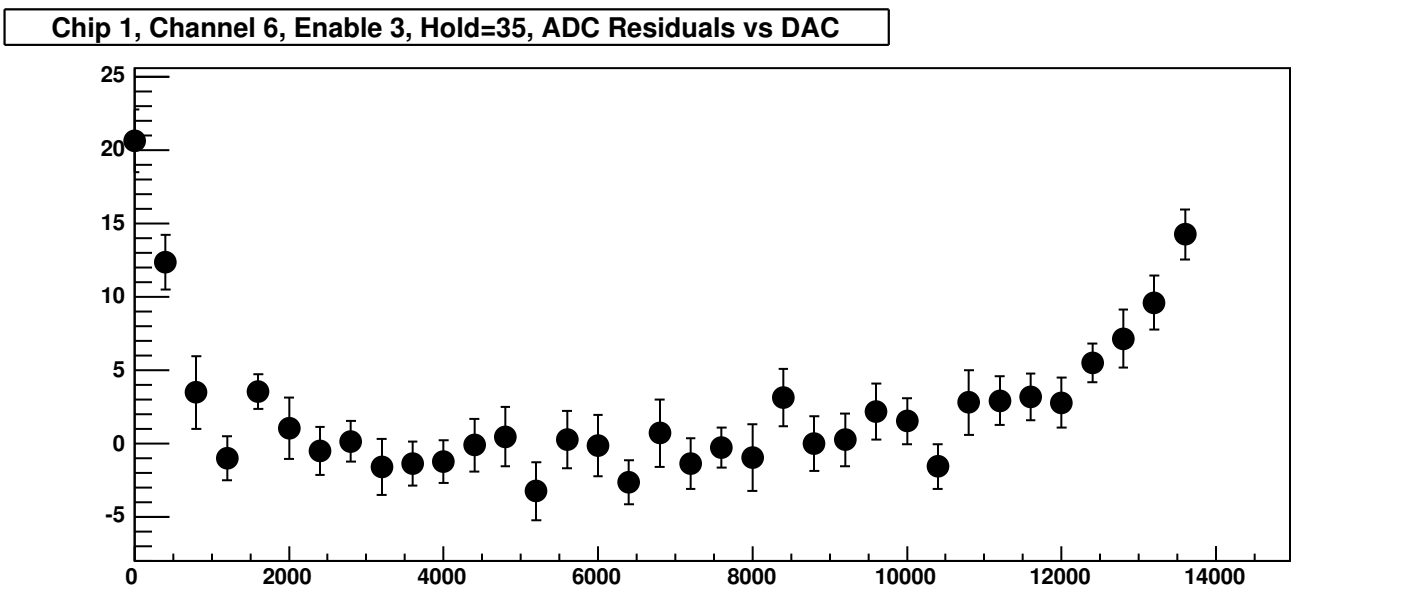

Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.65 / 23
p0 -538 ± 0.8319
p1 0.03099 ± 0.0001316
-100
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

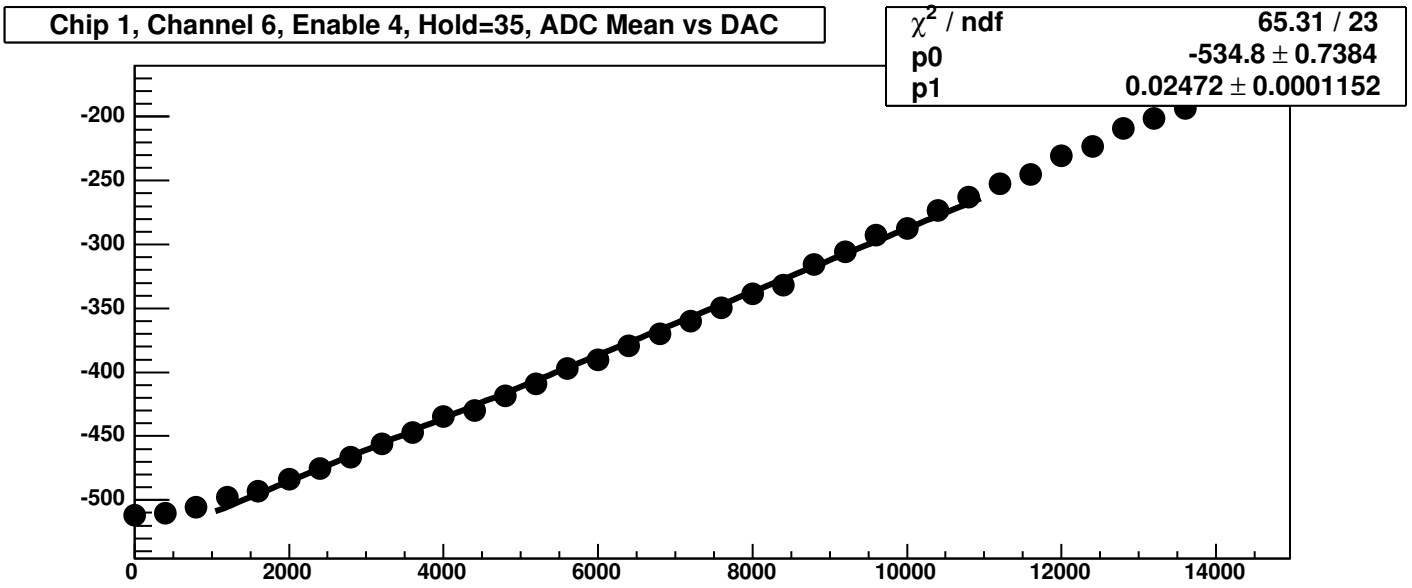

Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

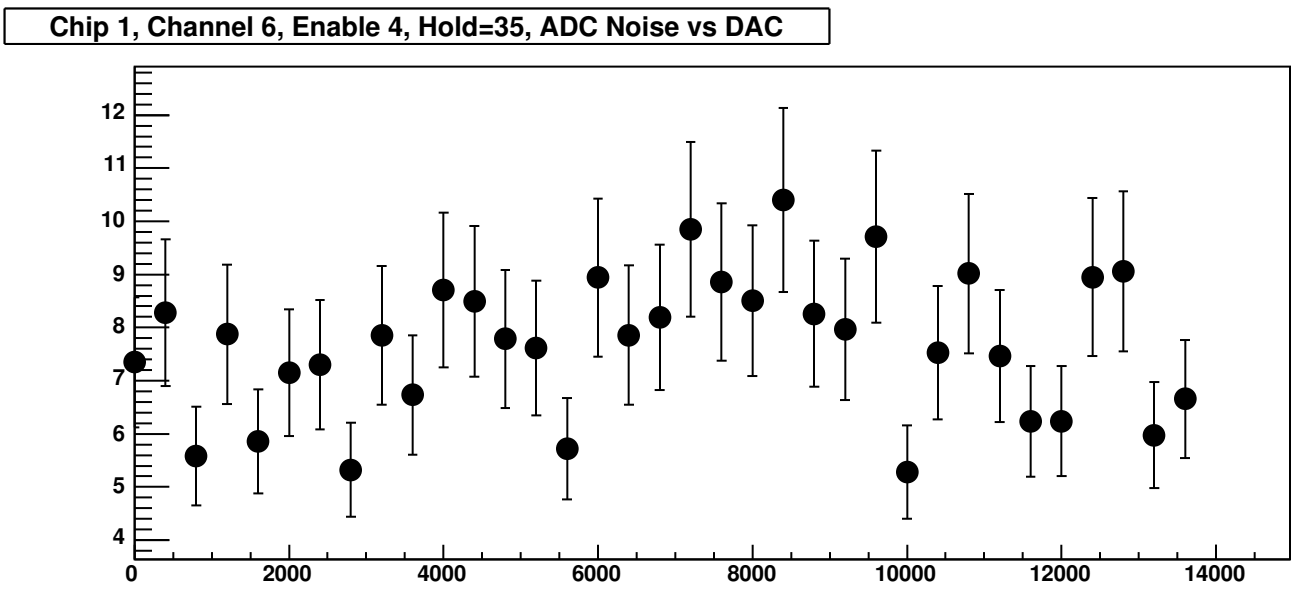

Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

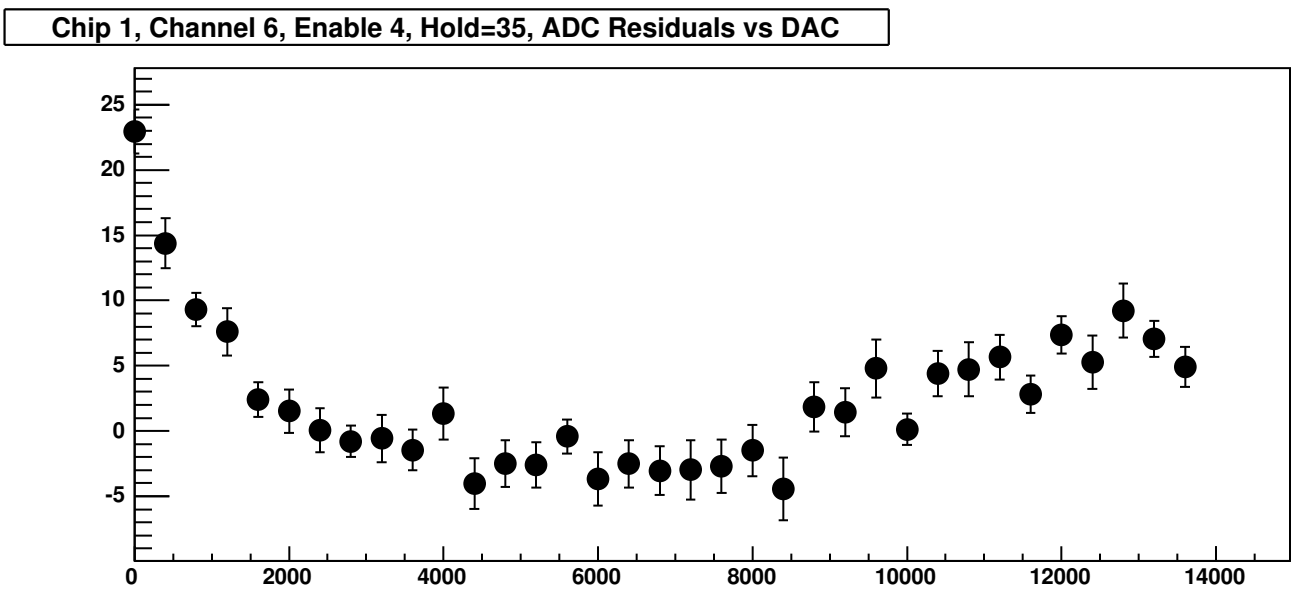

Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.36 / 23
p0 -535.7 ± 0.7249
p1 0.0244 ± 0.0001136
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

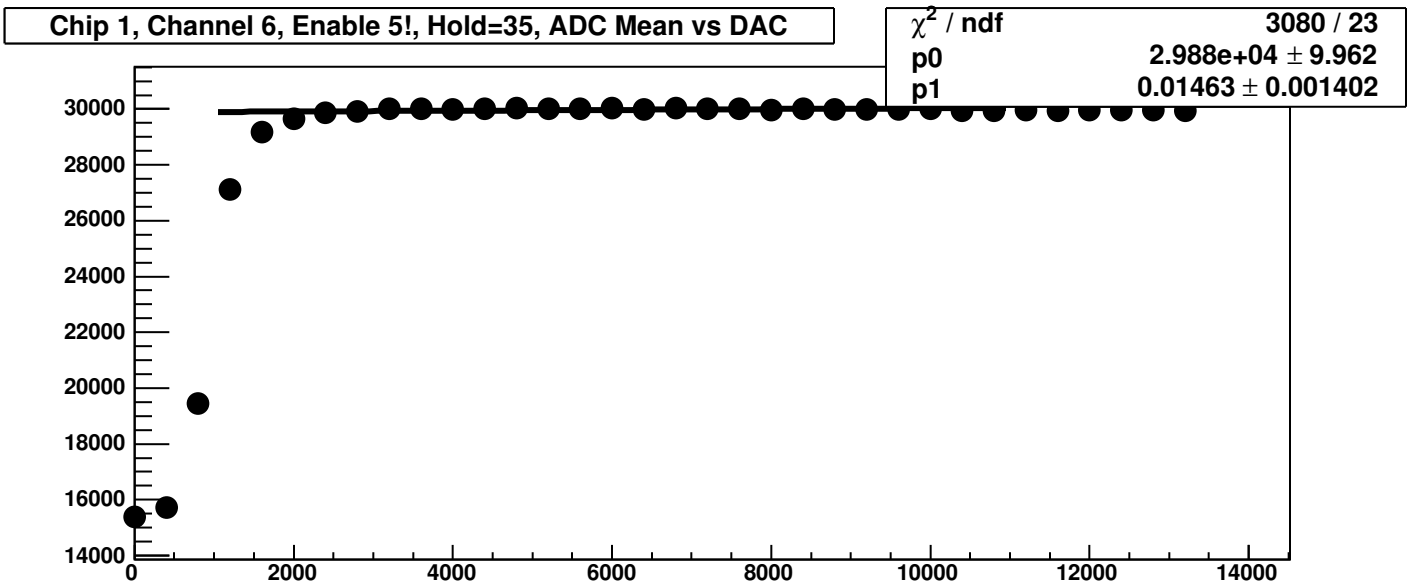

Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

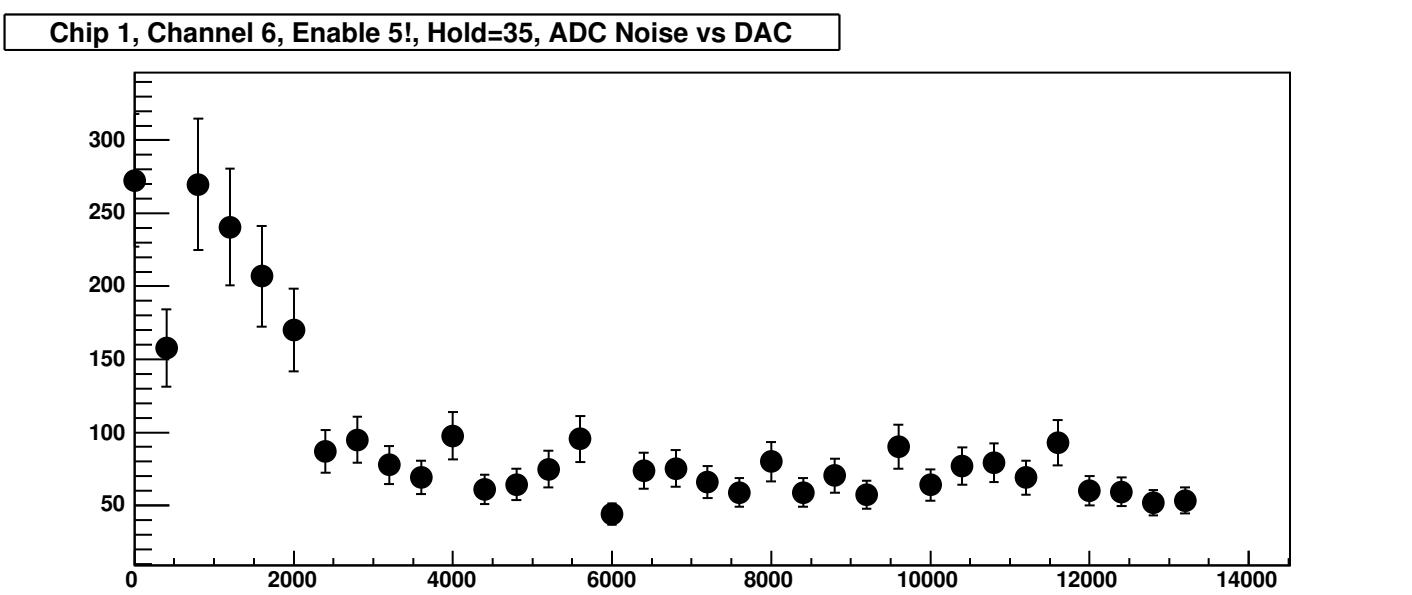

Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

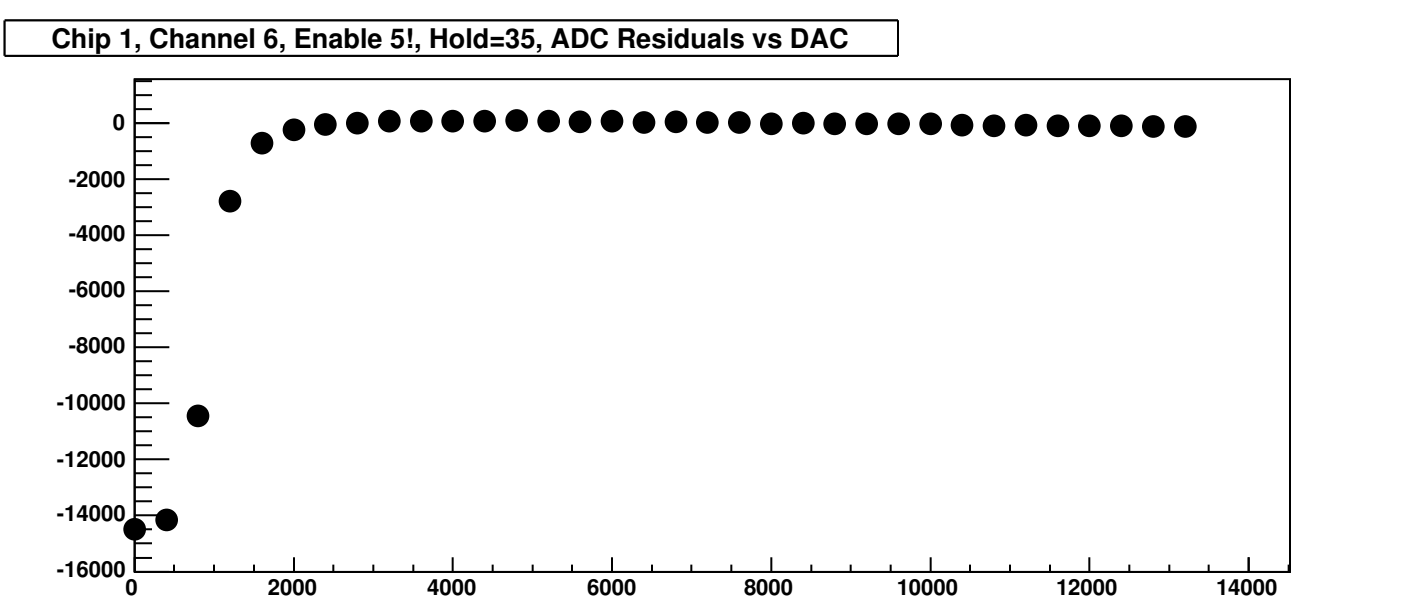

Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 65.31 / 23
p0 -534.8 ± 0.7384
p1 0.02472 ± 0.0001152
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

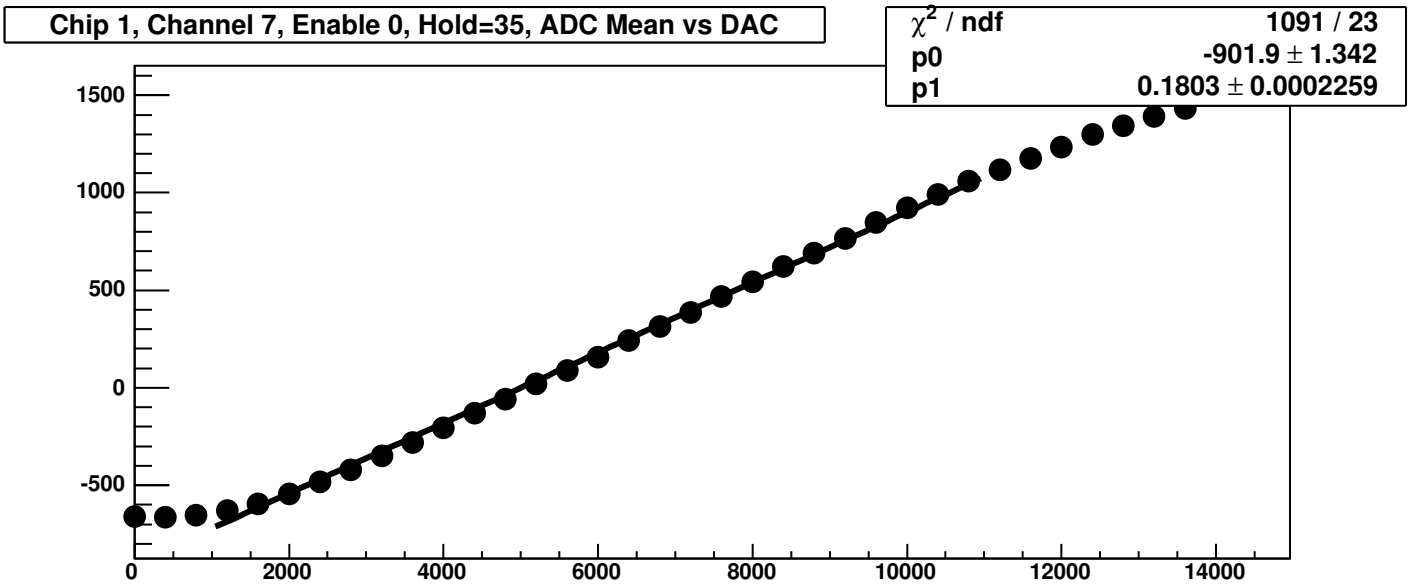

Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

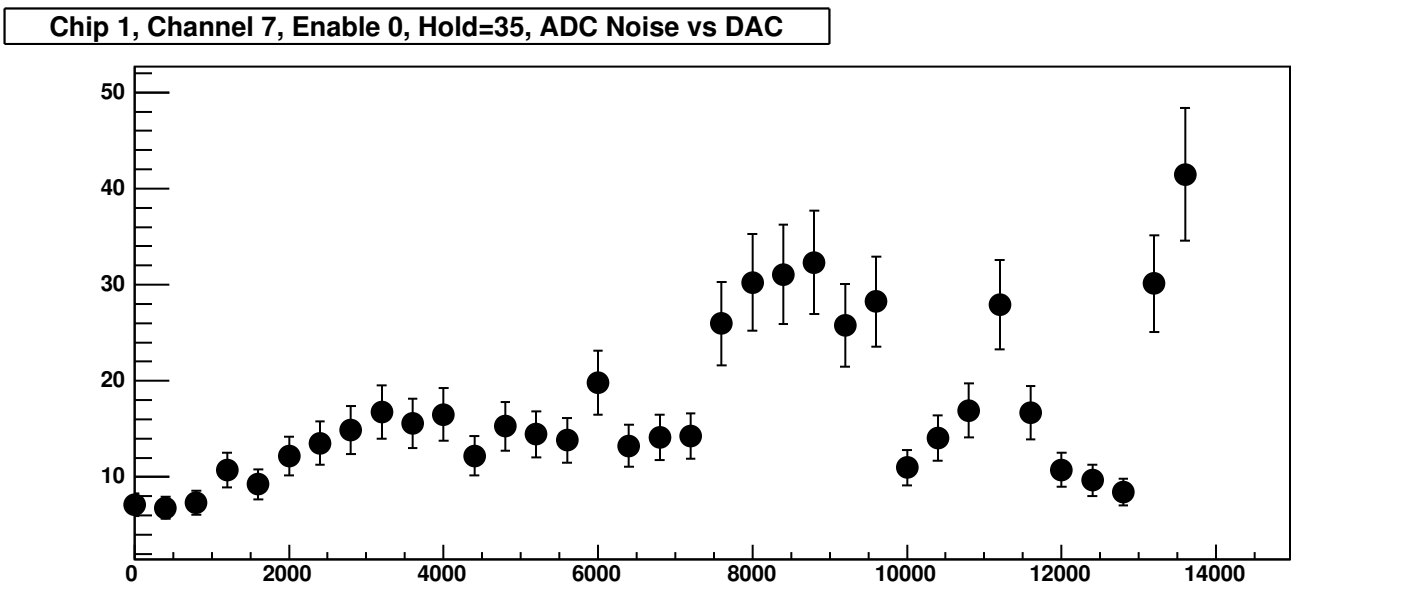

Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

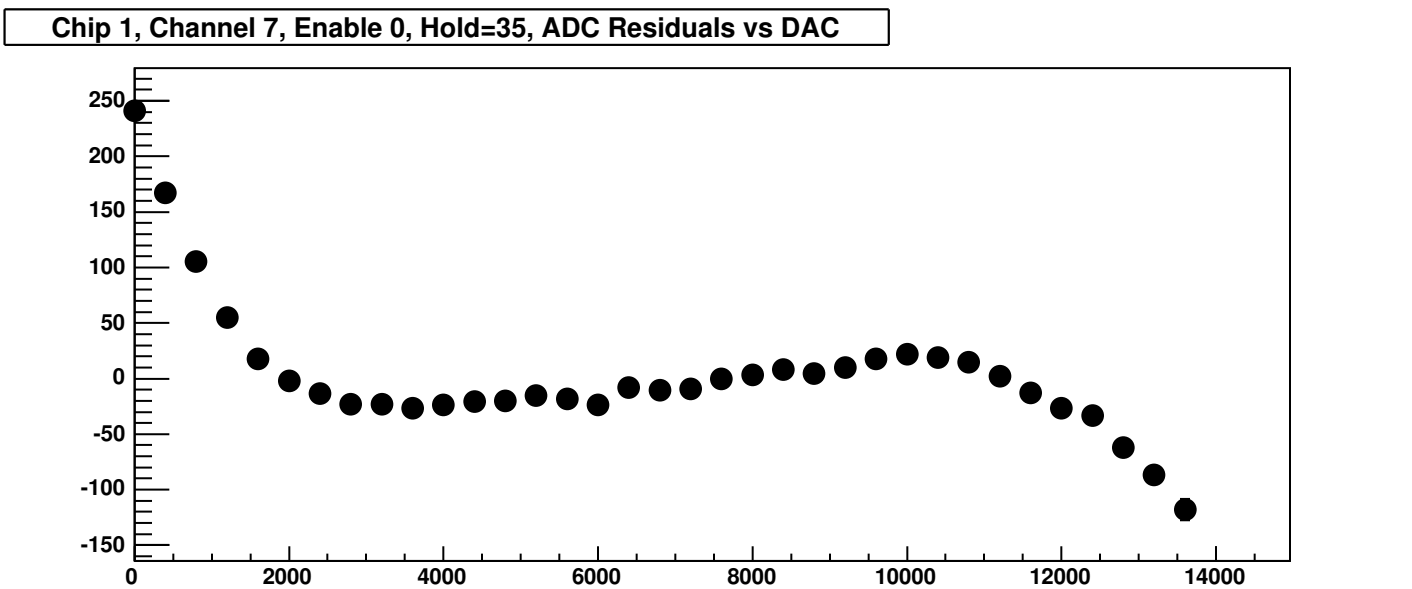

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3080 / 23
p0 2.988e+04 ± 9.962
p1 0.01463 ± 0.001402
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

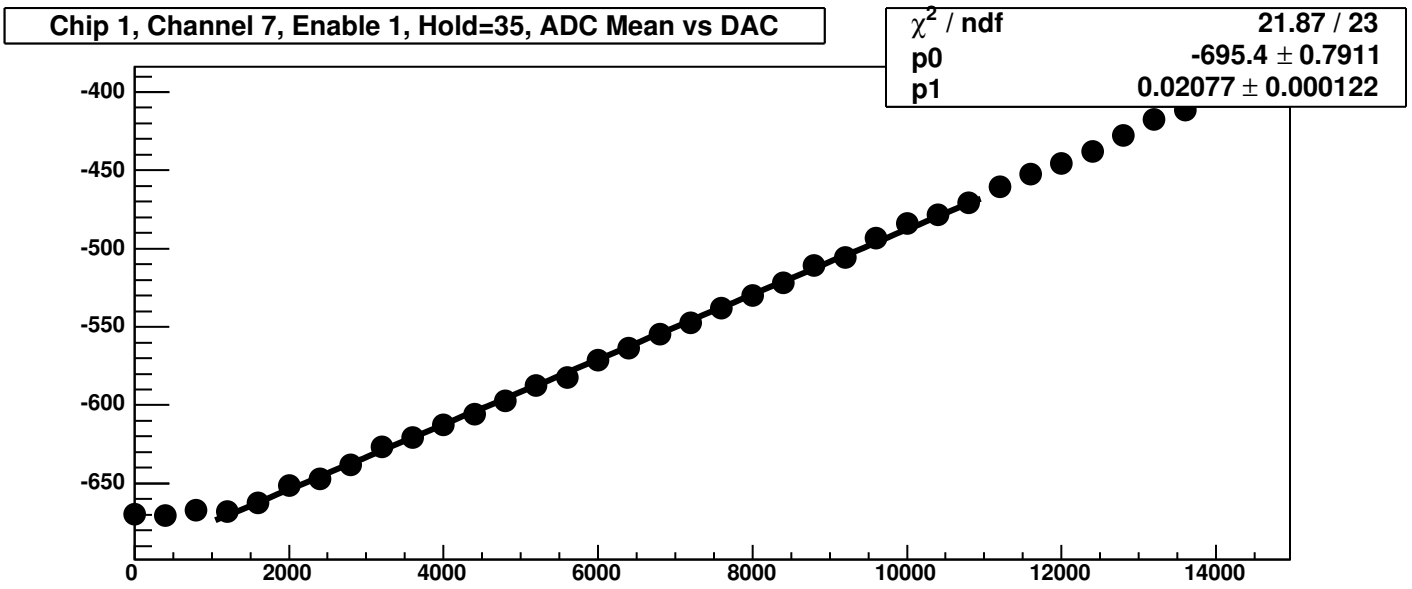

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

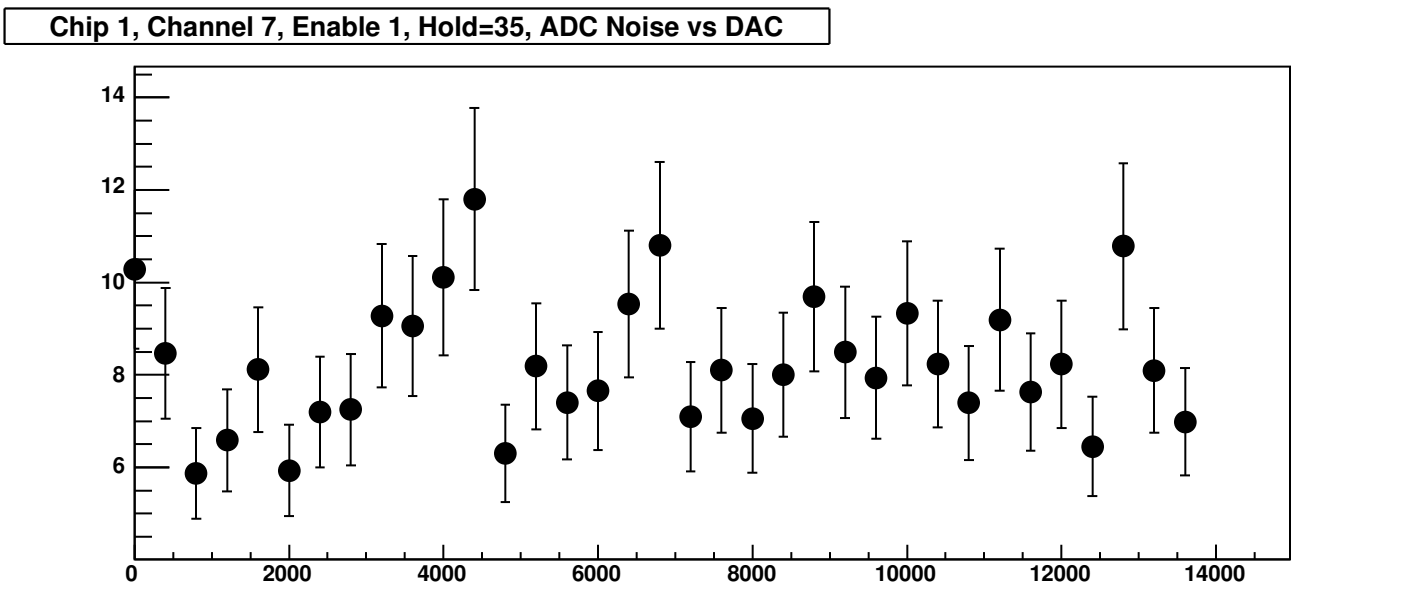

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

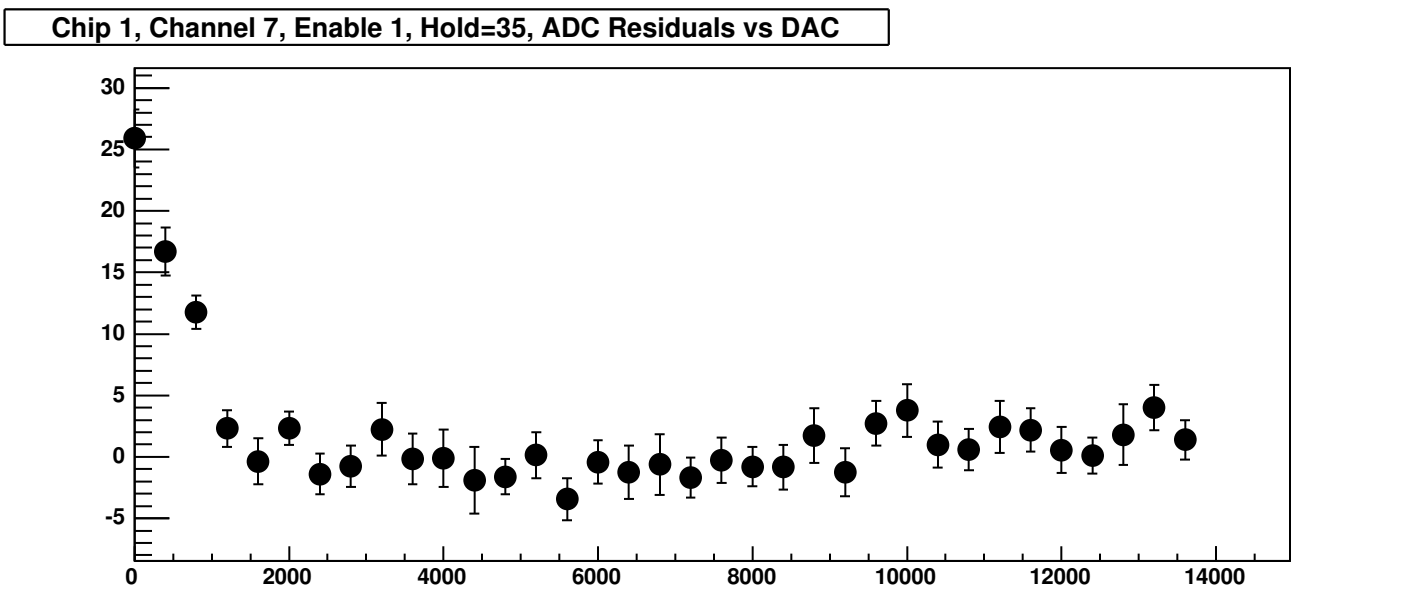

Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1091 / 23
p0 -901.9 ± 1.342
p1 0.1803 ± 0.0002259
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

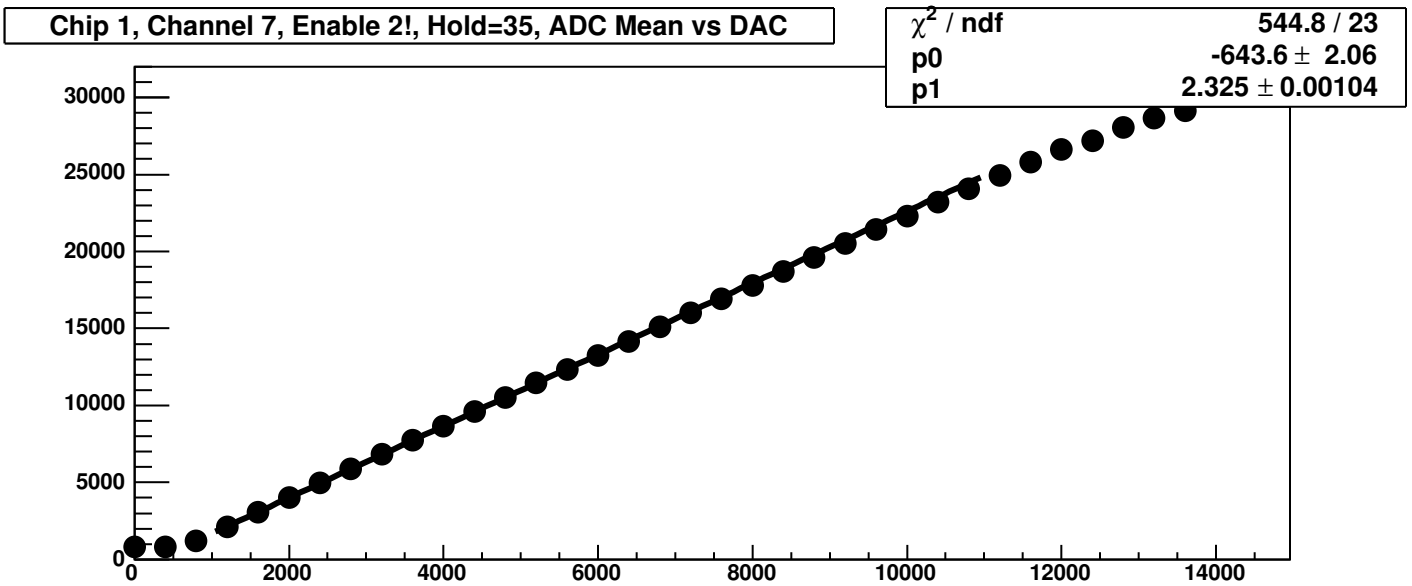

Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

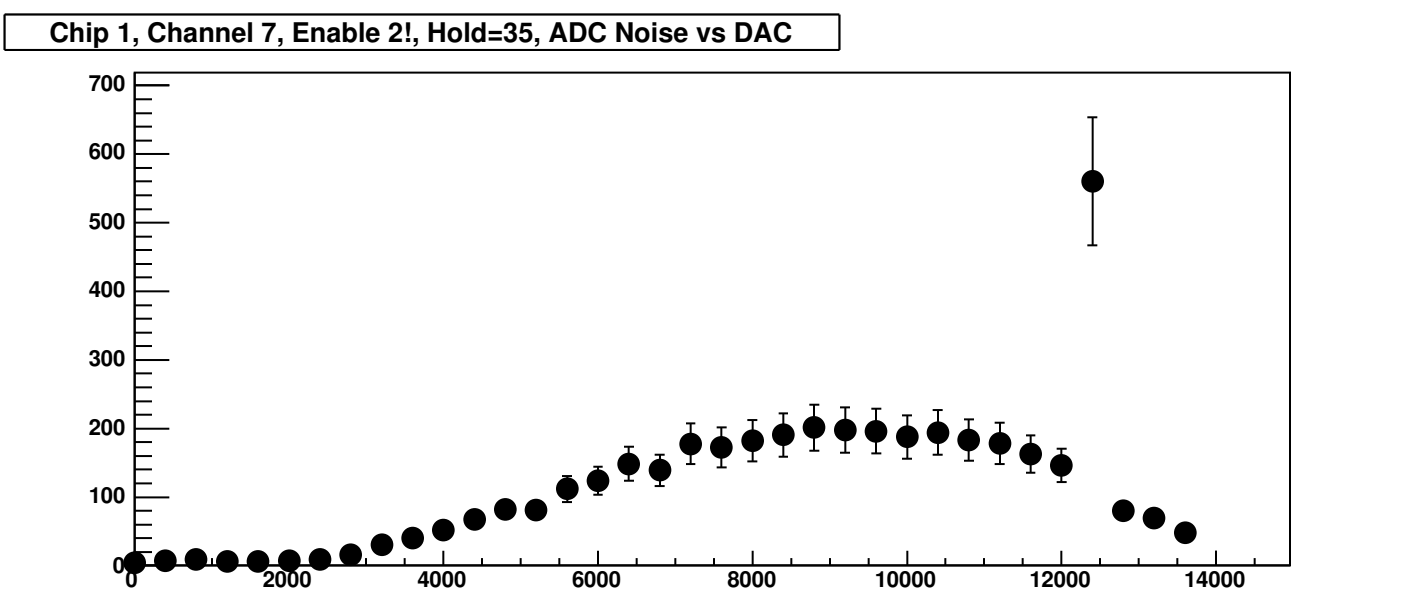

Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

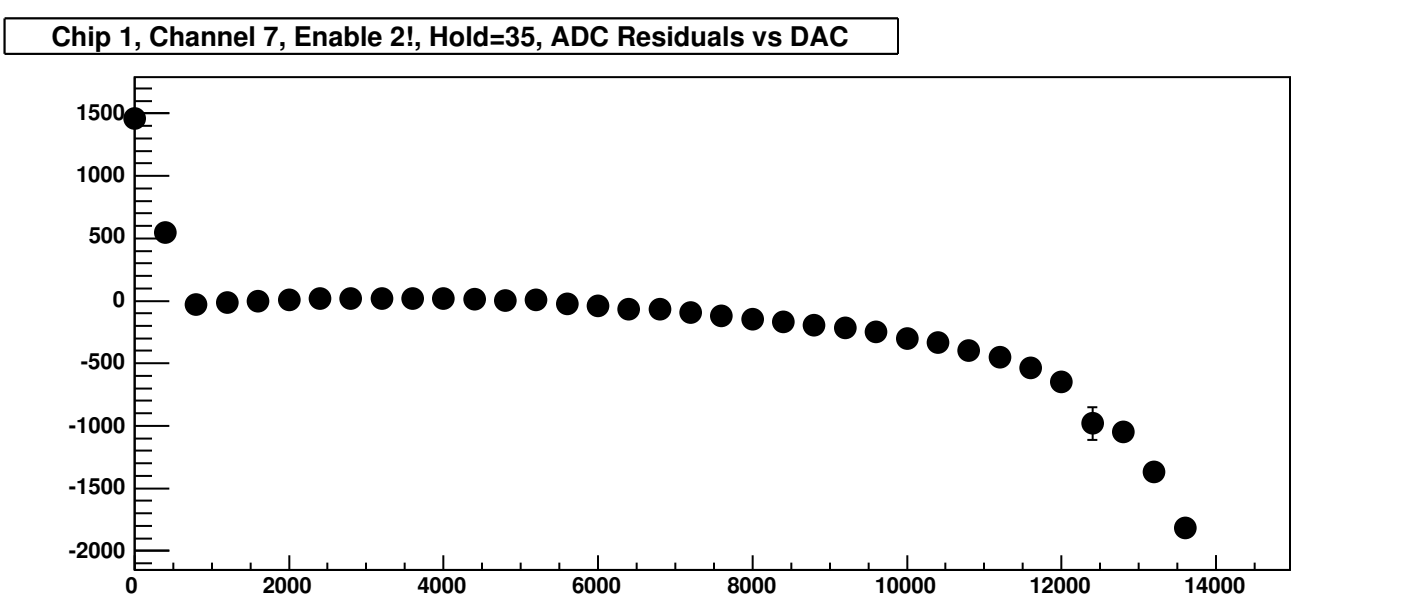

Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.87 / 23
p0 -695.4 ± 0.7911
p1 0.02077 ± 0.000122
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

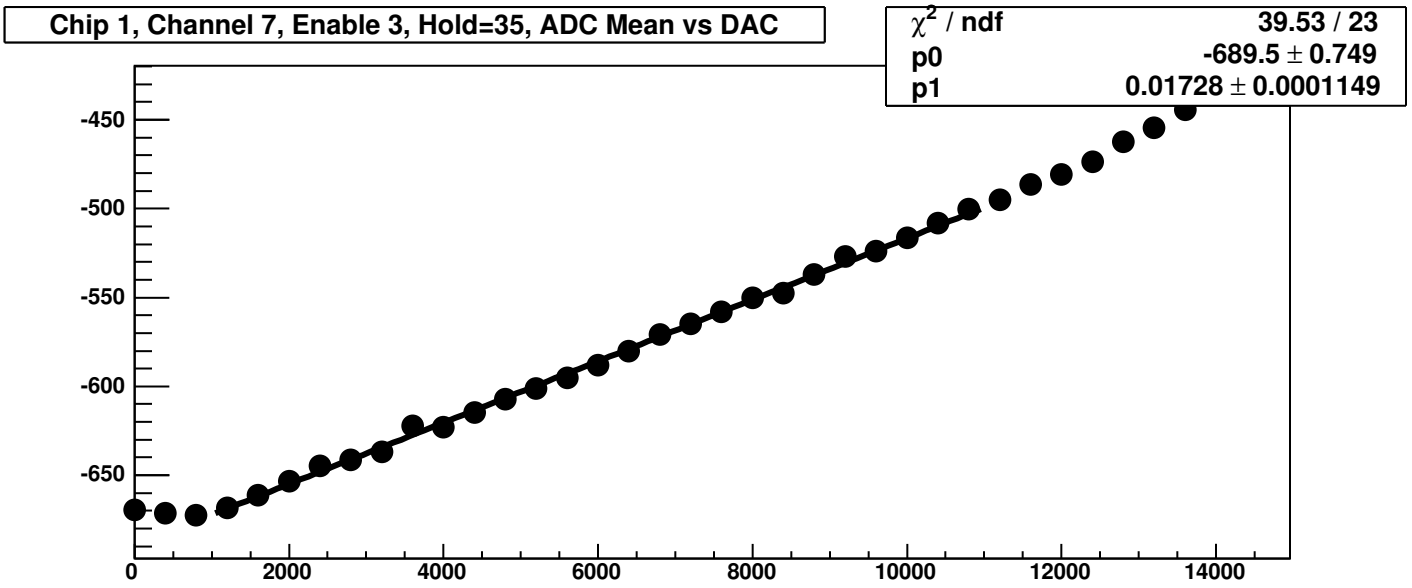

Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

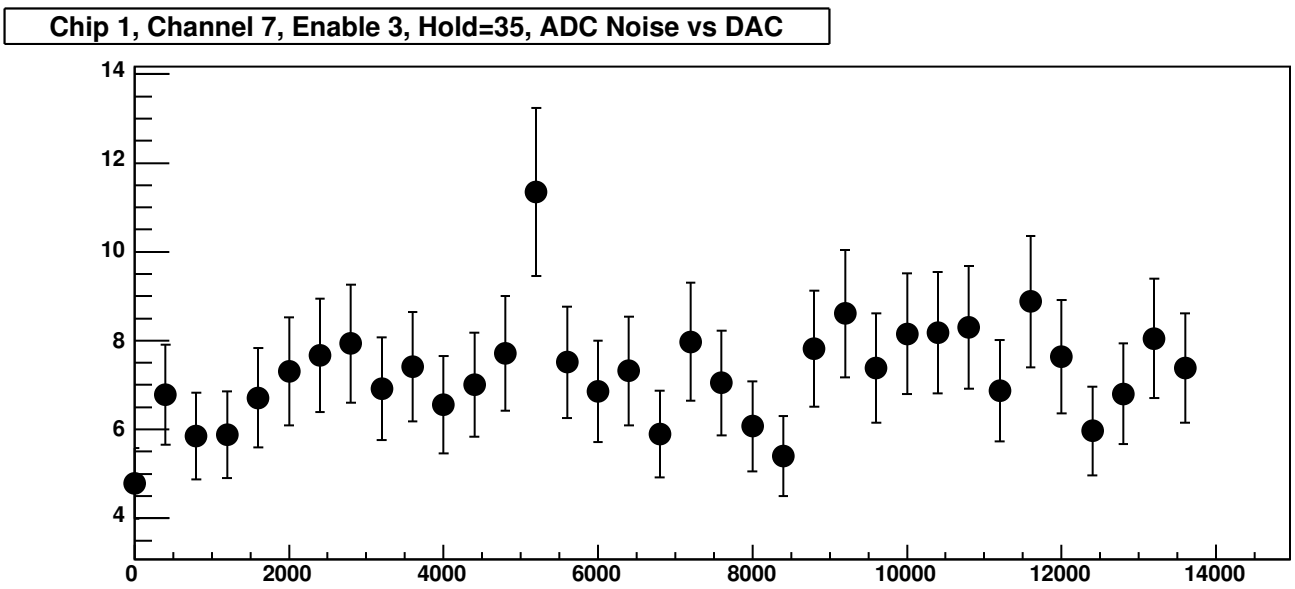

Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

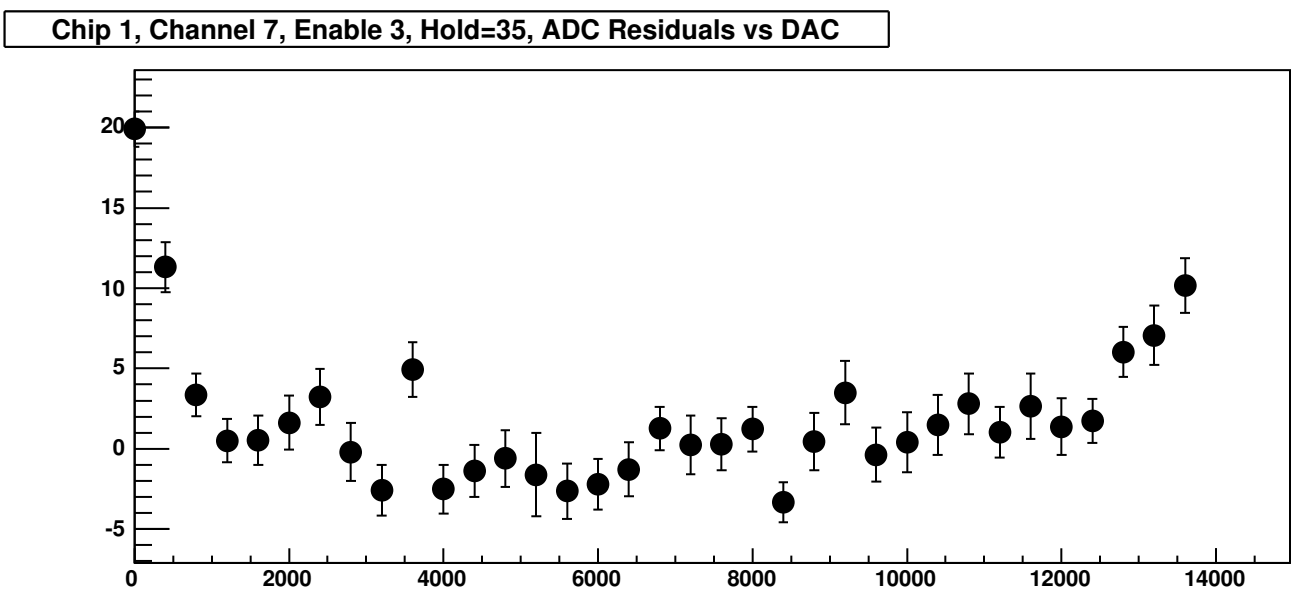

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 544.8 / 23
p0 -643.6 ± 2.06
p1 2.325 ± 0.00104
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

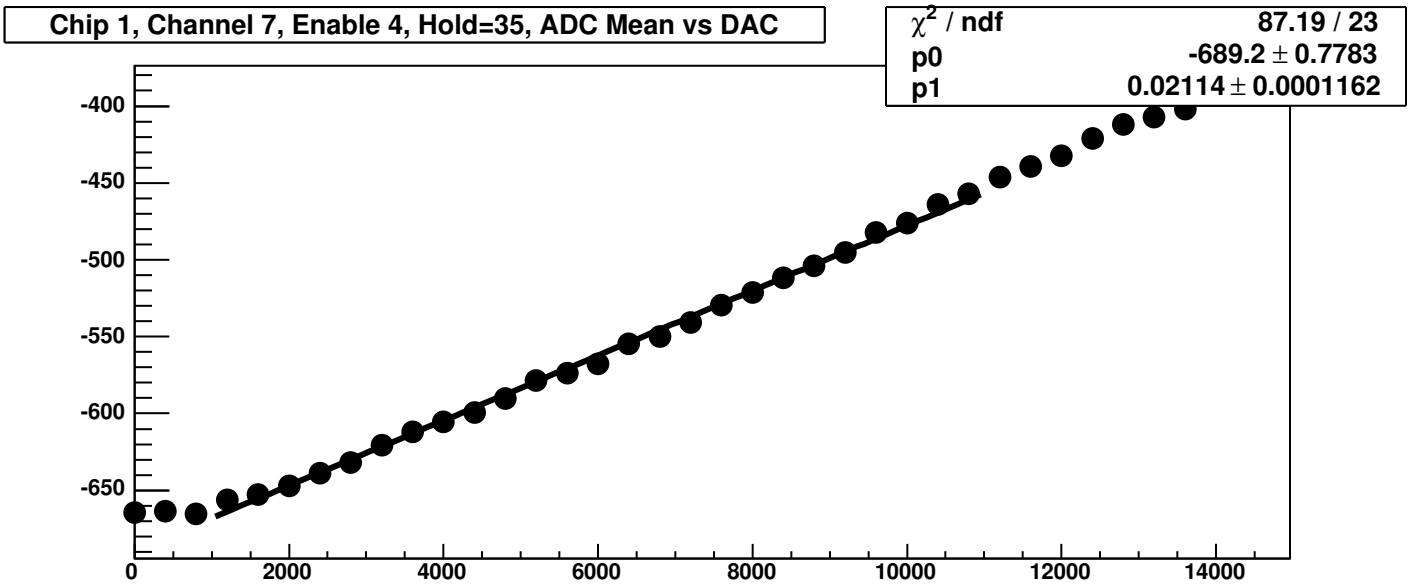

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

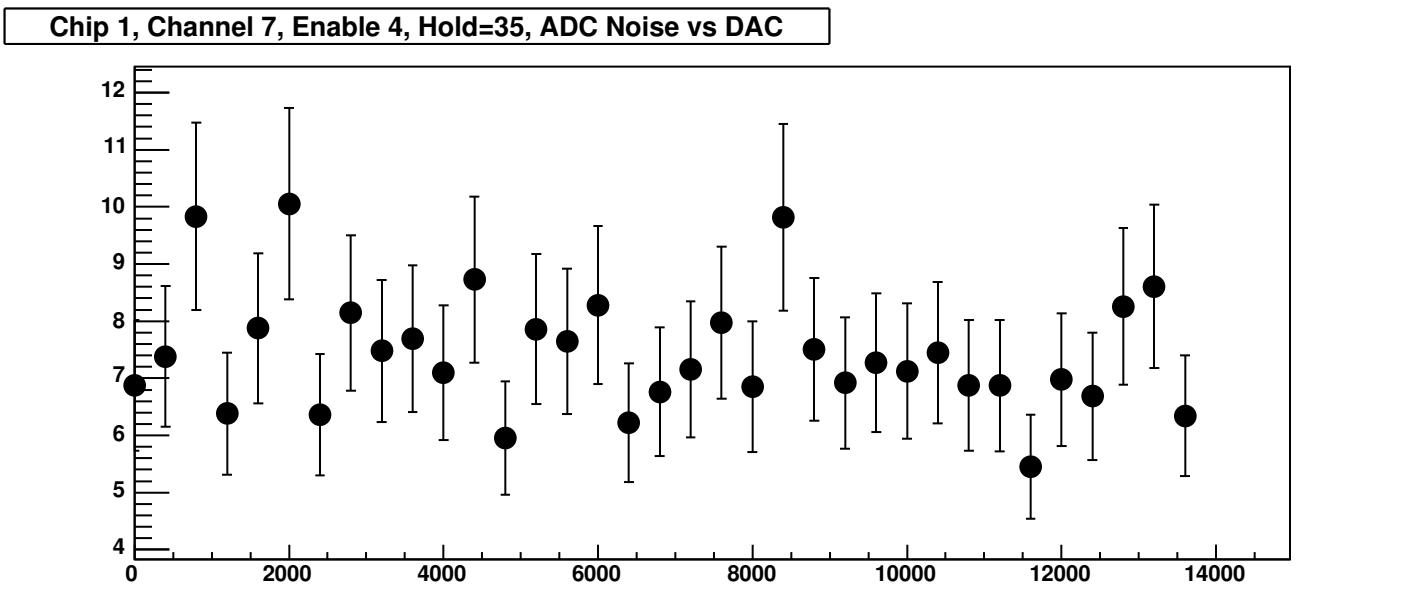

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

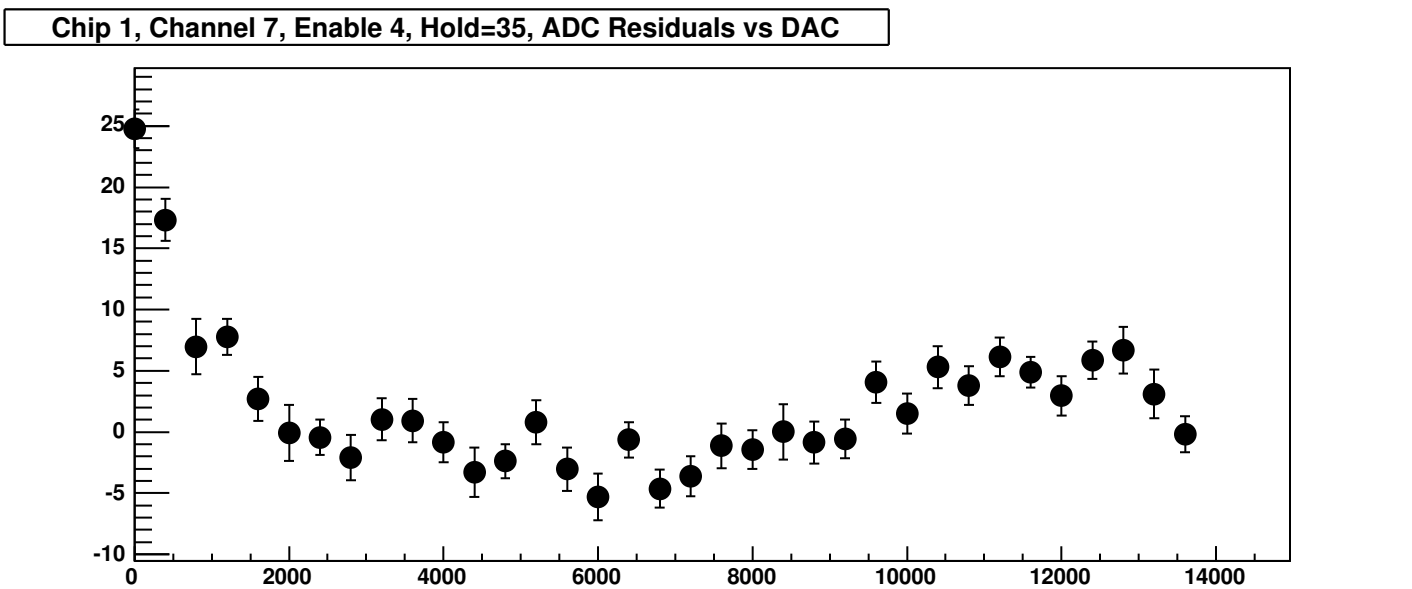

Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.53 / 23
p0 -689.5 ± 0.749
p1 0.01728 ± 0.0001149
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

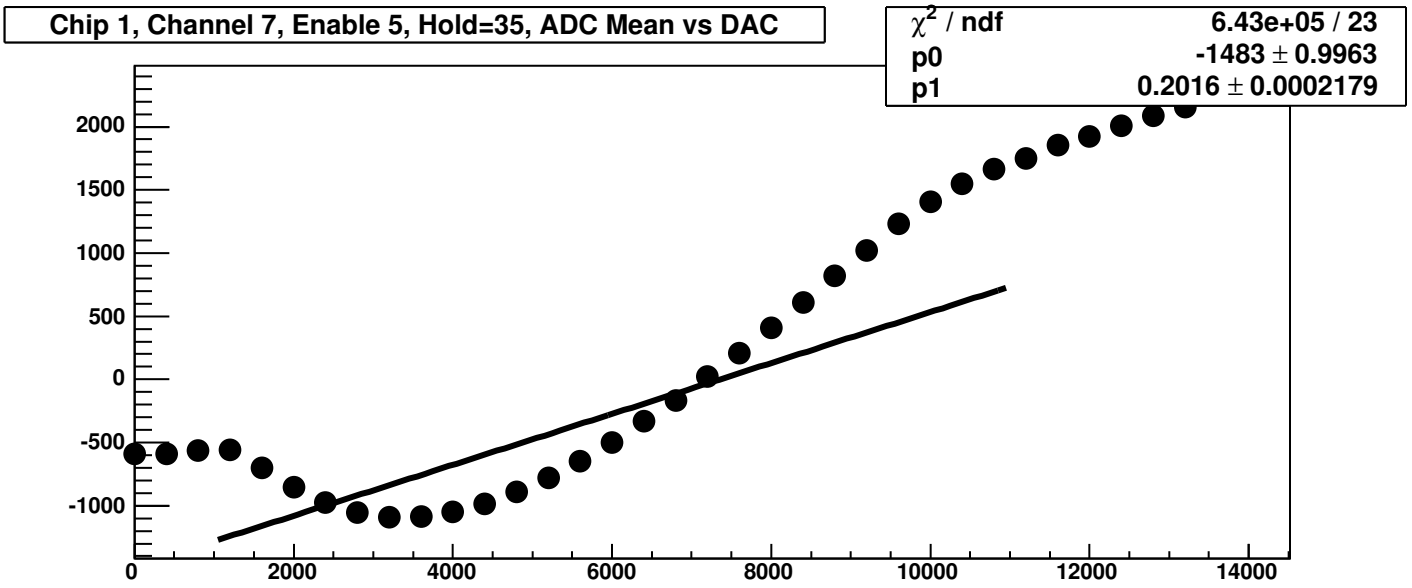

Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

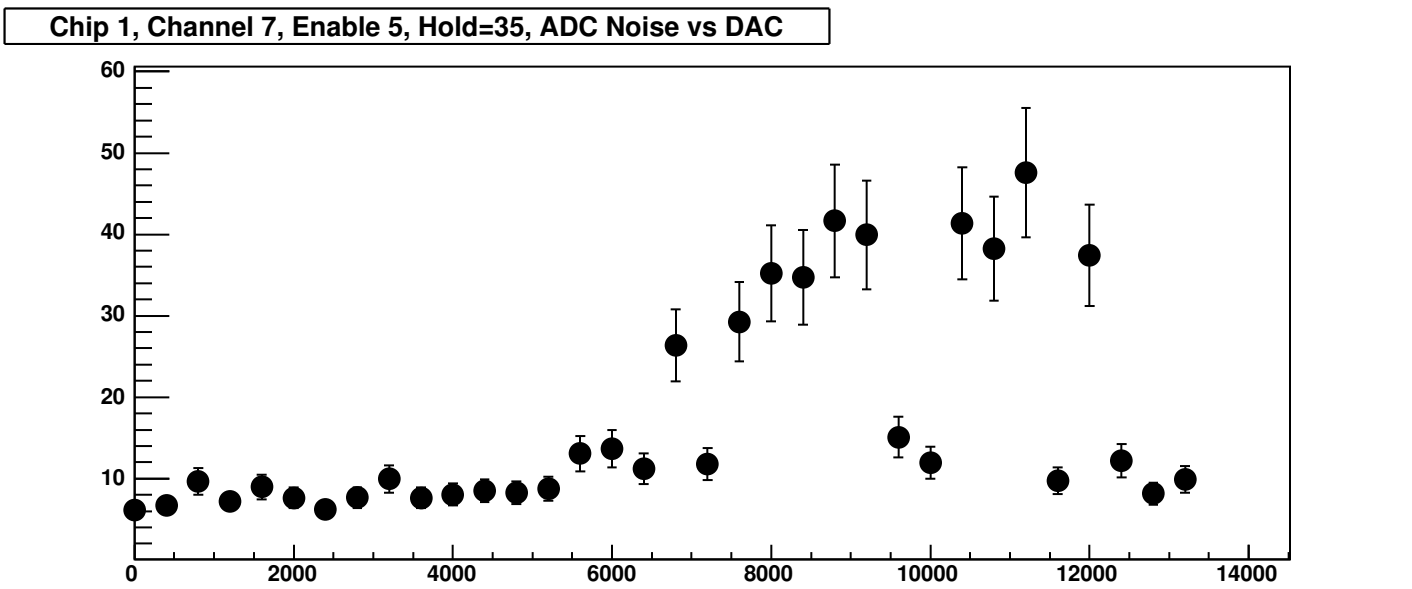

Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

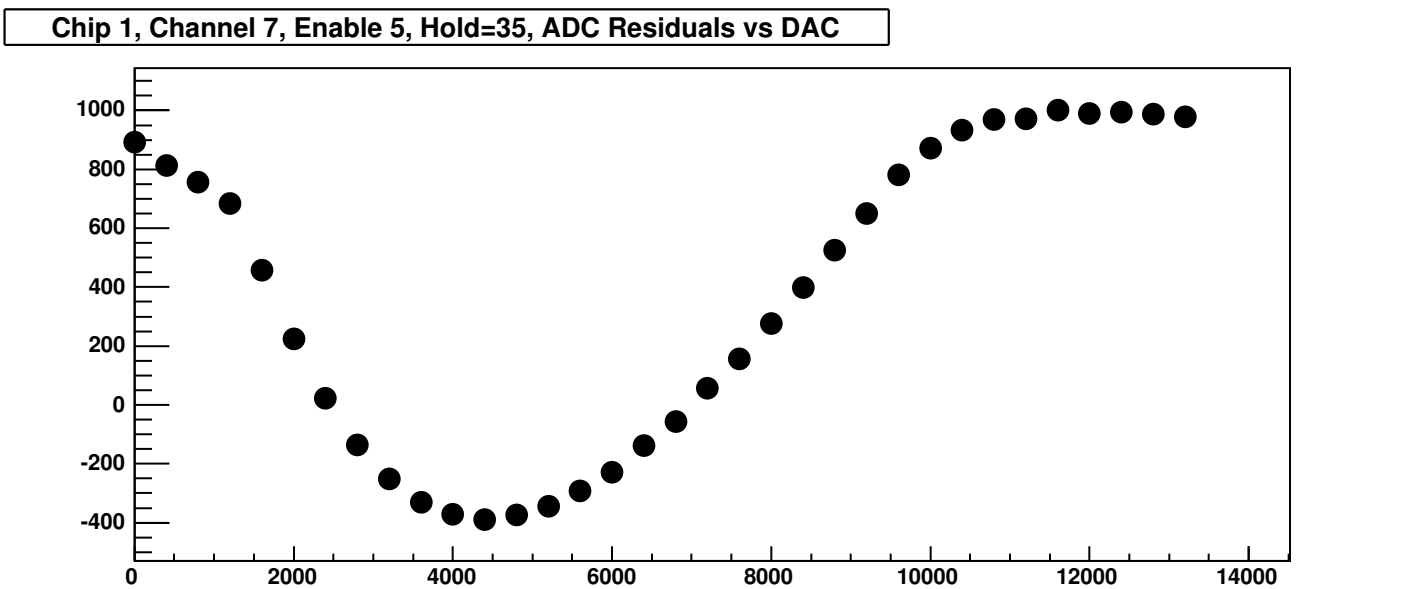

Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 87.19 / 23
p0 -689.2 ± 0.7783
p1 0.02114 ± 0.0001162
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

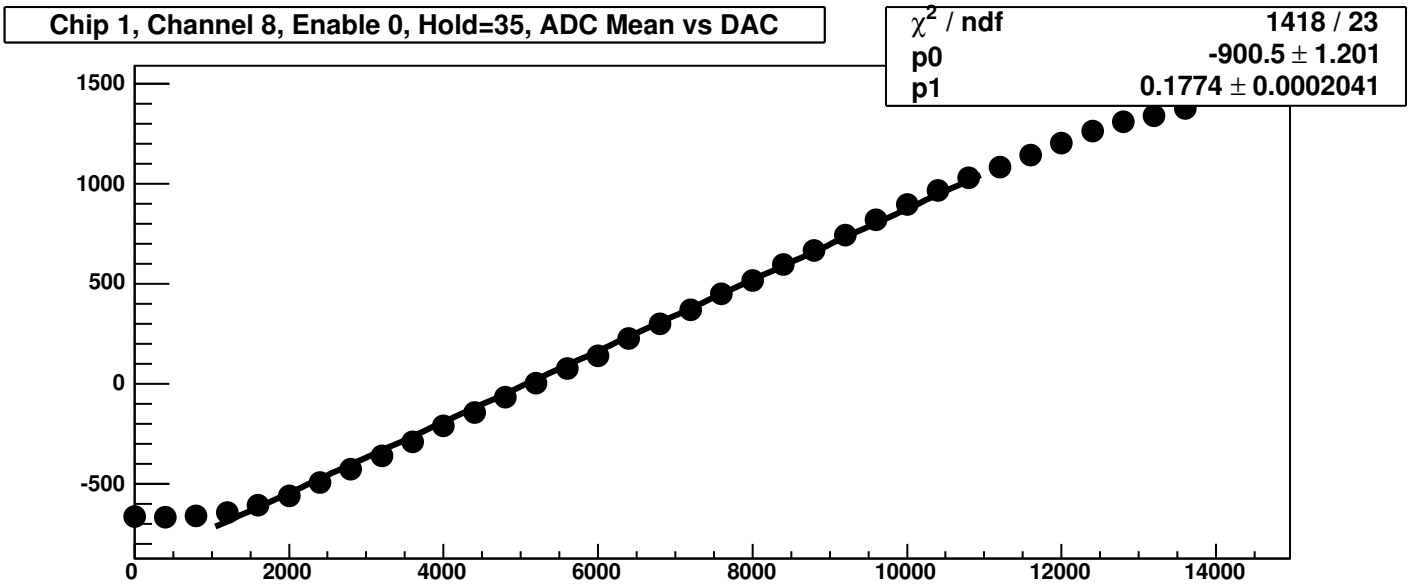

Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

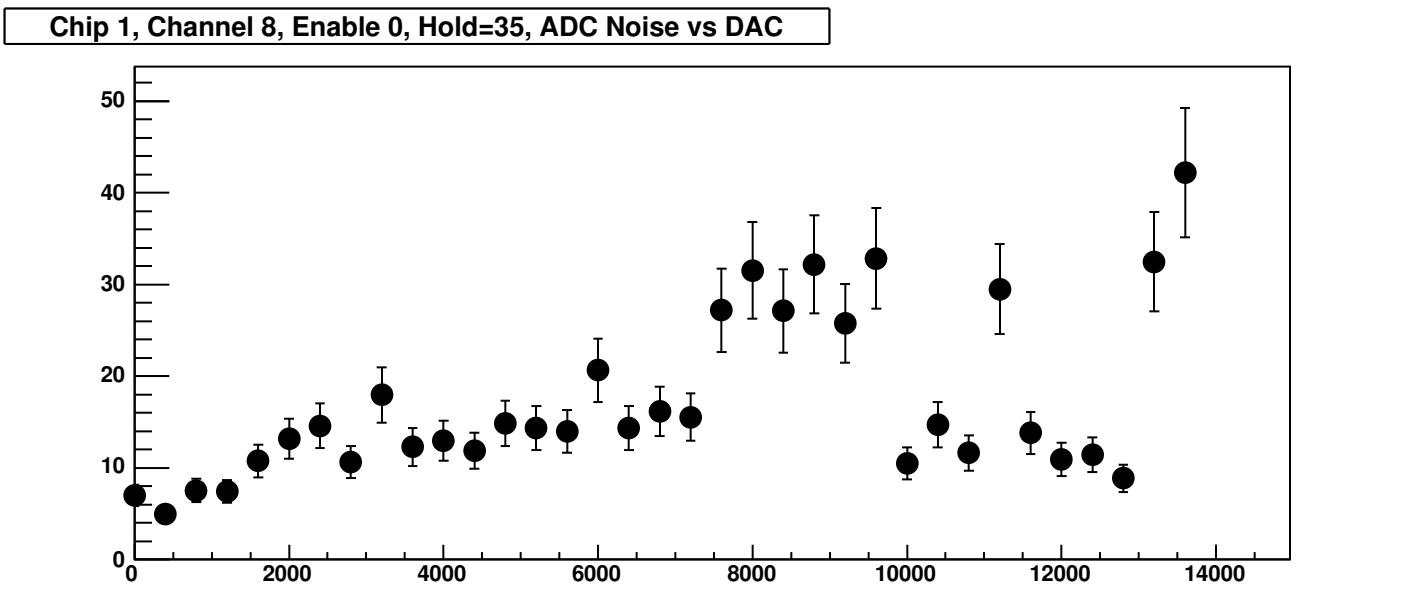

Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

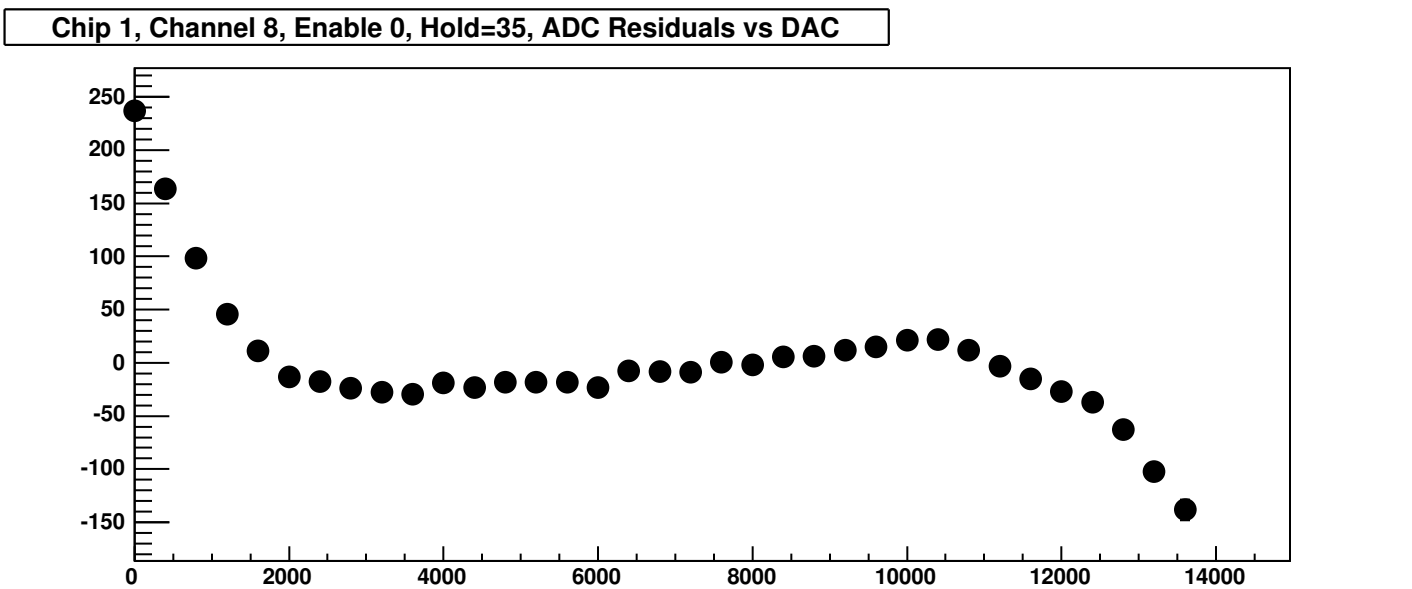

Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.43e+05 / 23
p0 -1483 ± 0.9963
p1 0.2016 ± 0.0002179
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

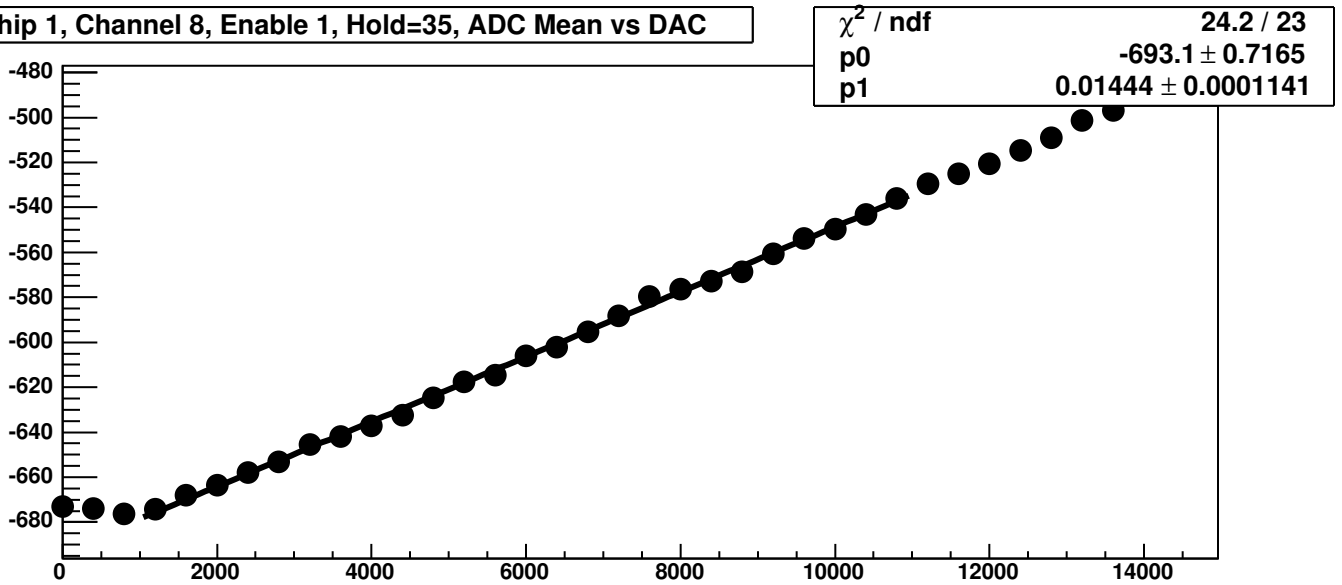

Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1418 / 23
p0 -900.5 ± 1.201
p1 0.1774 ± 0.0002041
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

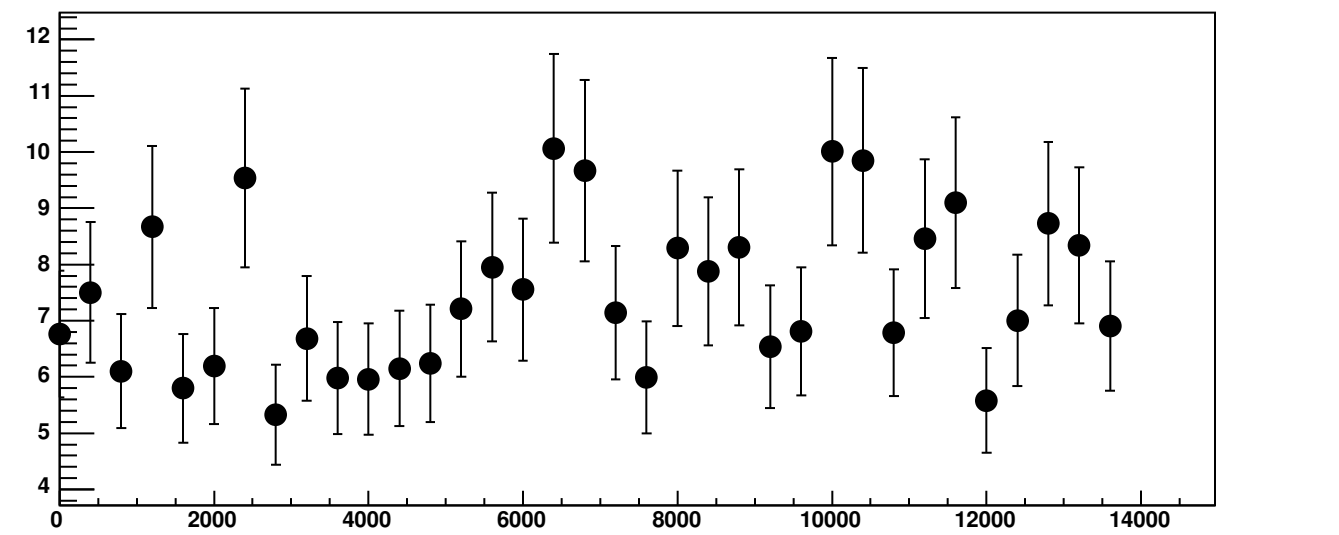

Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

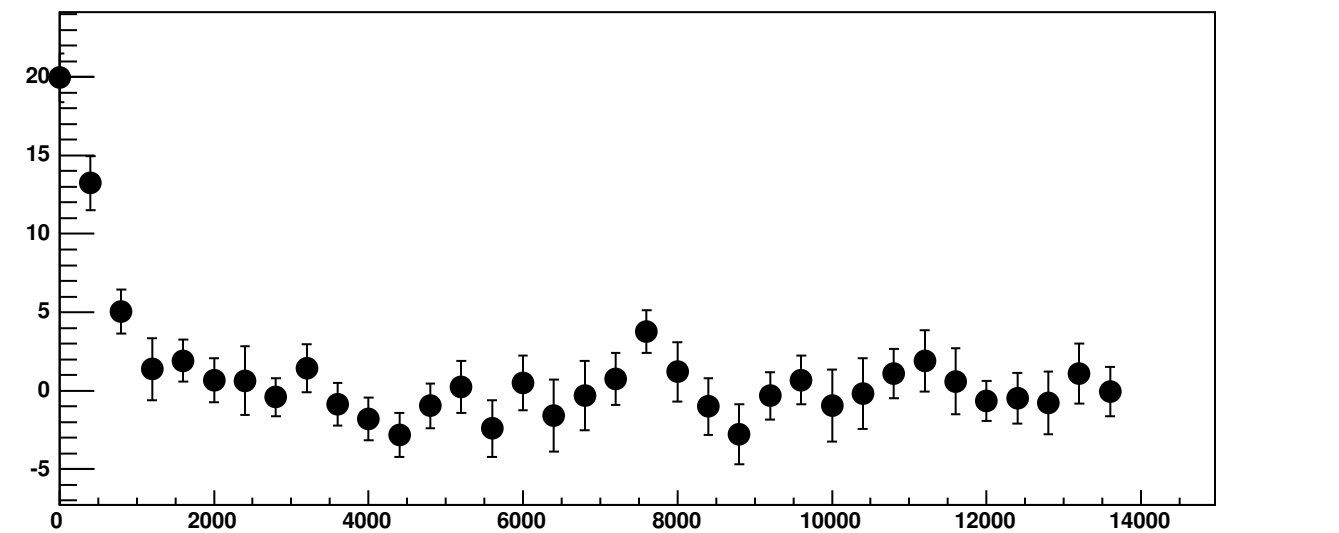
Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

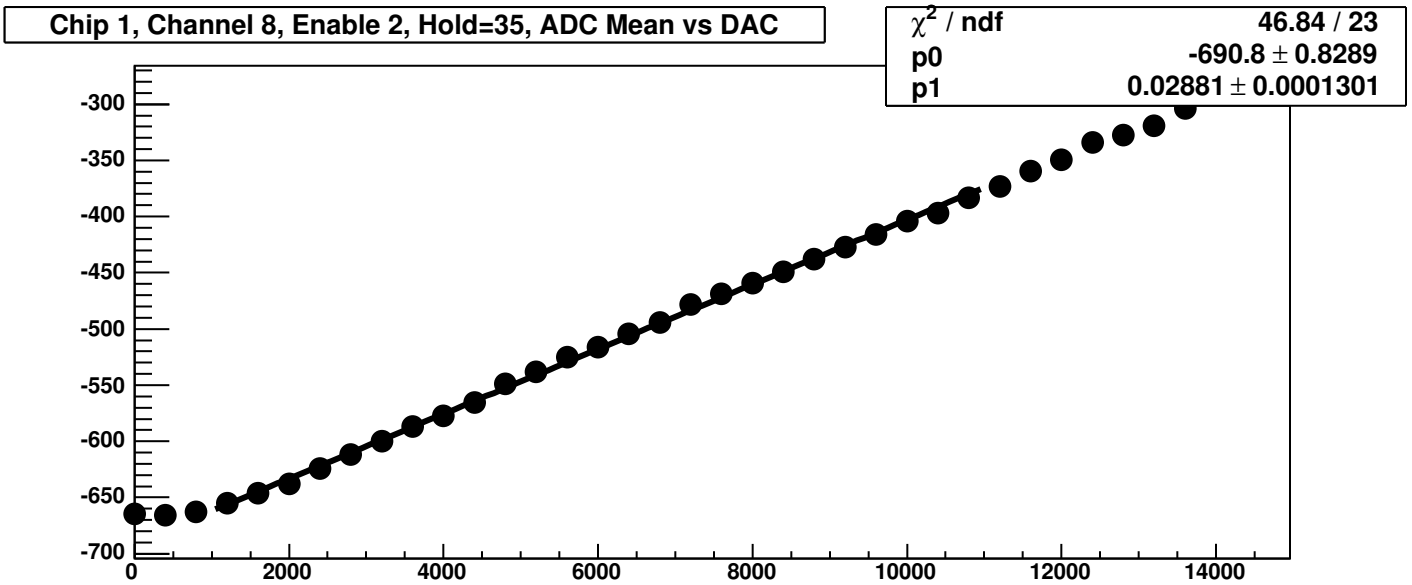


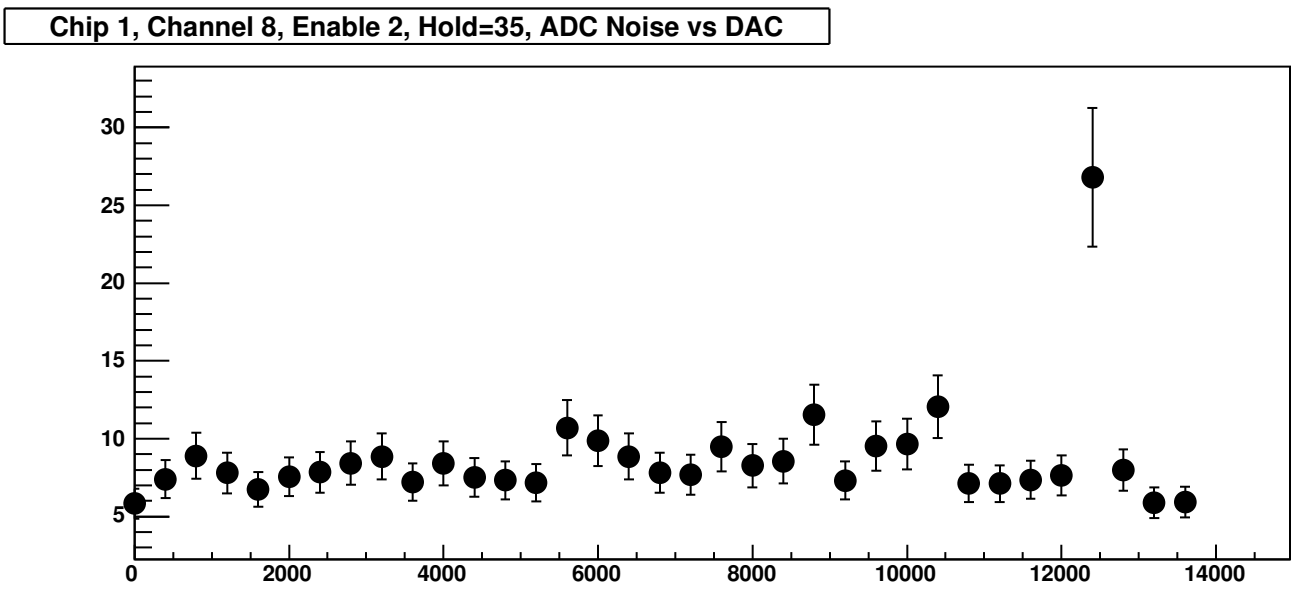
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

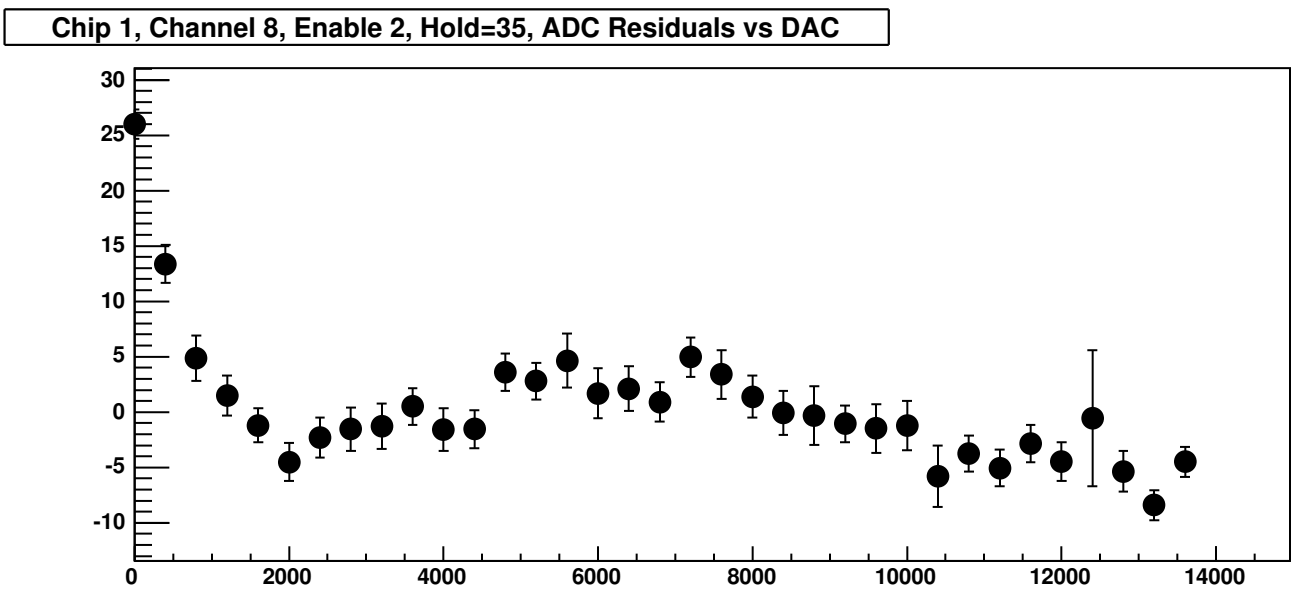


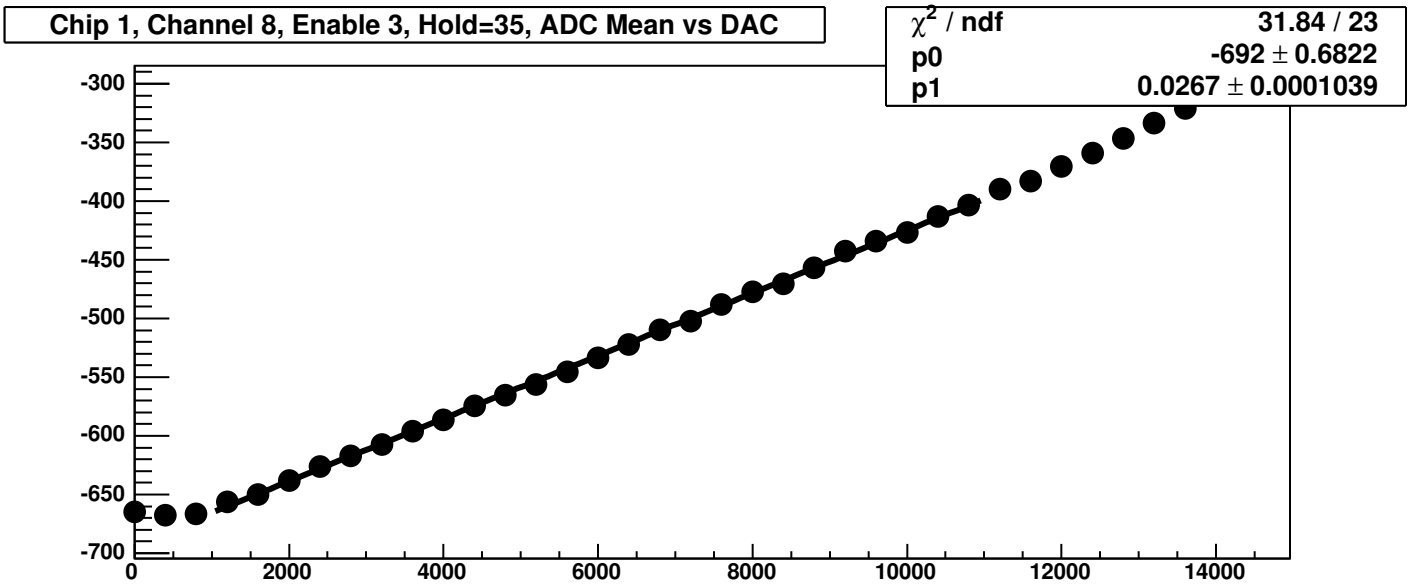
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

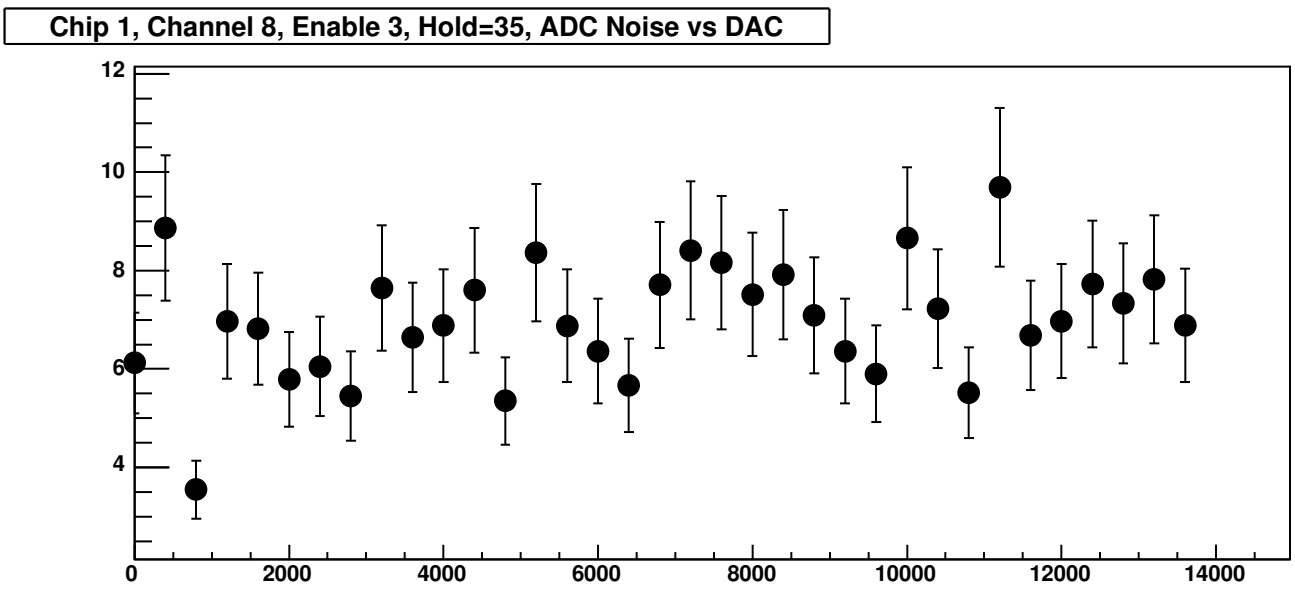


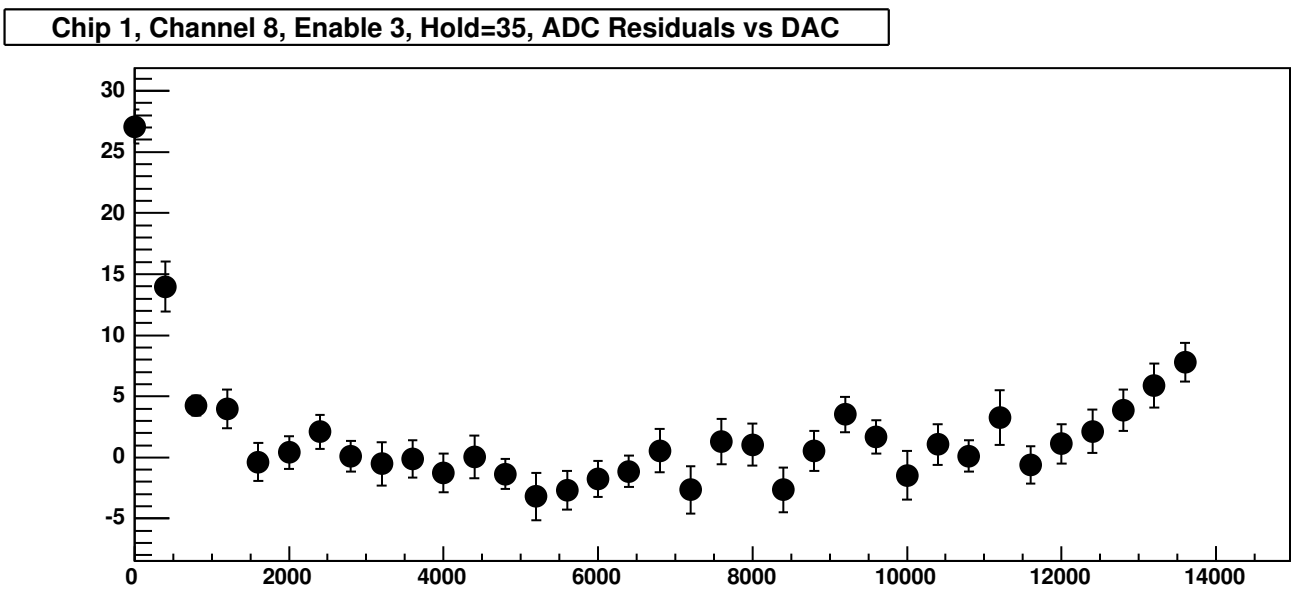

Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 46.84 / 23
p0 -690.8 ± 0.8289
p1 0.02881 ± 0.0001301
-300
-350
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000

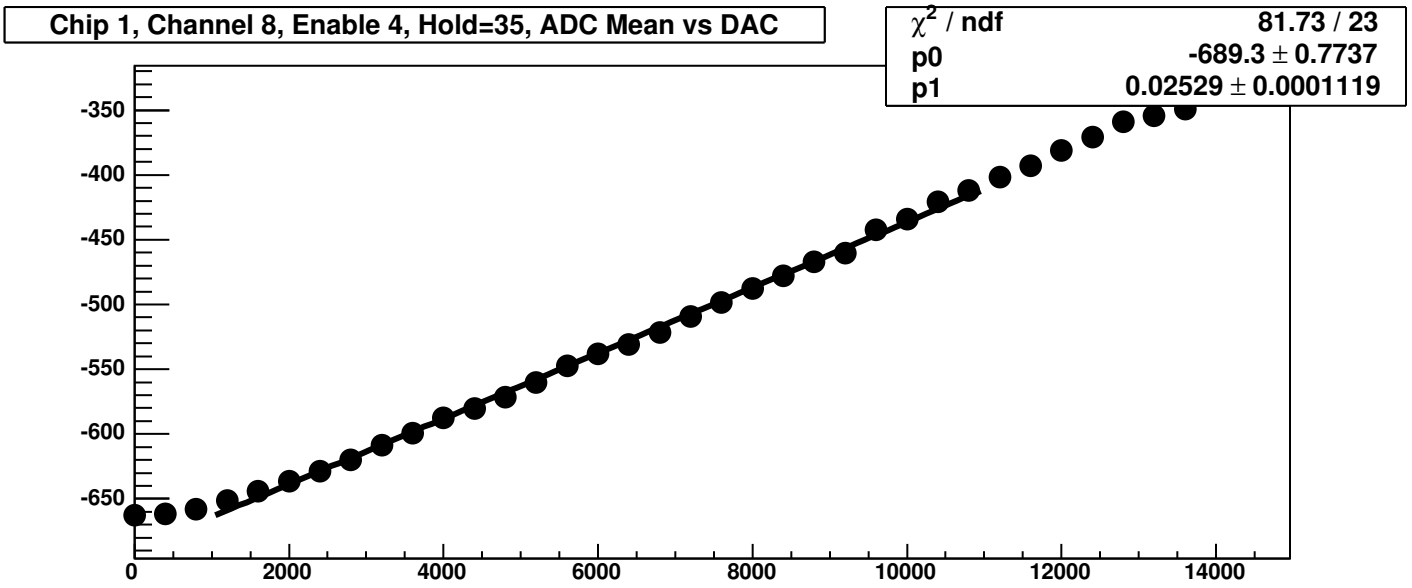

Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

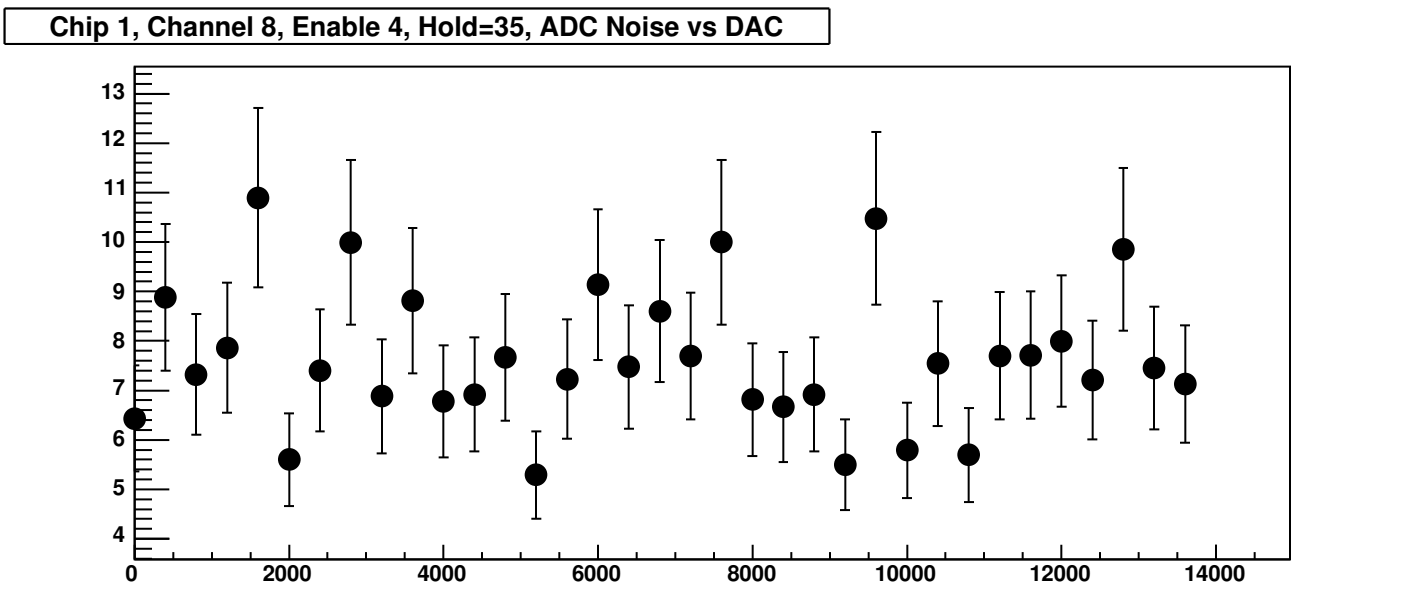

Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

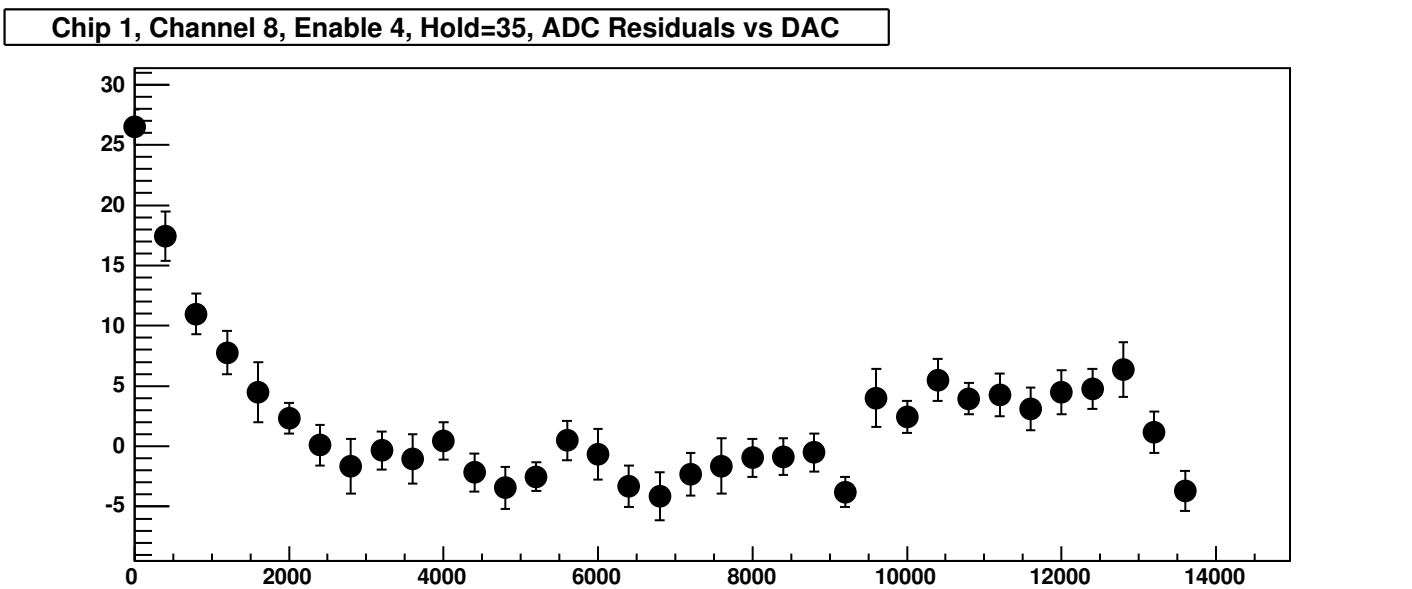

Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.84 / 23
p0 -692 ± 0.6822
p1 0.0267 ± 0.0001039
-300
-350
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000

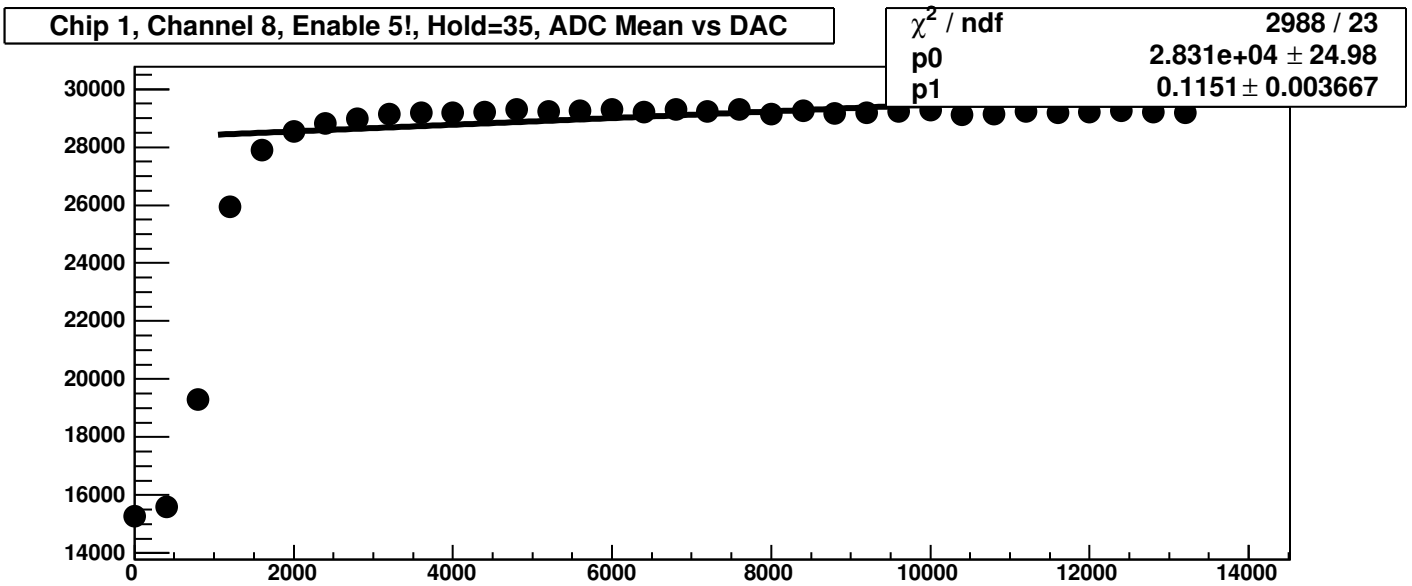

Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

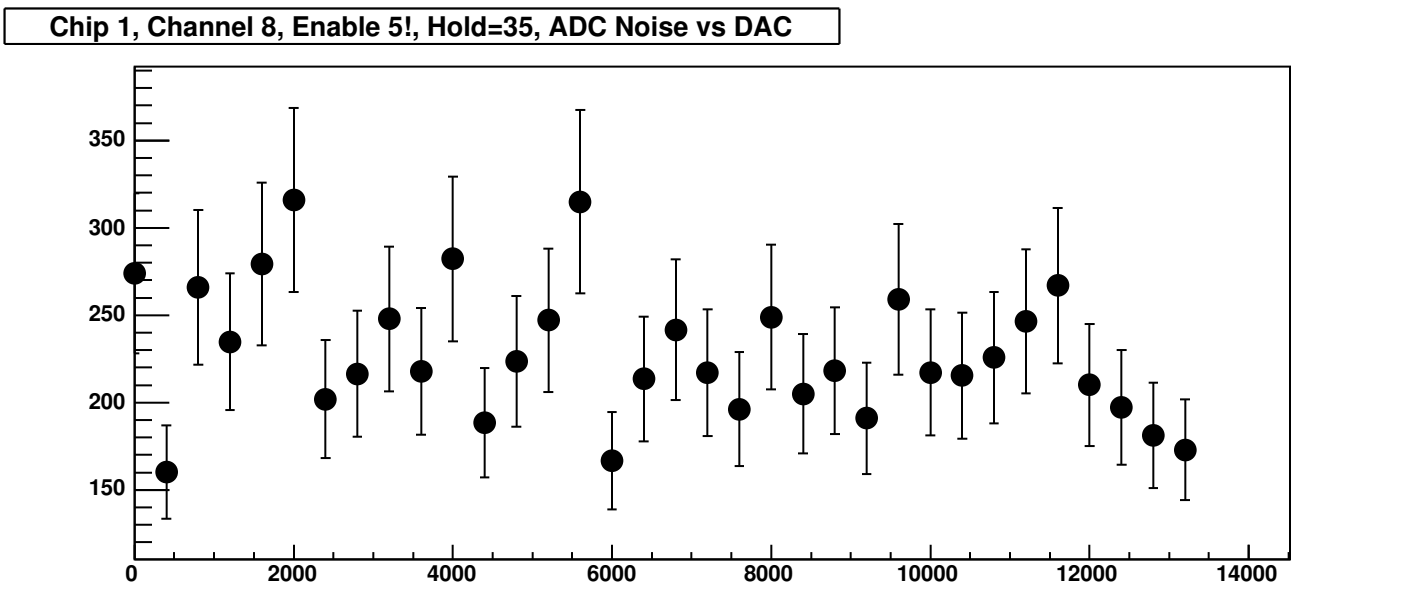

Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

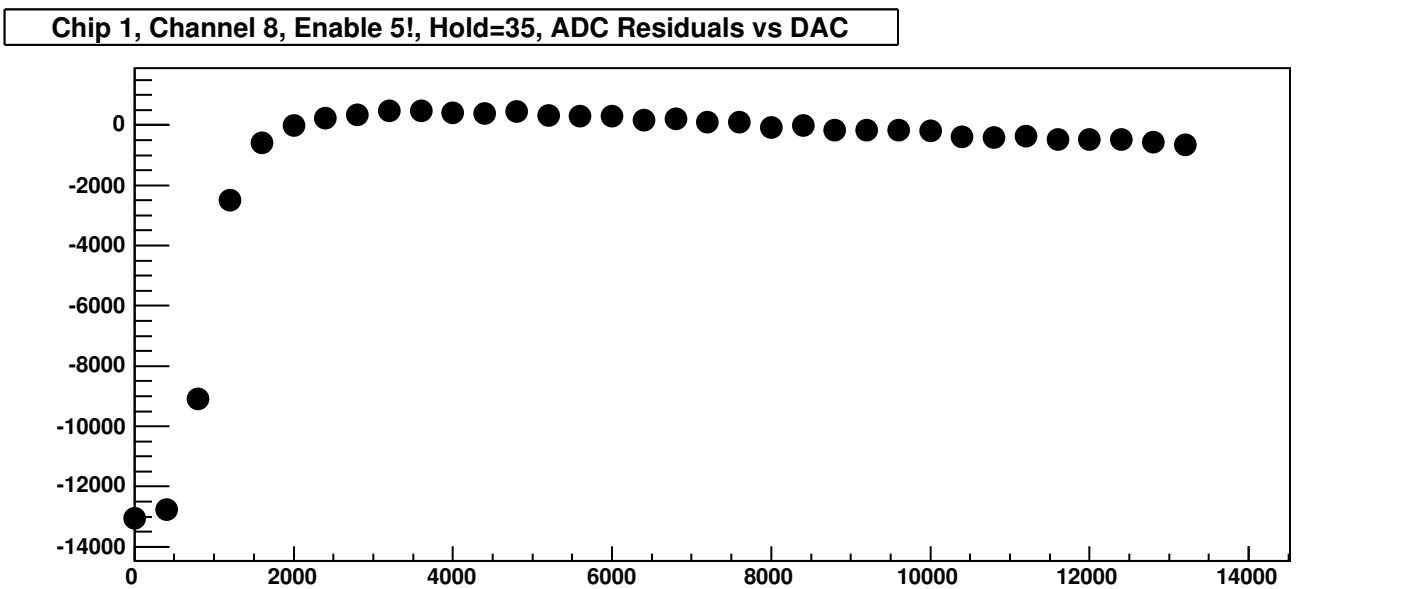

Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 81.73 / 23
p0 -689.3 ± 0.7737
p1 0.02529 ± 0.0001119
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

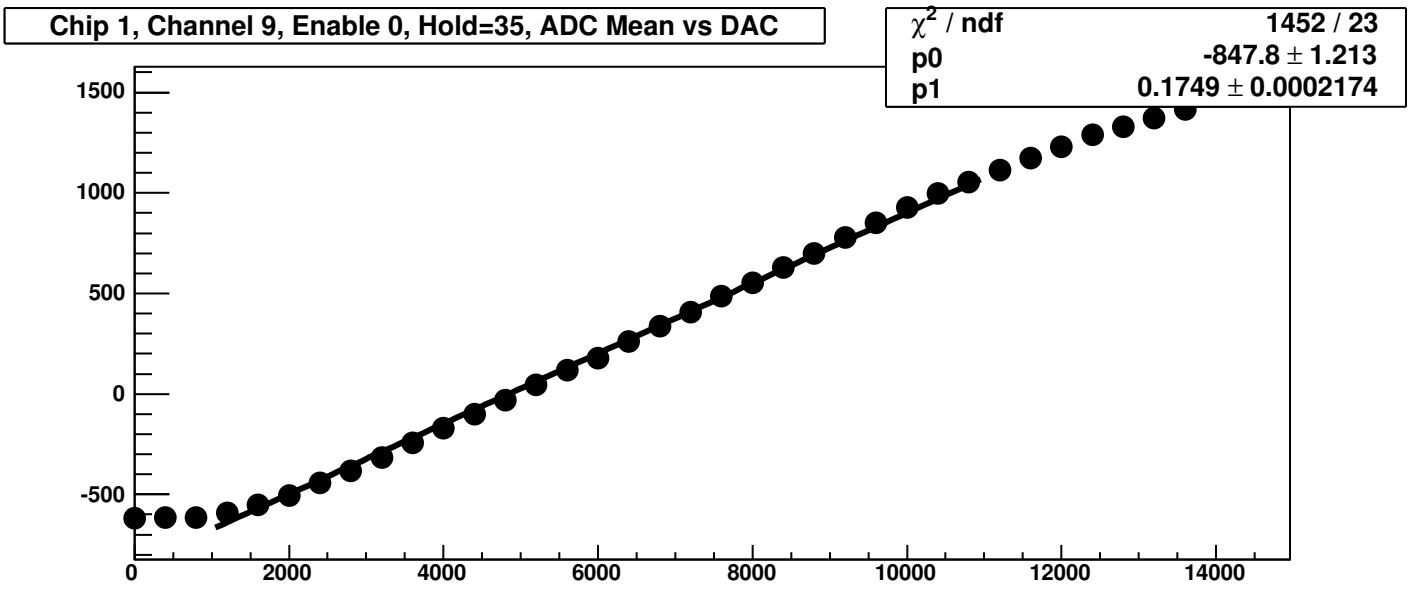

Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

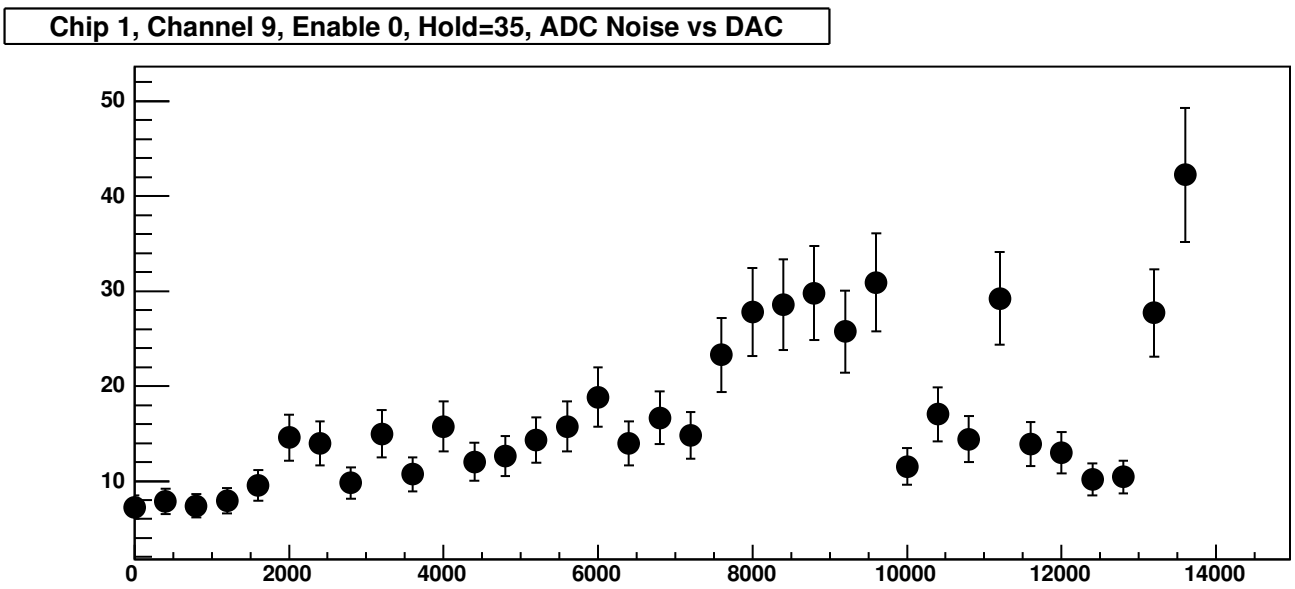

Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

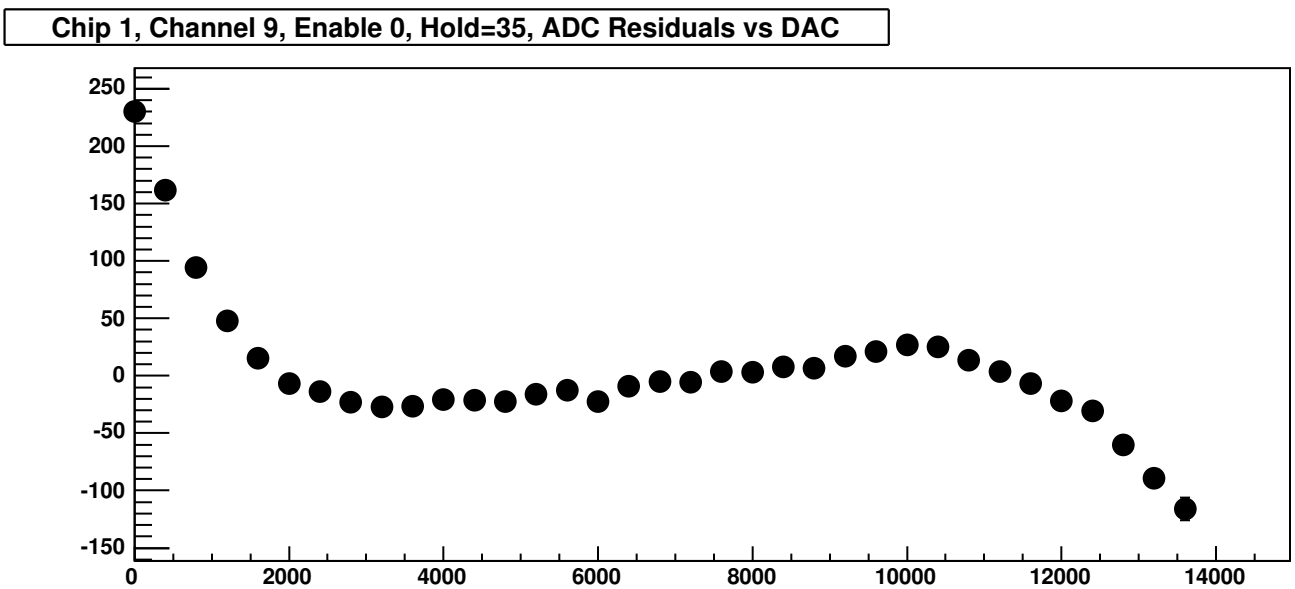

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2988 / 23
p0 2.831e+04 ± 24.98
p1 0.1151 ± 0.003667
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

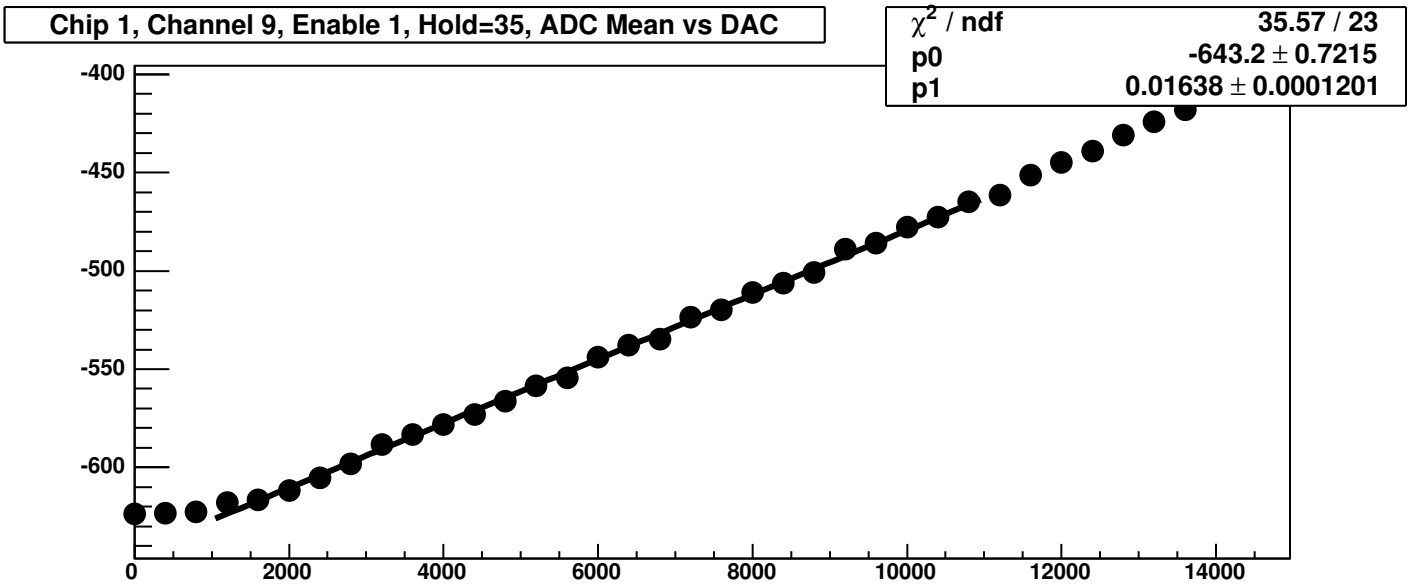

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

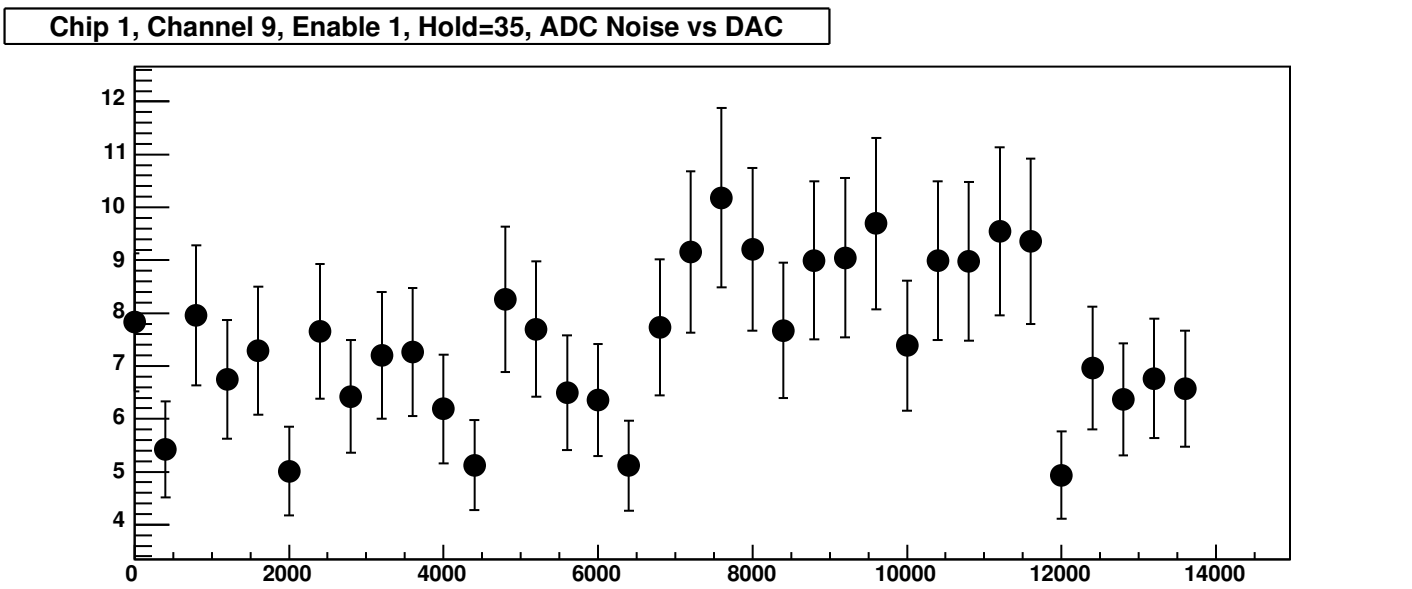

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

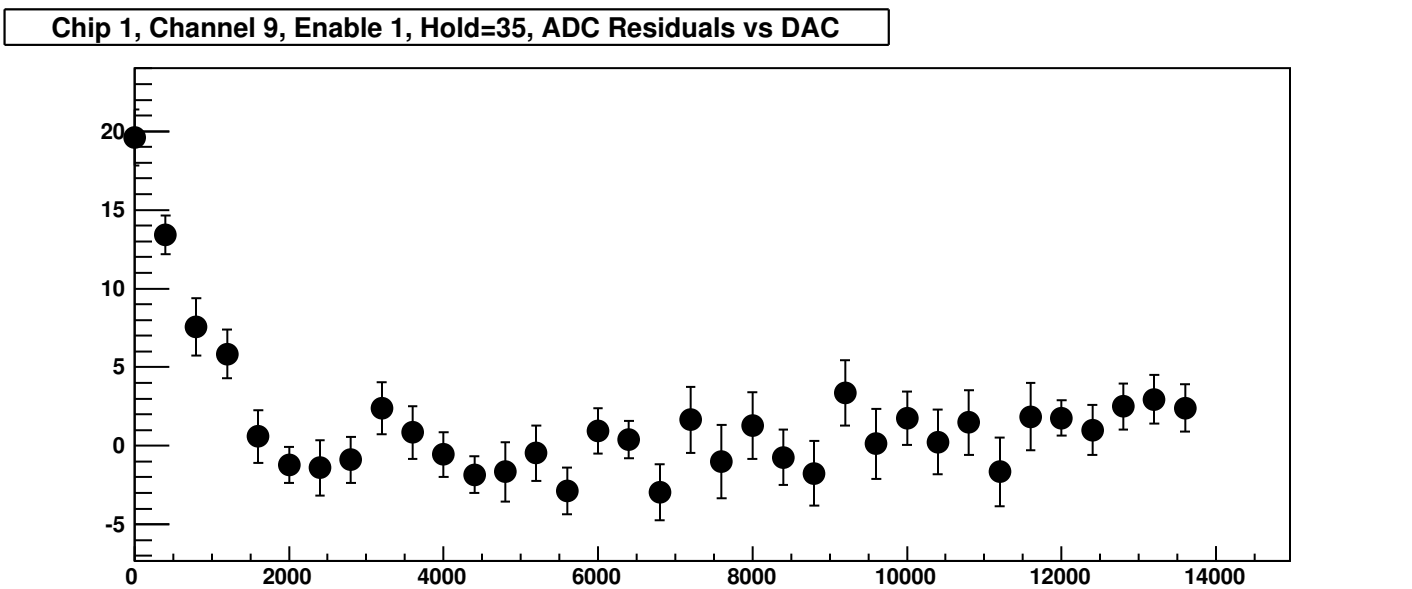

Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1452 / 23
p0 -847.8 ± 1.213
p1 0.1749 ± 0.0002174
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

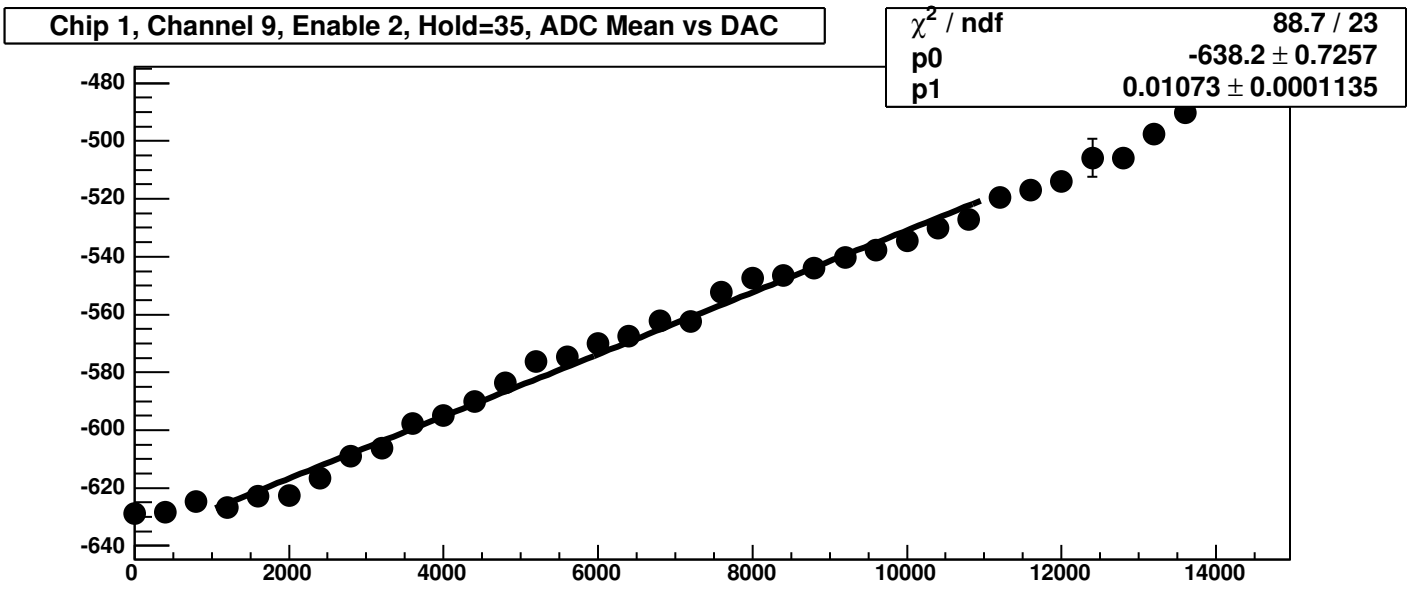

Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

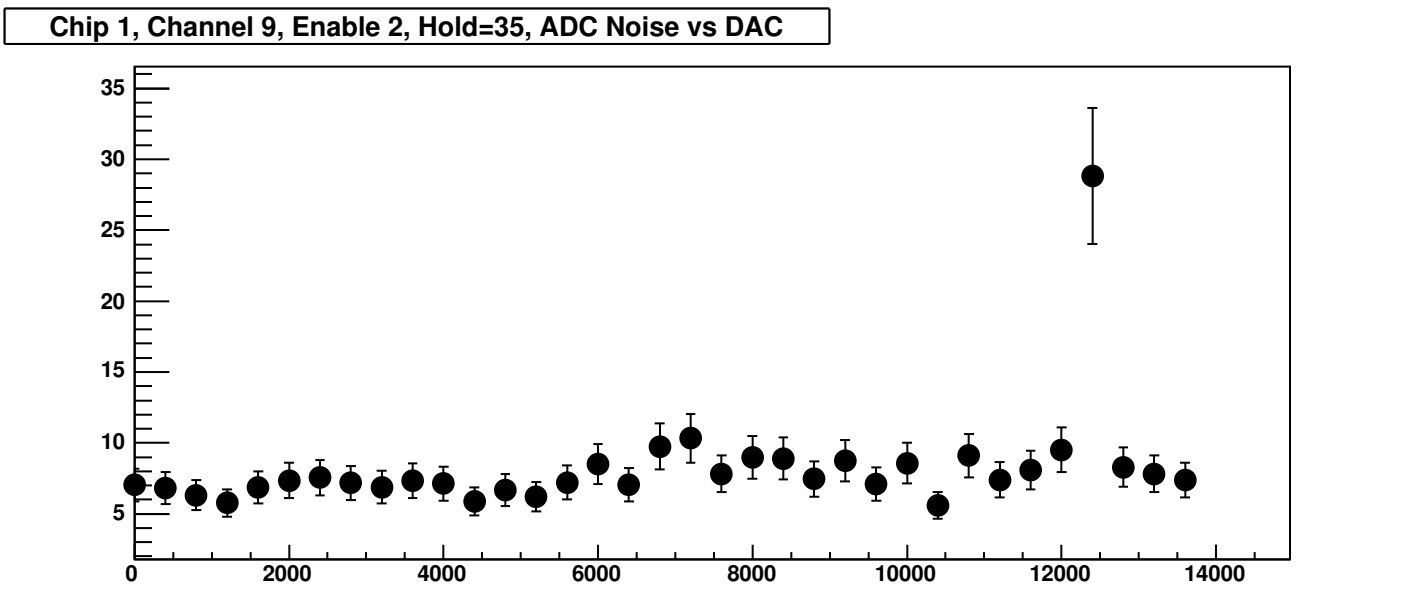

Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

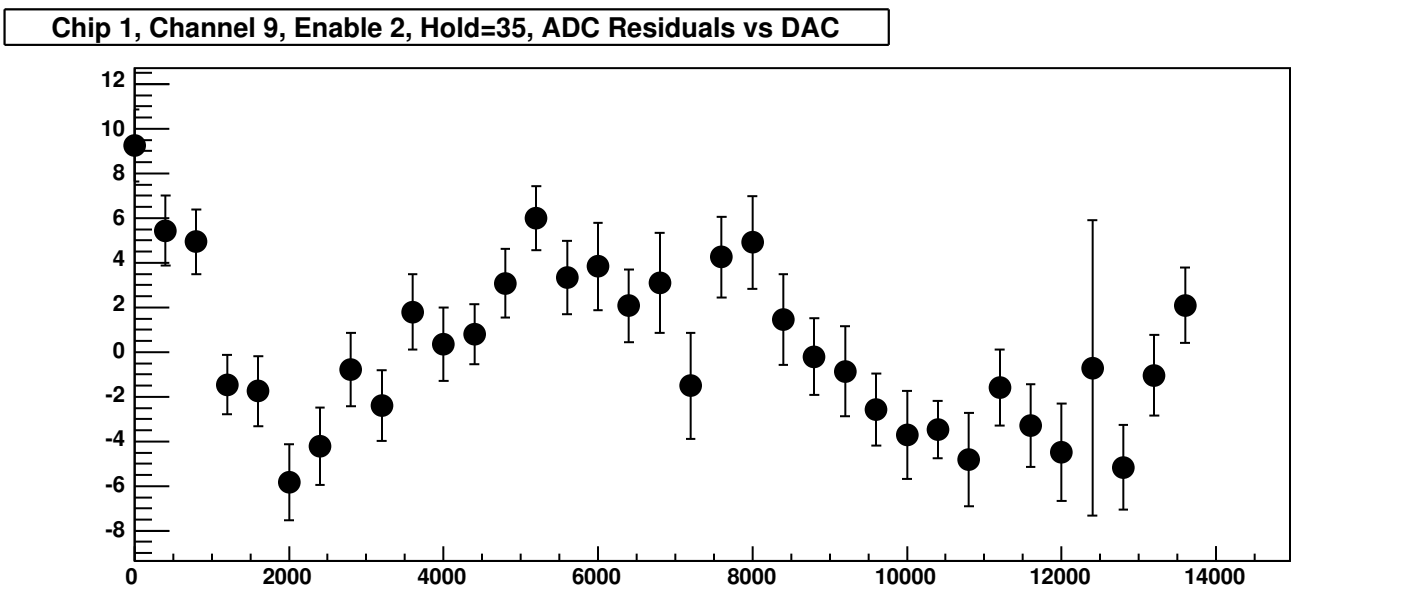

Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.57 / 23
p0 -643.2 ± 0.7215
p1 0.01638 ± 0.0001201
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

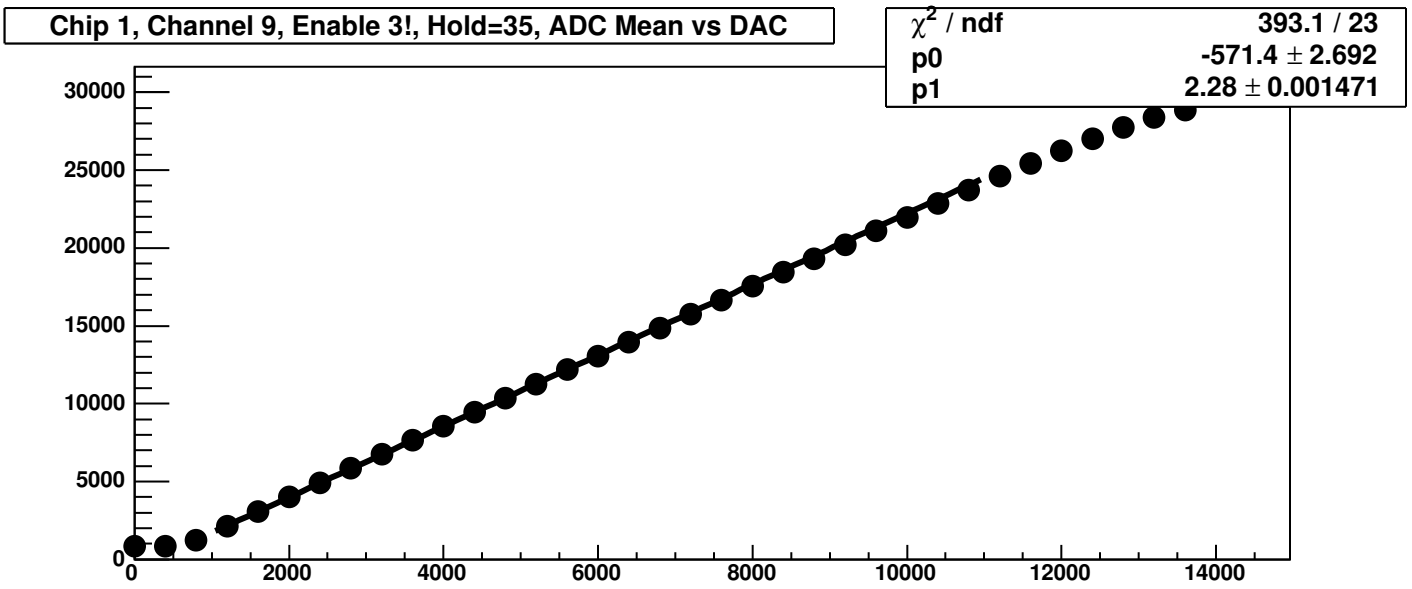

Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

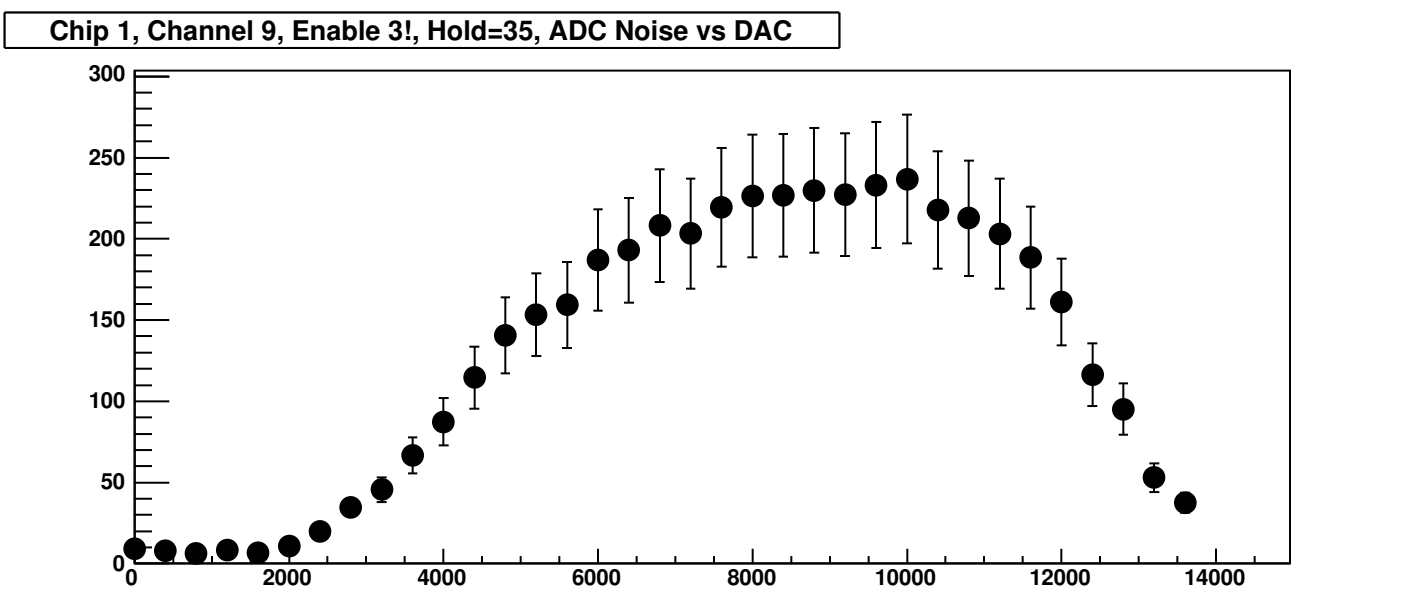

Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

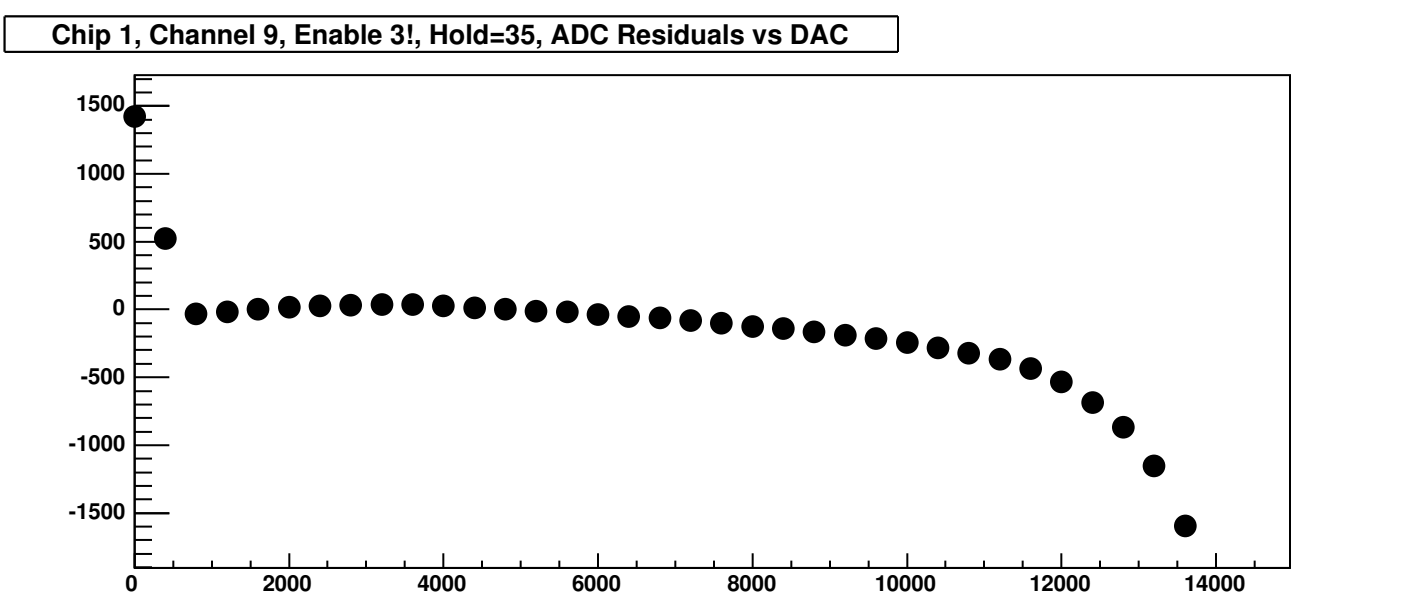

Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 88.7 / 23
p0 -638.2 ± 0.7257
p1 0.01073 ± 0.0001135
-480
-500
-520
-540
-560
-580
-600
-620
-640
0
2000
4000
6000
8000
10000
12000
14000

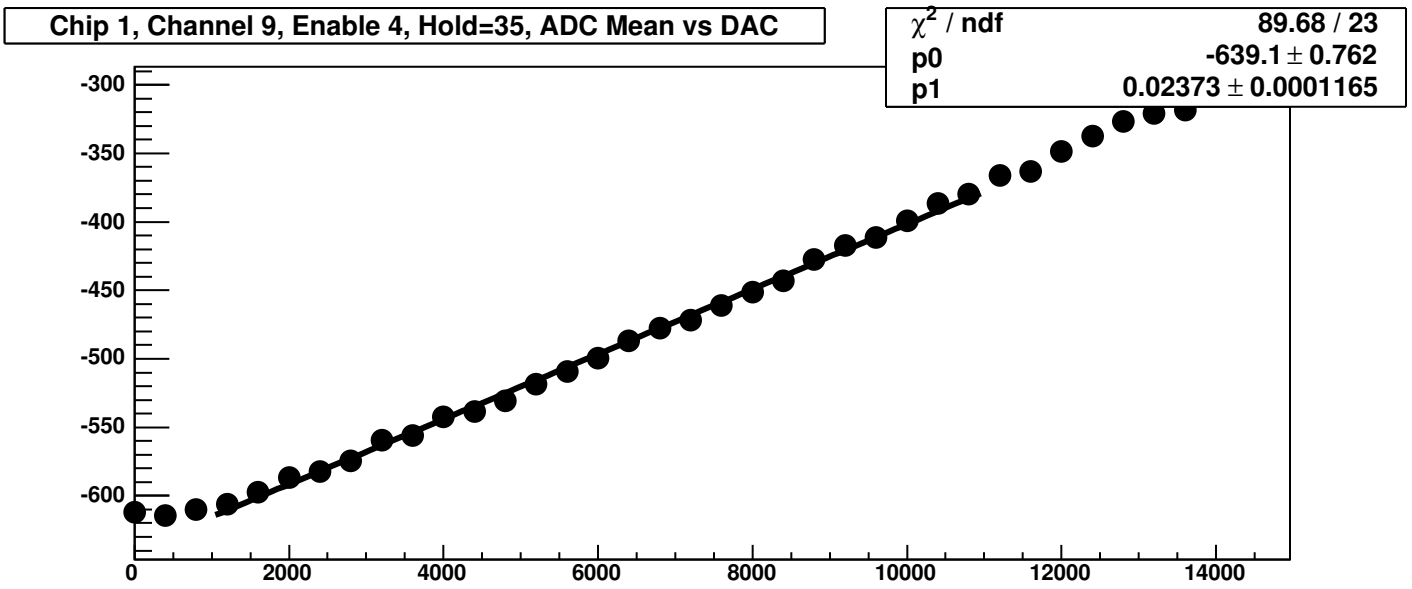

Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

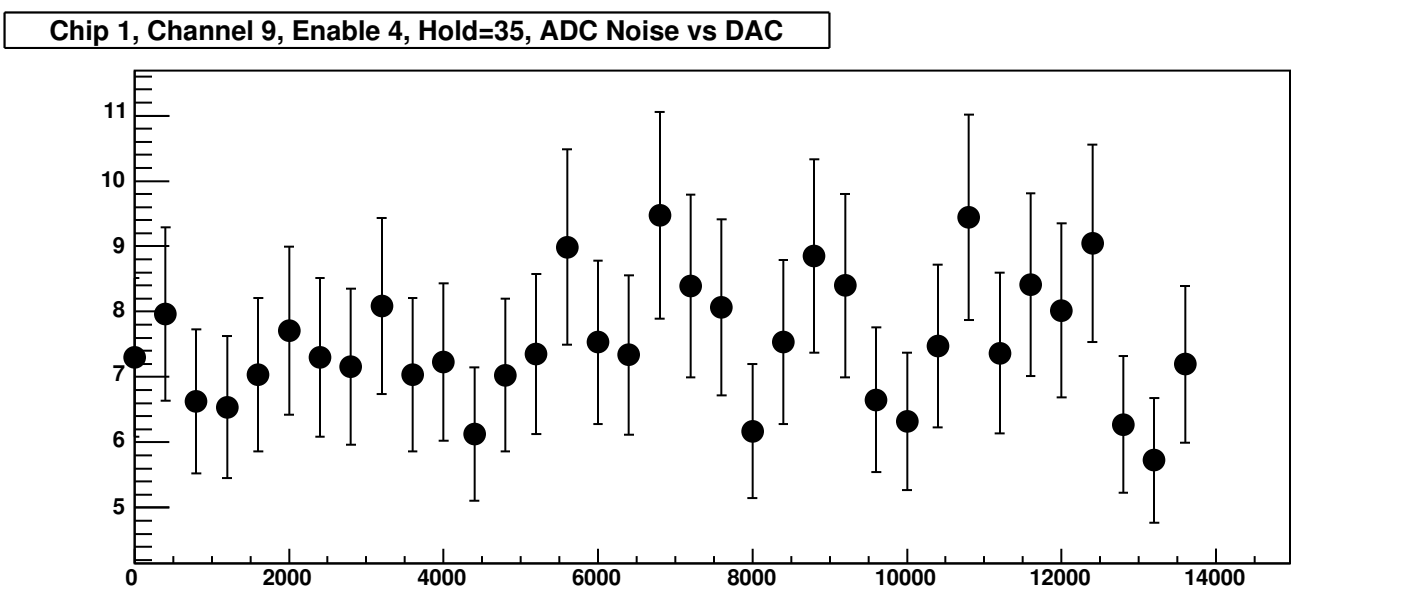

Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

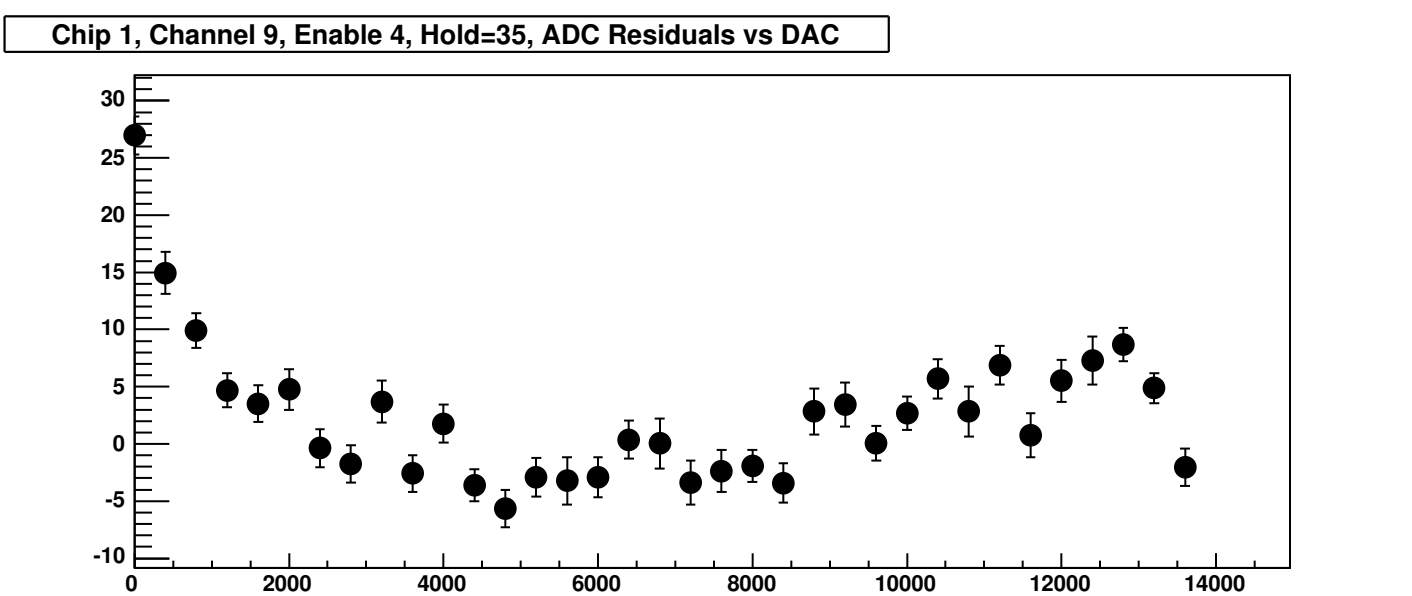

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 393.1 / 23
p0 -571.4 ± 2.692
p1 2.28 ± 0.001471
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

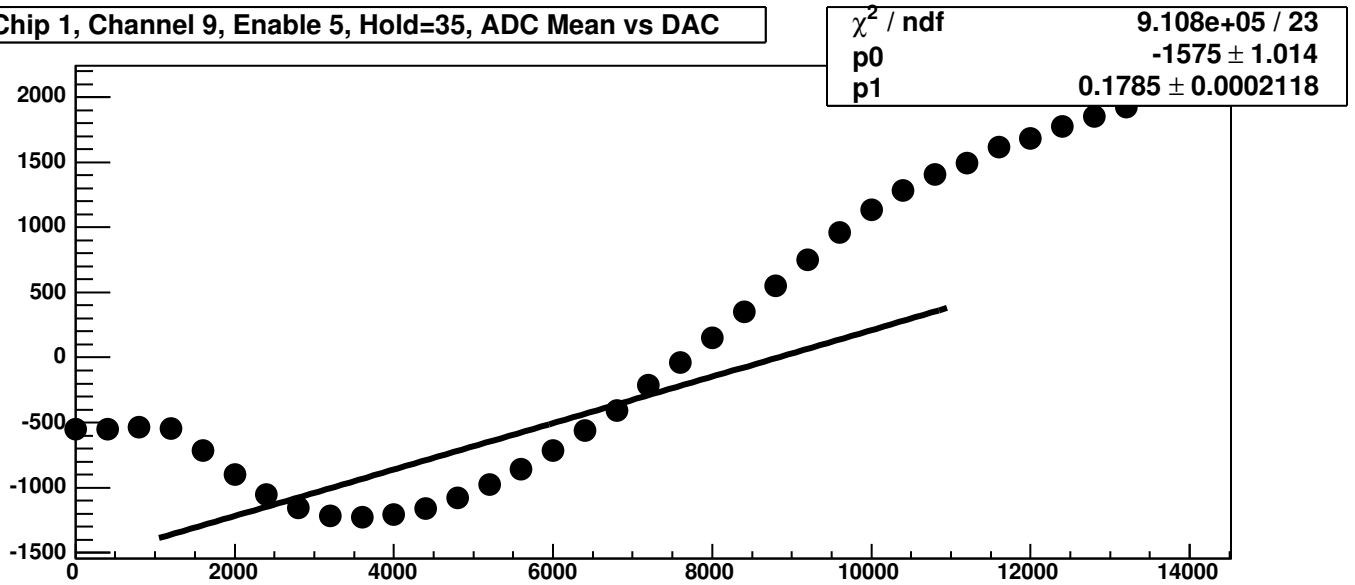

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 89.68 / 23
p0 -639.1 ± 0.762
p1 0.02373 ± 0.0001165
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

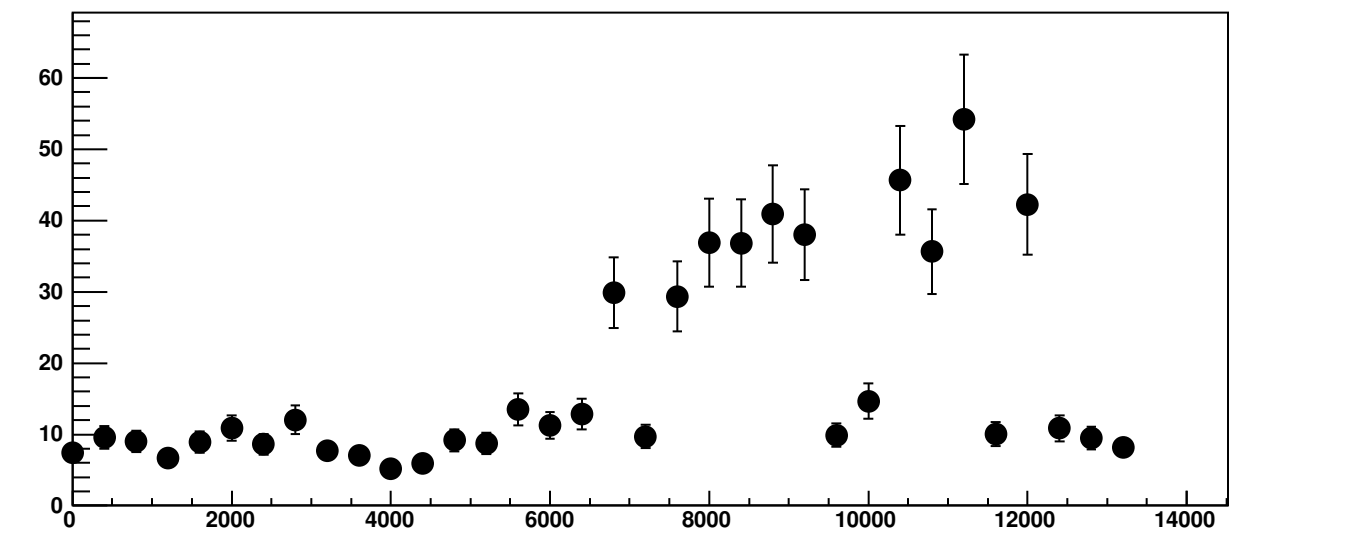

Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

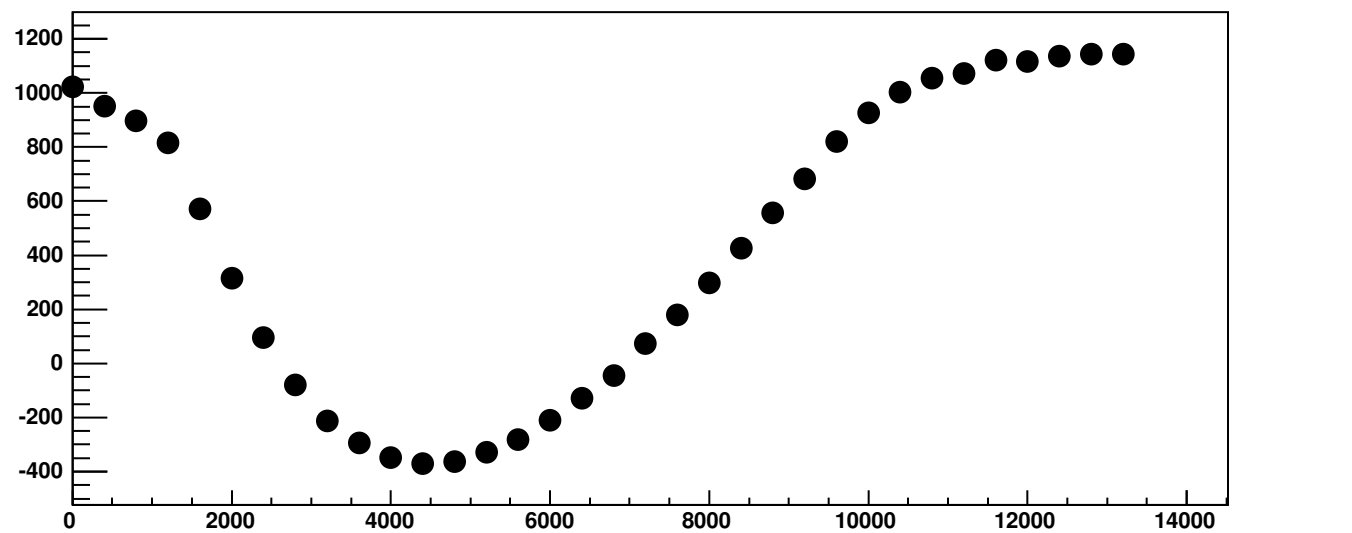
Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

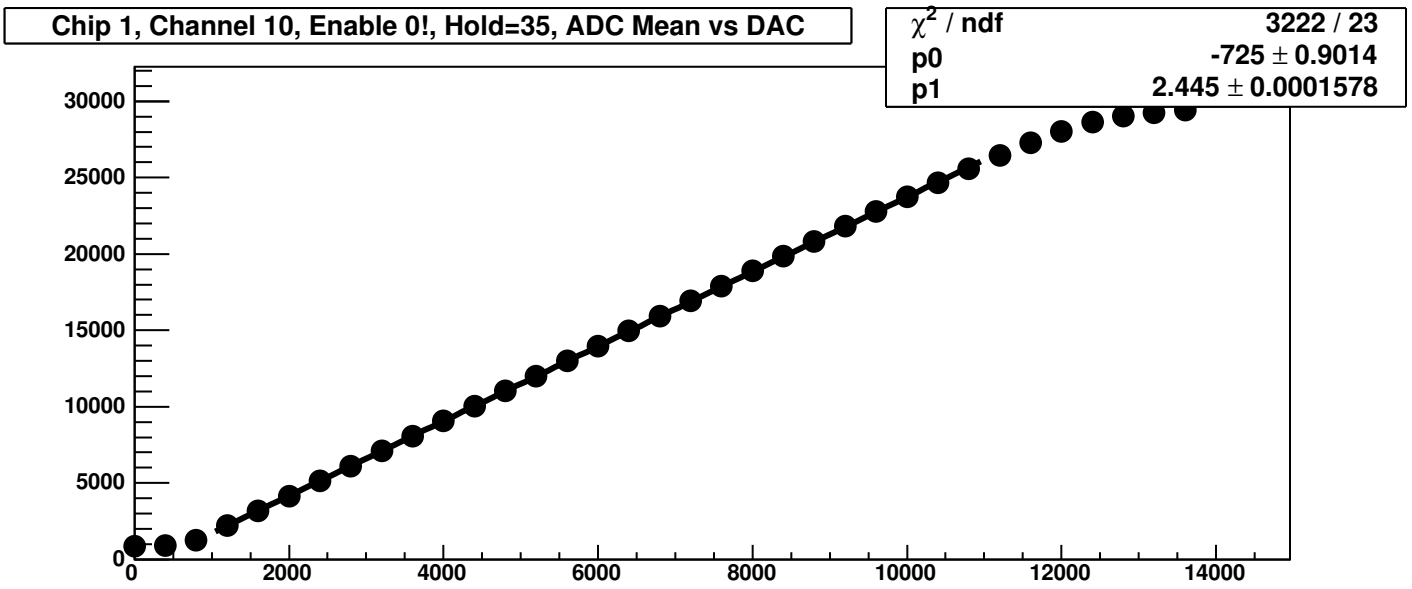


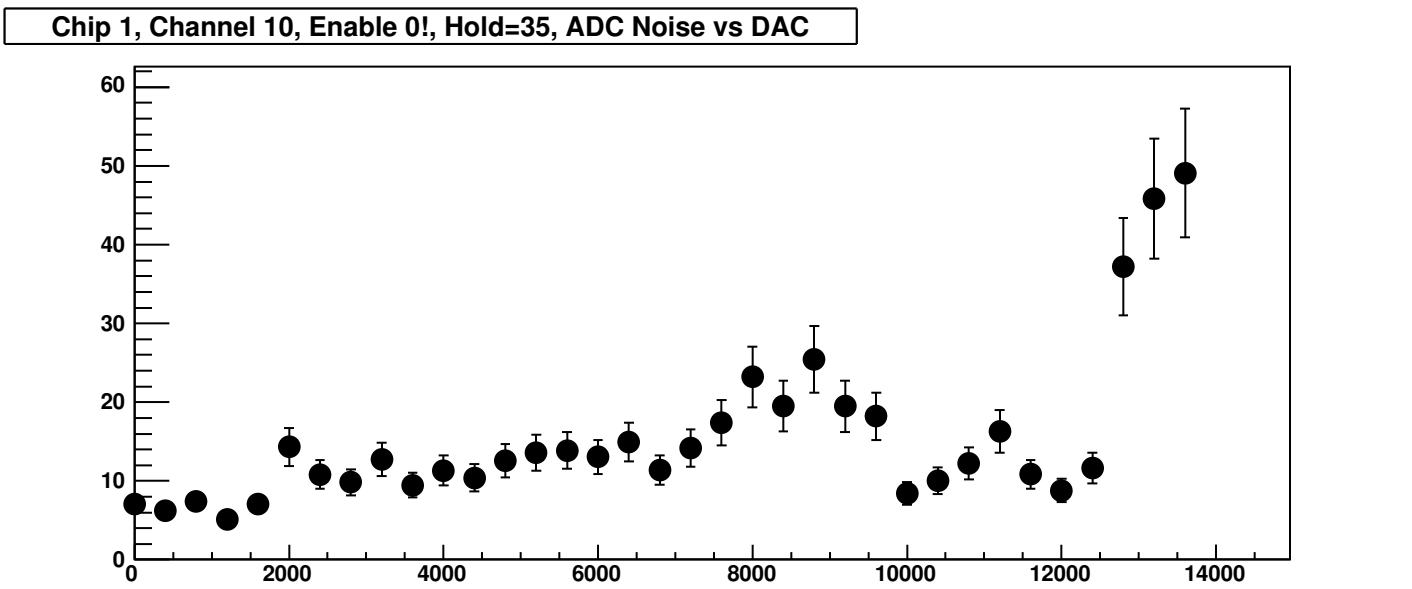
Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

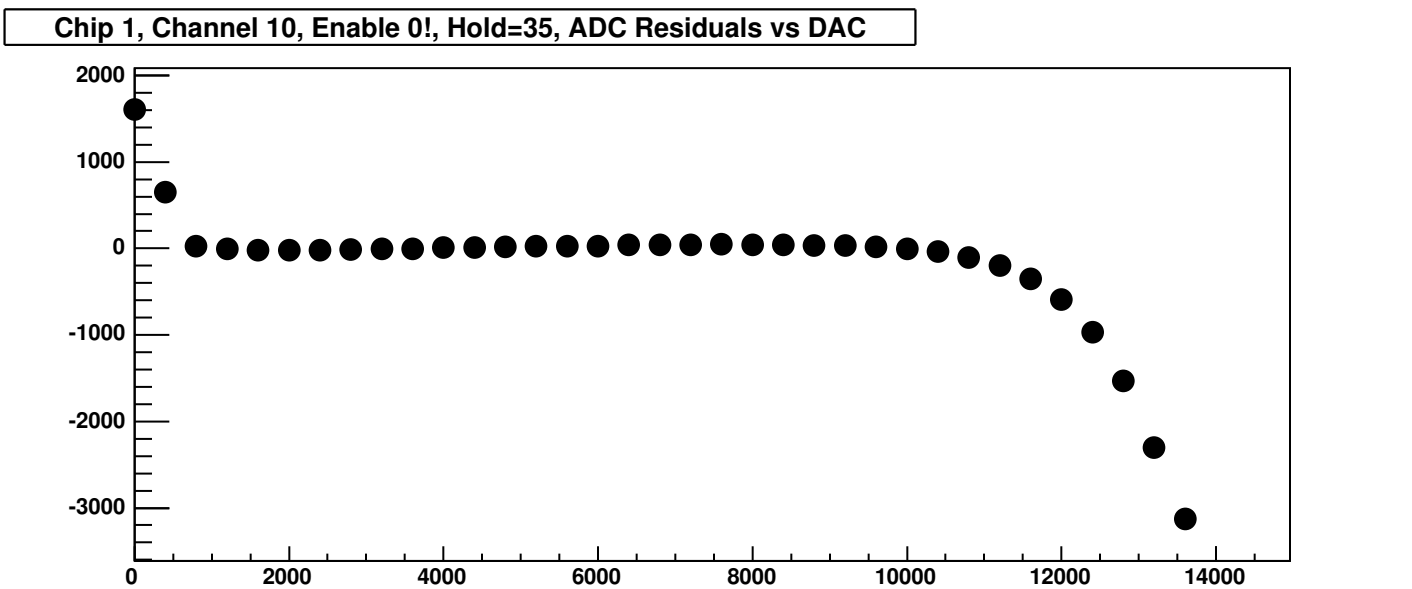


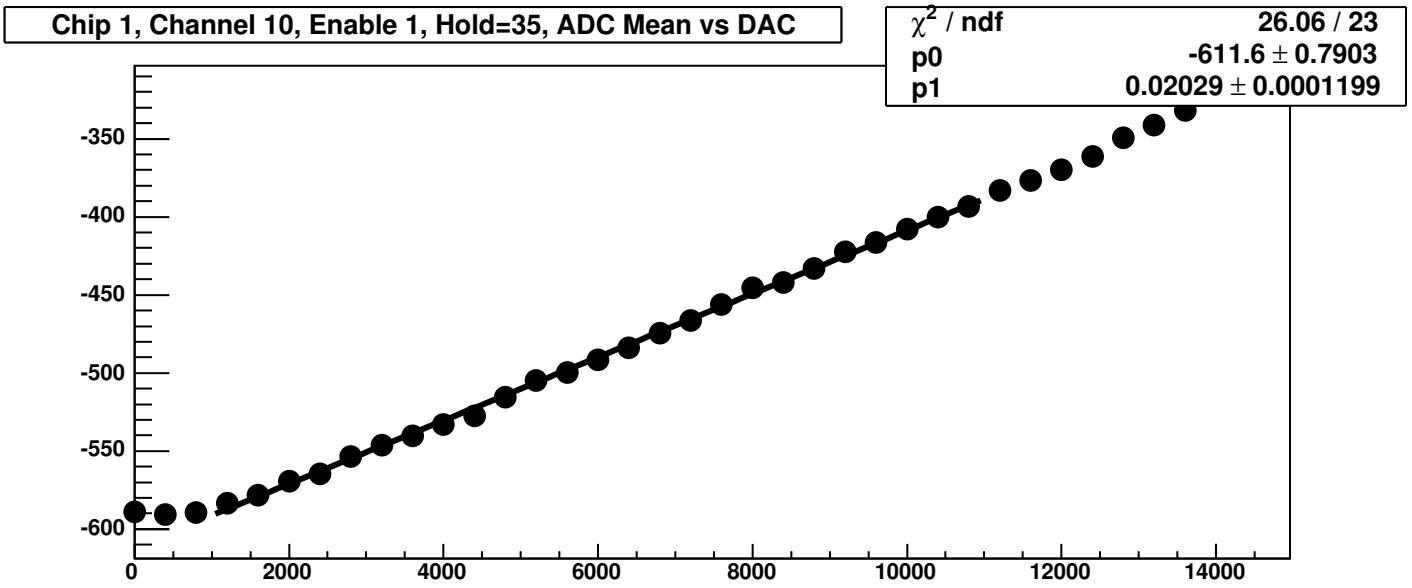
Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

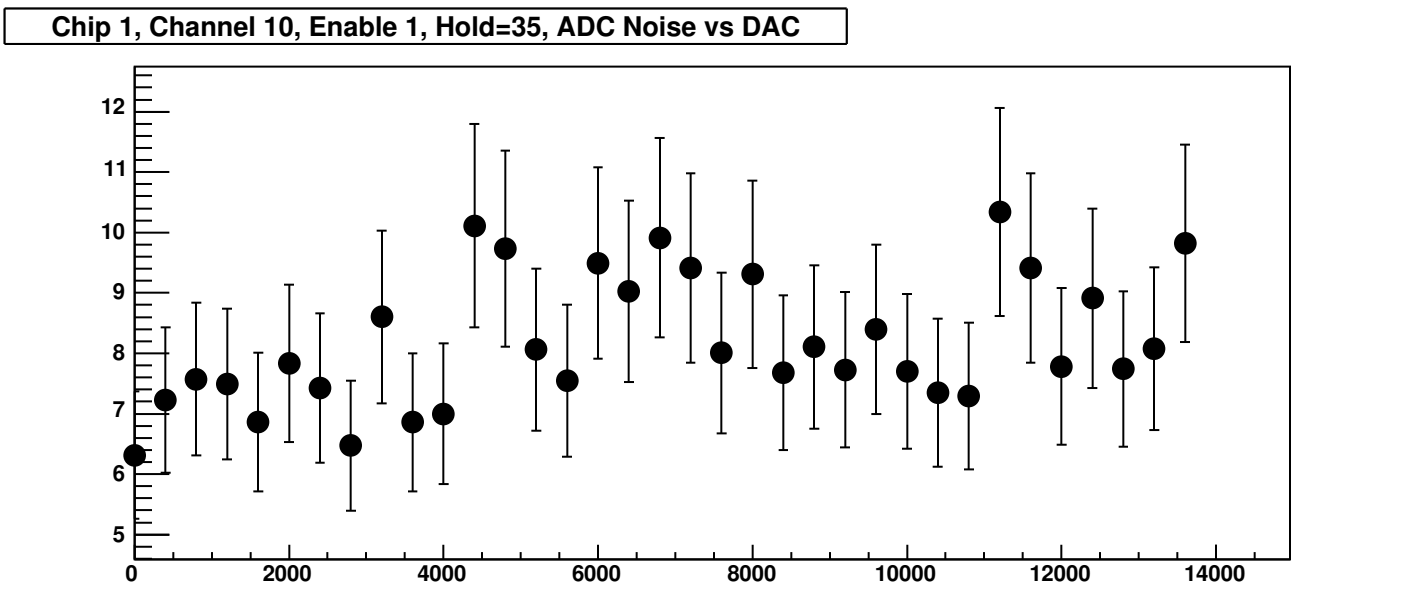


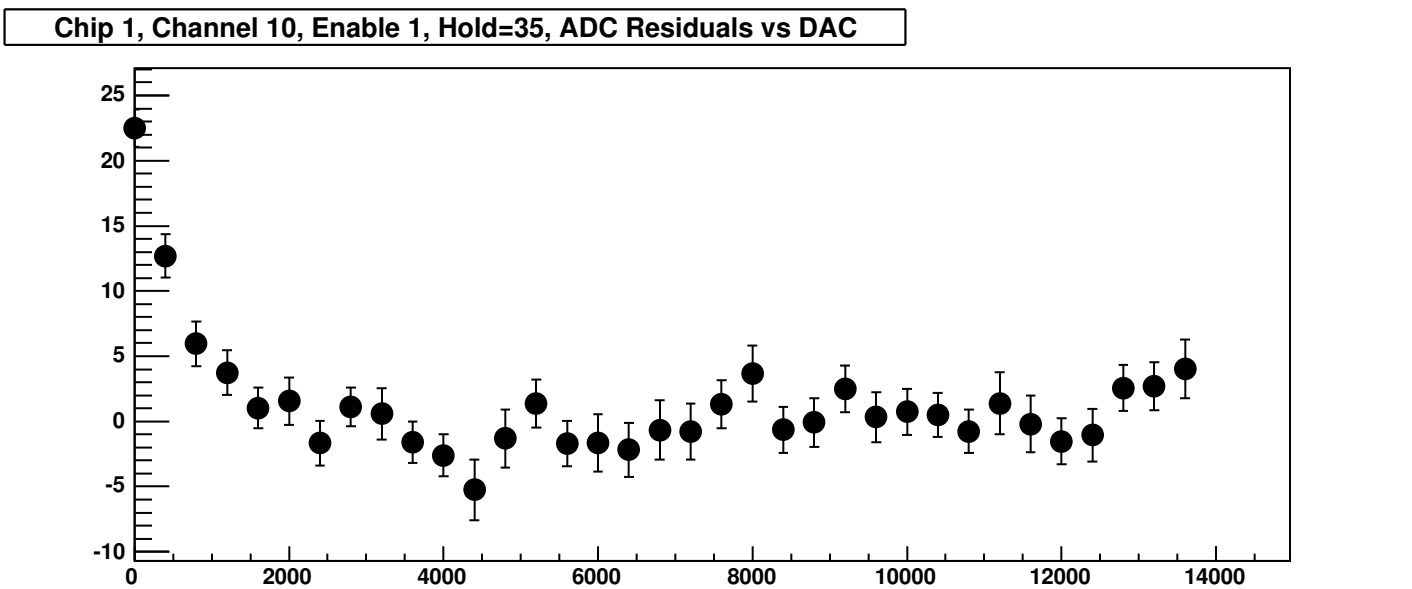

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 3222 / 23
p0 -725 ± 0.9014
p1 2.445 ± 0.0001578
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

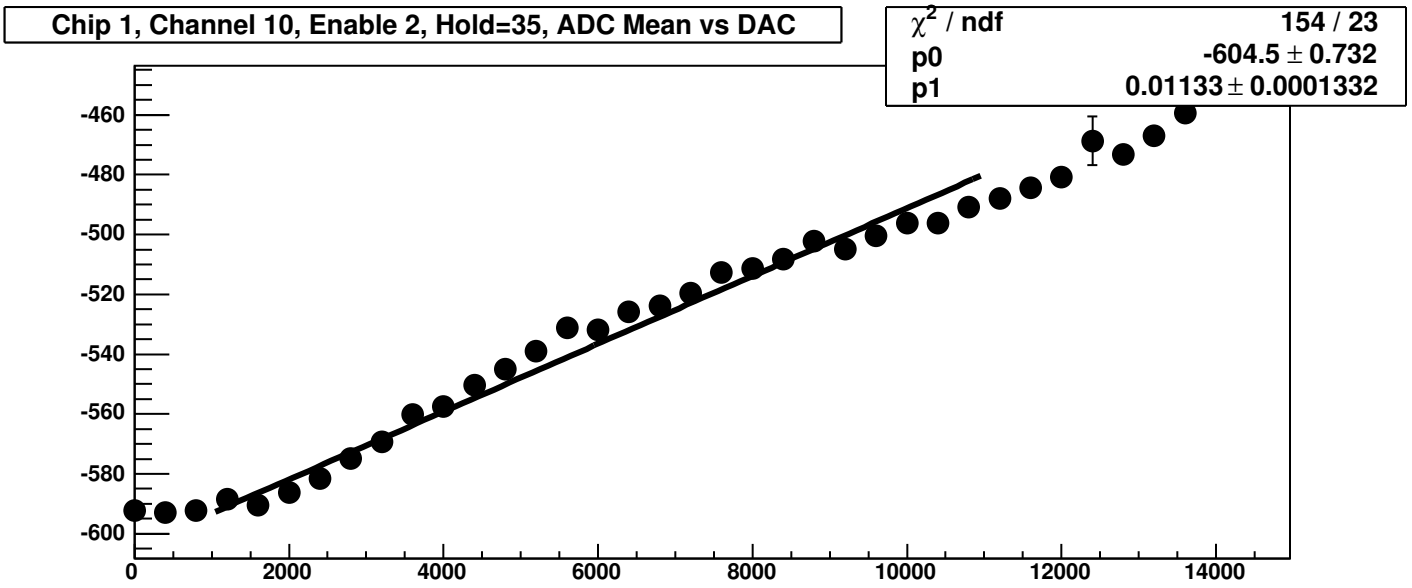

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

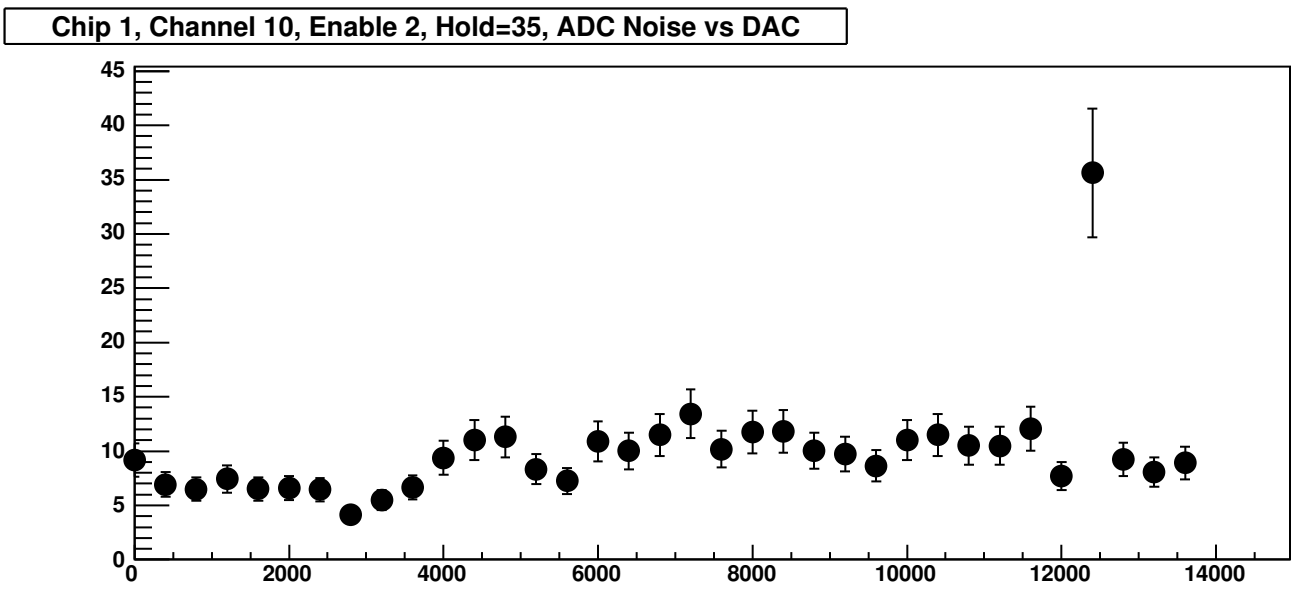

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

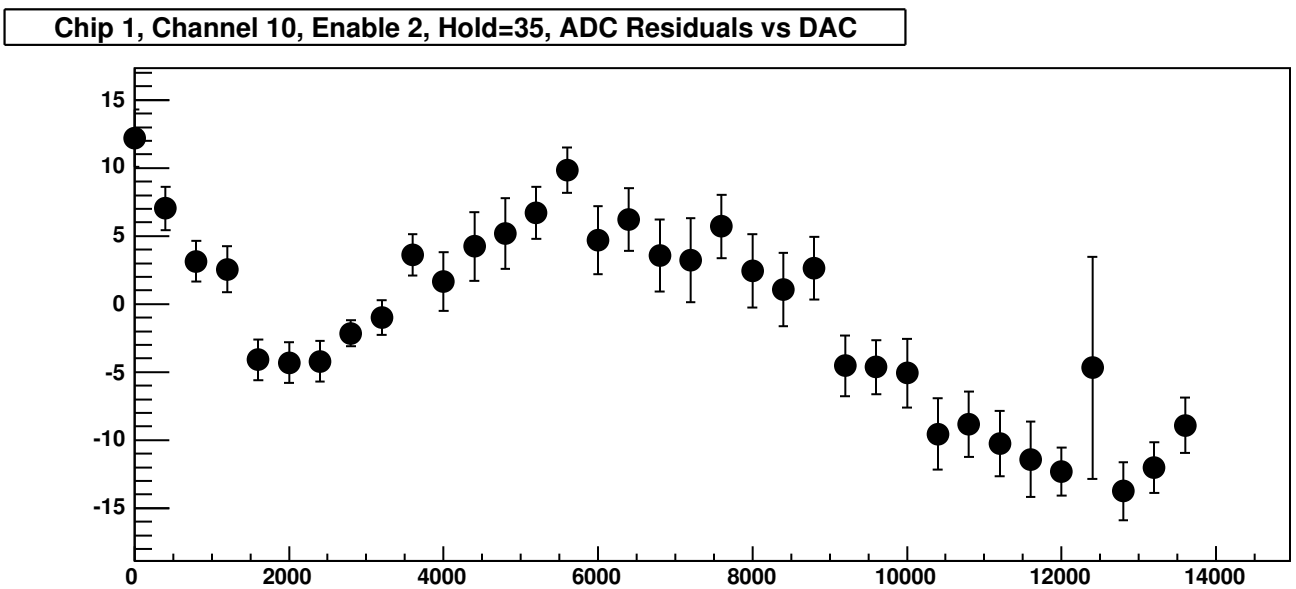

Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.06 / 23
p0 -611.6 ± 0.7903
p1 0.02029 ± 0.0001199
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

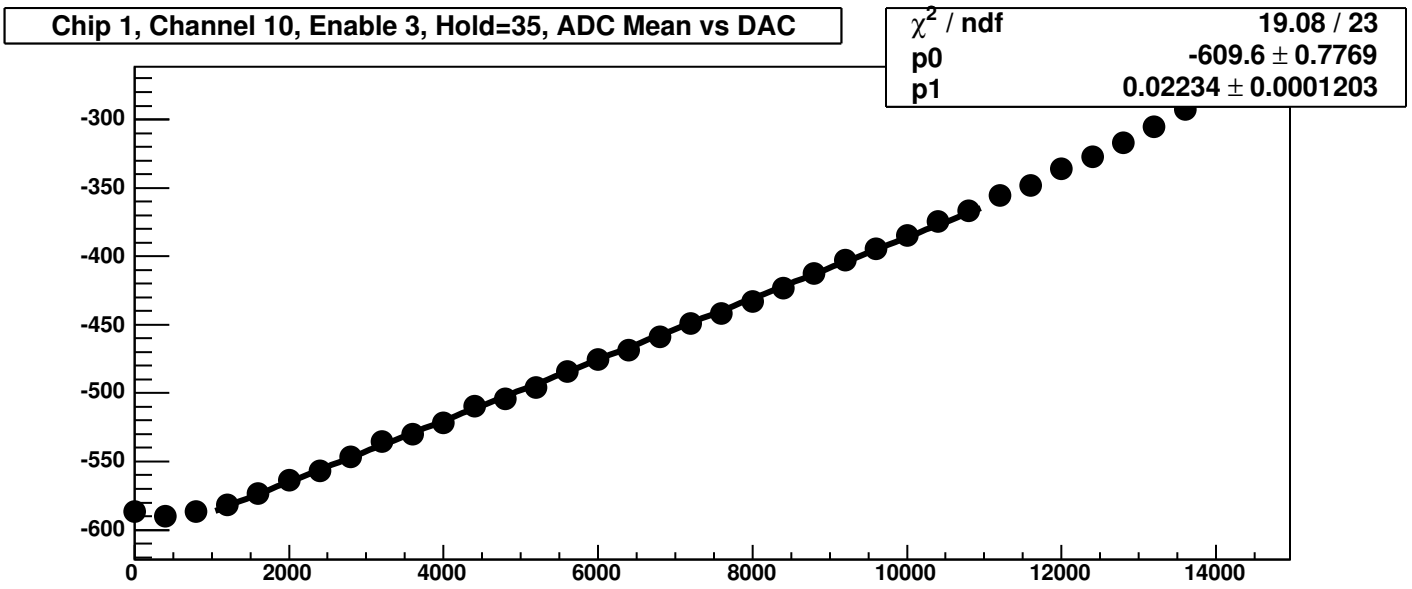

Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

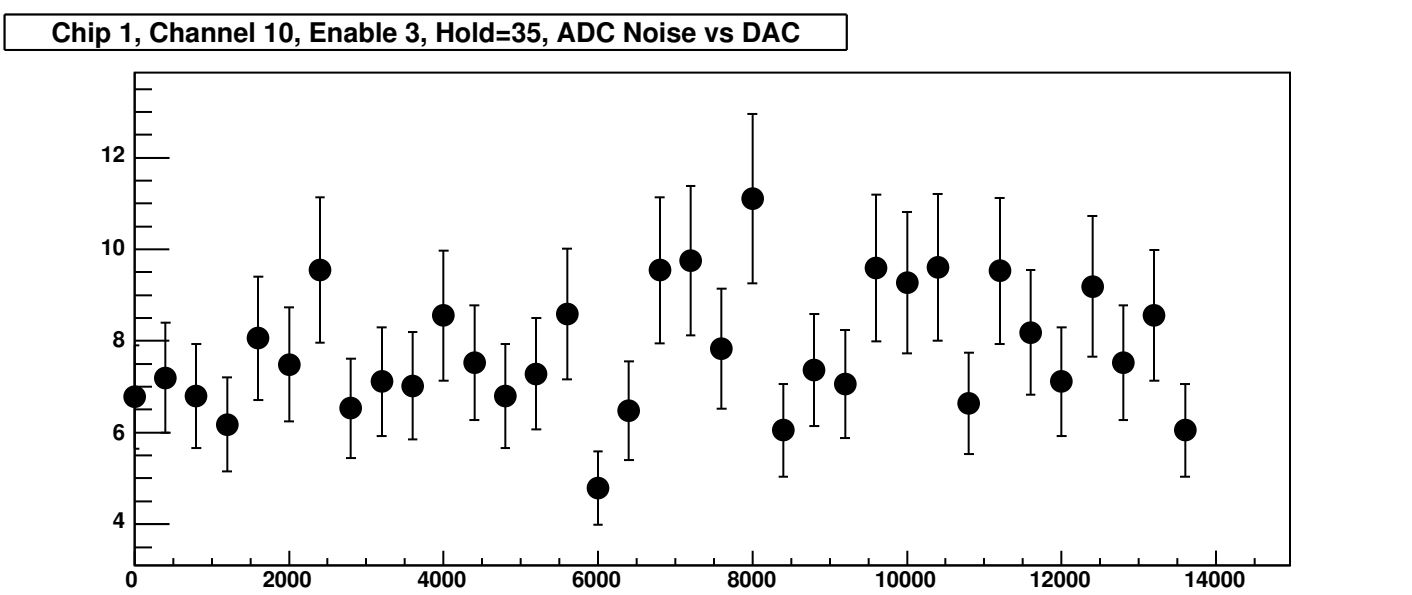

Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

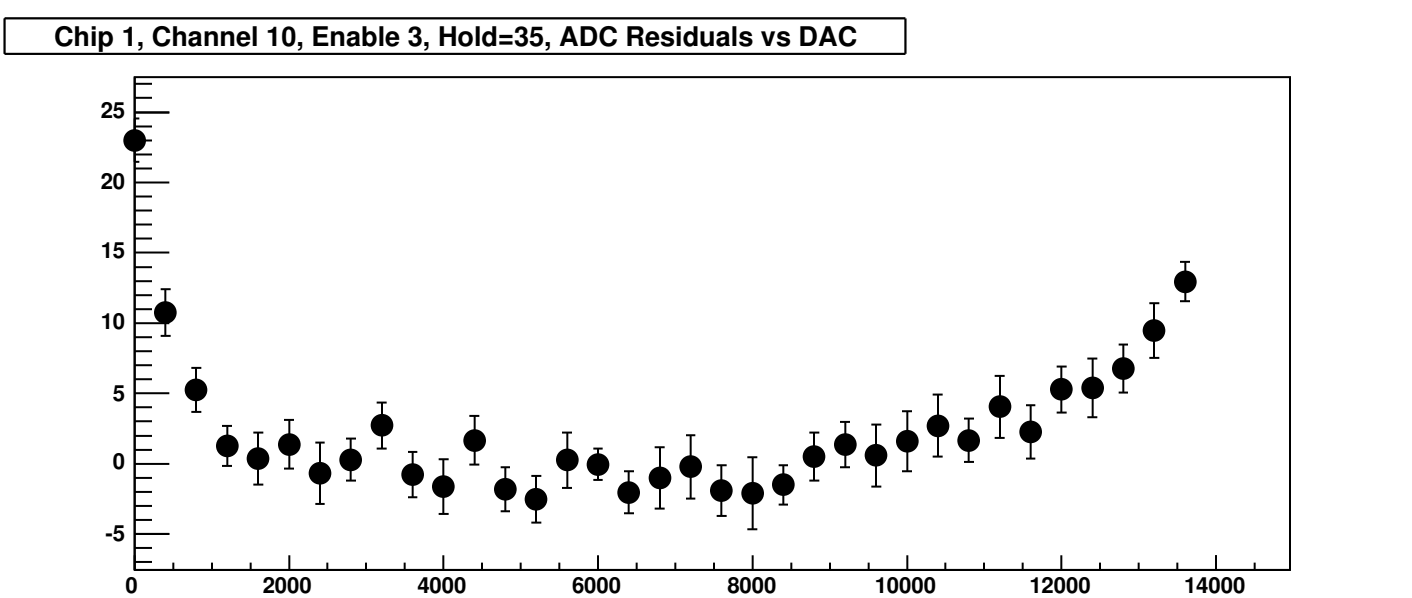

Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 154 / 23
p0 -604.5 ± 0.732
p1 0.01133 ± 0.0001332
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

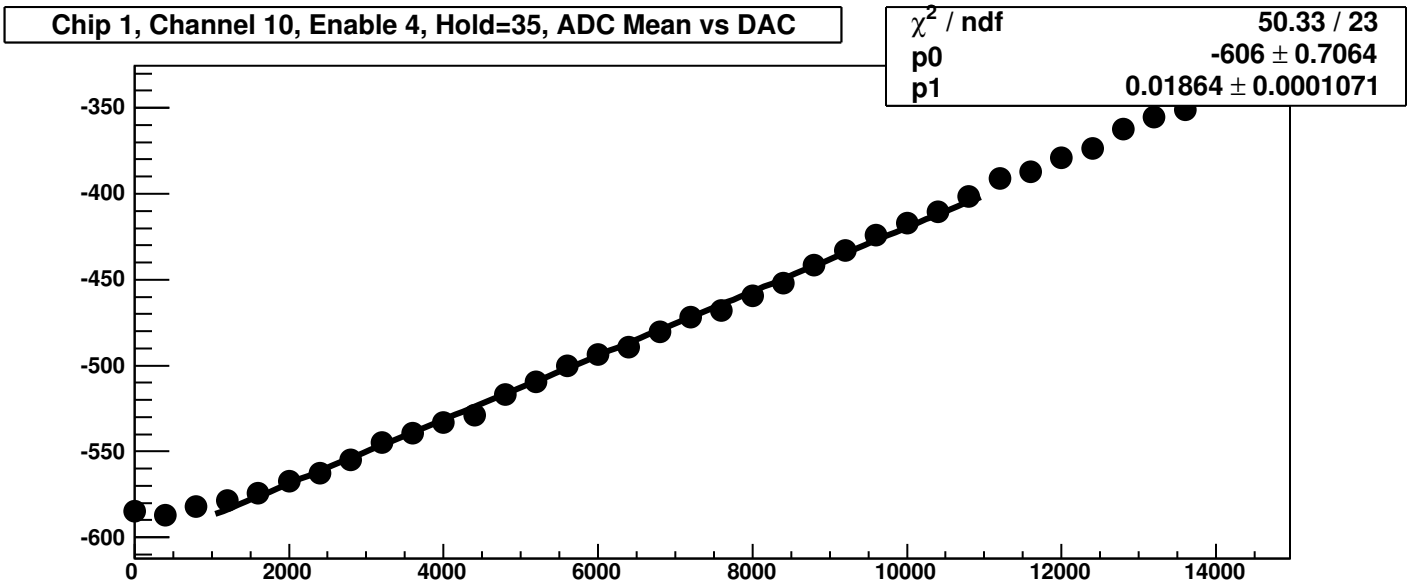

Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

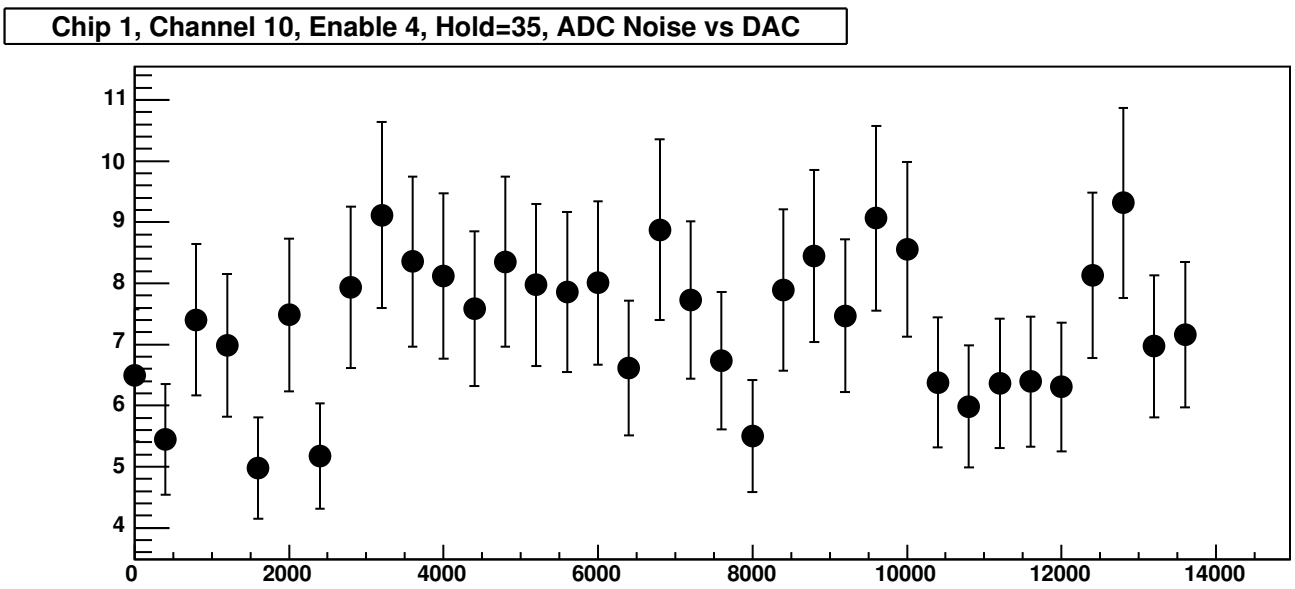

Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

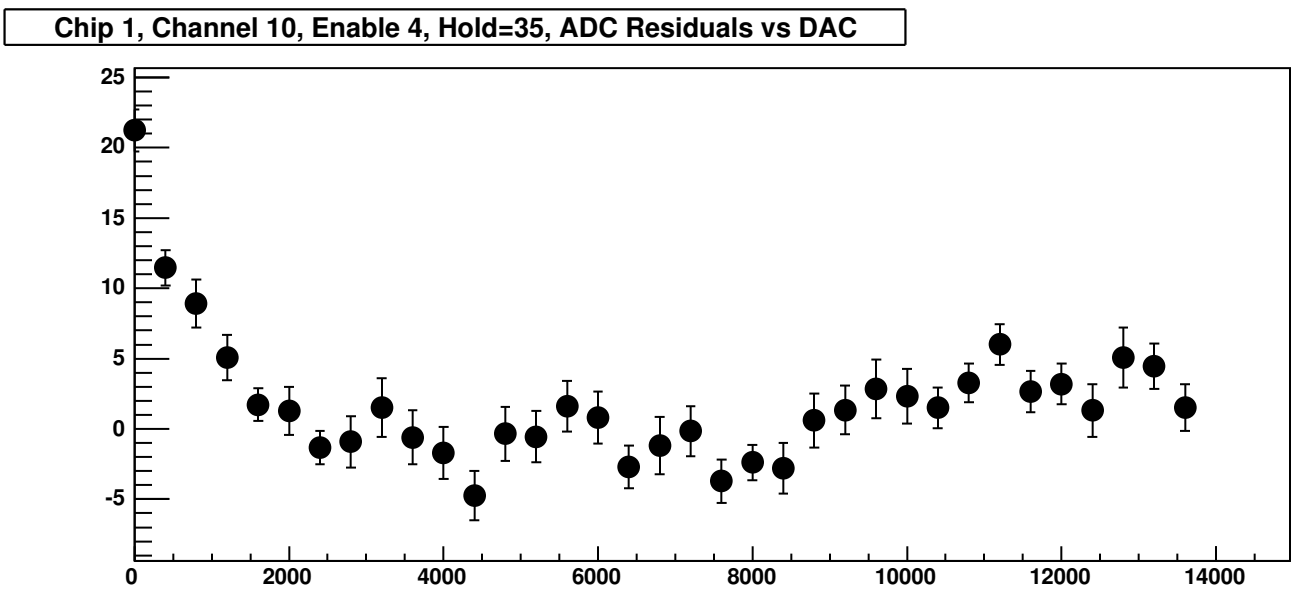

Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.08 / 23
p0 -609.6 ± 0.7769
p1 0.02234 ± 0.0001203
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

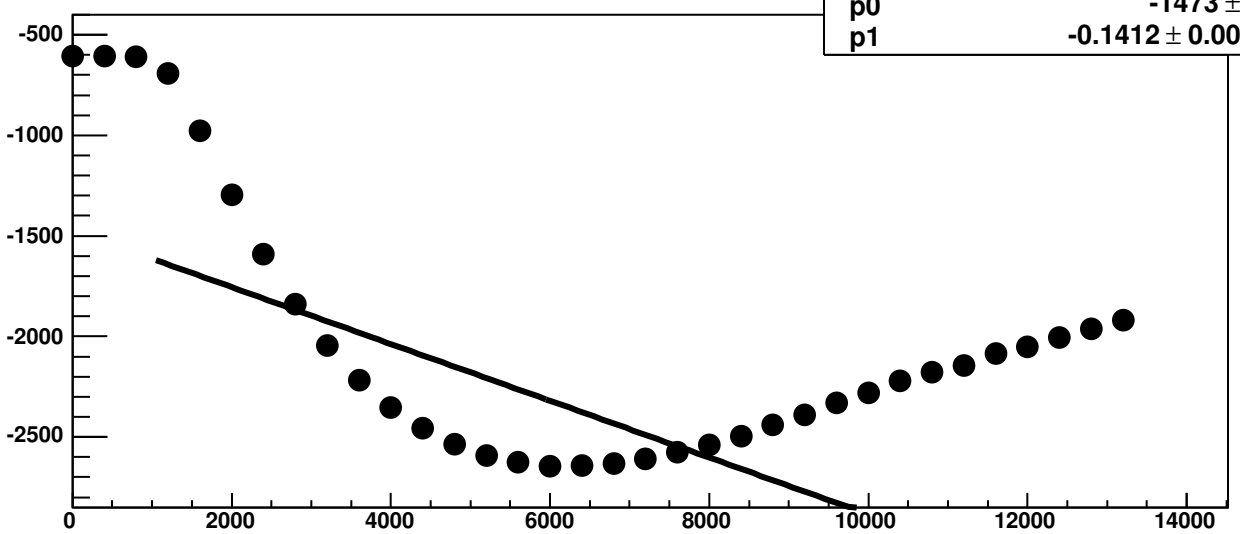

Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.33 / 23
p0 -606 ± 0.7064
p1 0.01864 ± 0.0001071
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

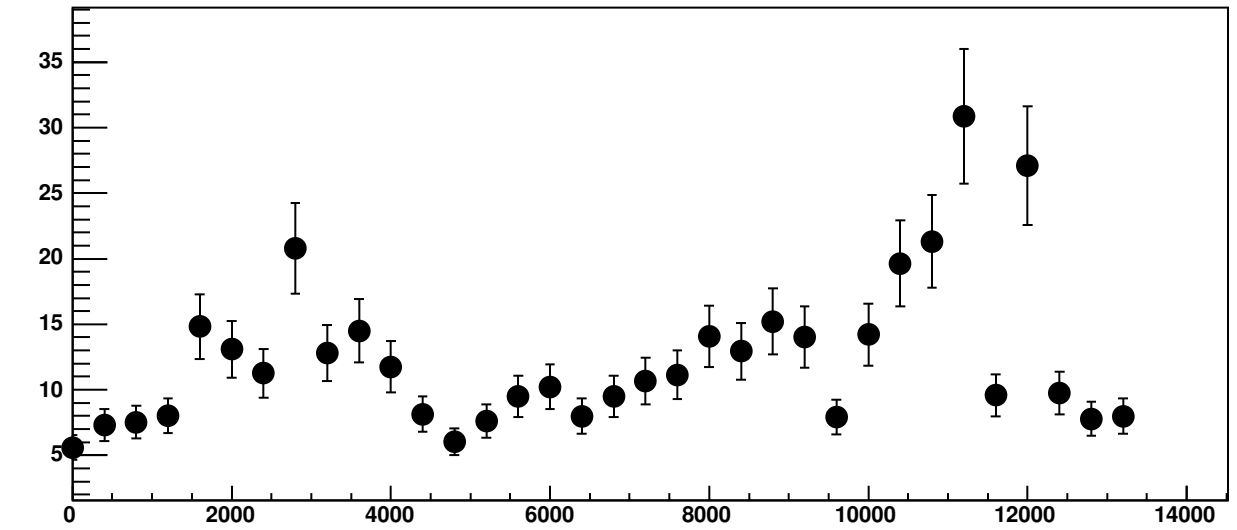

Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

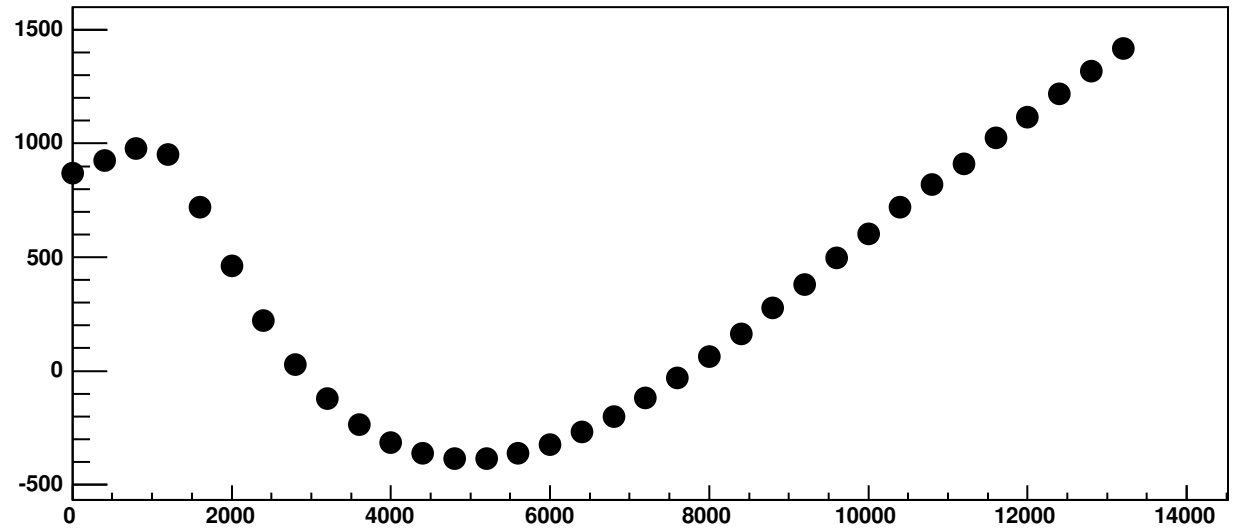
χ^2 / ndf	7.922e+05 / 23
p0	-1473 ± 1.195
p1	-0.1412 ± 0.0001938

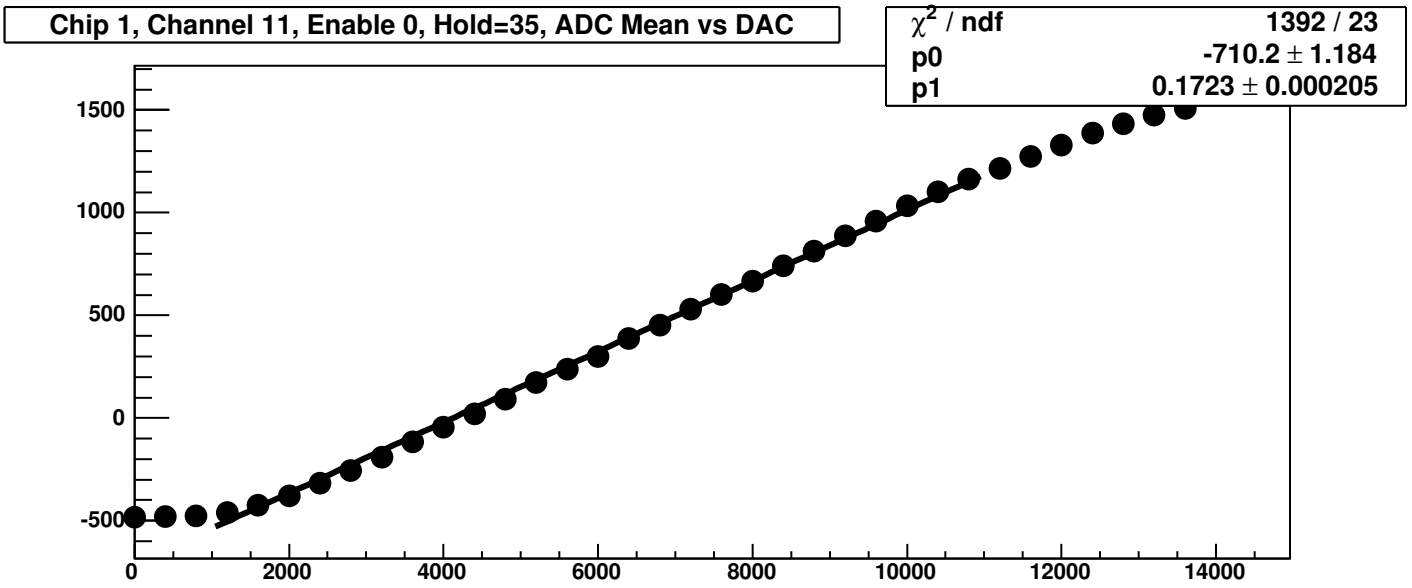


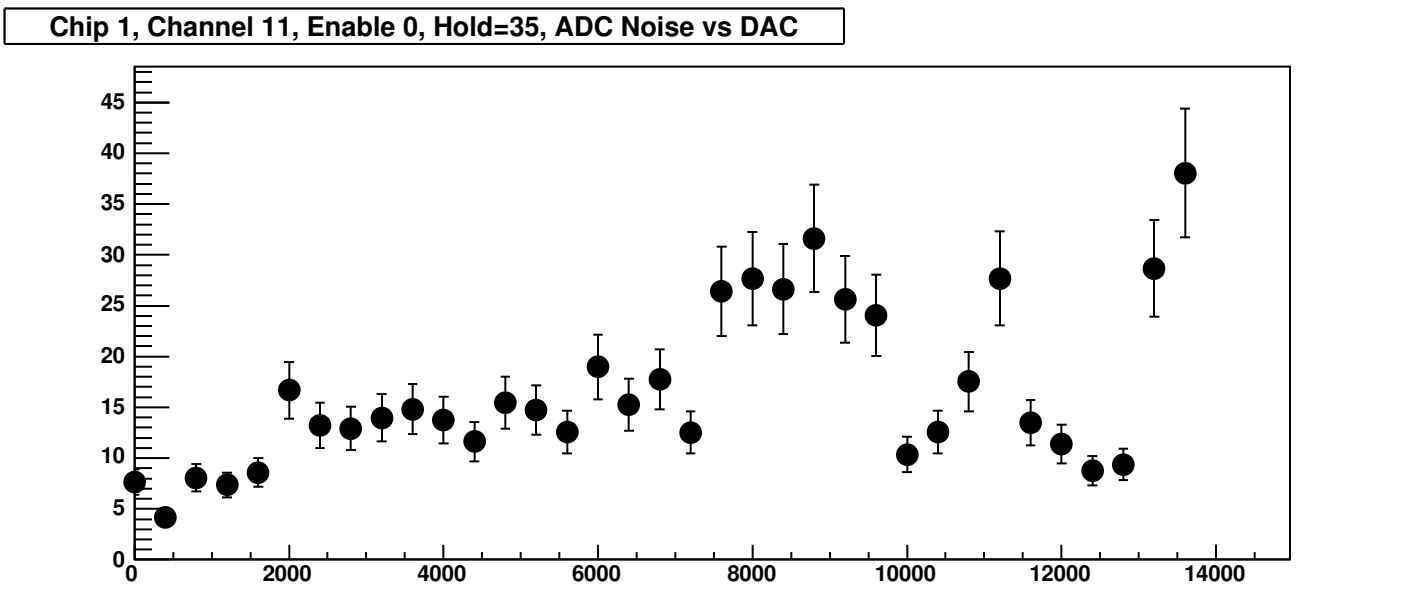
Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

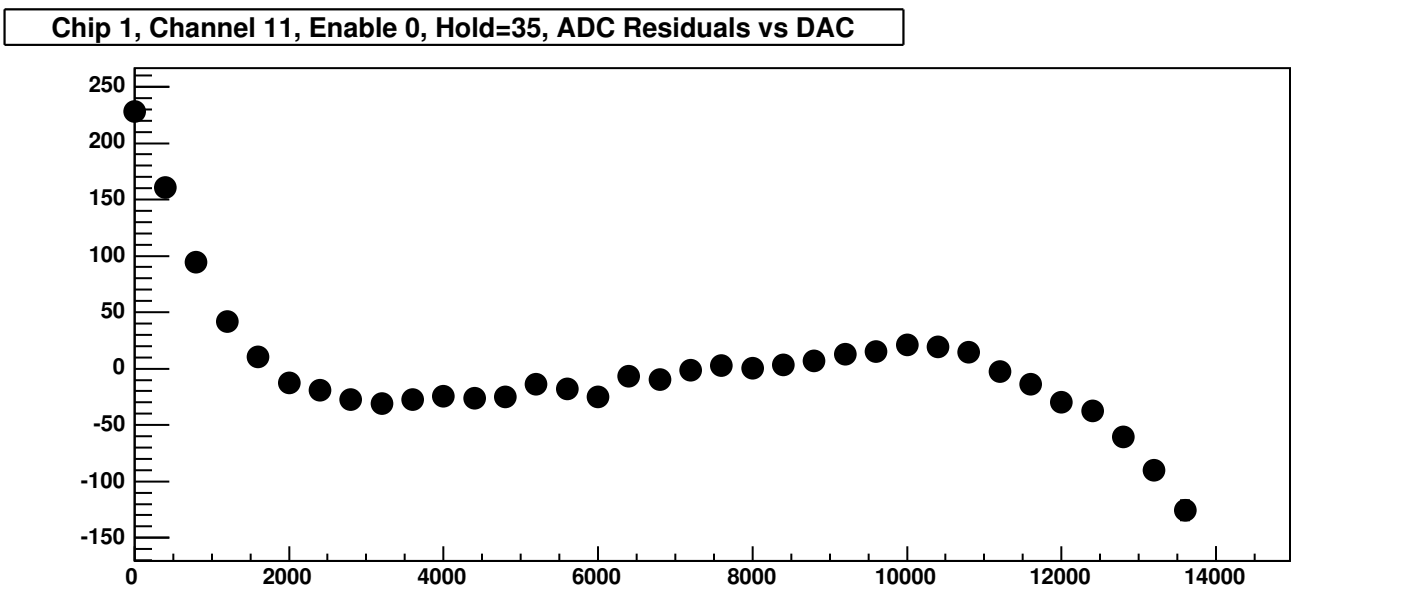


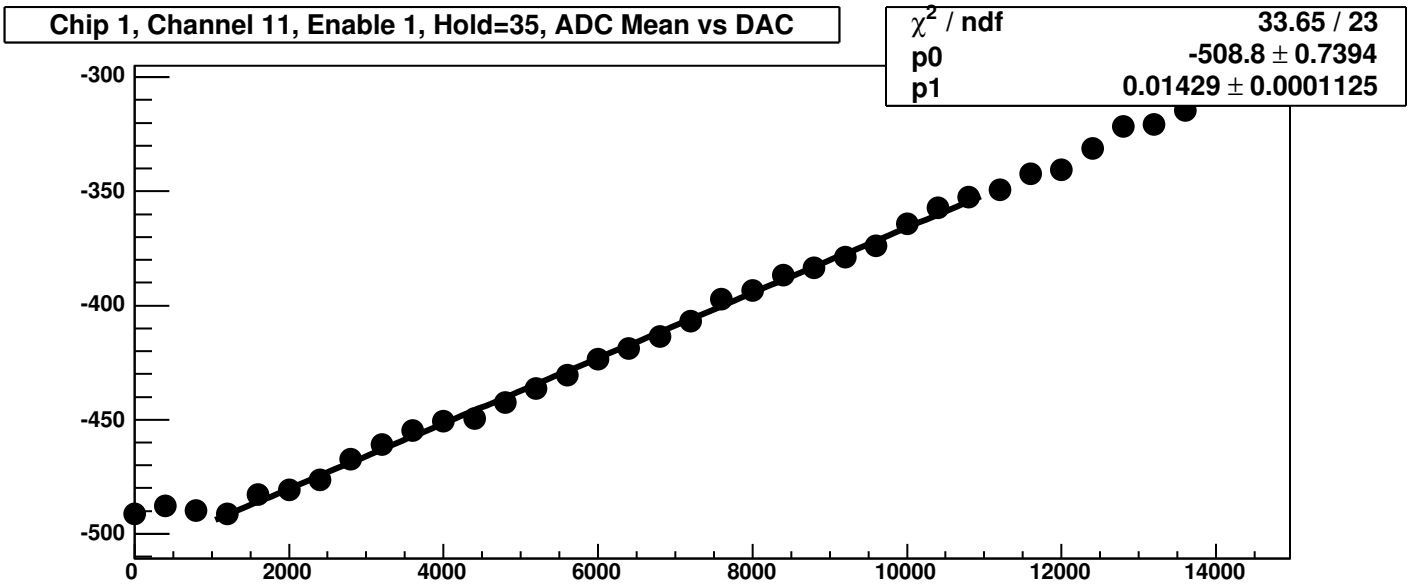
Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

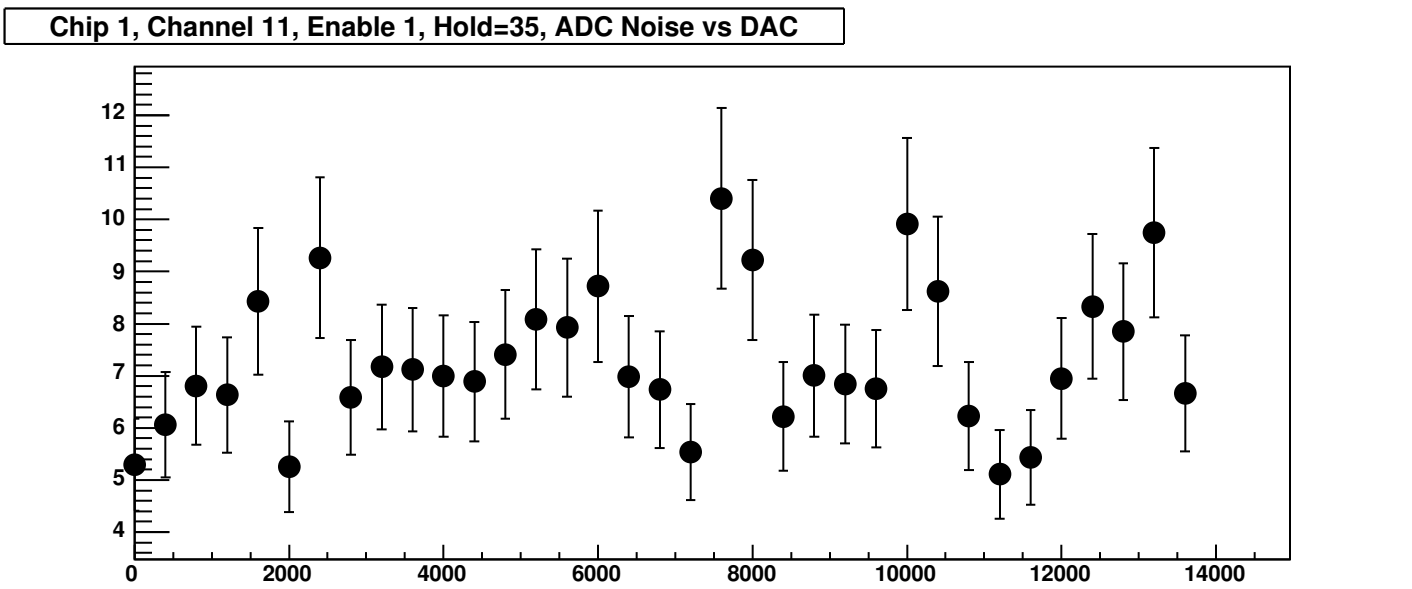


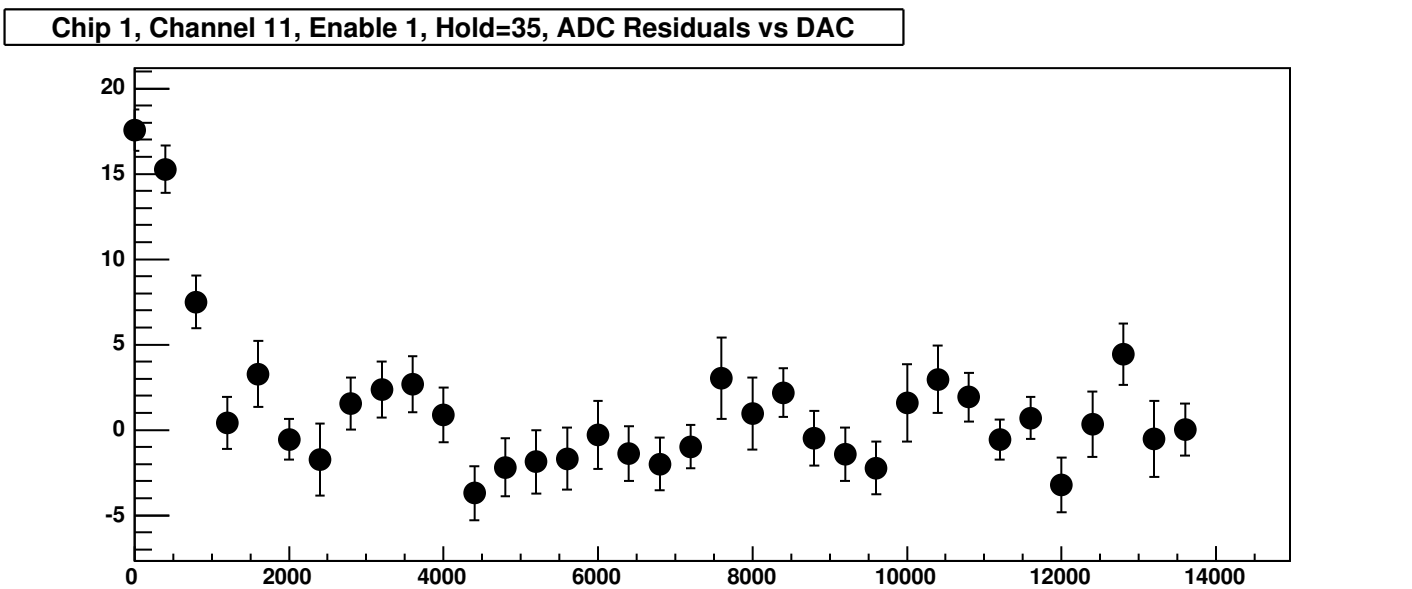

Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1392 / 23
p0 -710.2 ± 1.184
p1 0.1723 ± 0.000205
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

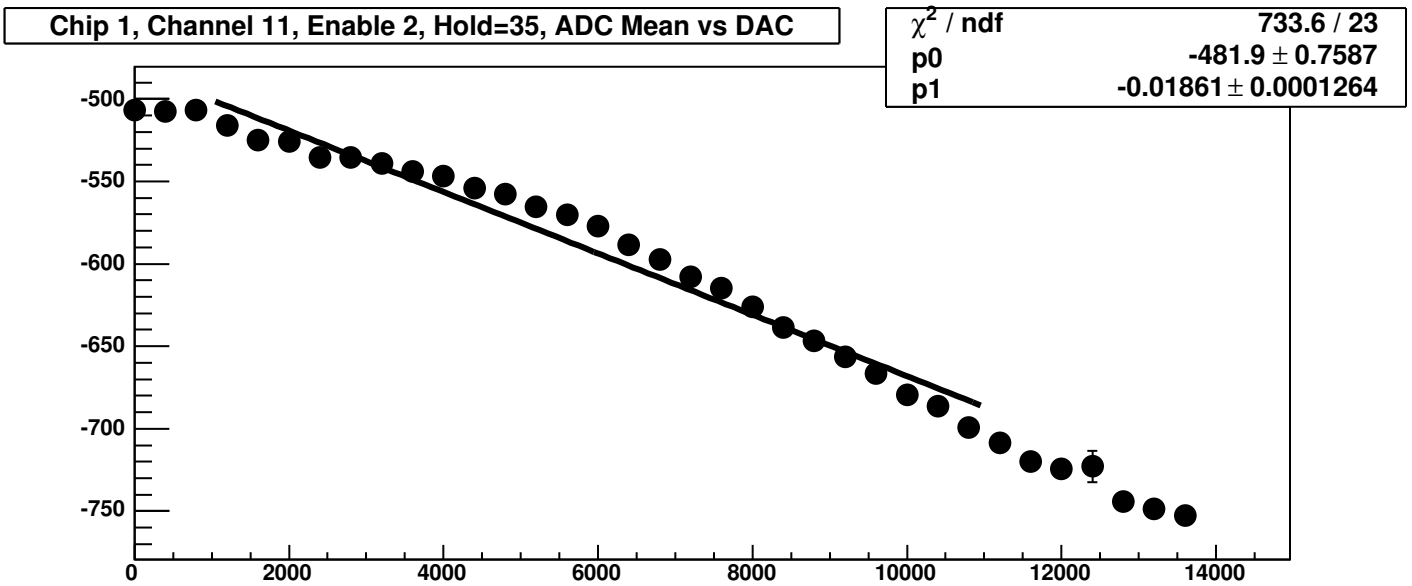

Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

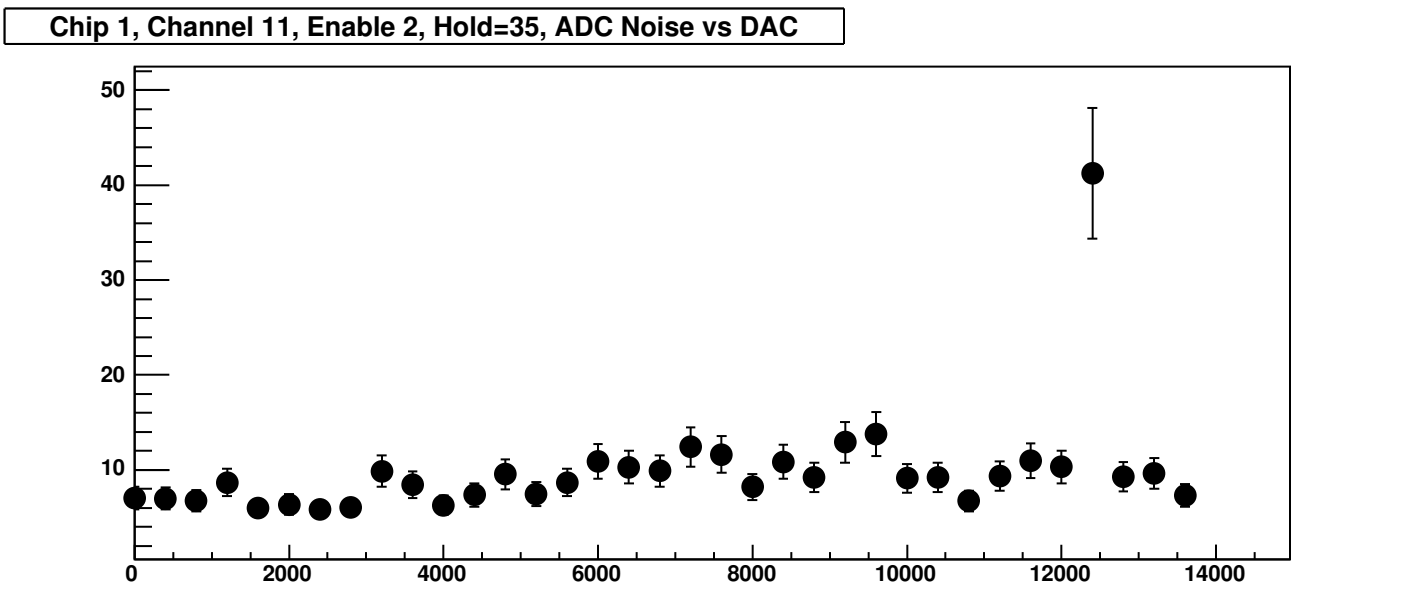

Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

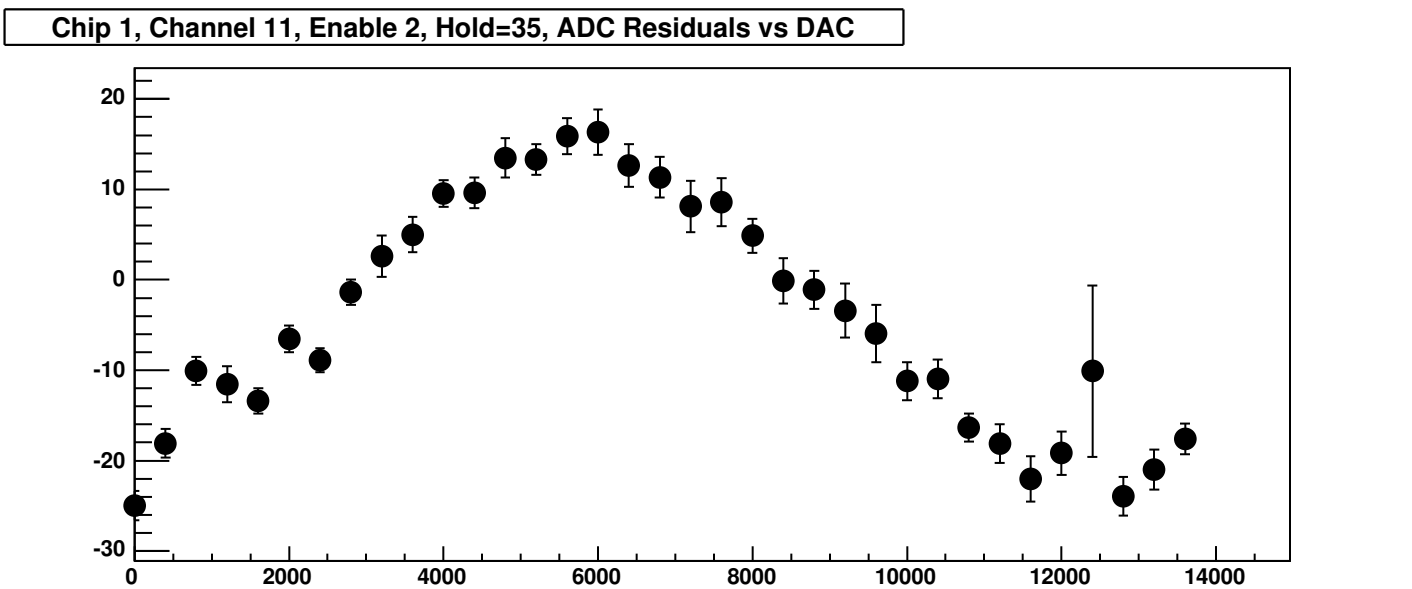

Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.65 / 23
p0 -508.8 ± 0.7394
p1 0.01429 ± 0.0001125
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

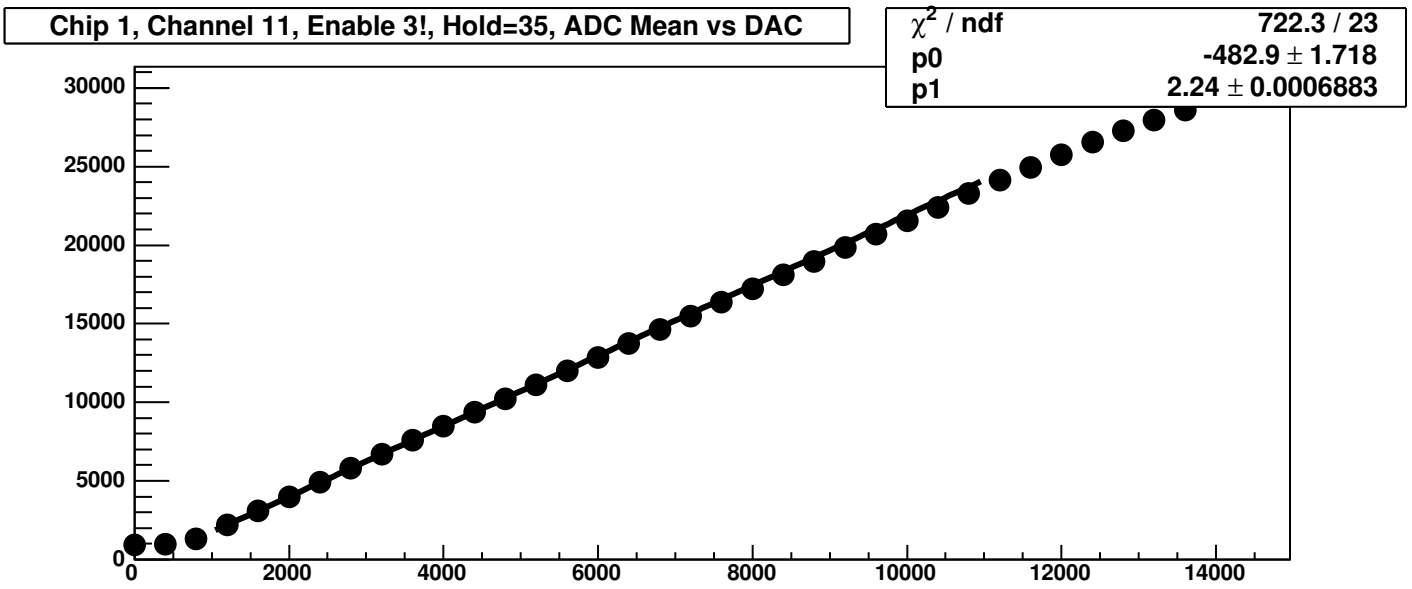

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

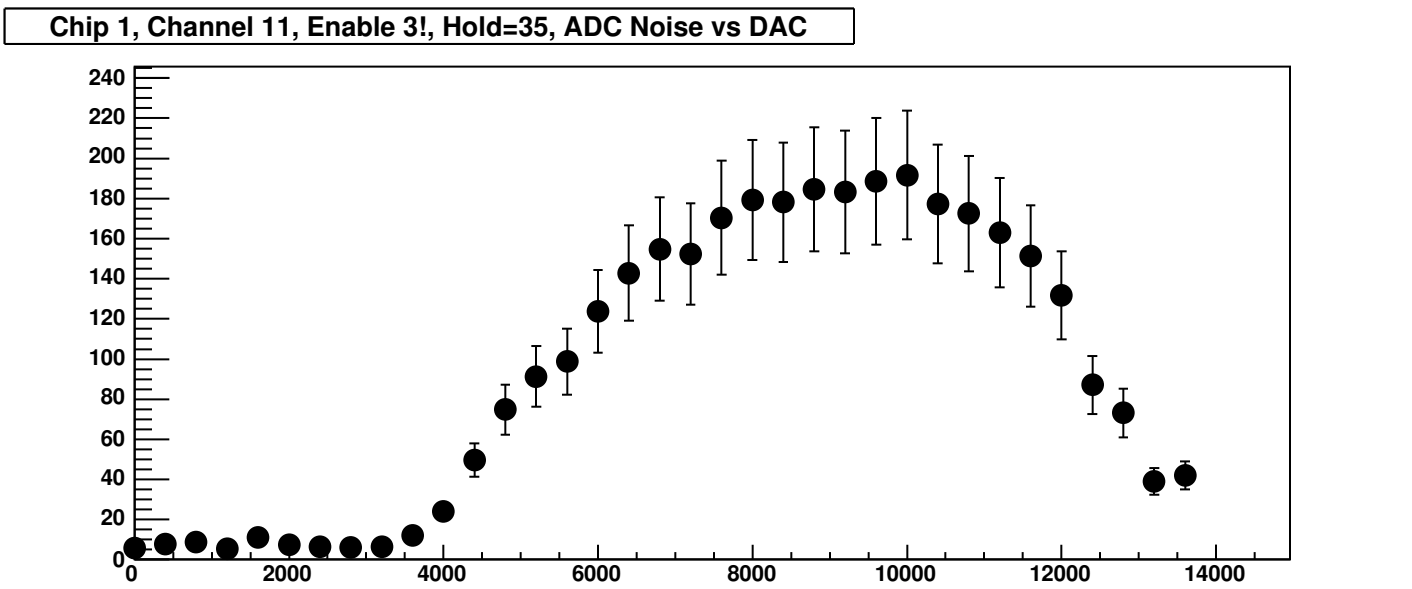

Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

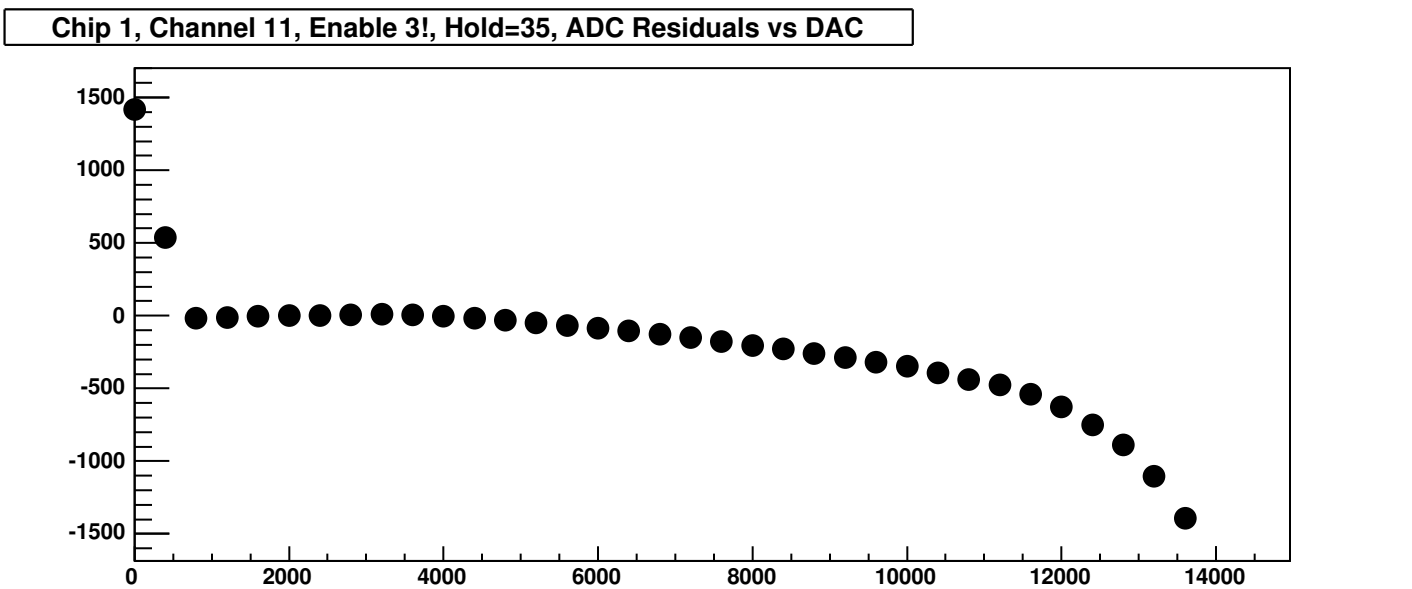

Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 733.6 / 23
p0 -481.9 ± 0.7587
p1 -0.01861 ± 0.0001264
-500
-550
-600
-650
-700
-750
0
2000
4000
6000
8000
10000
12000
14000

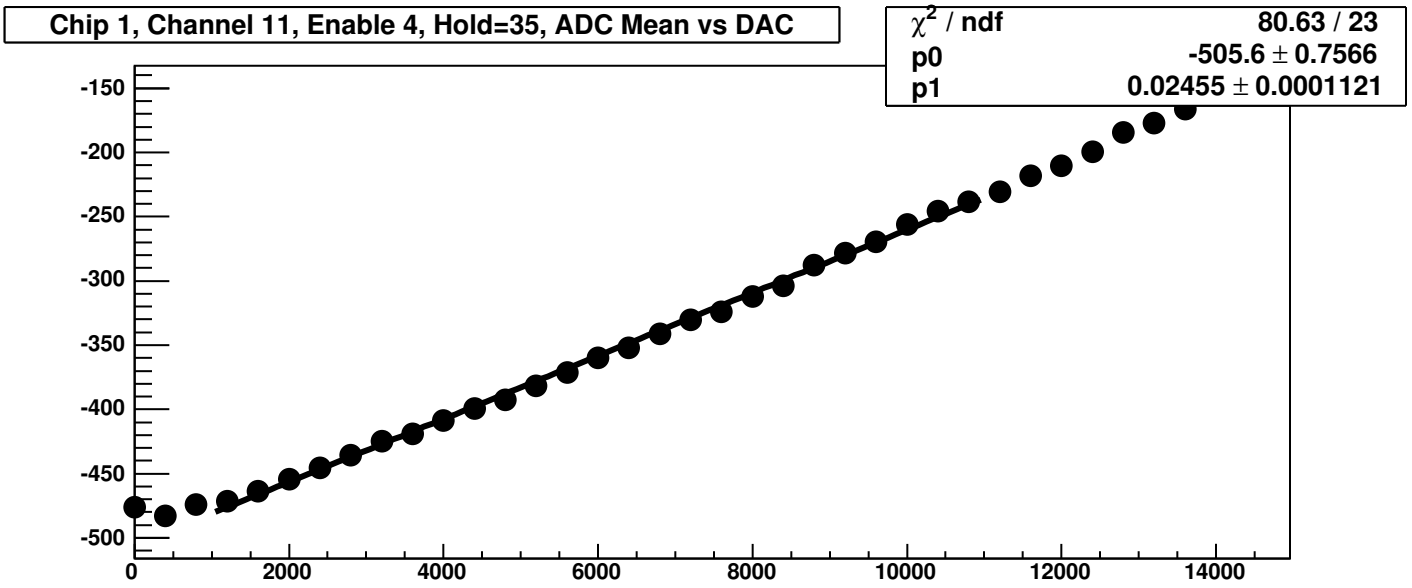

Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

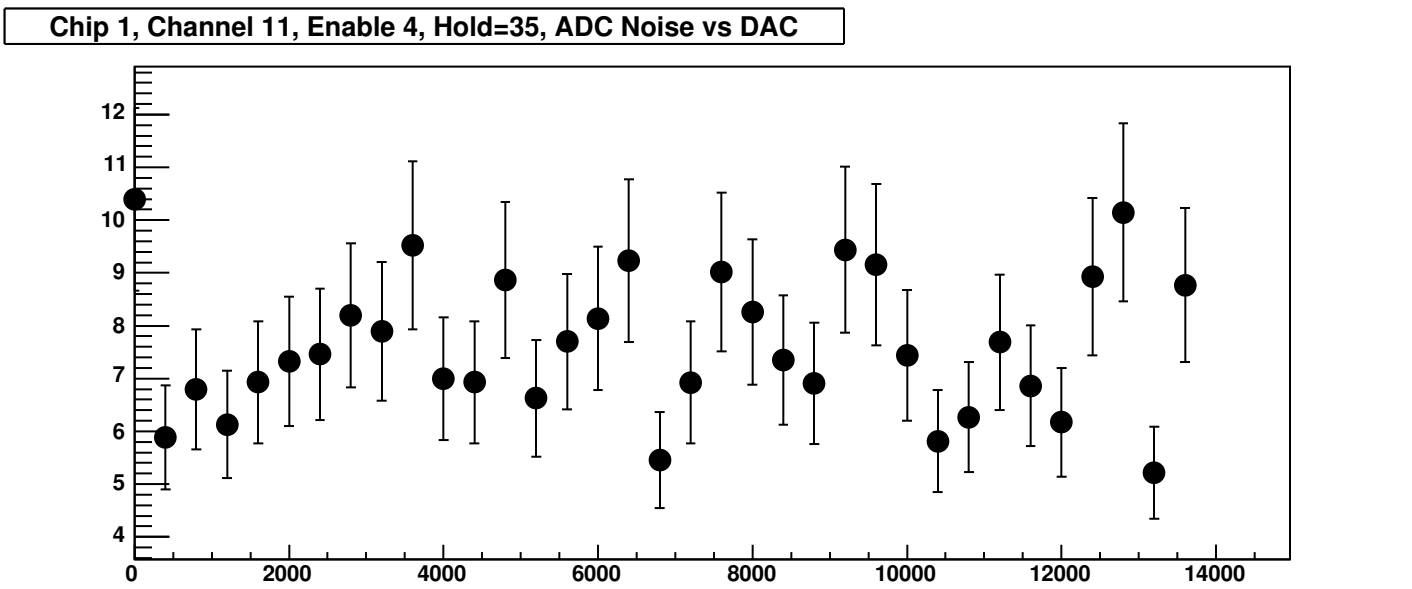

Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

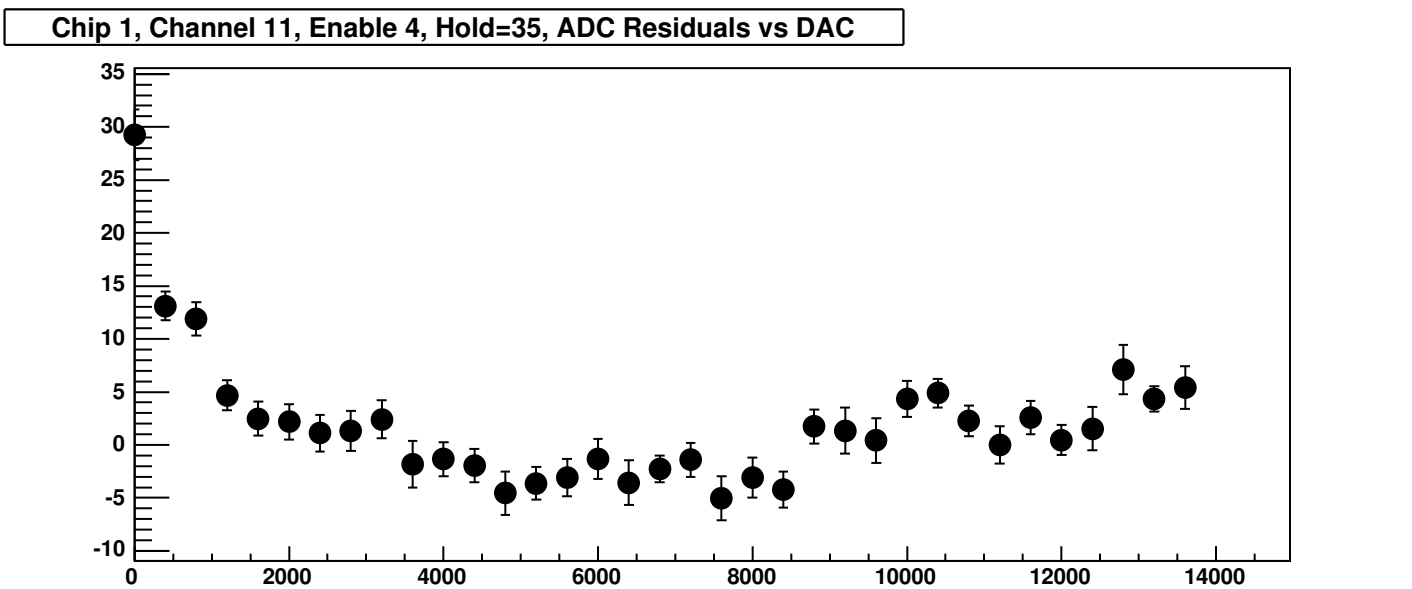

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 722.3 / 23
p0 -482.9 ± 1.718
p1 2.24 ± 0.0006883
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

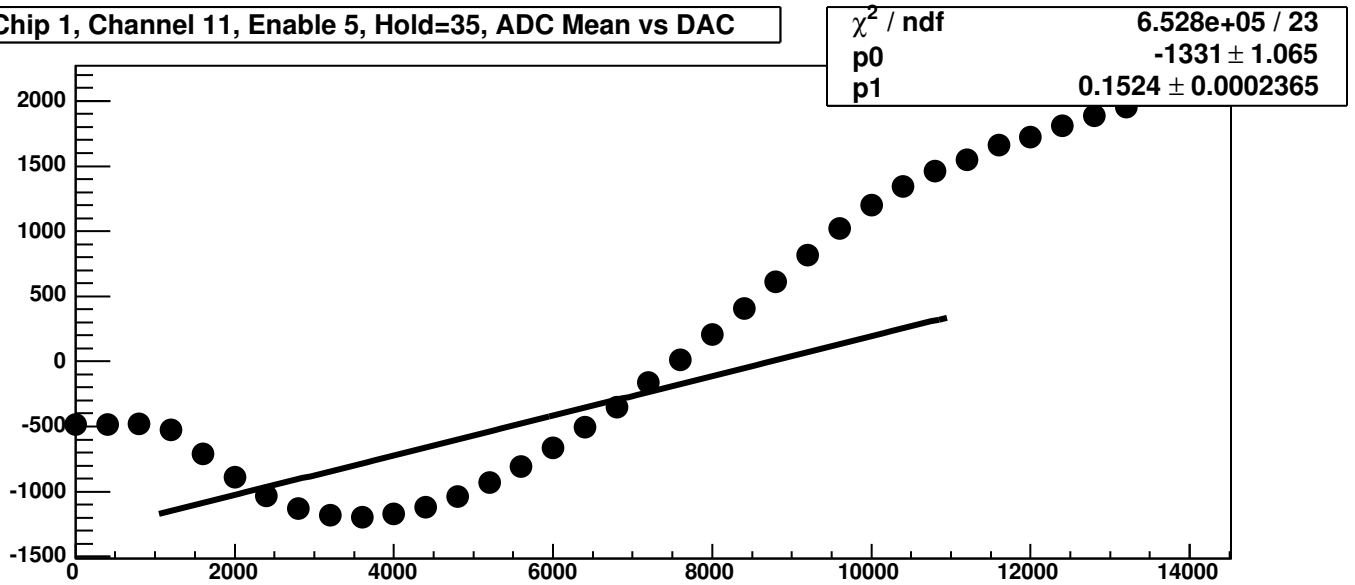

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 80.63 / 23
p0 -505.6 ± 0.7566
p1 0.02455 ± 0.0001121
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

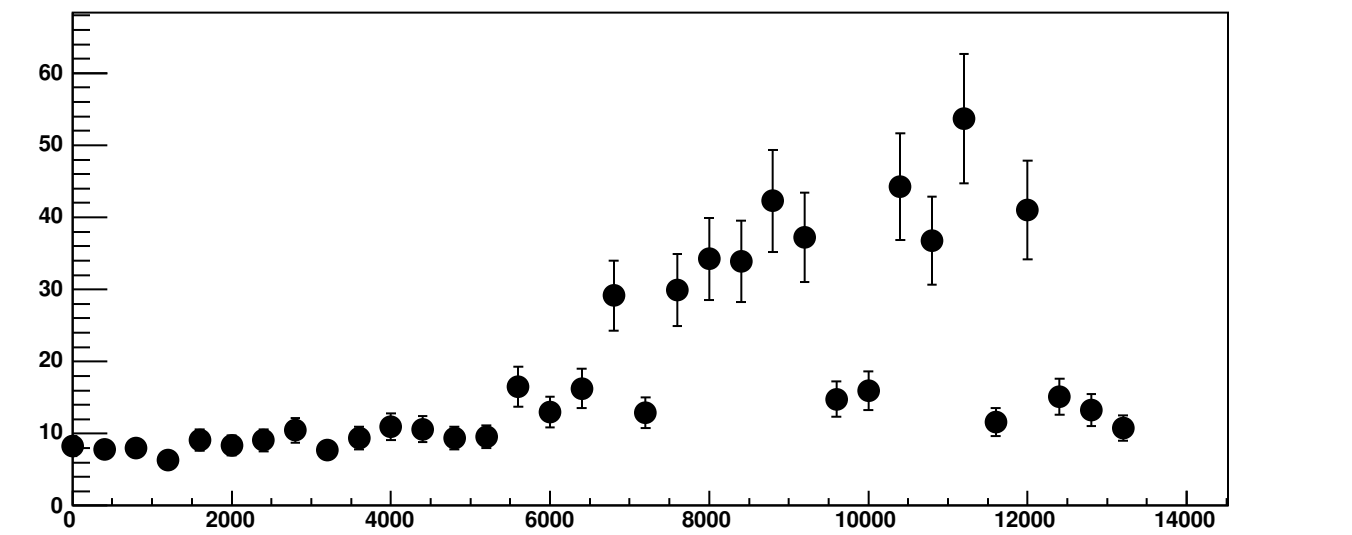

Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

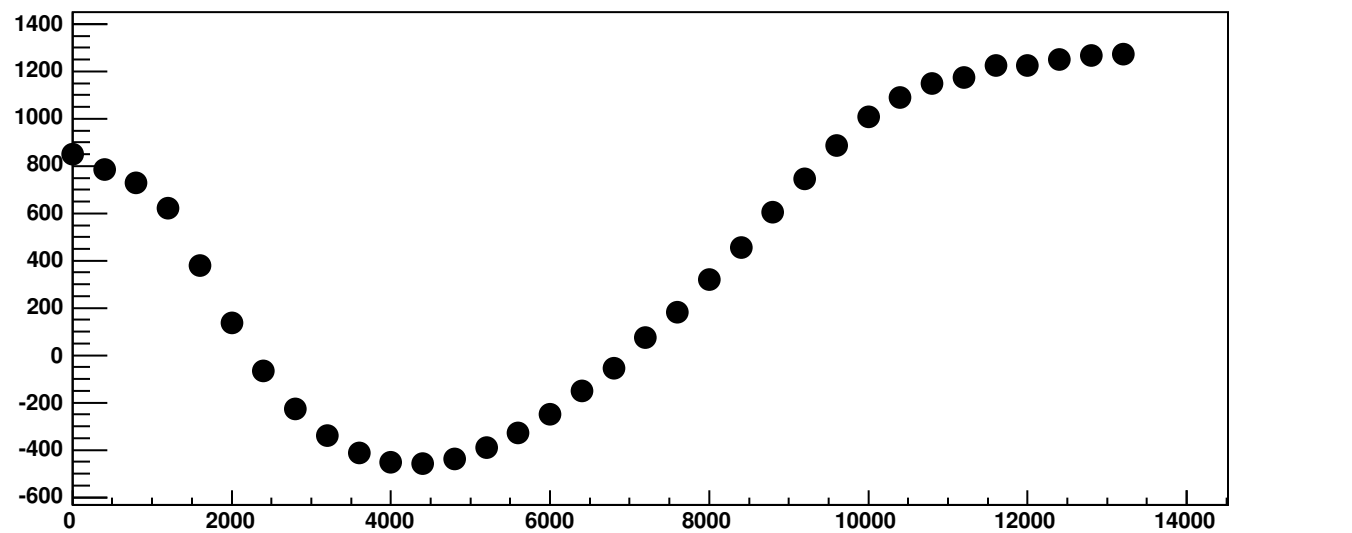
Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

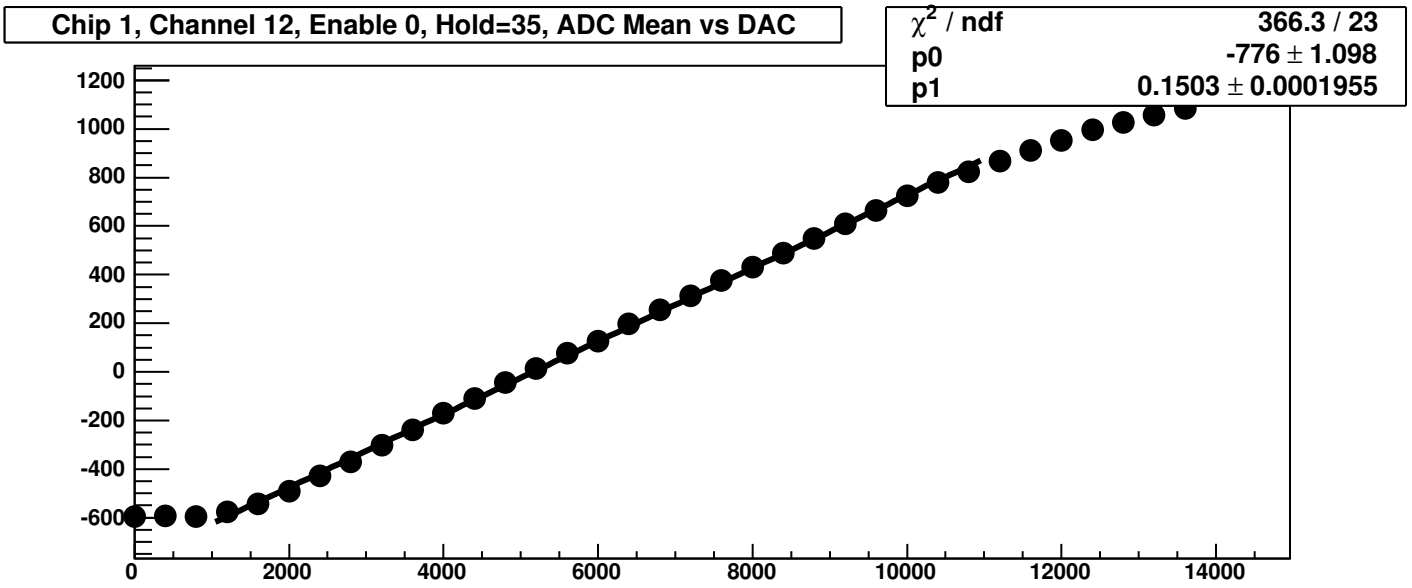


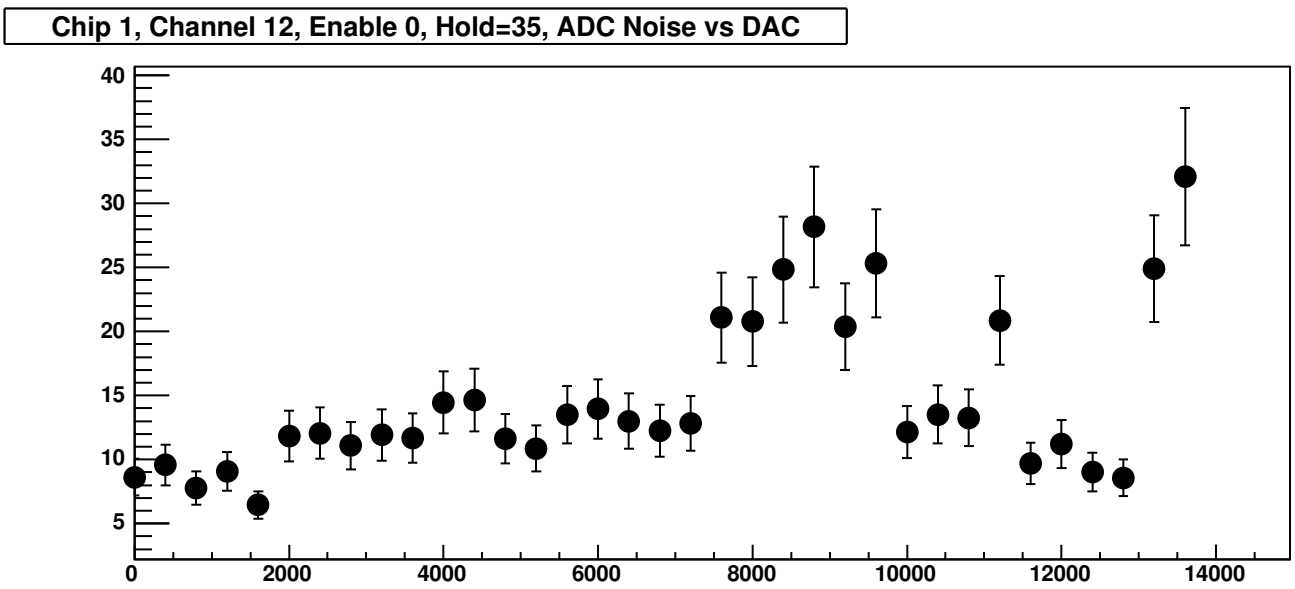
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

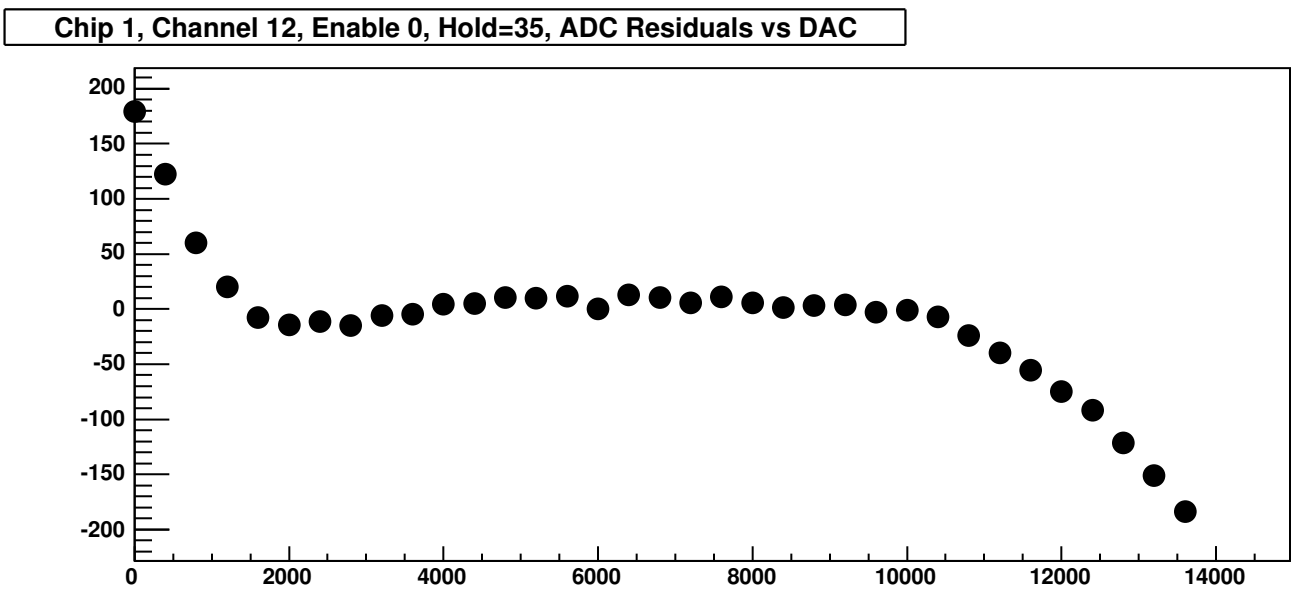


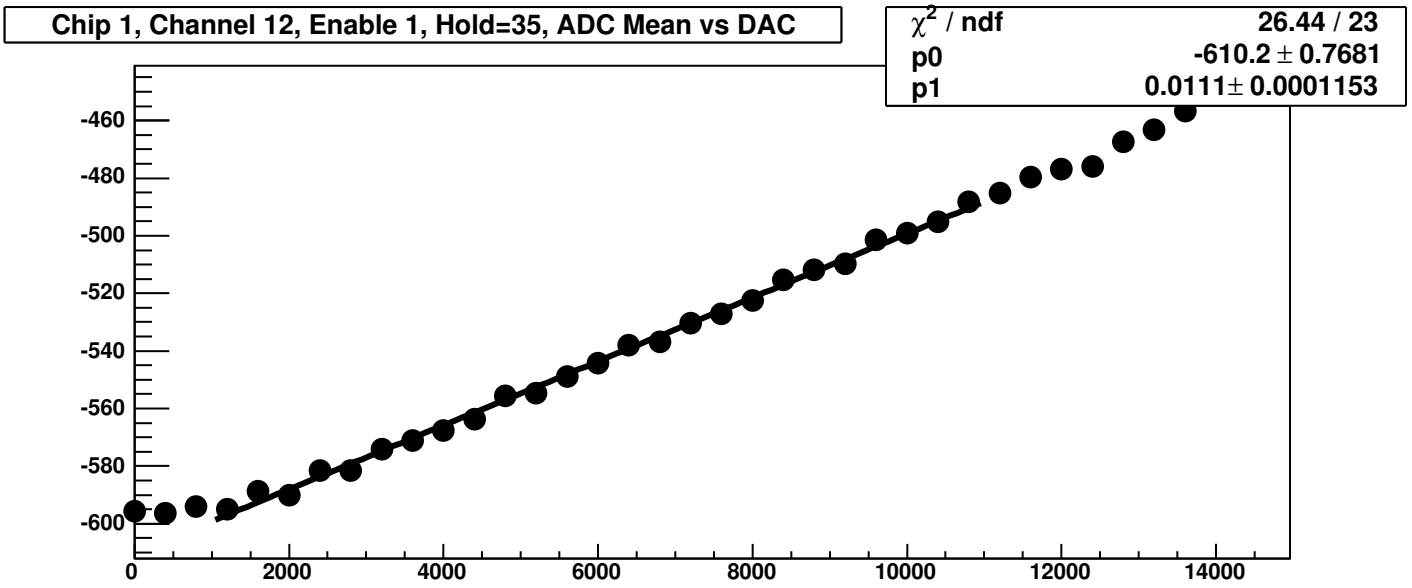
Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

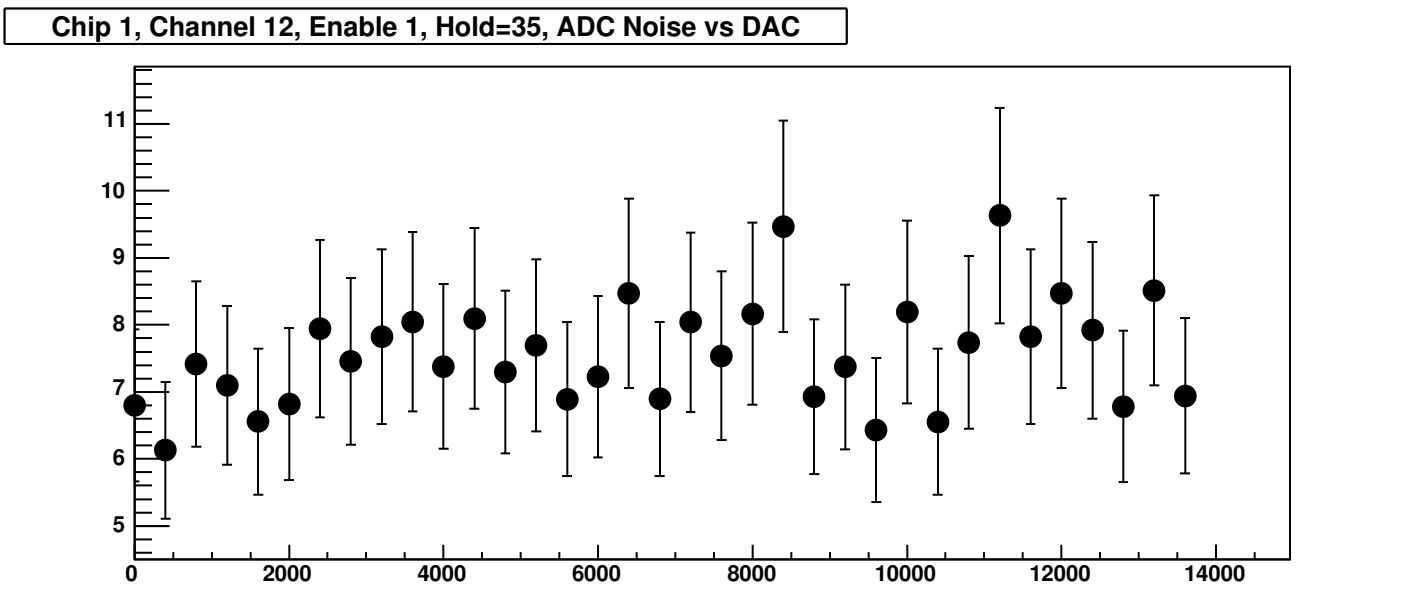


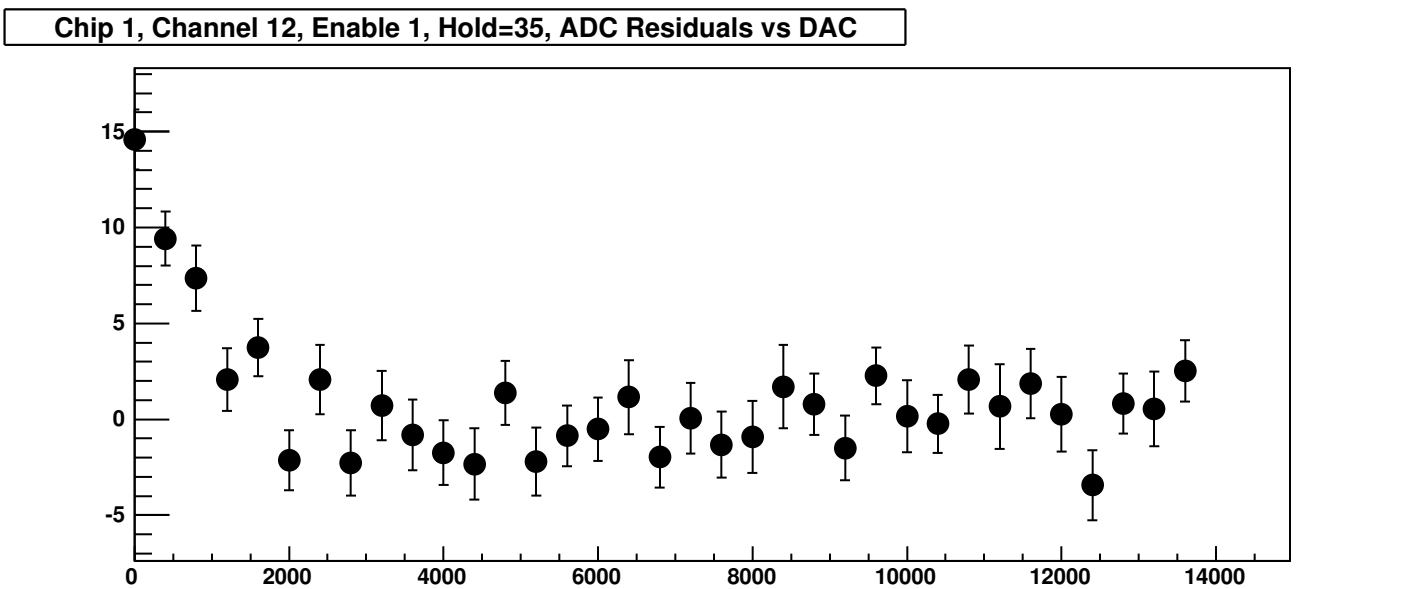

Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 366.3 / 23
p0 -776 ± 1.098
p1 0.1503 ± 0.0001955
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

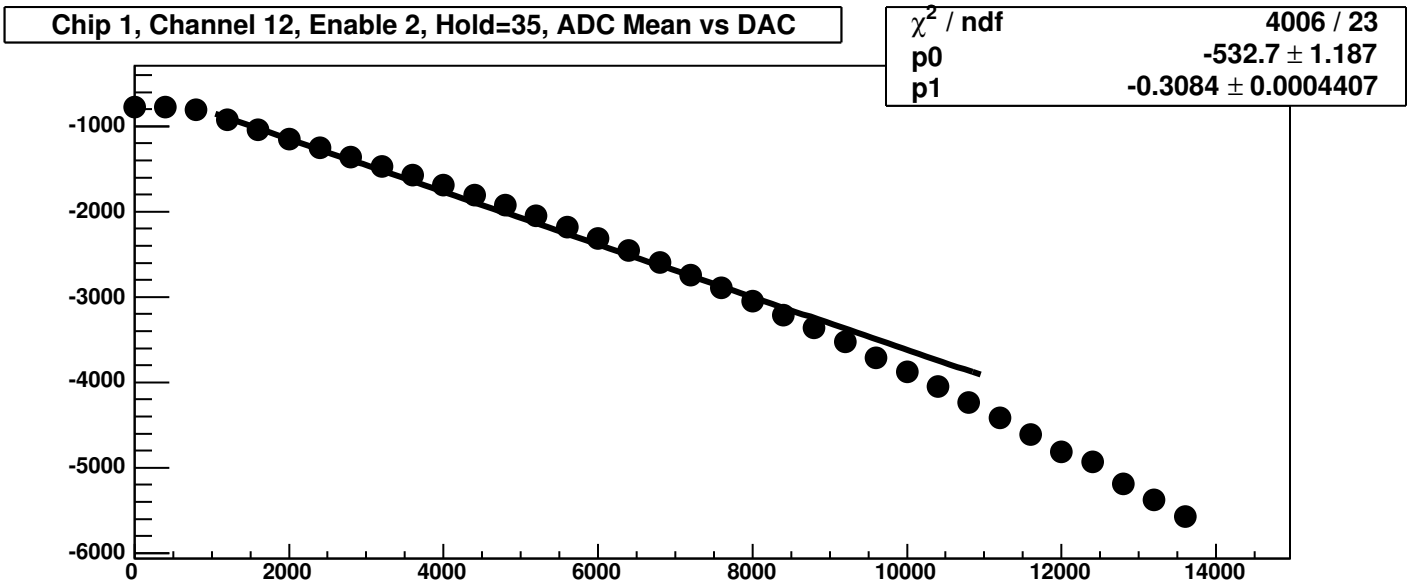

Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

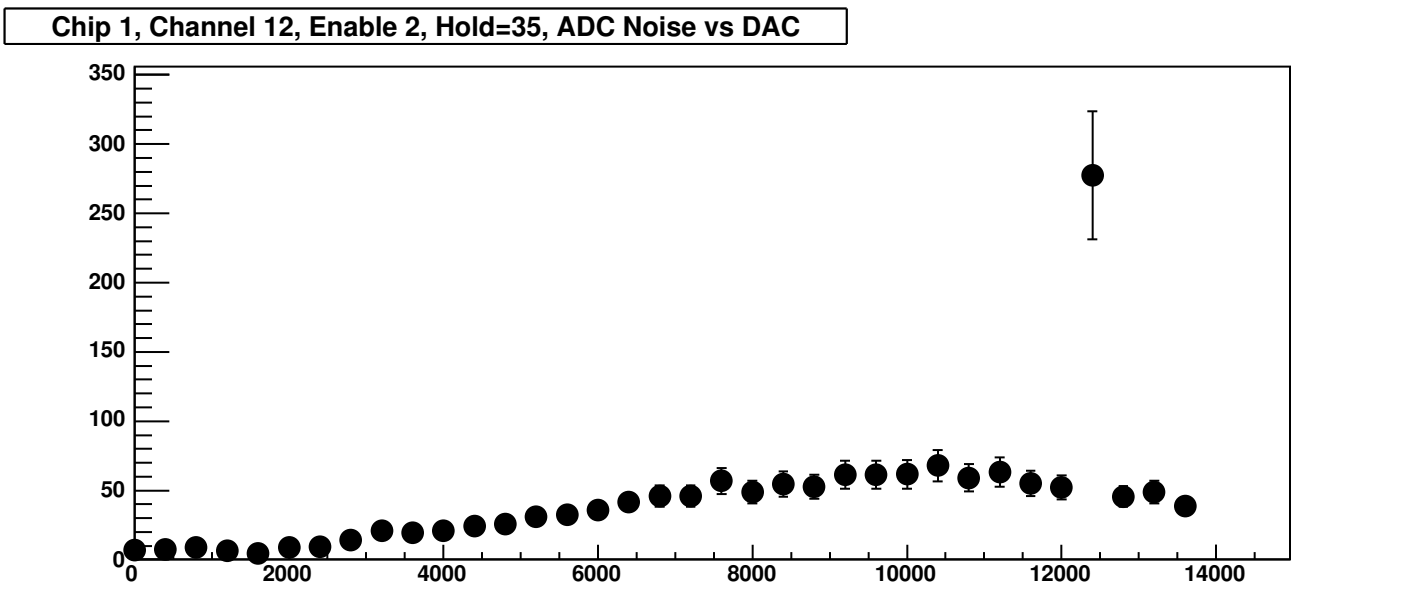

Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

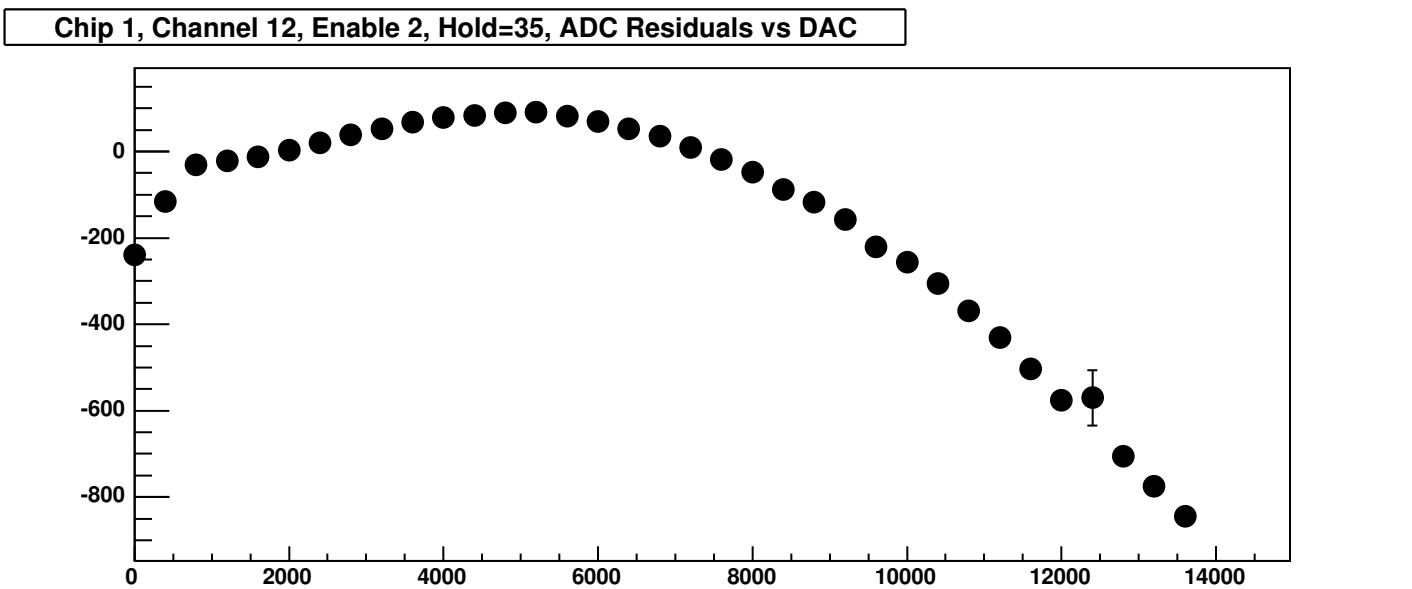

Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.44 / 23
p0 -610.2 ± 0.7681
p1 0.0111 ± 0.0001153
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

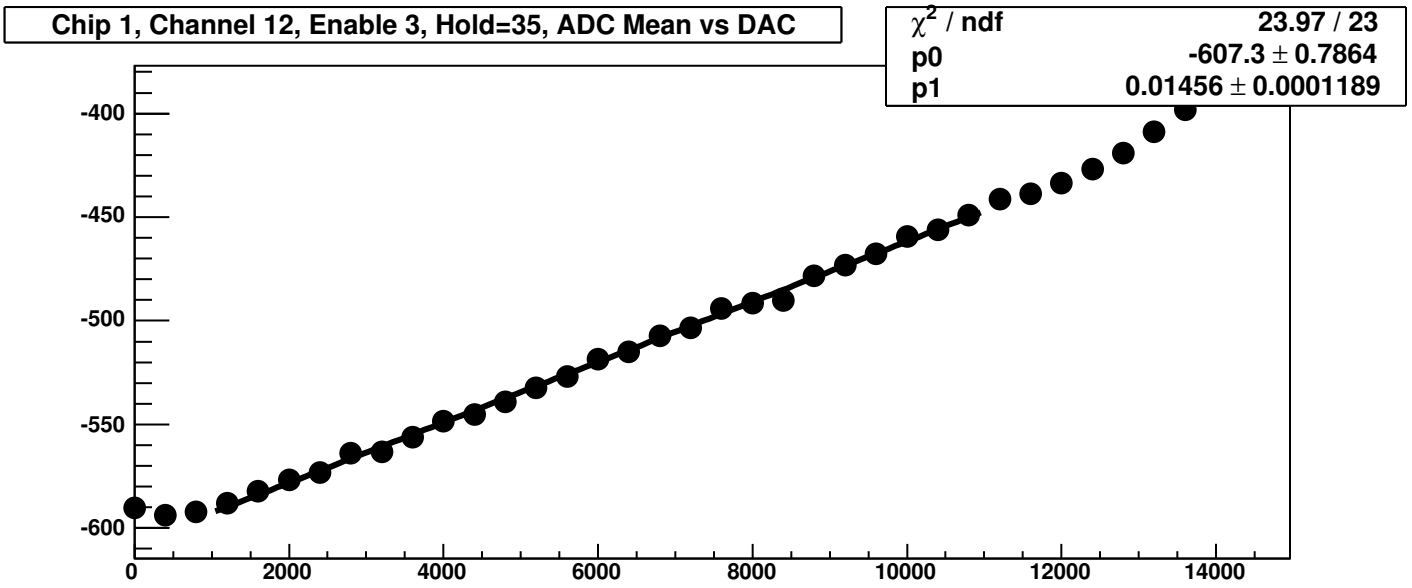

Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

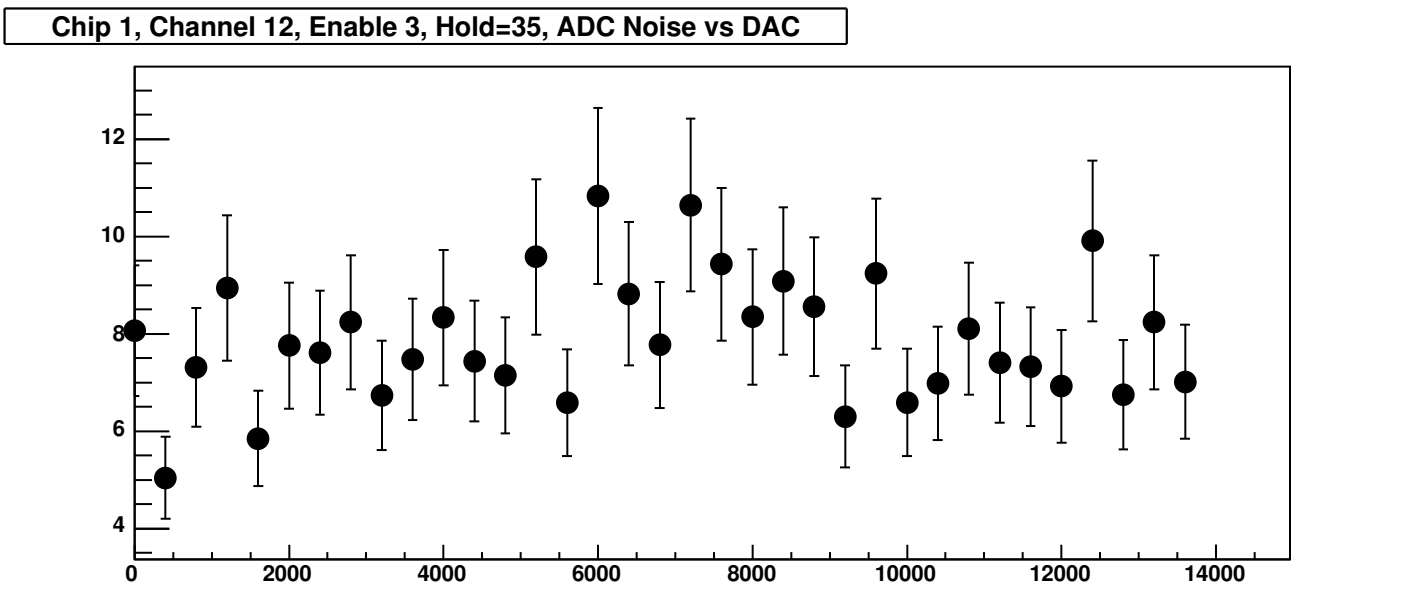

Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

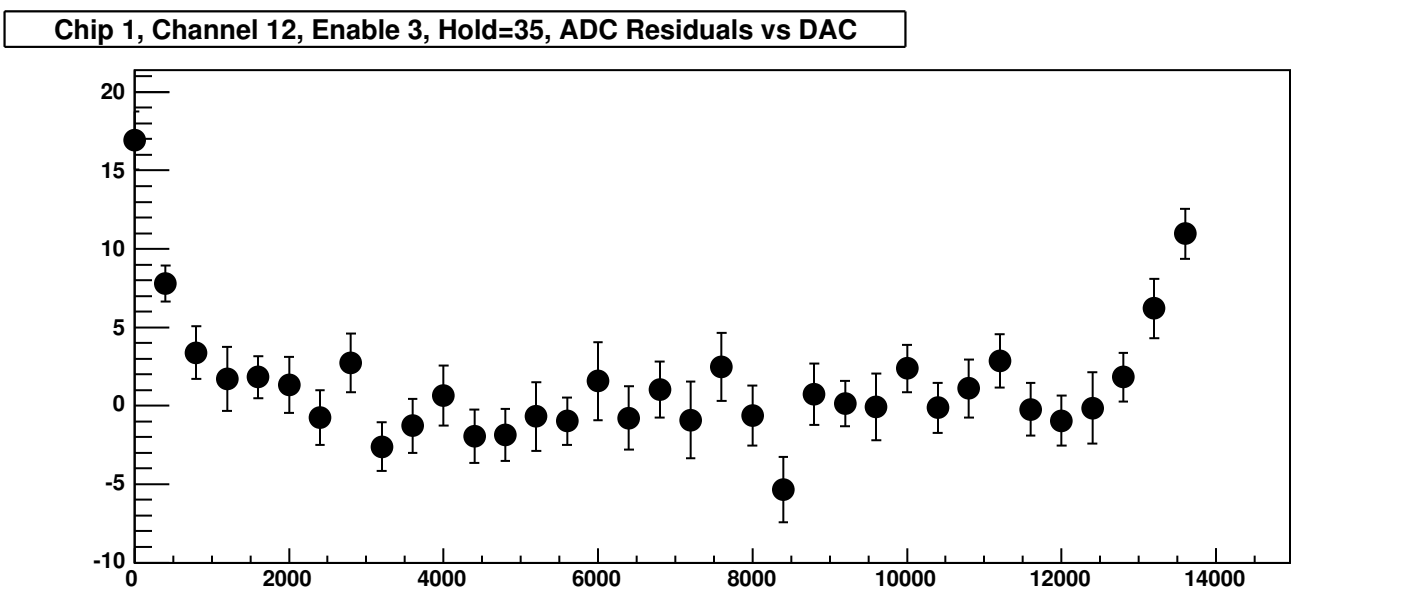

Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4006 / 23
p0 -532.7 ± 1.187
p1 -0.3084 ± 0.0004407
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

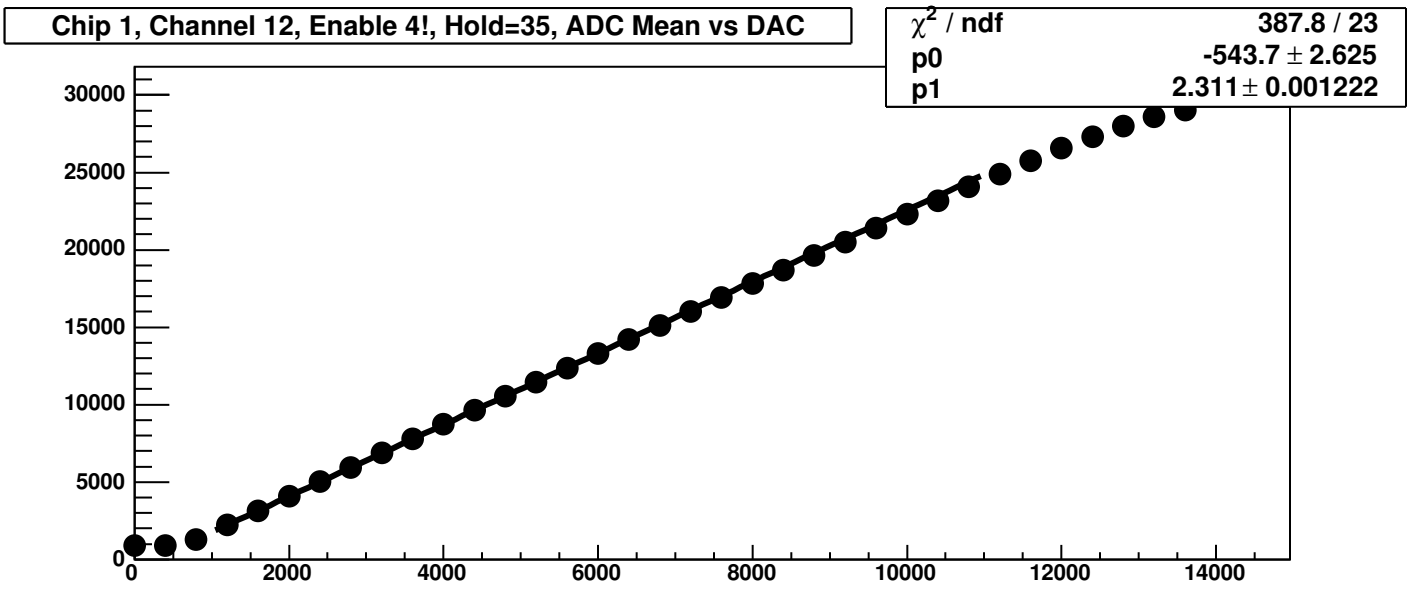

Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

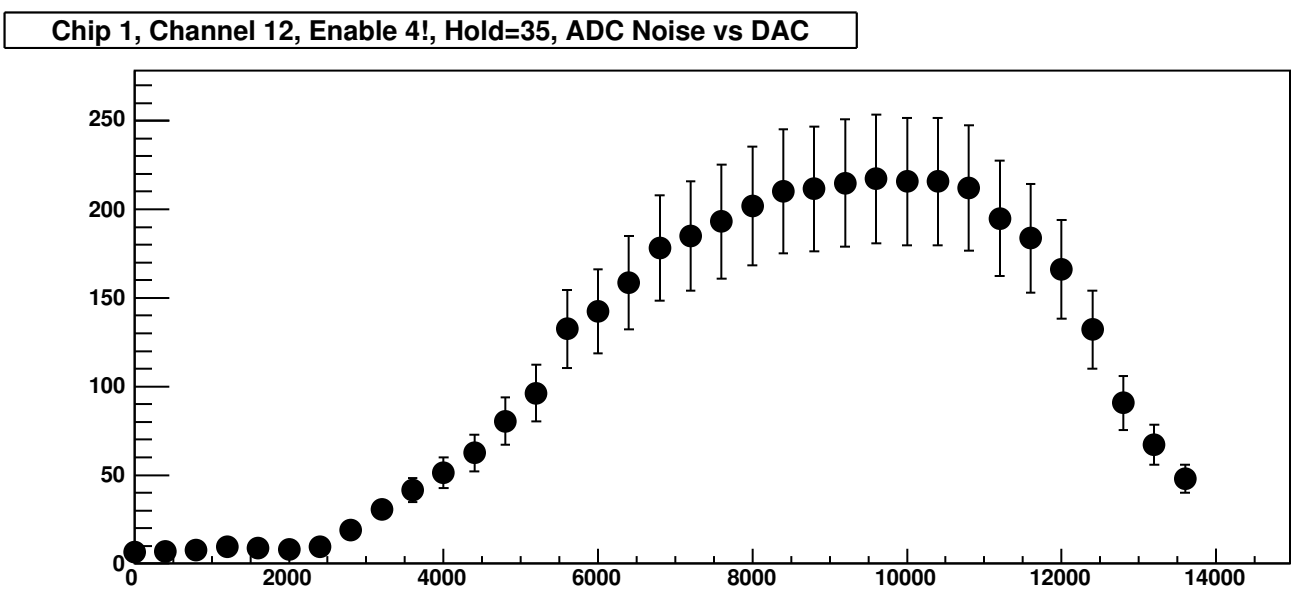

Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

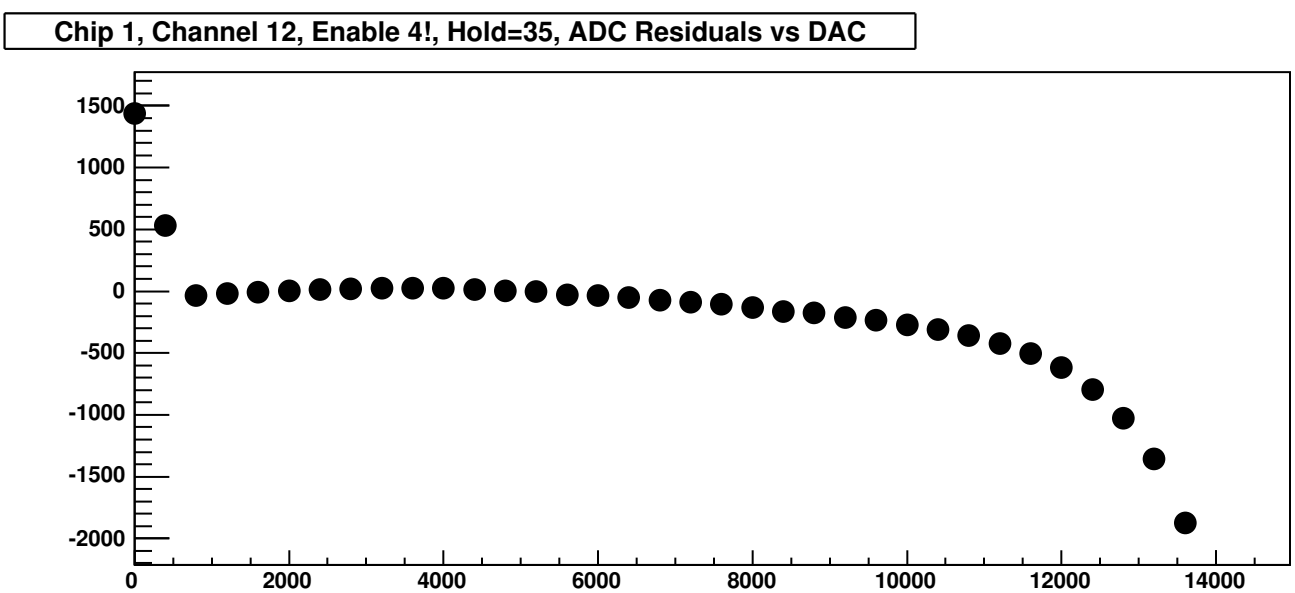

Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.97 / 23
p0 -607.3 ± 0.7864
p1 0.01456 ± 0.0001189
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

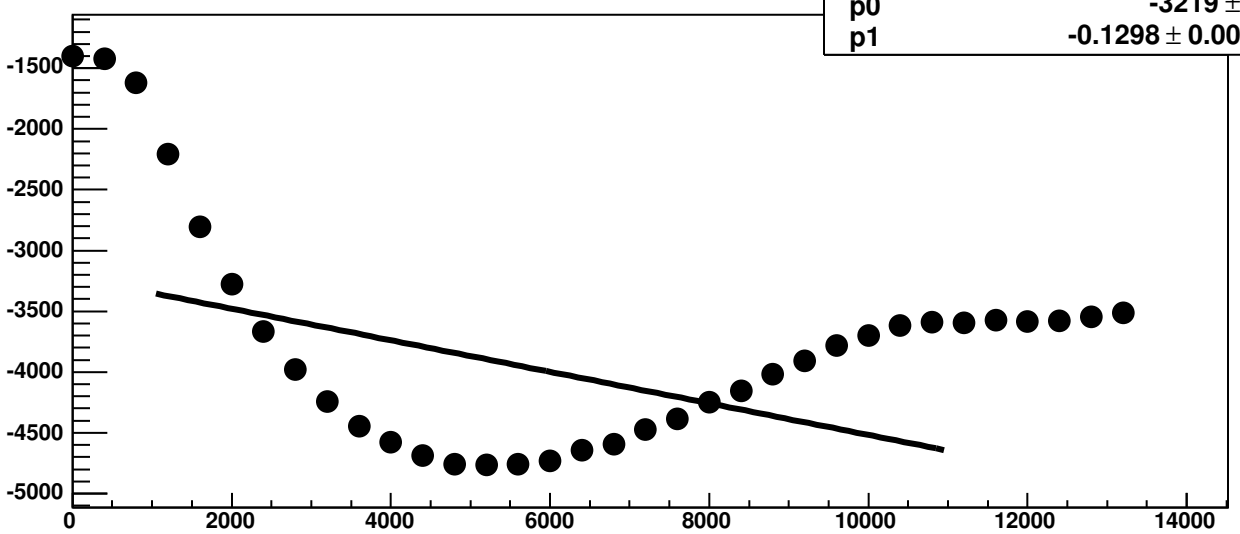

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 387.8 / 23
p0 -543.7 ± 2.625
p1 2.311 ± 0.001222
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

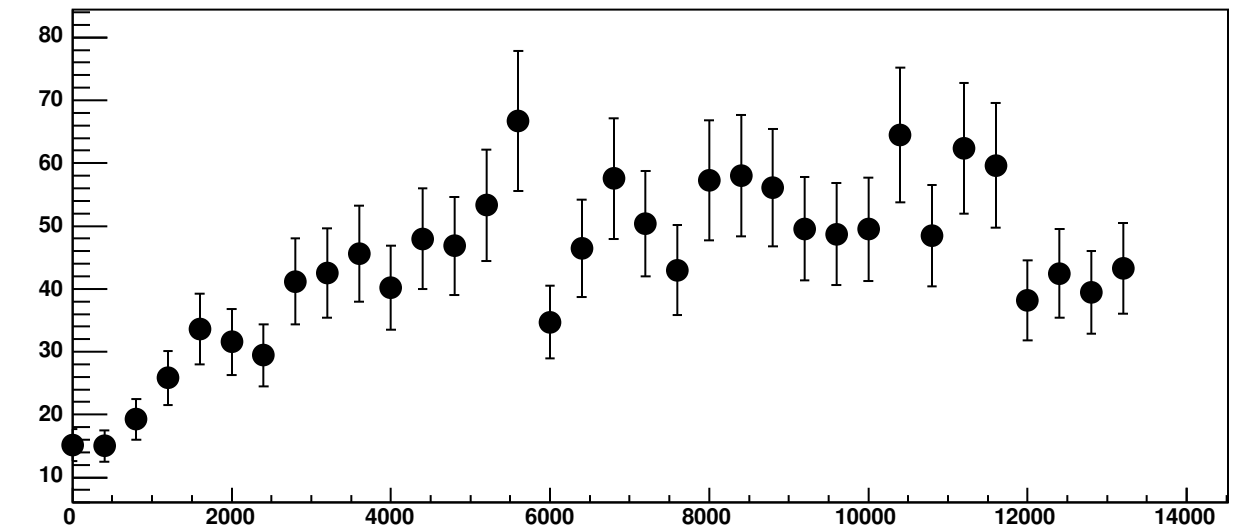

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

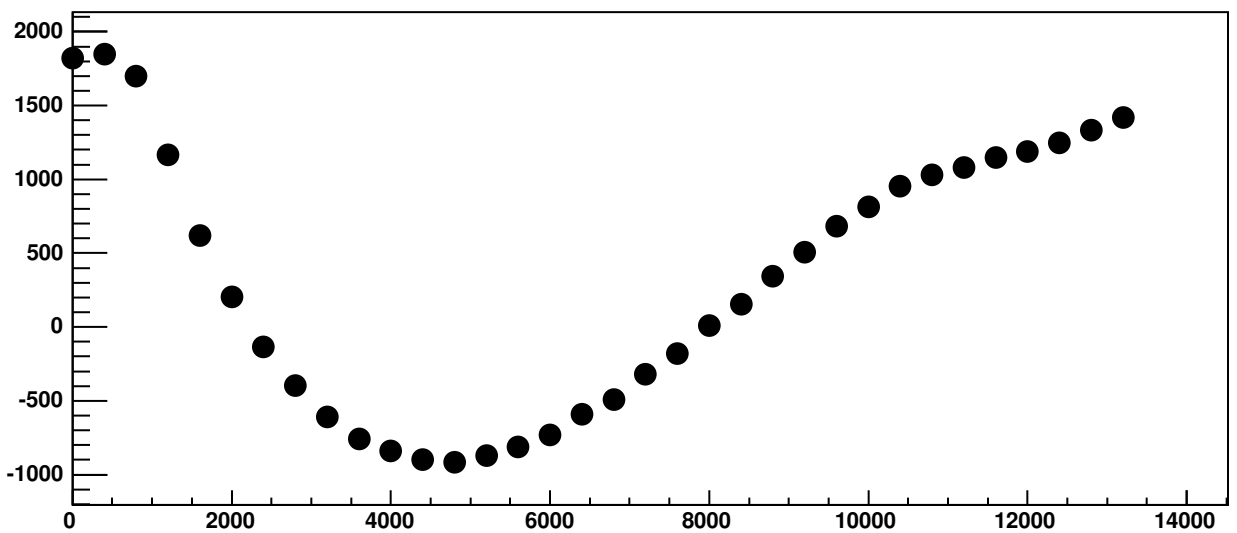
χ^2 / ndf	1.259e+05 / 23
p0	-3219 ± 3.768
p1	-0.1298 ± 0.0006577

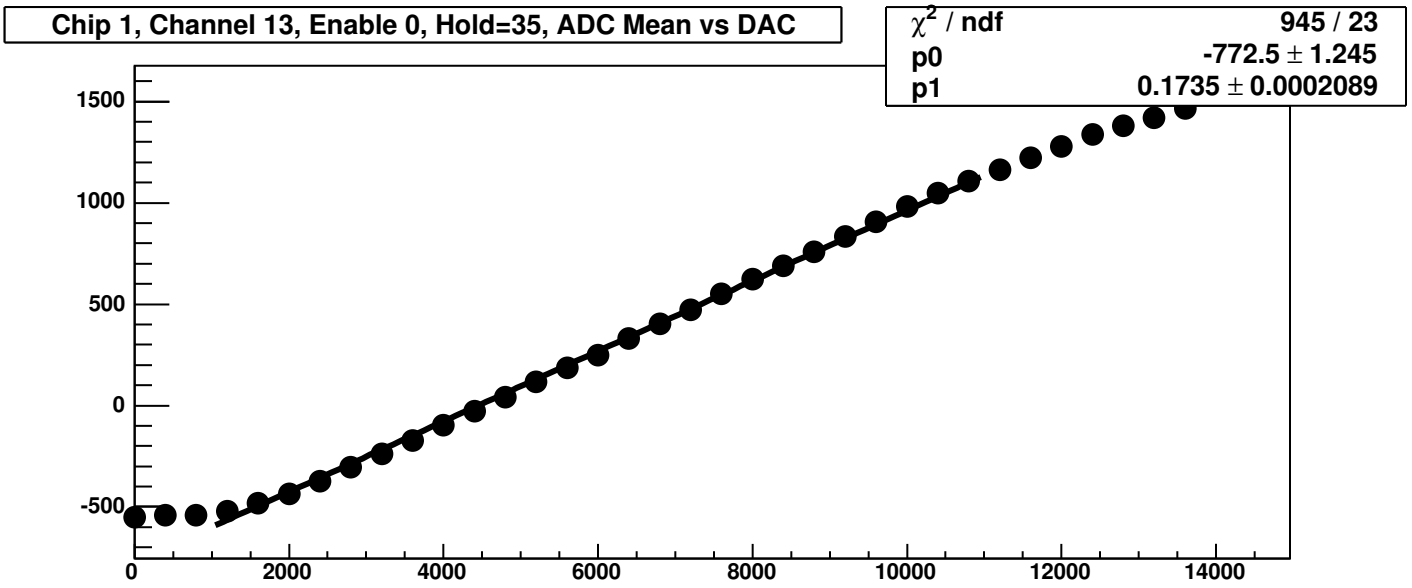


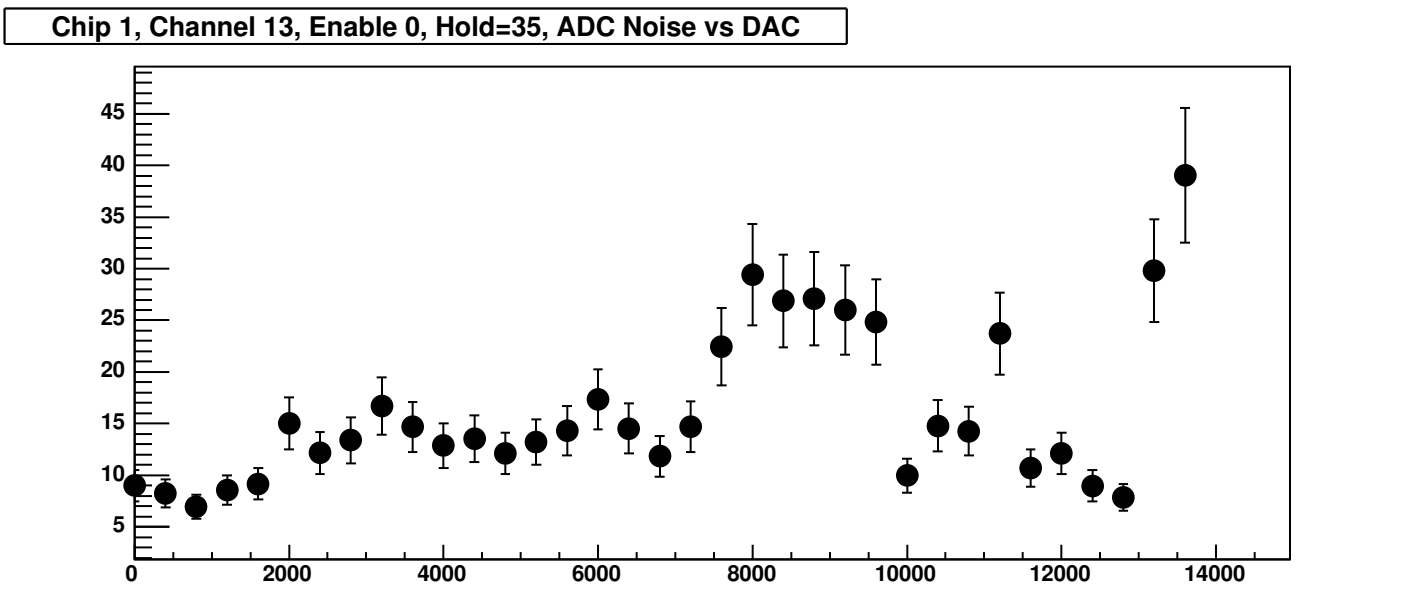
Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

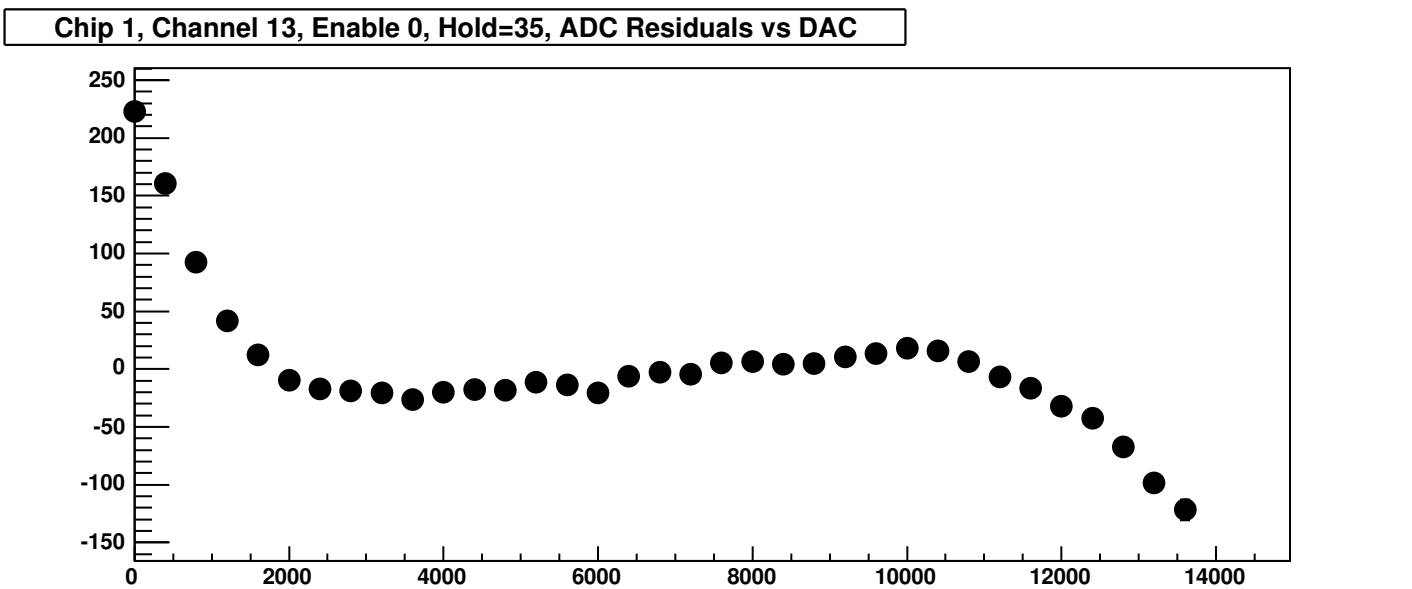


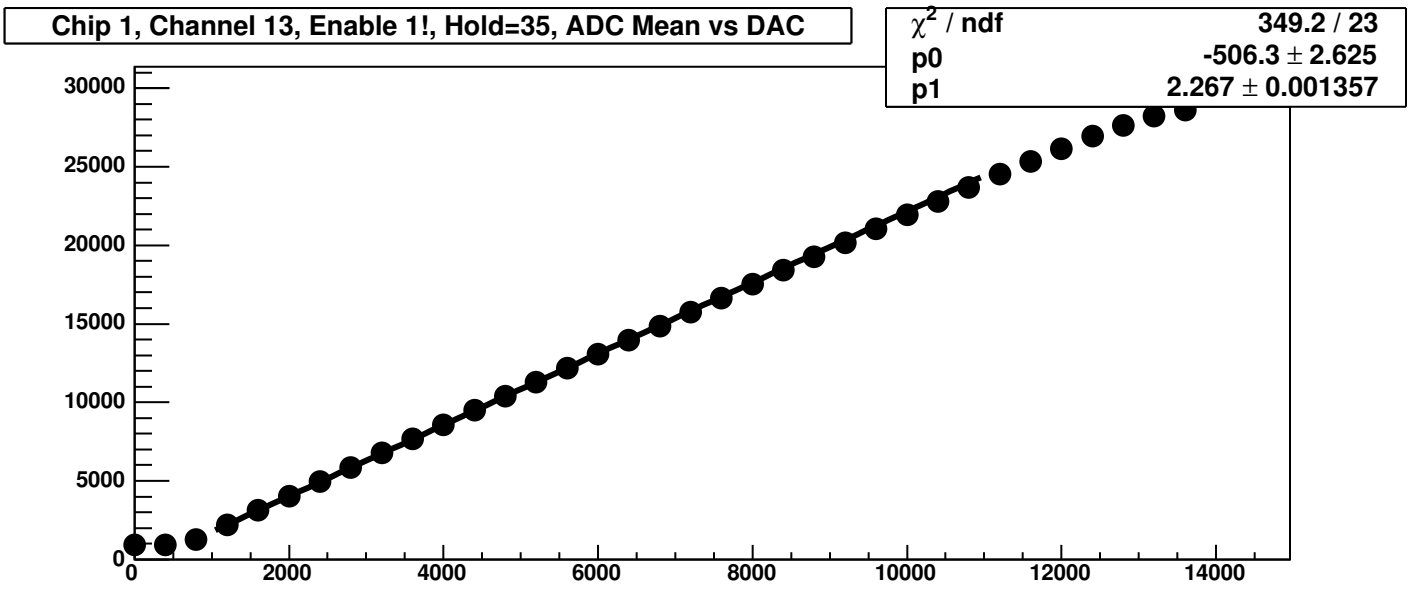
Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

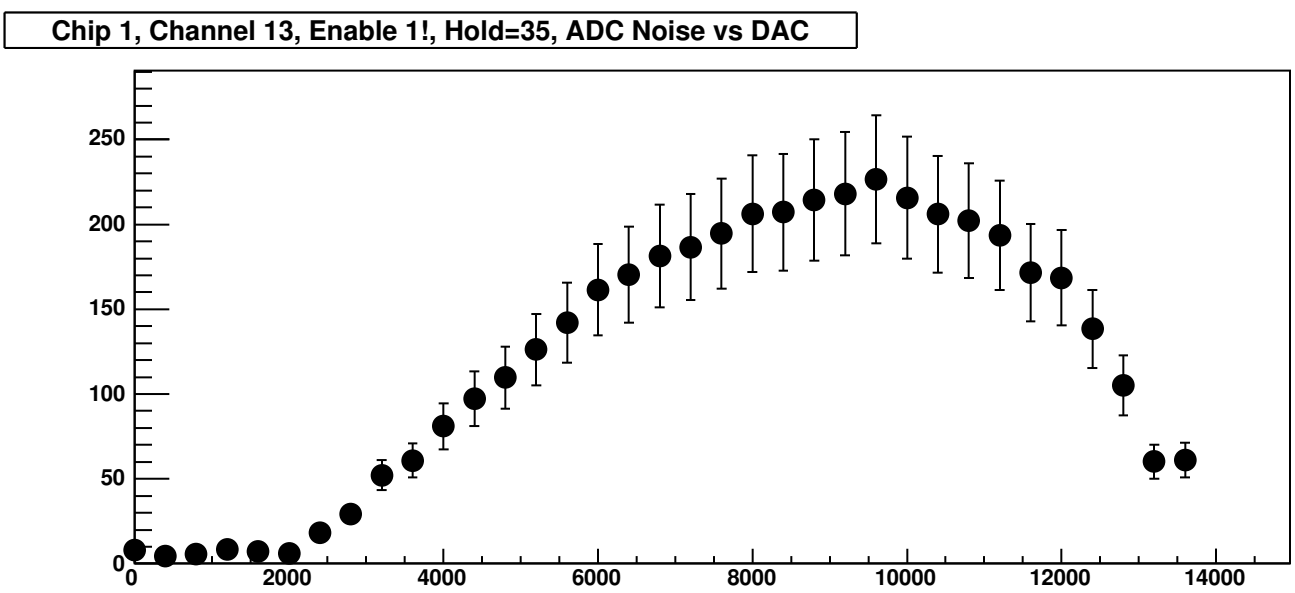


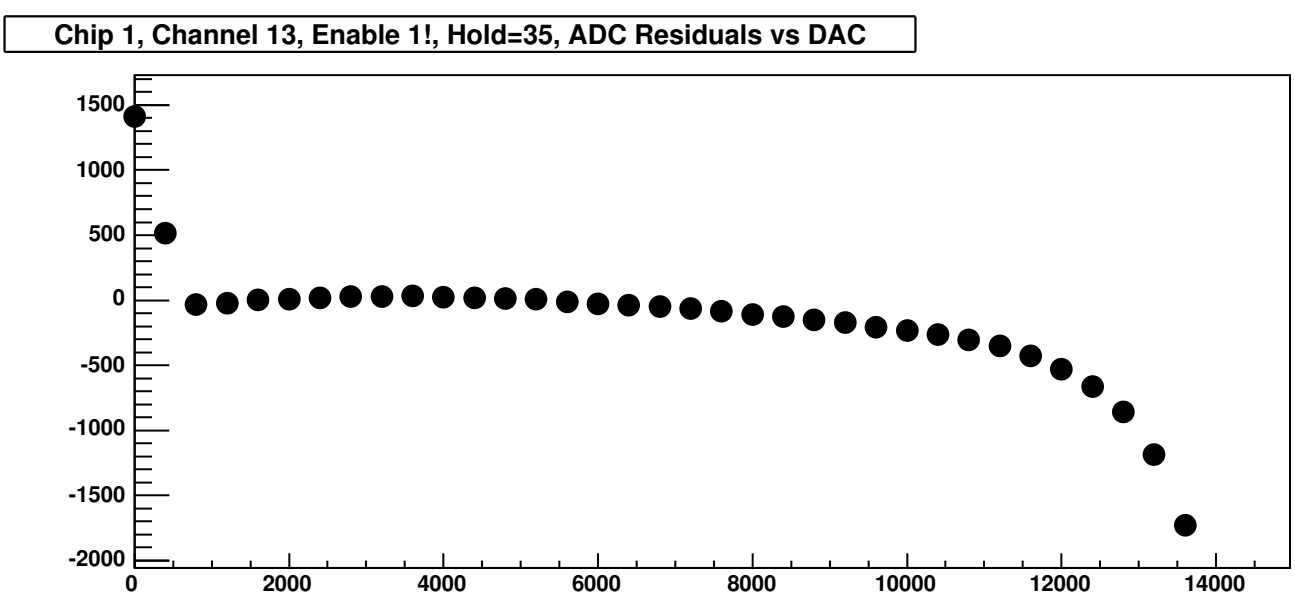

Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 945 / 23
p0 -772.5 ± 1.245
p1 0.1735 ± 0.0002089
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

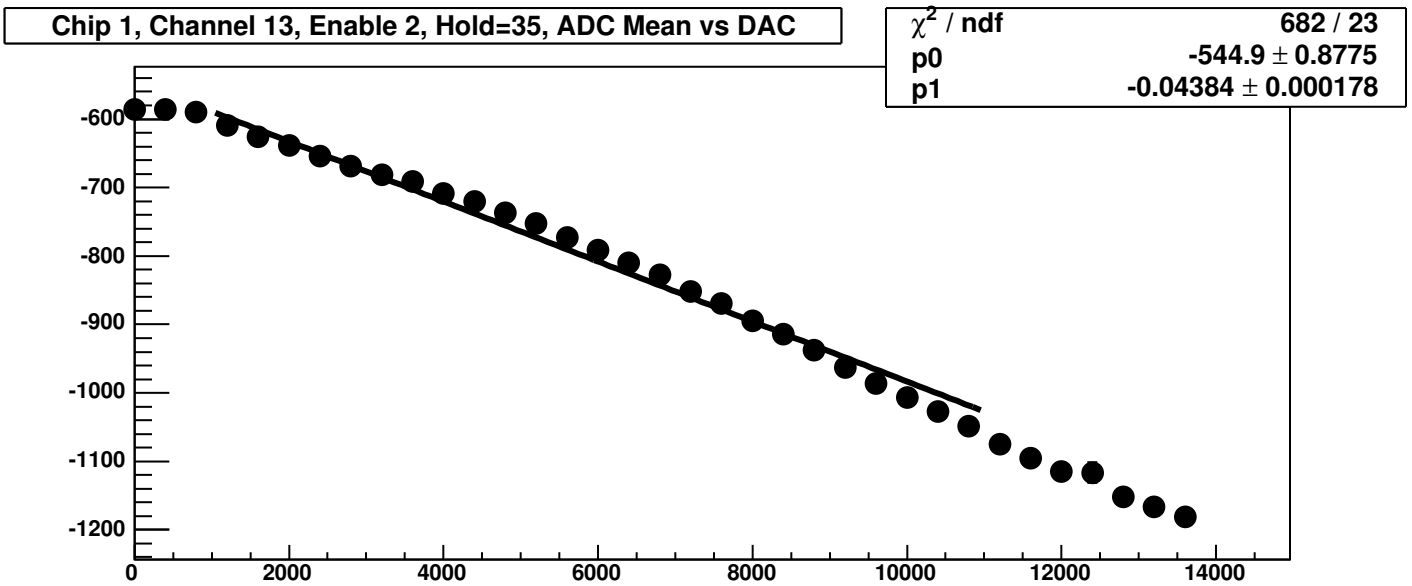

Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

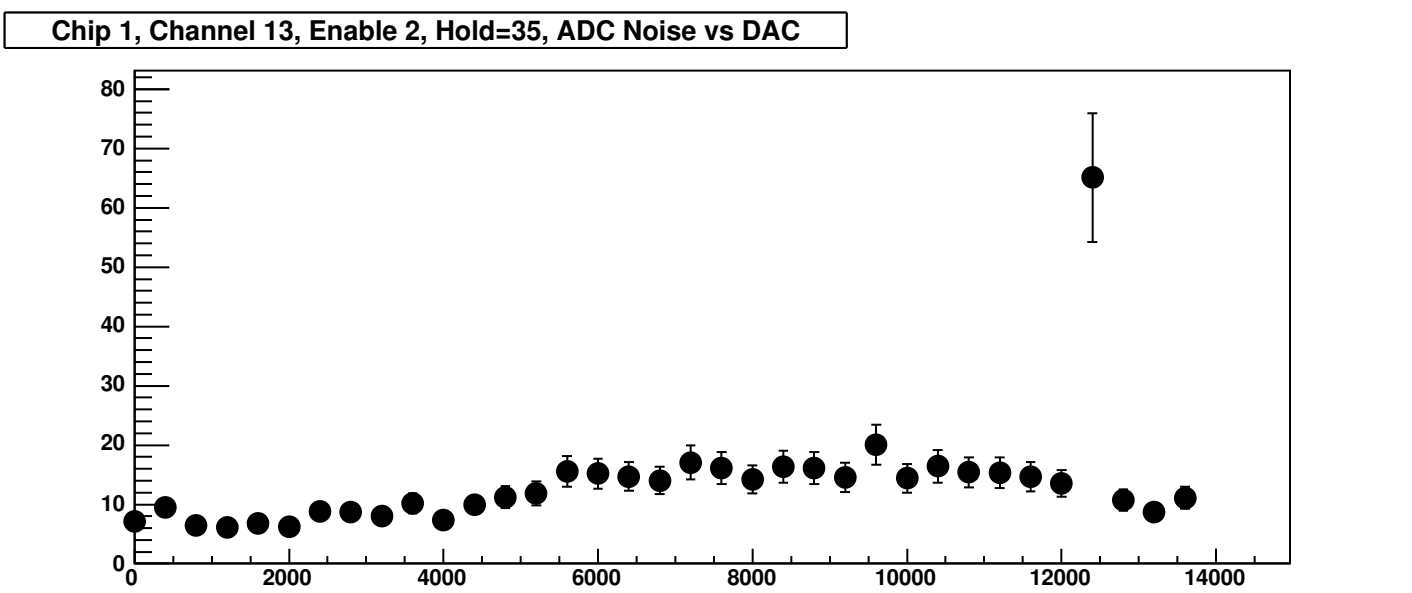

Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

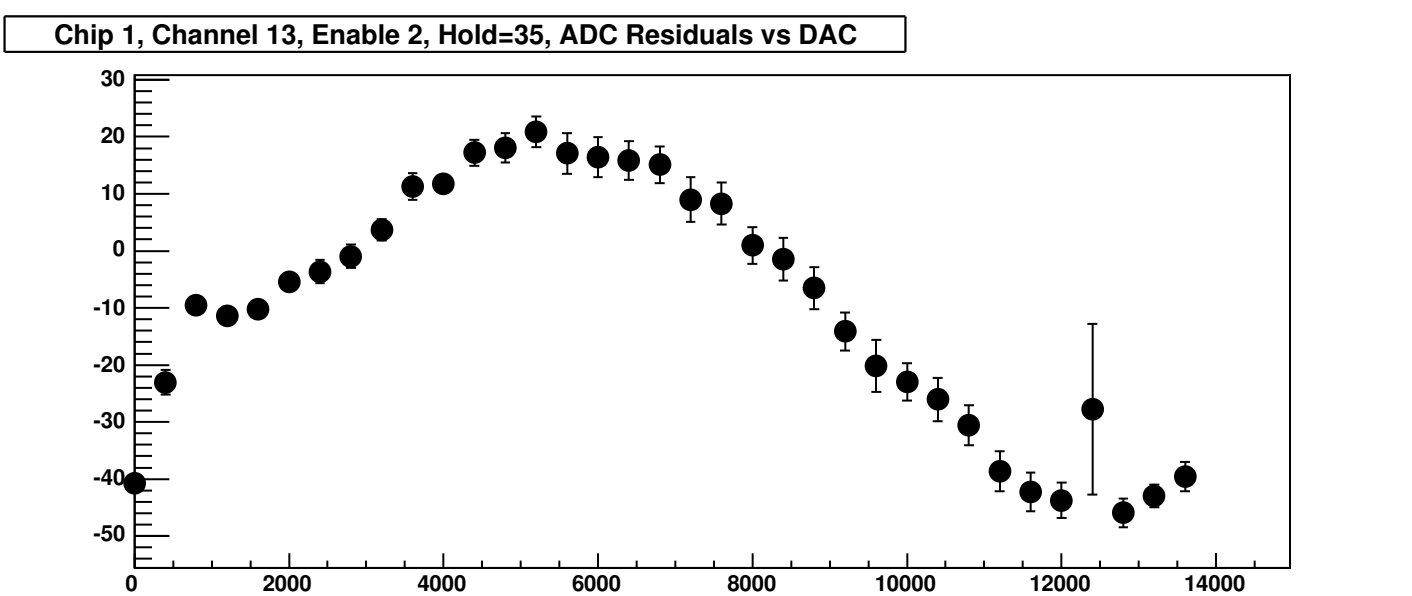

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 349.2 / 23
p0 -506.3 ± 2.625
p1 2.267 ± 0.001357
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

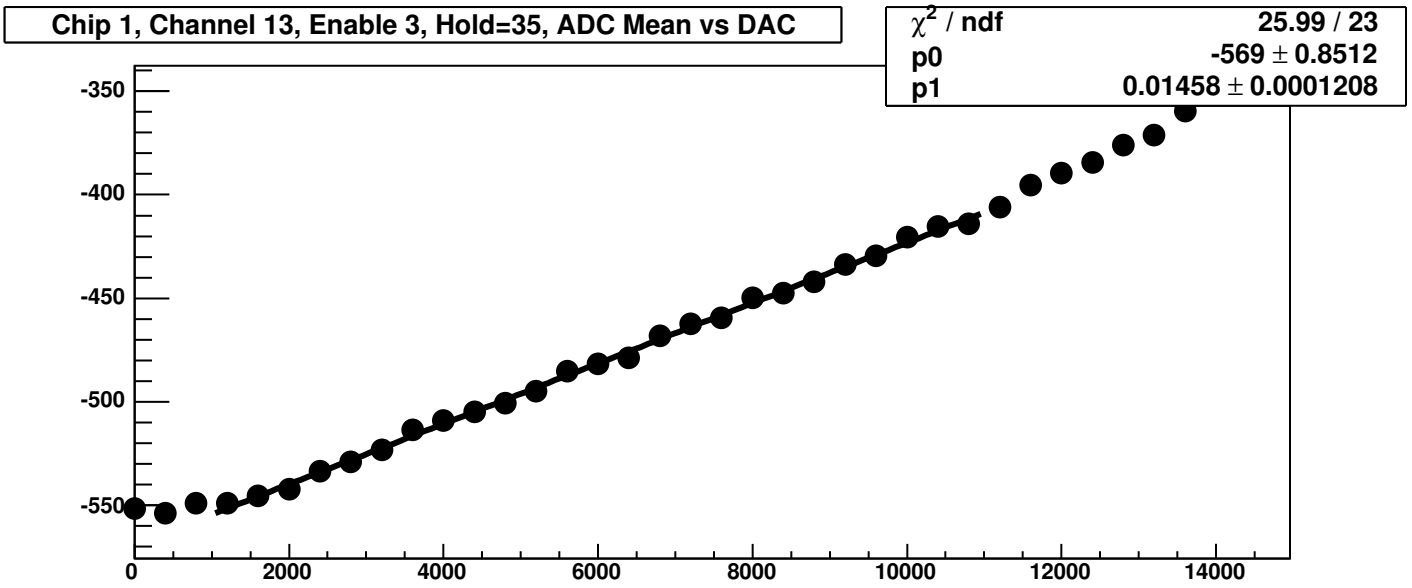

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

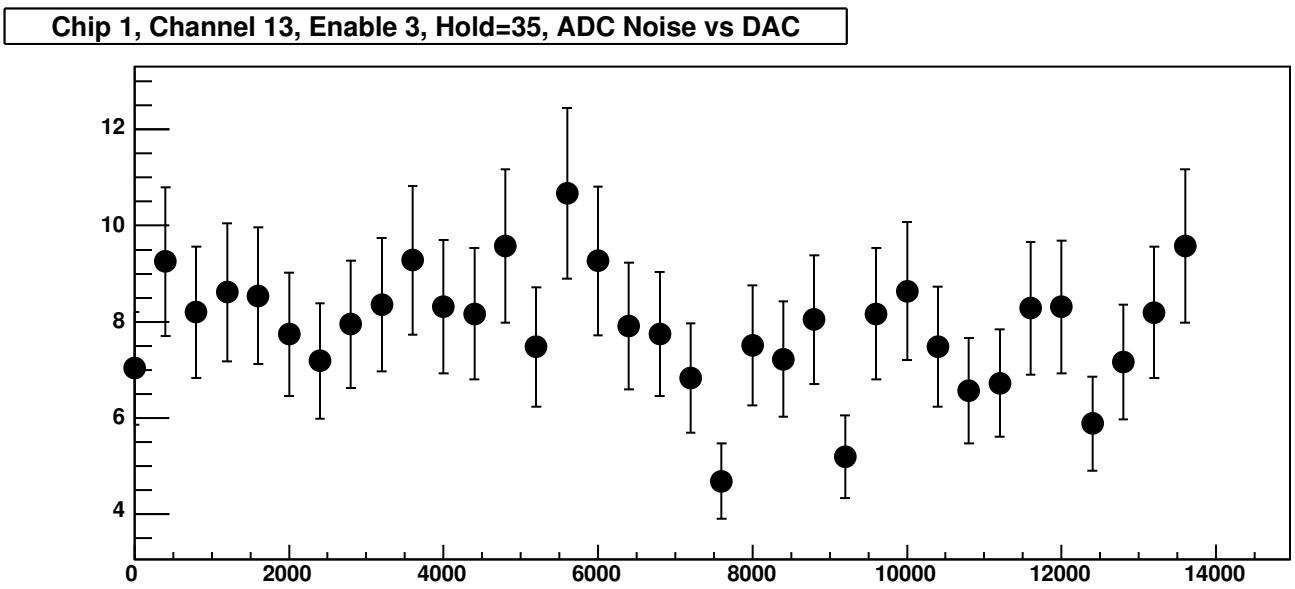

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

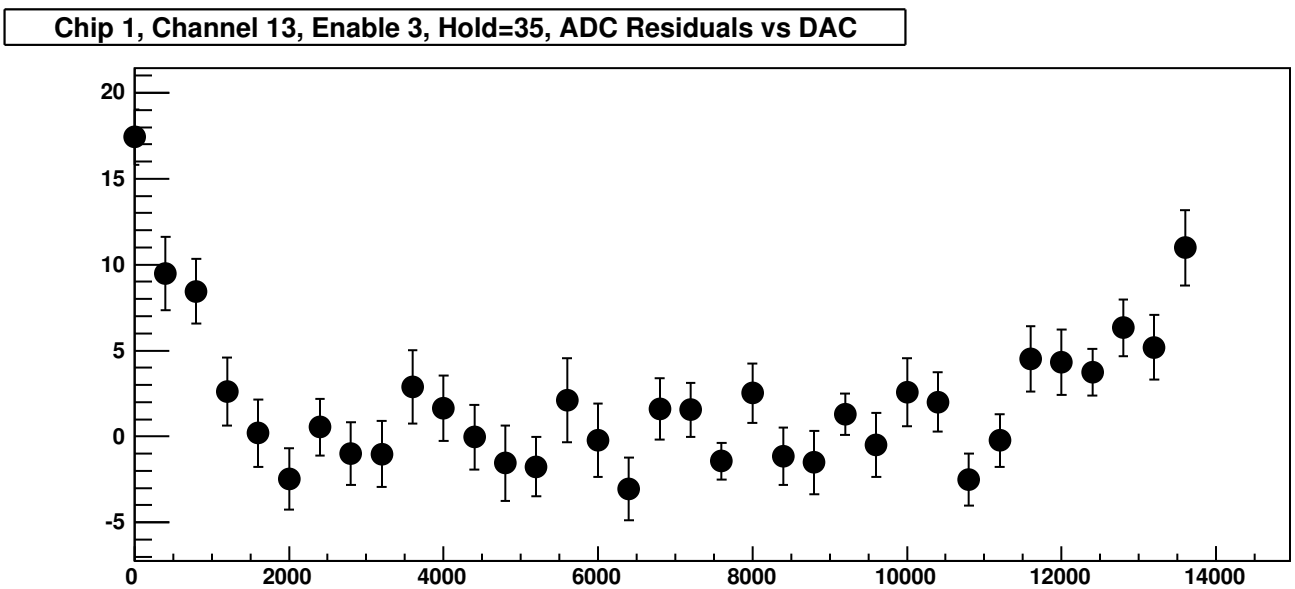

Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 682 / 23
p0 -544.9 ± 0.8775
p1 -0.04384 ± 0.000178
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

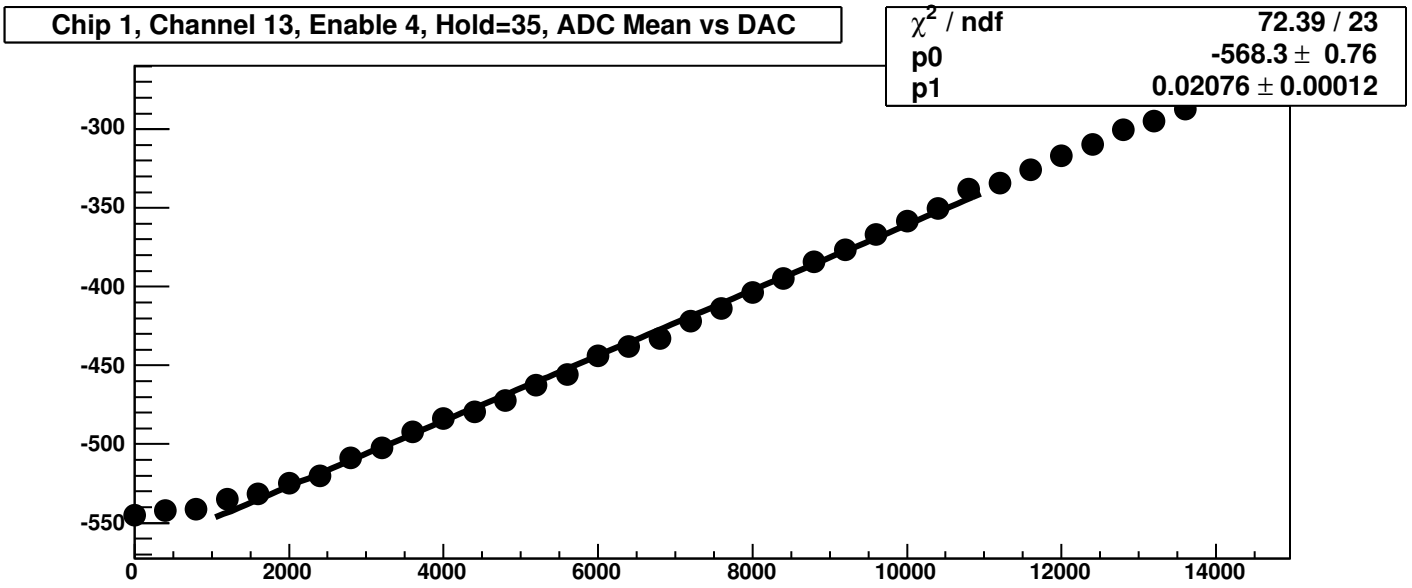

Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

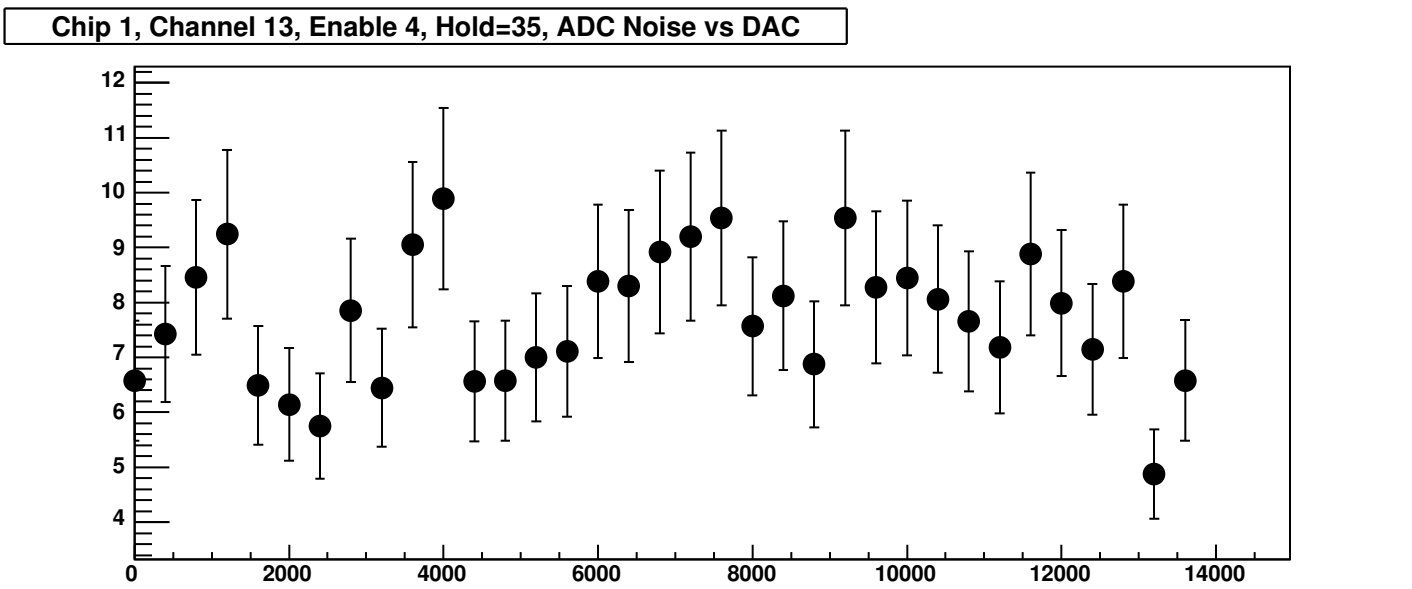

Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

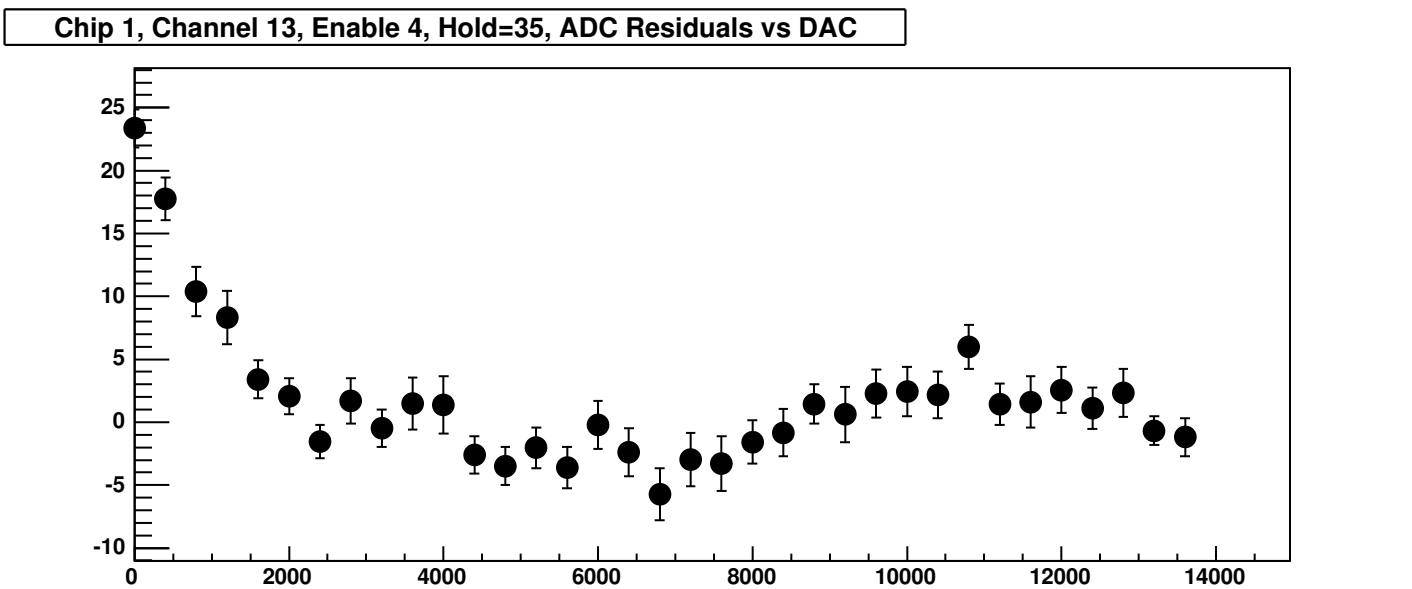

Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.99 / 23
p0 -569 ± 0.8512
p1 0.01458 ± 0.0001208
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

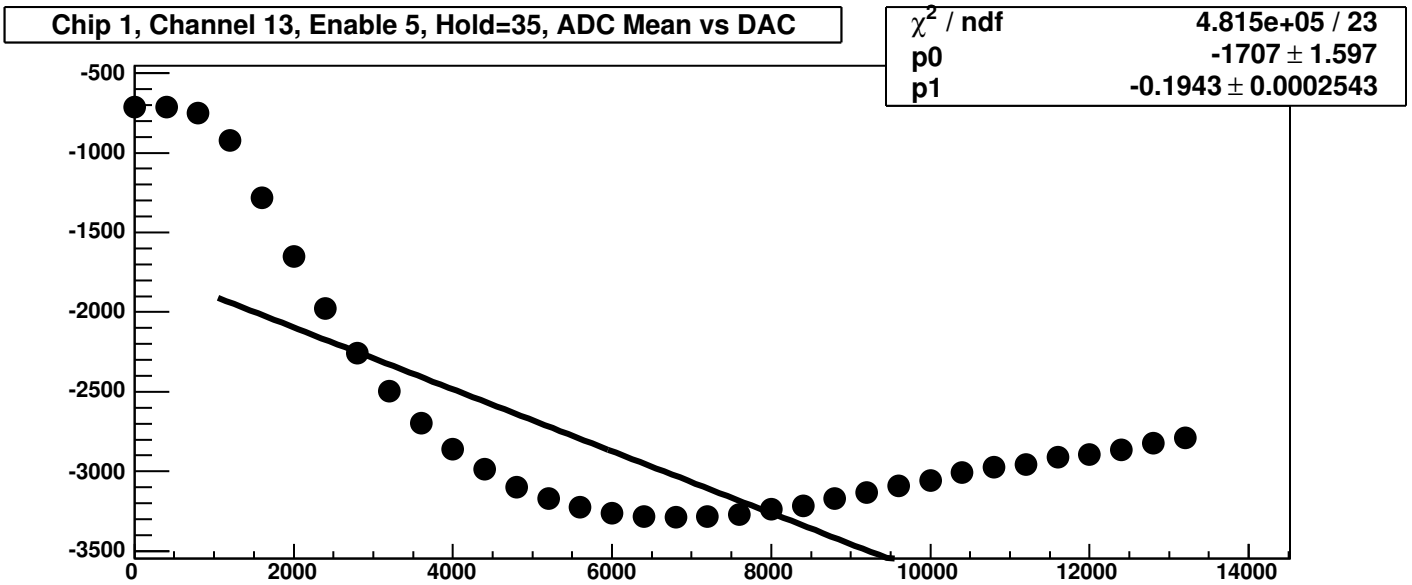

Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

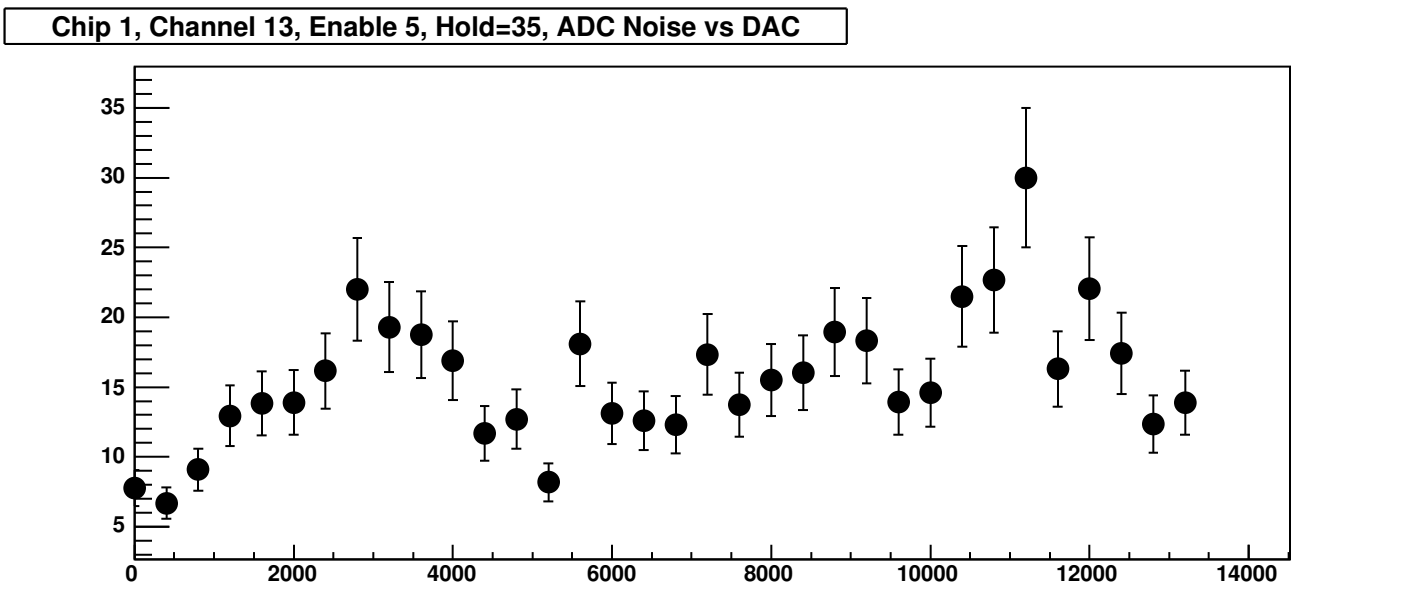

Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

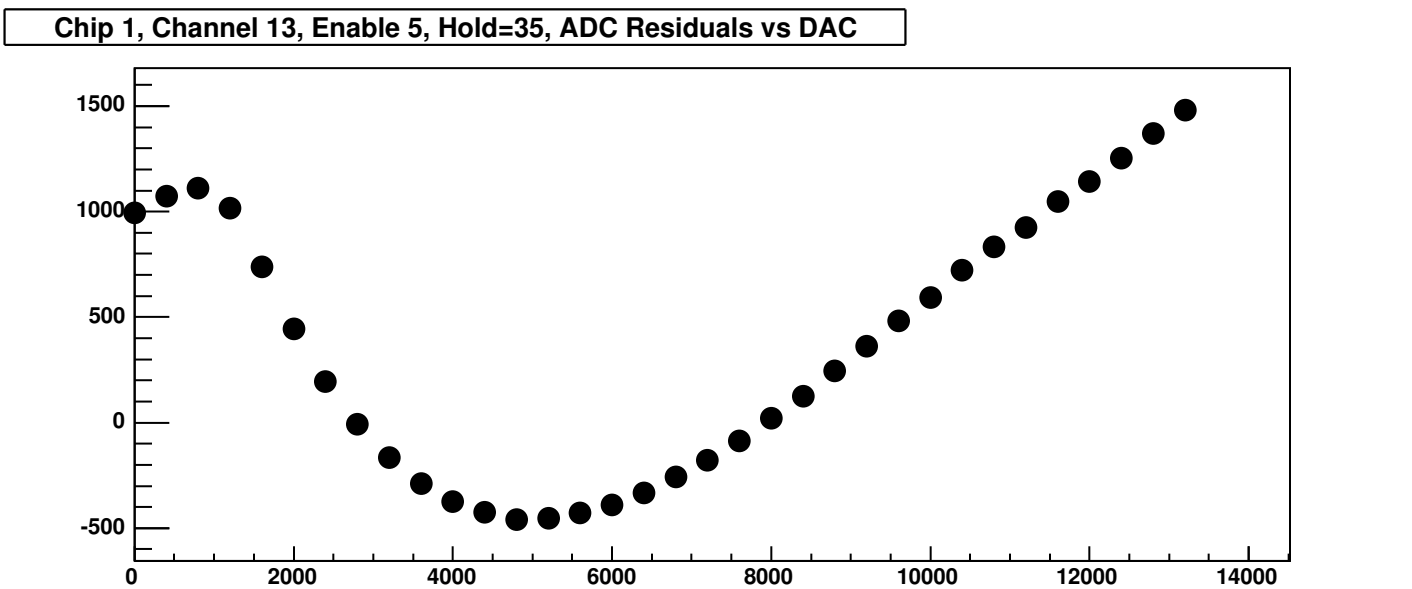

Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 72.39 / 23
p0 -568.3 ± 0.76
p1 0.02076 ± 0.00012
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

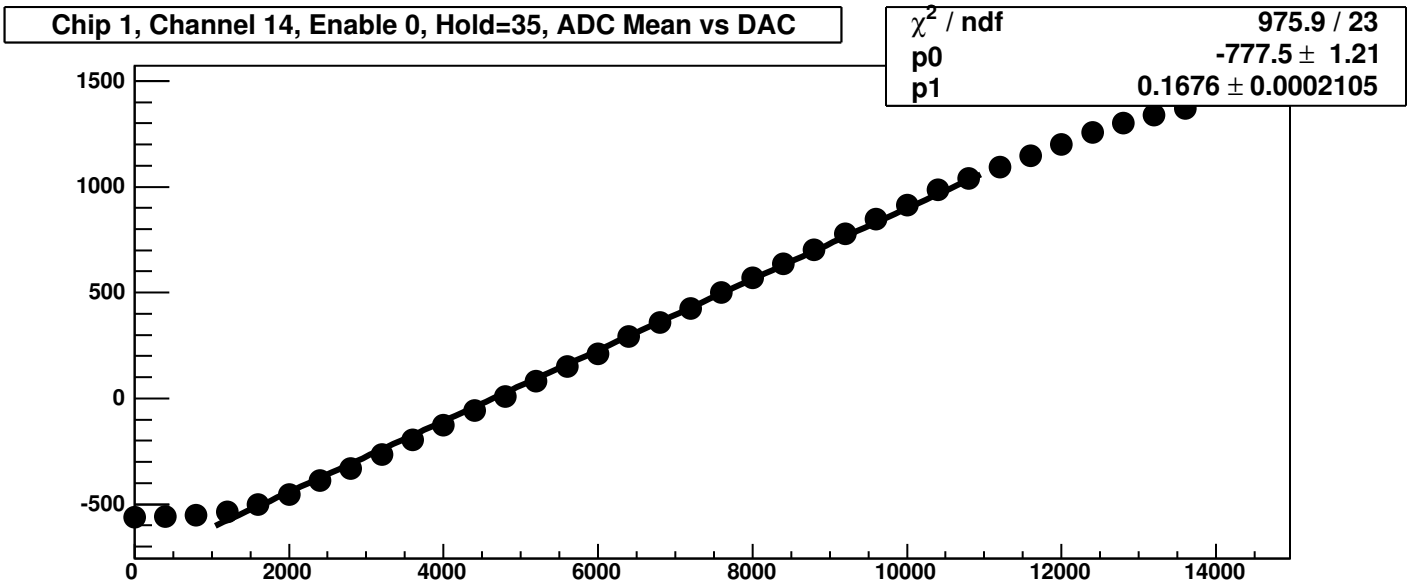

Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

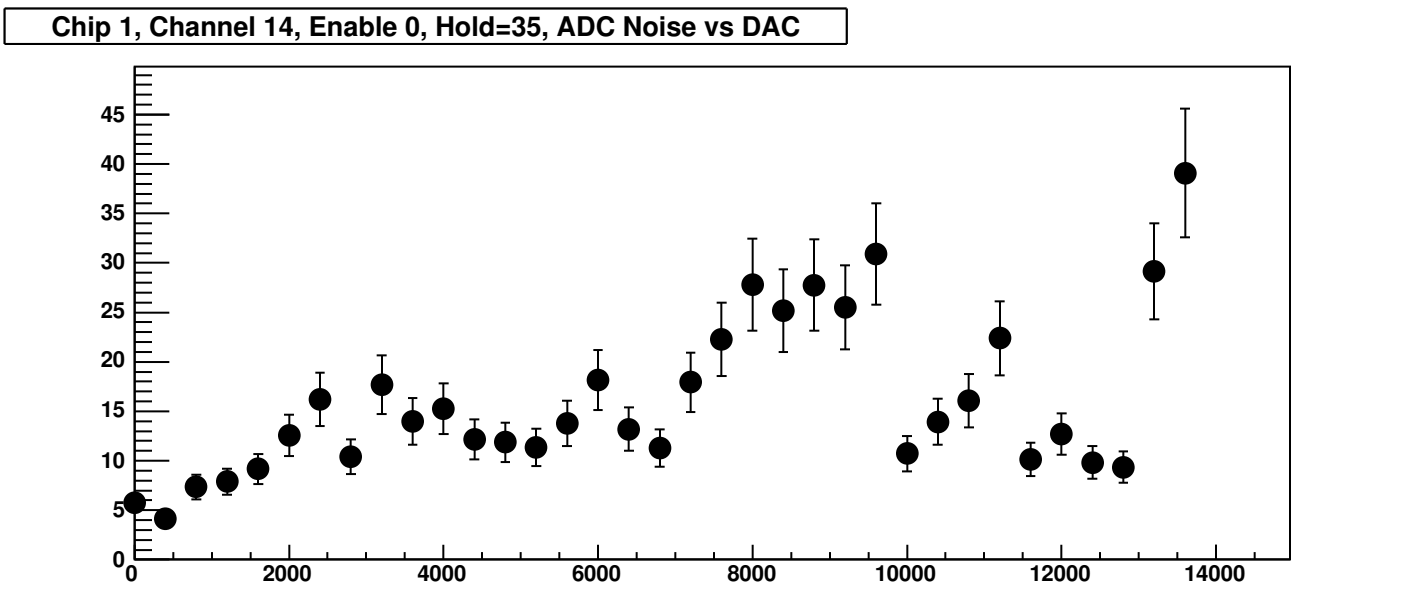

Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

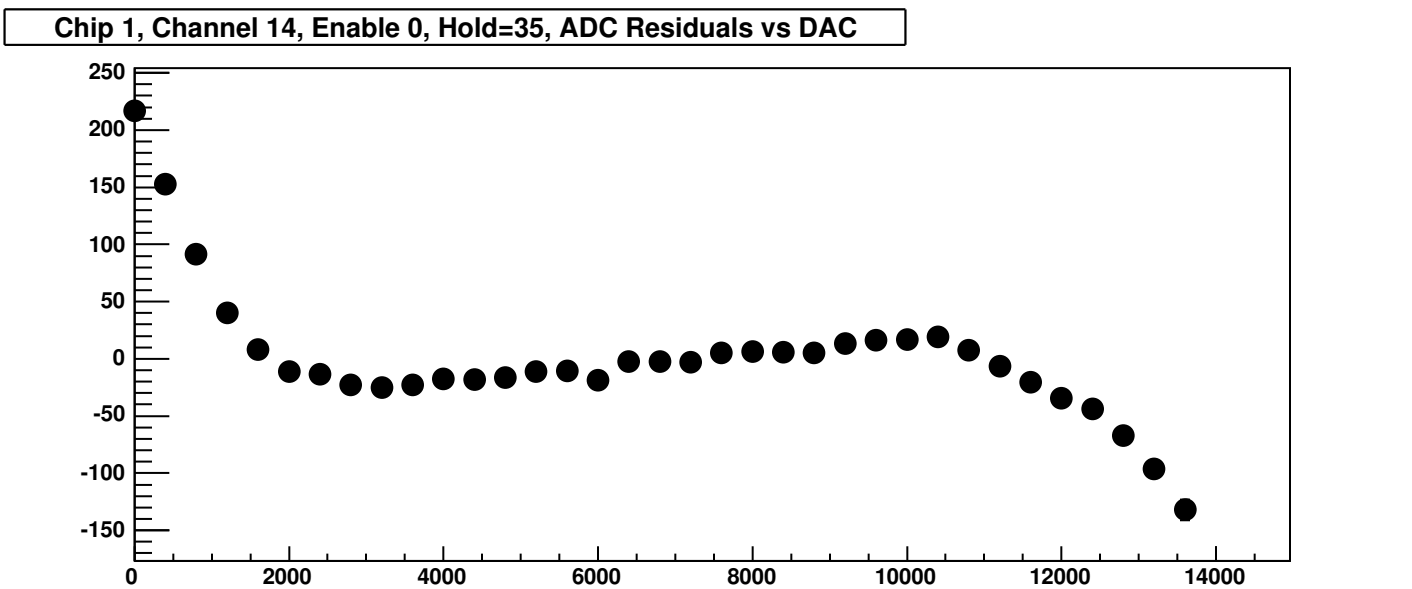

Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.815e+05 / 23
p0 -1707 ± 1.597
p1 -0.1943 ± 0.0002543
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
2000
4000
6000
8000
10000
12000
14000

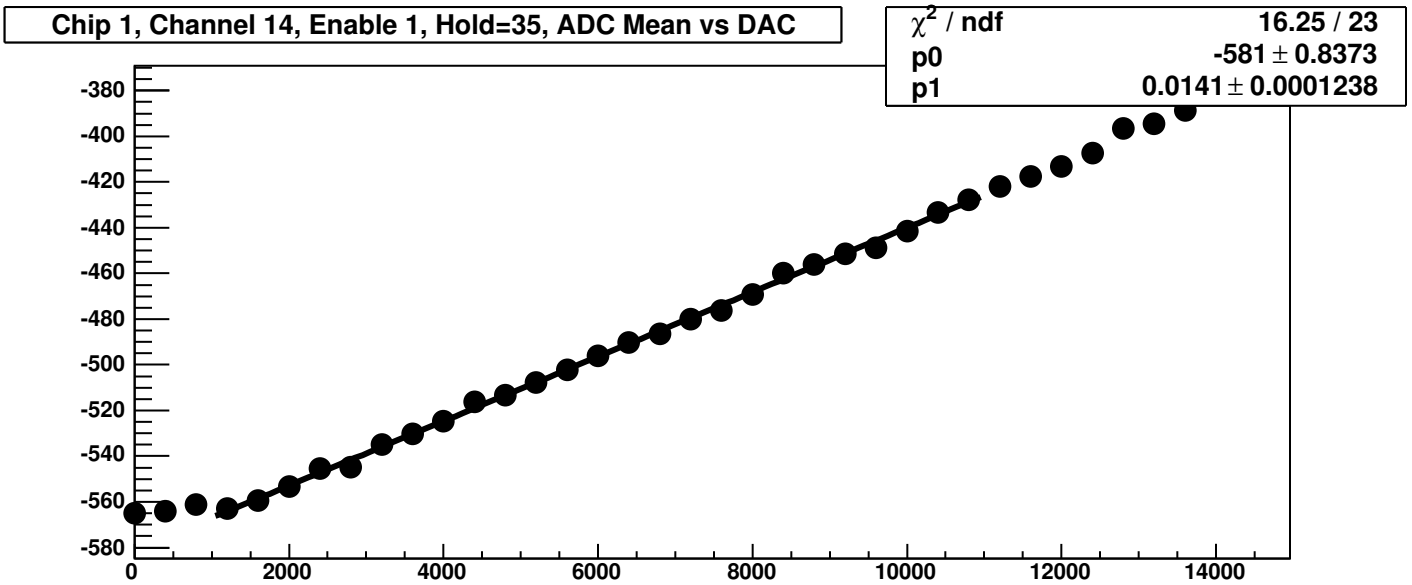

Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

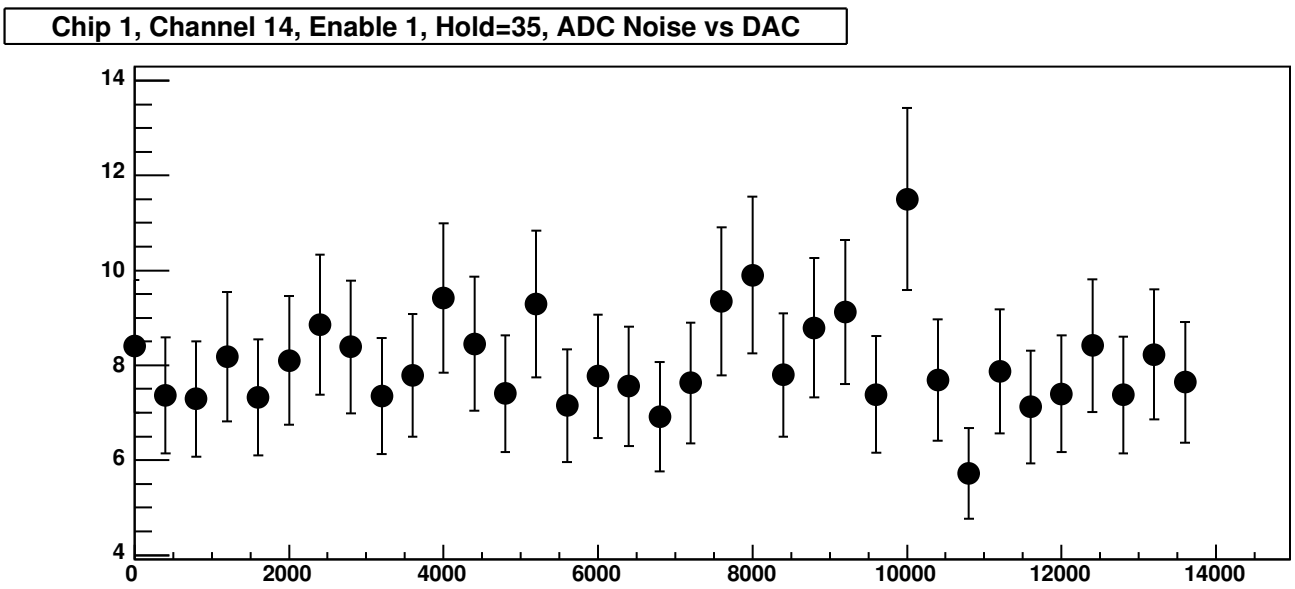

Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

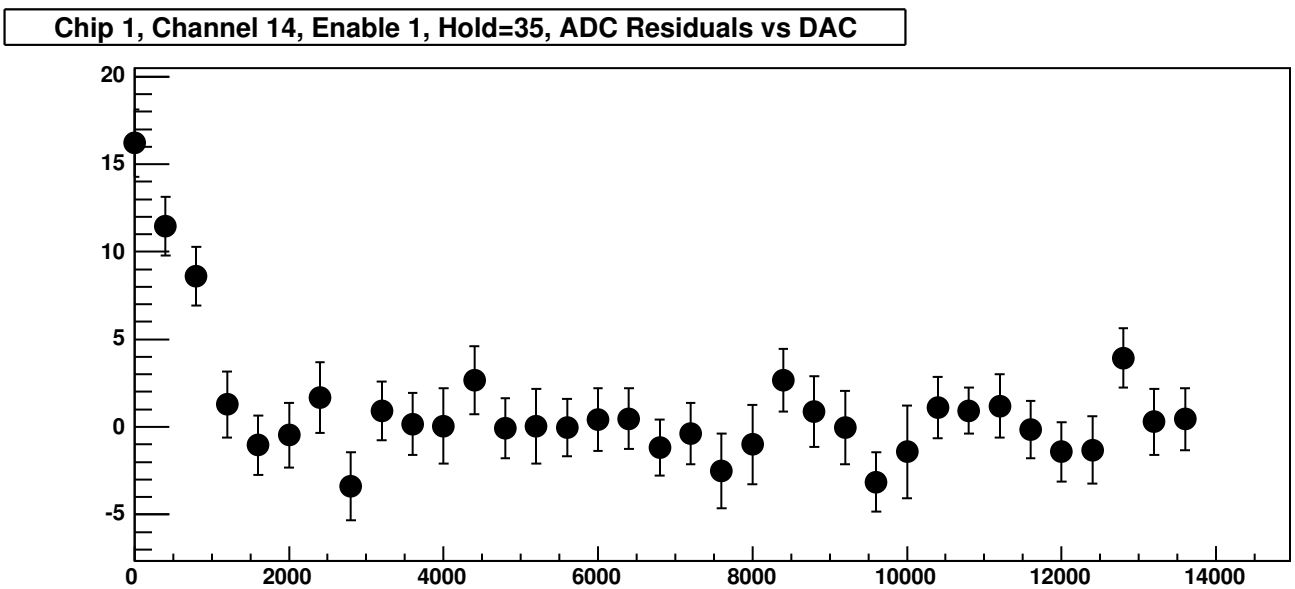

Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 975.9 / 23
p0 -777.5 ± 1.21
p1 0.1676 ± 0.0002105
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

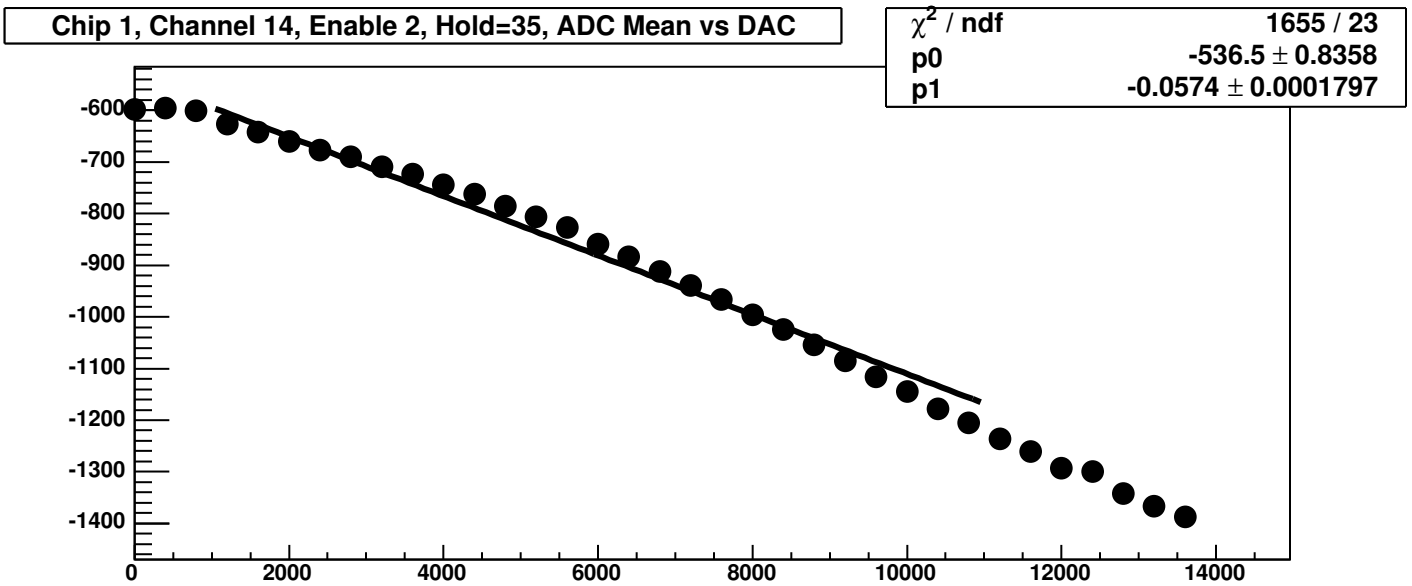

Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

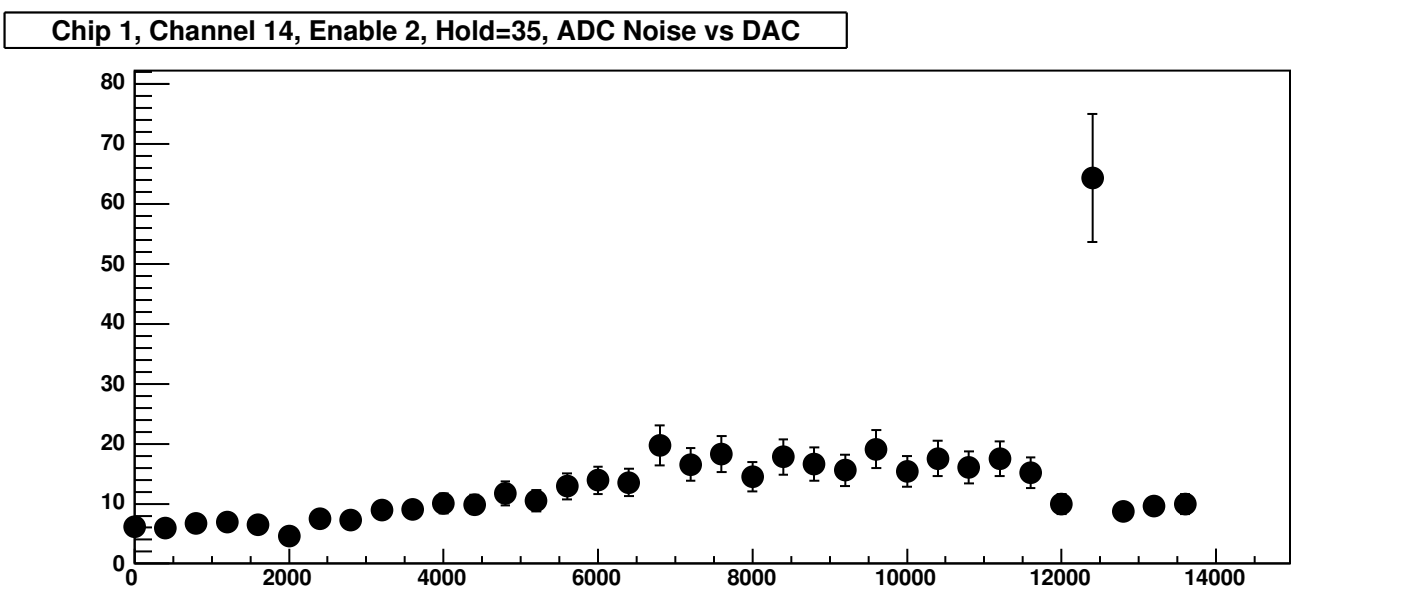

Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

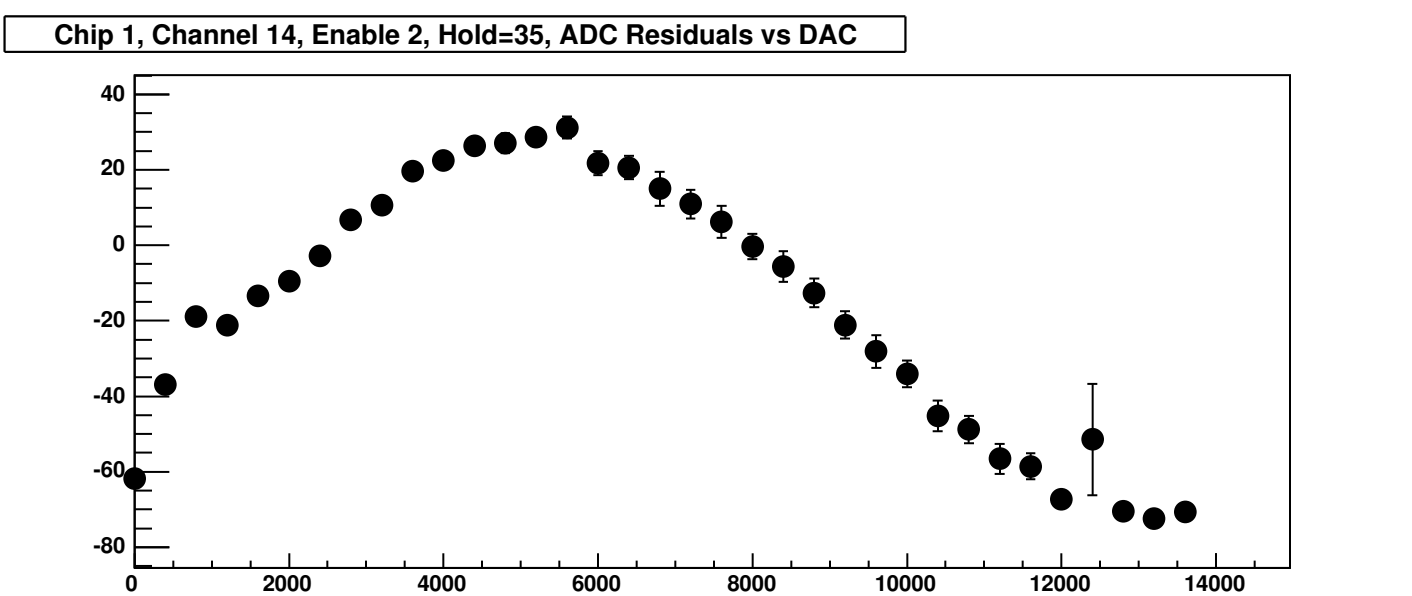

Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.25 / 23
p0 -581 ± 0.8373
p1 0.0141 ± 0.0001238
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

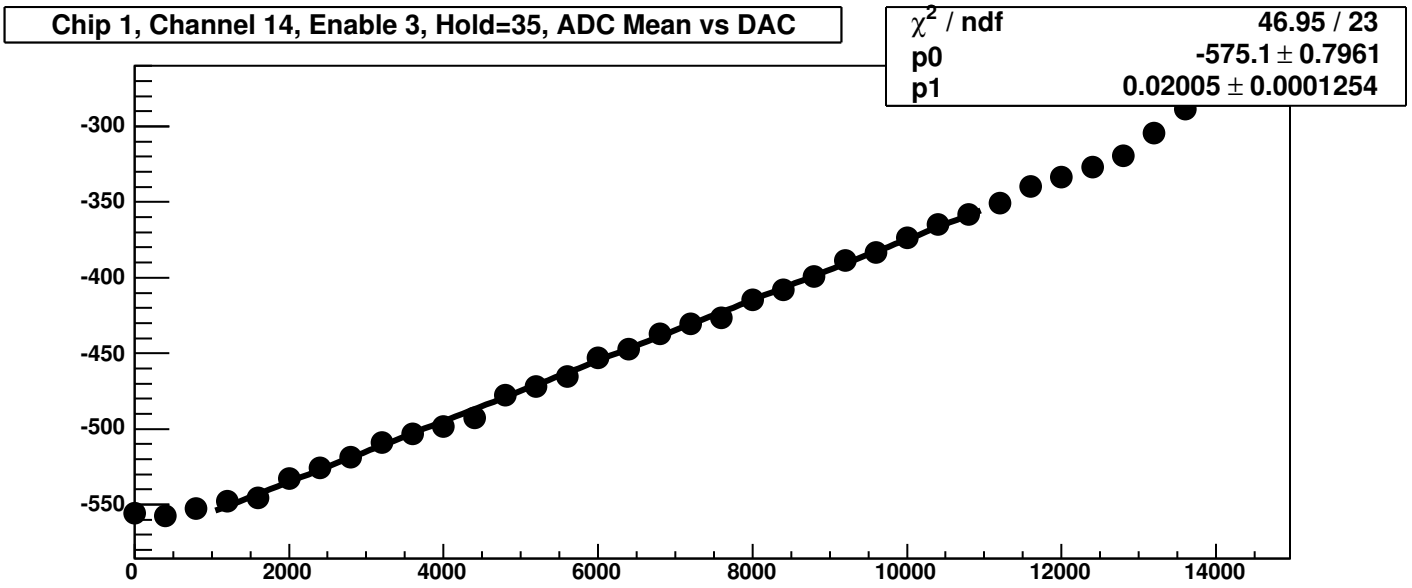

Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

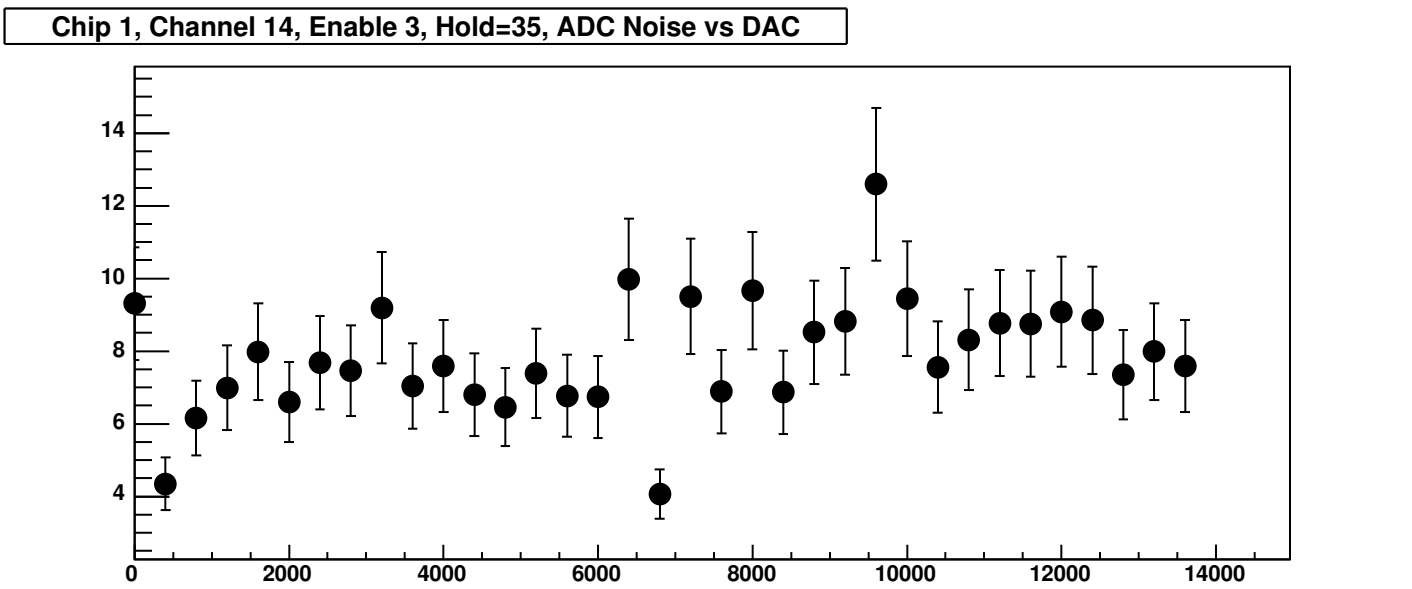

Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

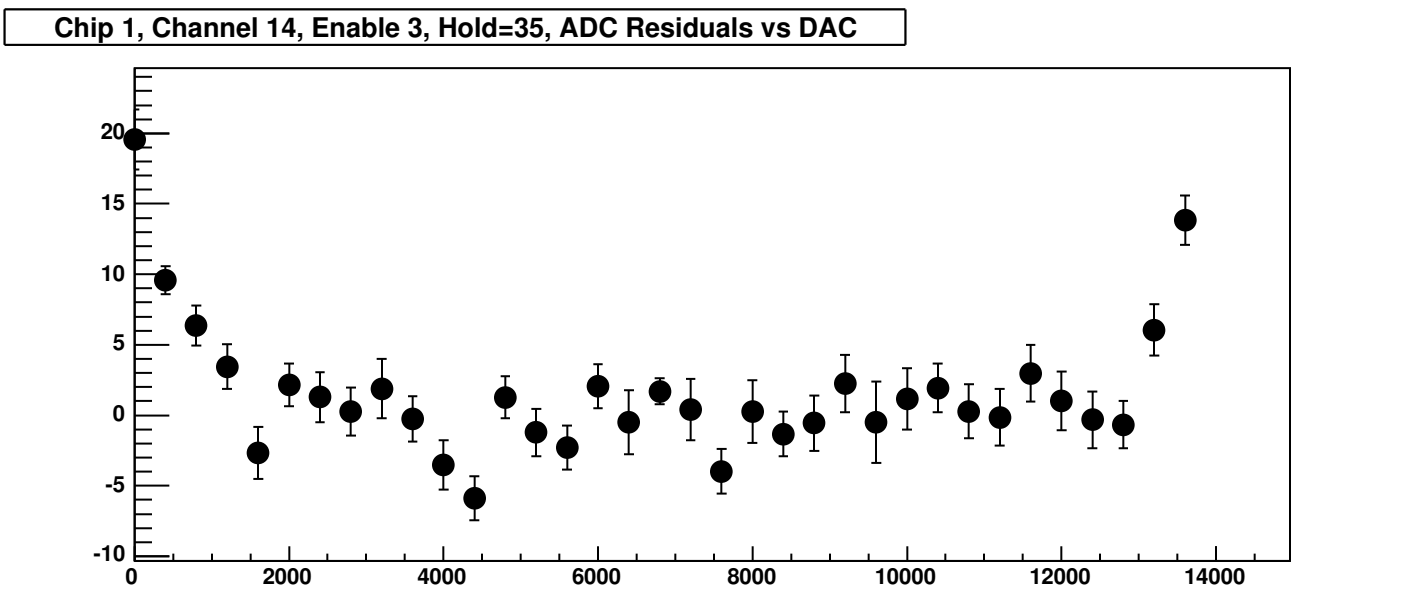

Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1655 / 23
p0 -536.5 ± 0.8358
p1 -0.0574 ± 0.0001797
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
2000
4000
6000
8000
10000
12000
14000

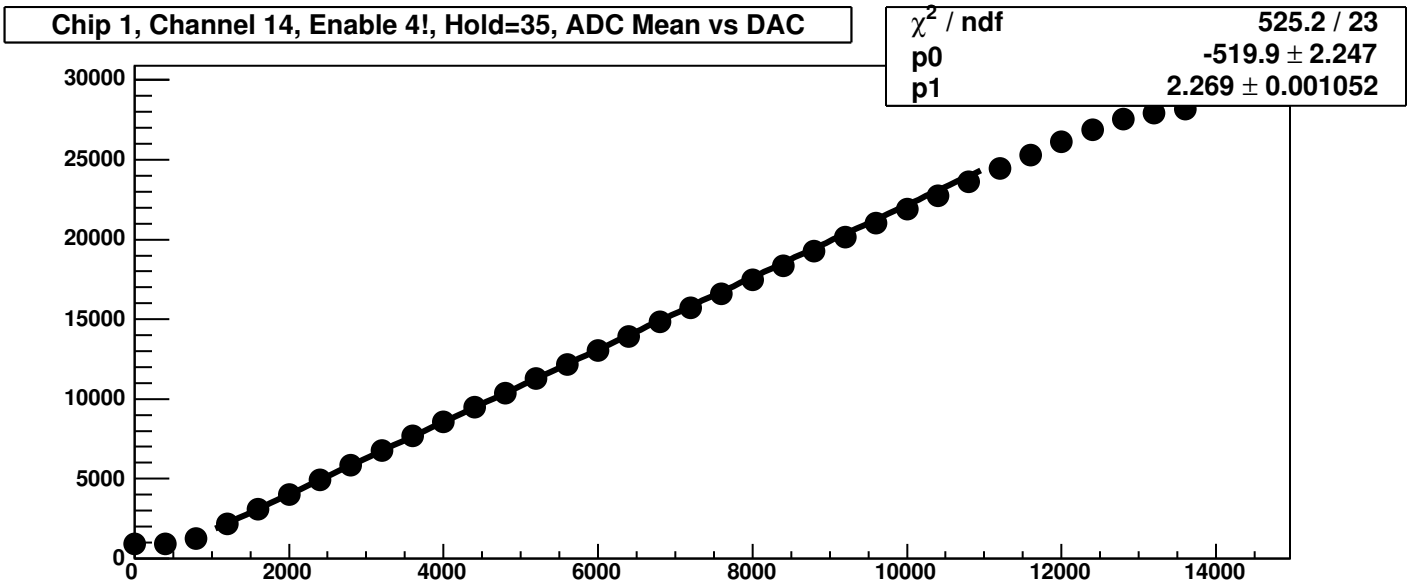

Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

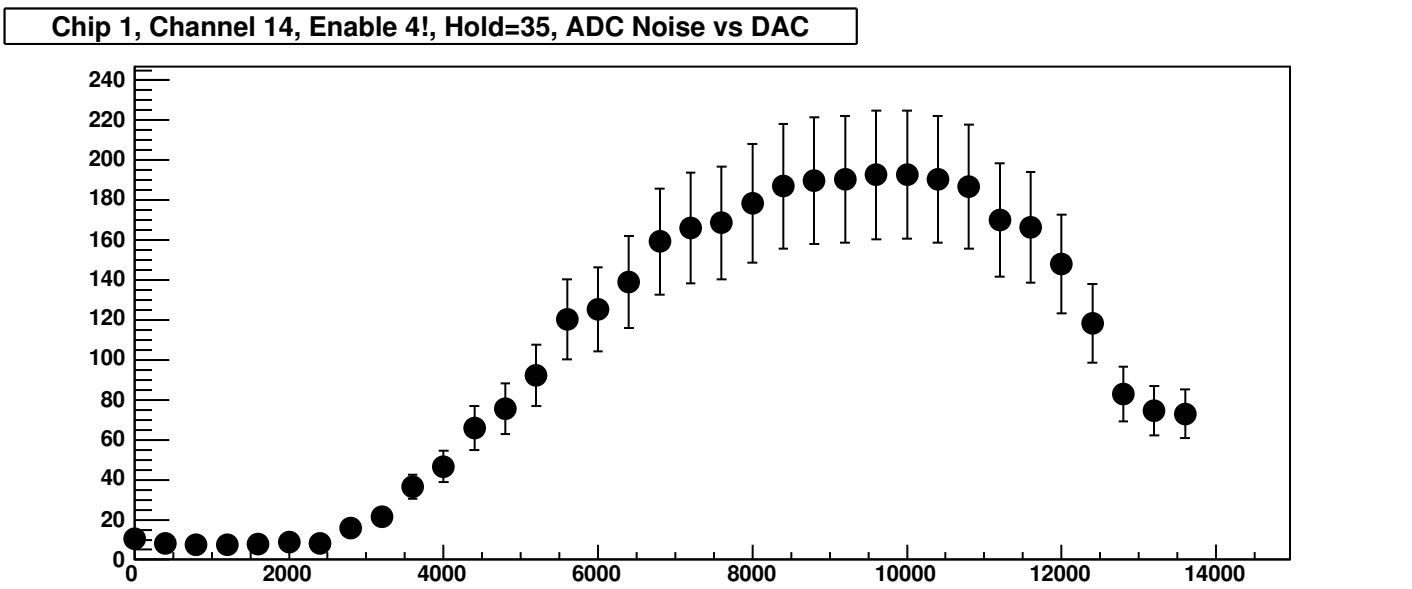

Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

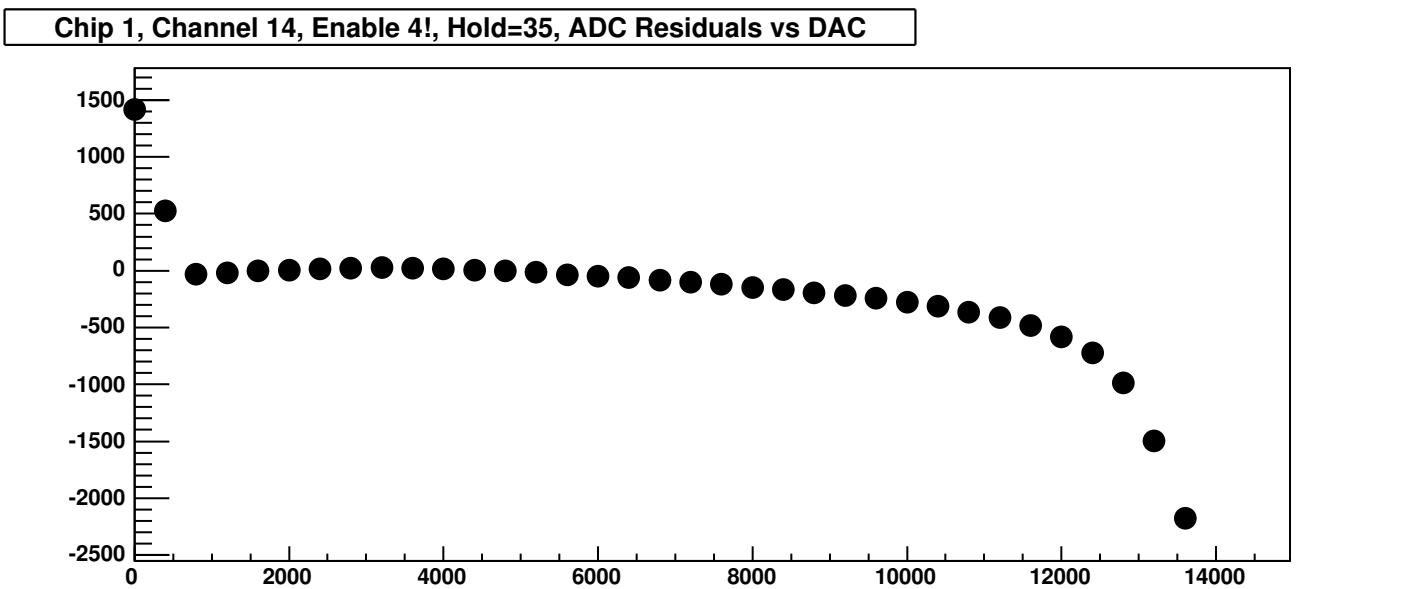

Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 46.95 / 23
p0 -575.1 ± 0.7961
p1 0.02005 ± 0.0001254
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

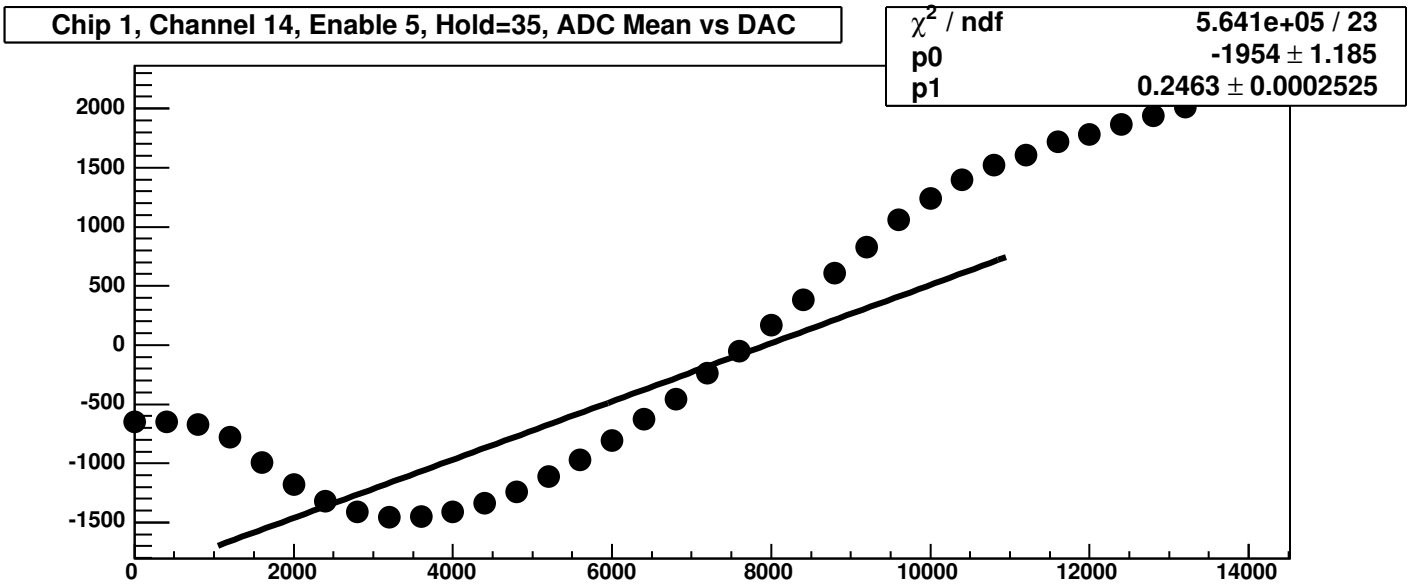

Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

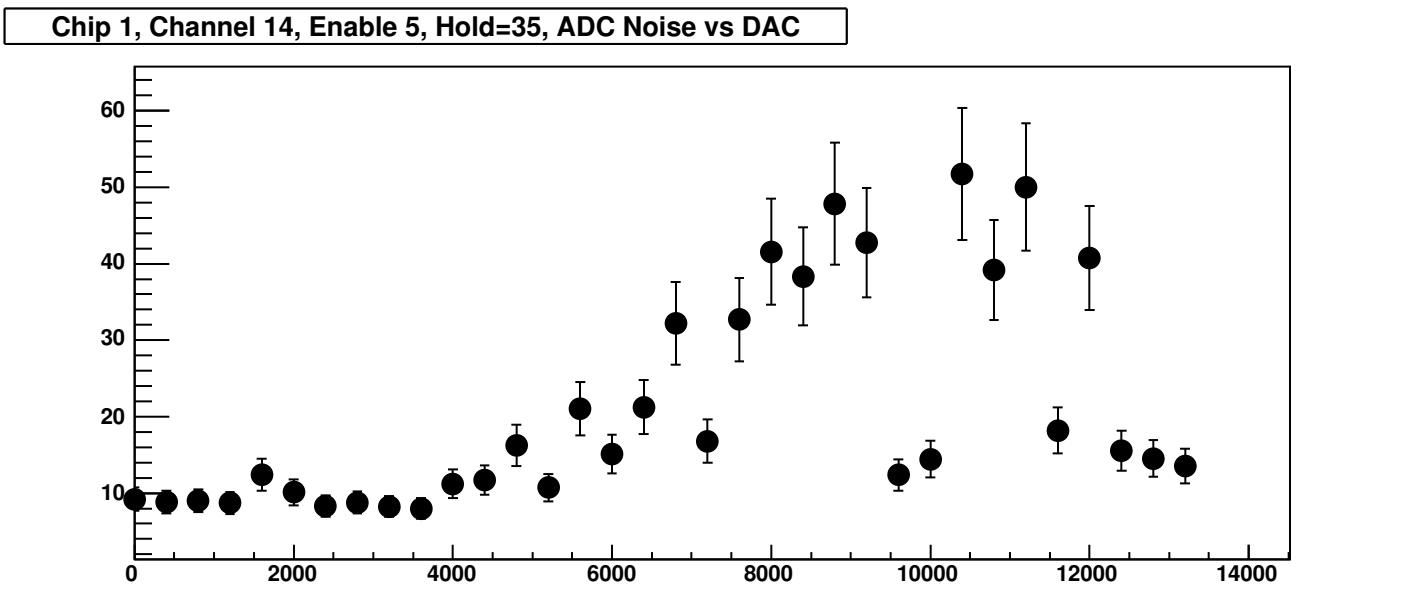

Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

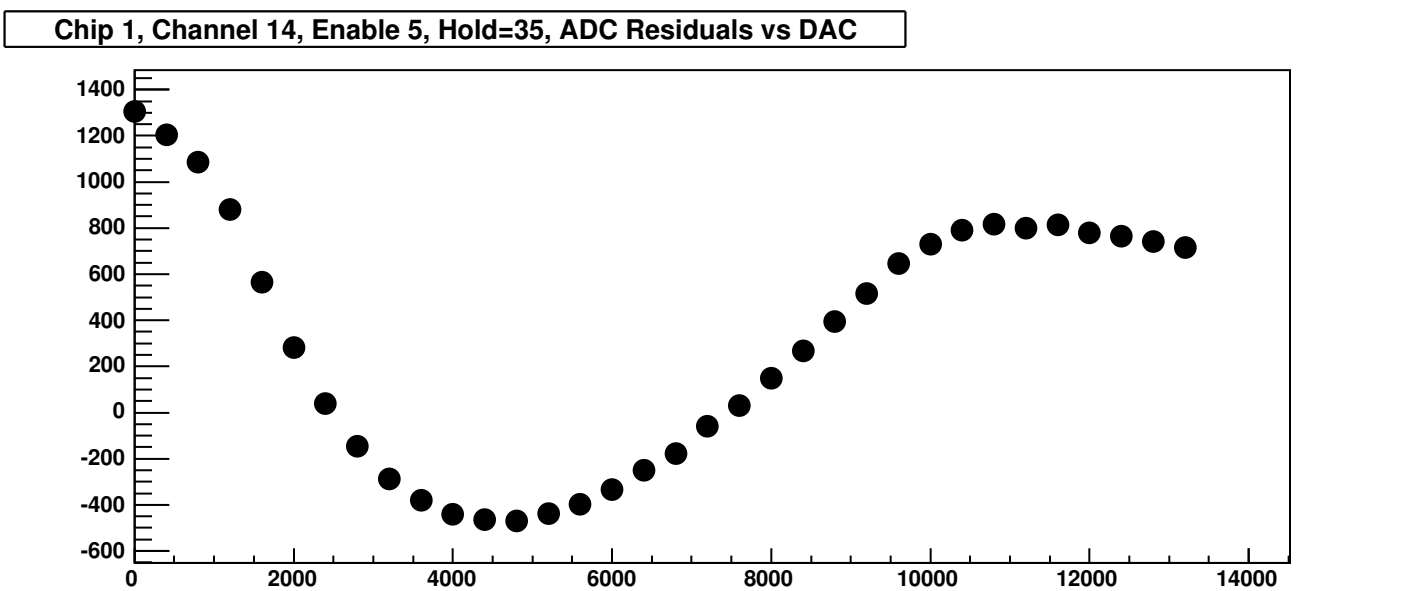

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 525.2 / 23
p0 -519.9 ± 2.247
p1 2.269 ± 0.001052
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

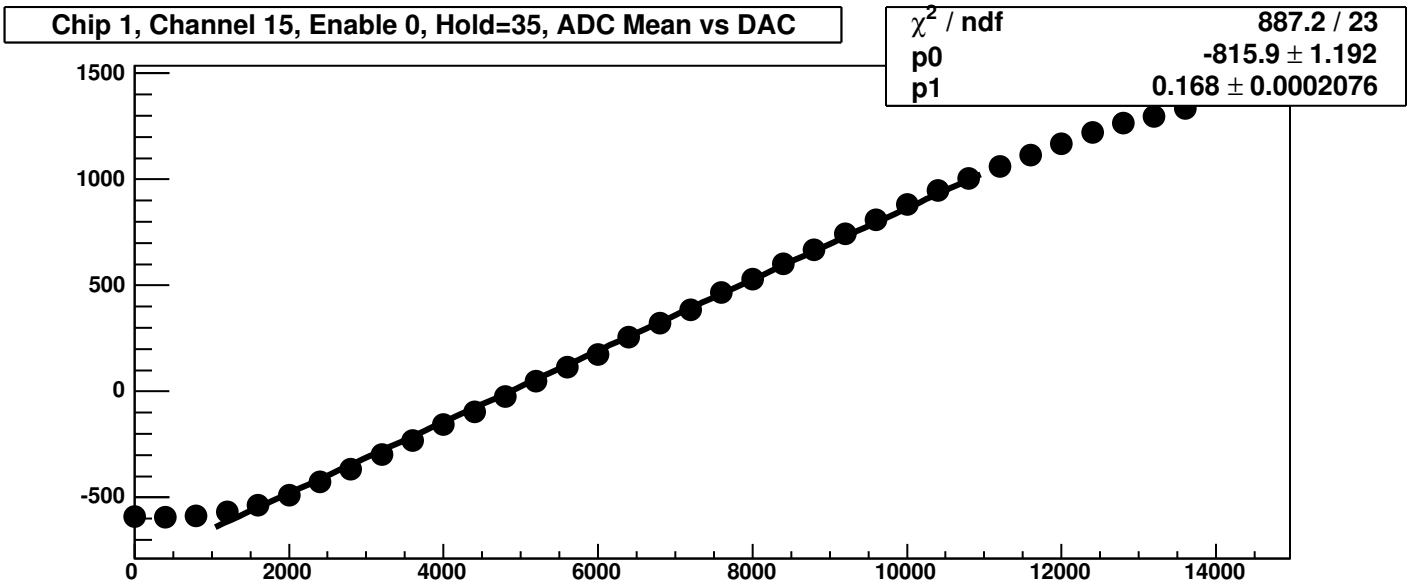

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

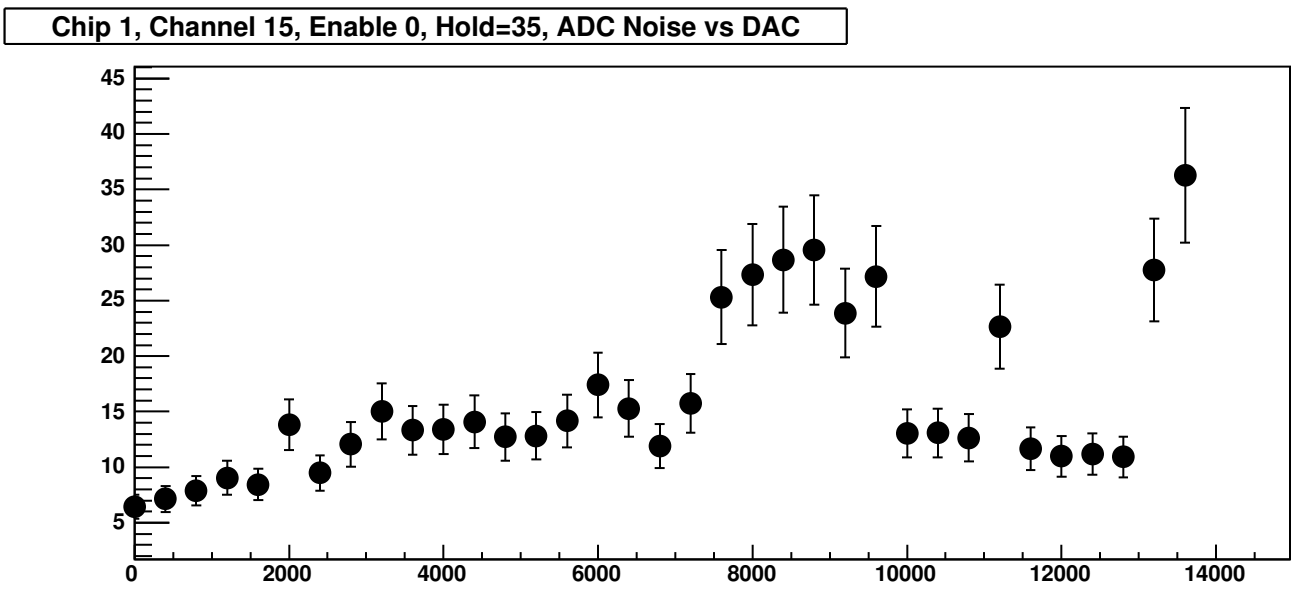

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

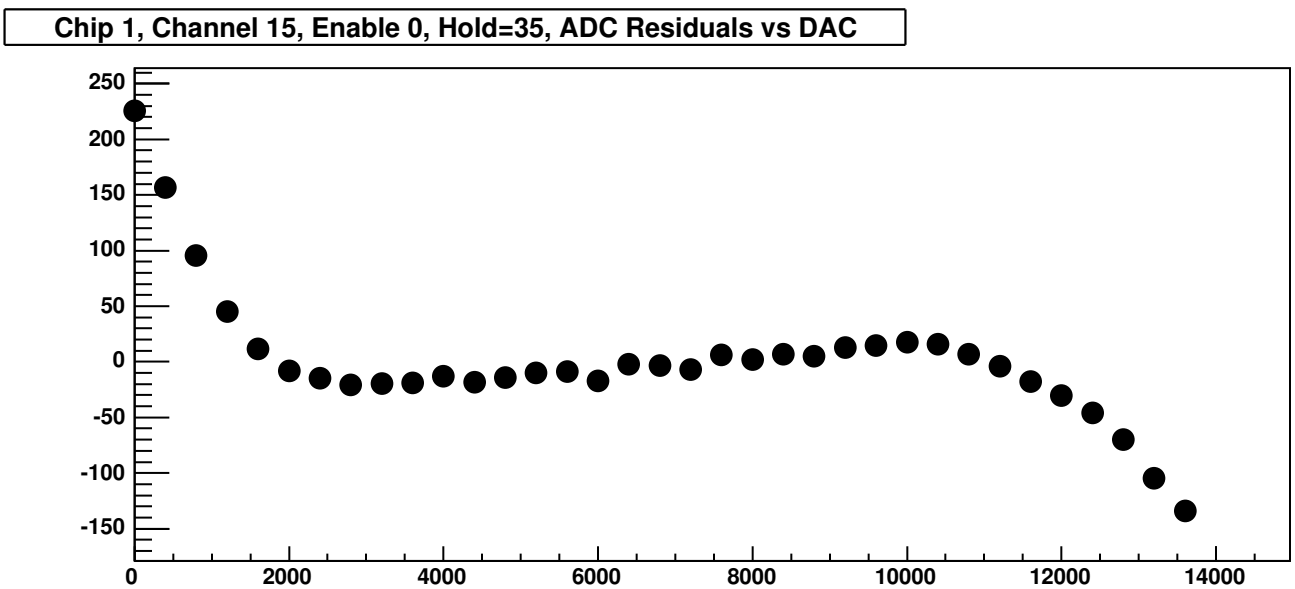

Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.641e+05 / 23
p0 -1954 ± 1.185
p1 0.2463 ± 0.0002525
2000
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

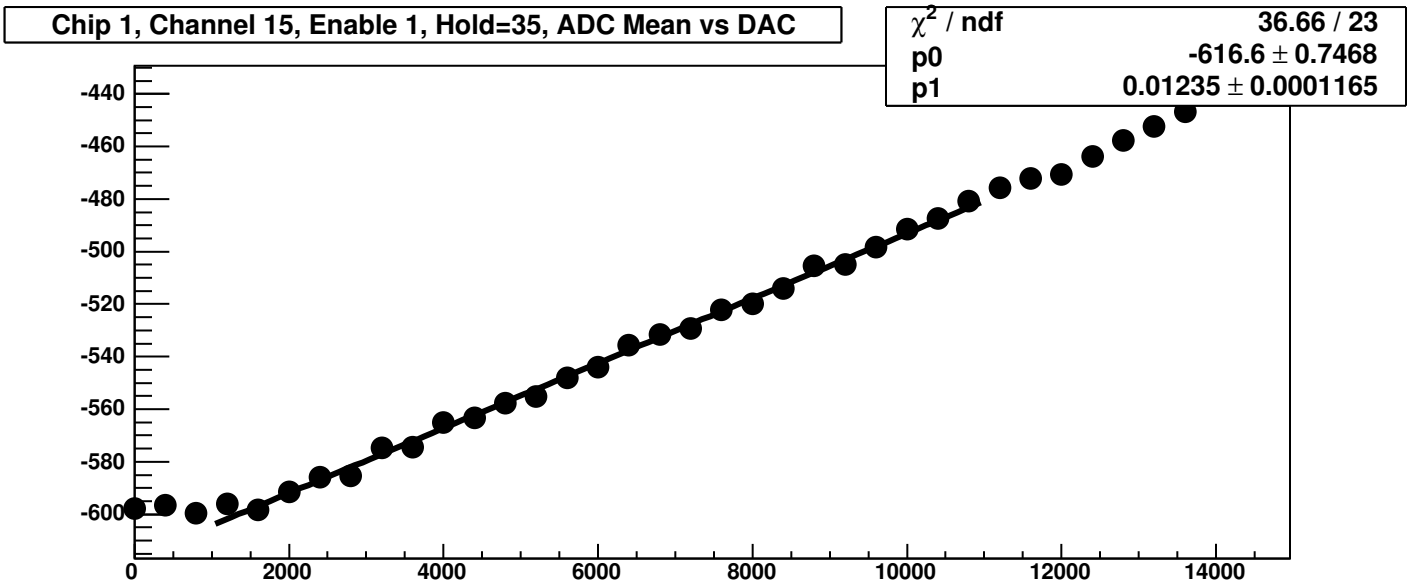

Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

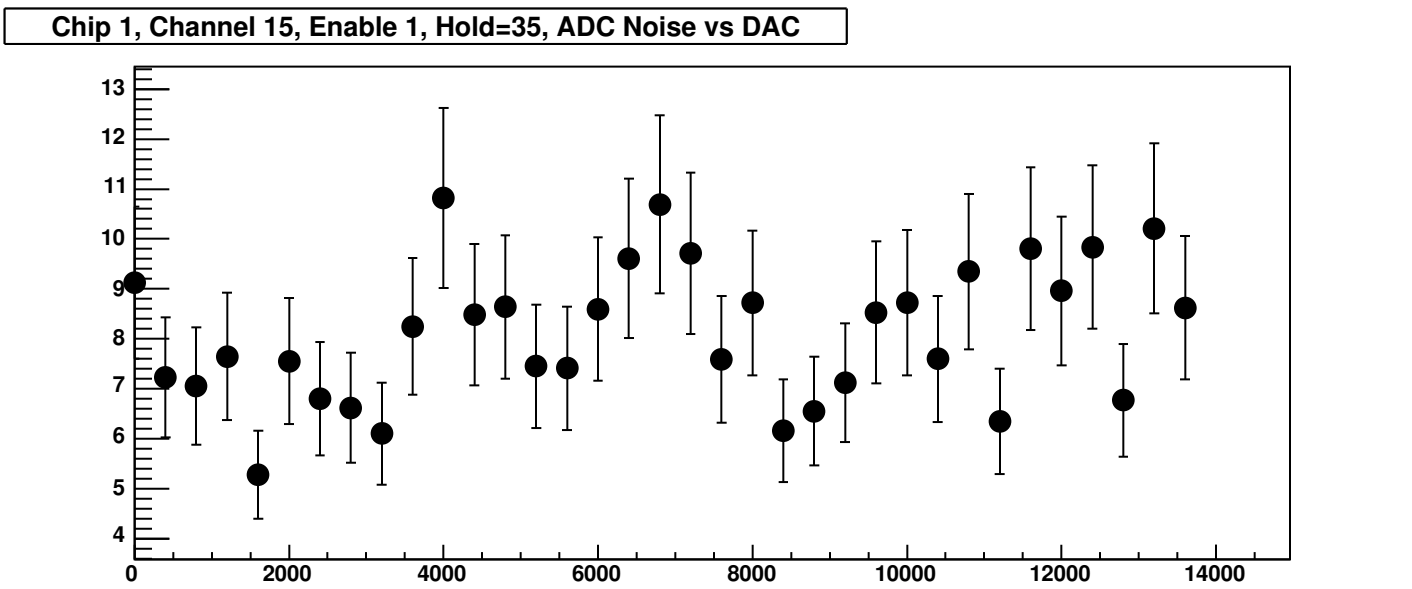

Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

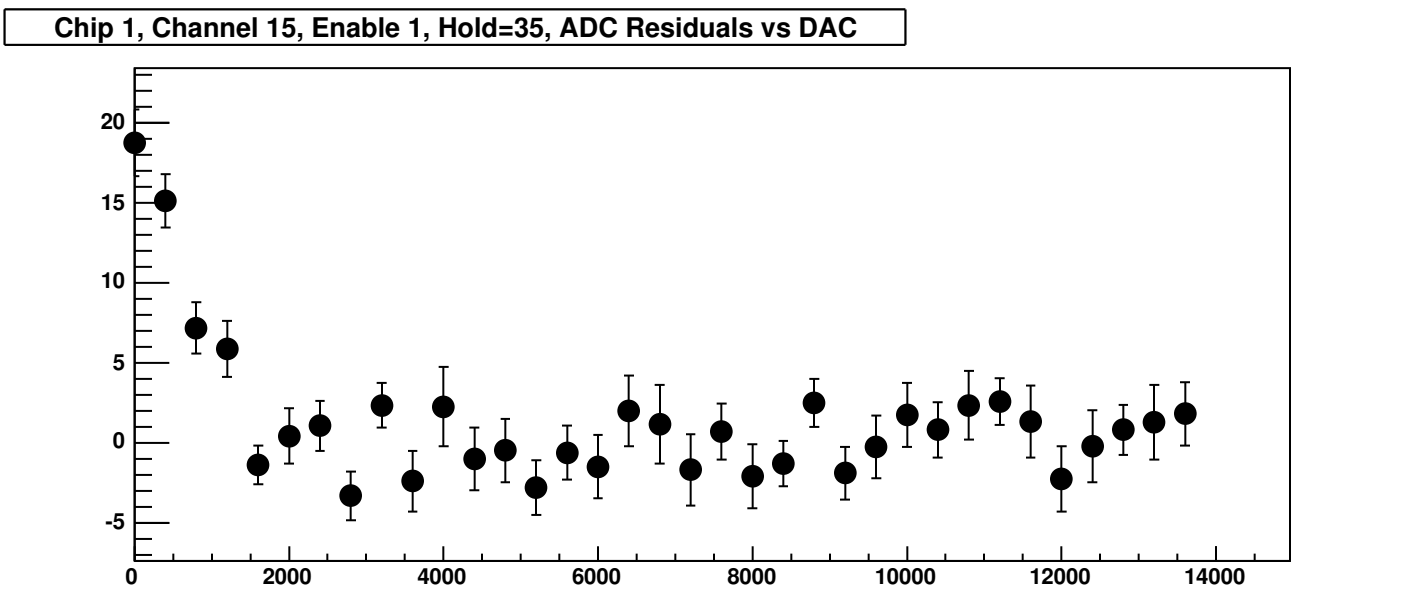

Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 887.2 / 23
p0 -815.9 ± 1.192
p1 0.168 ± 0.0002076
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

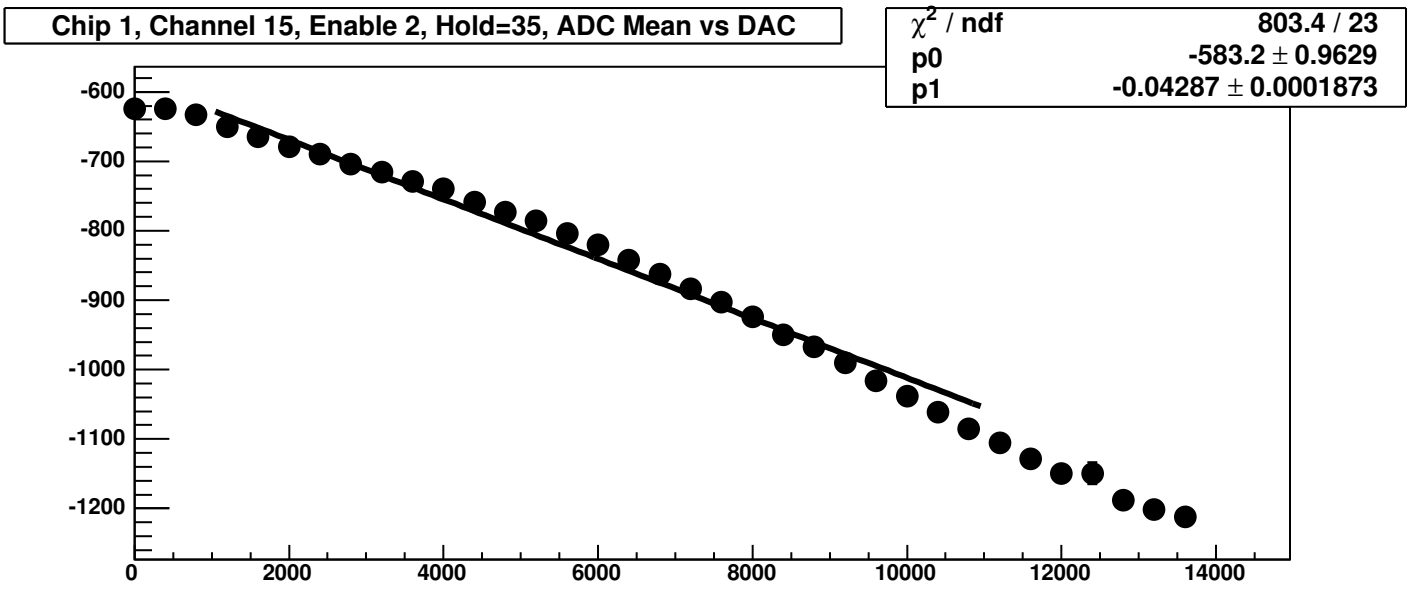

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

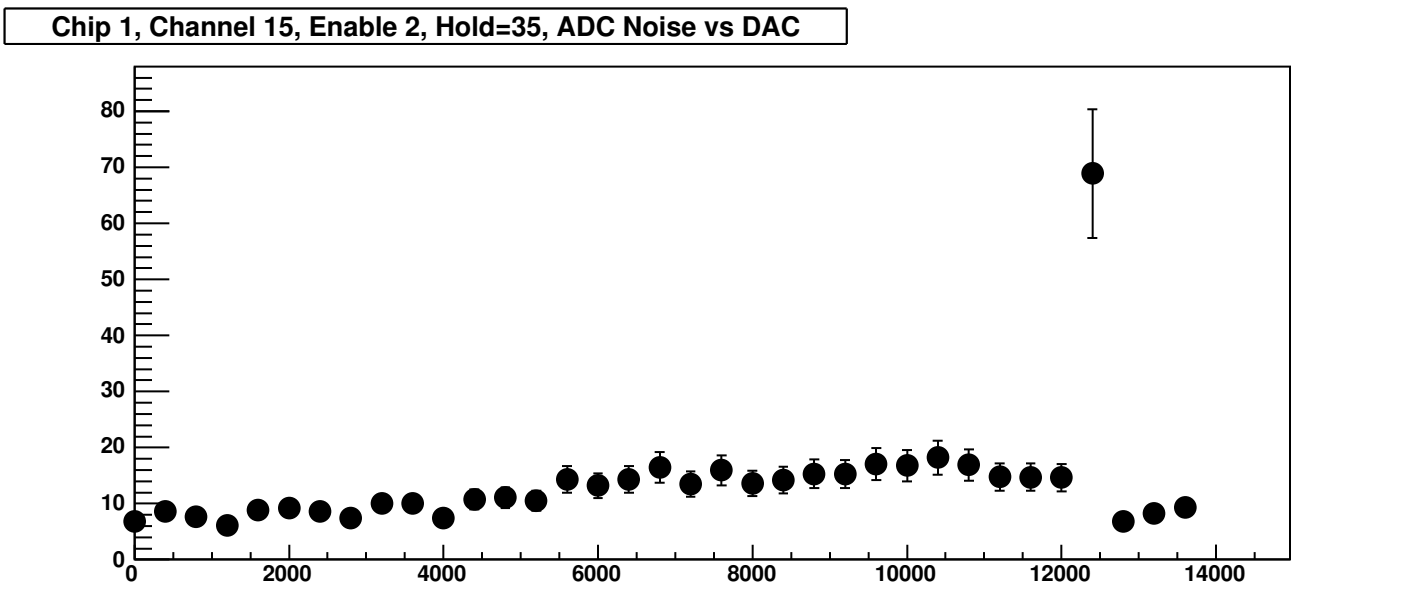

Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

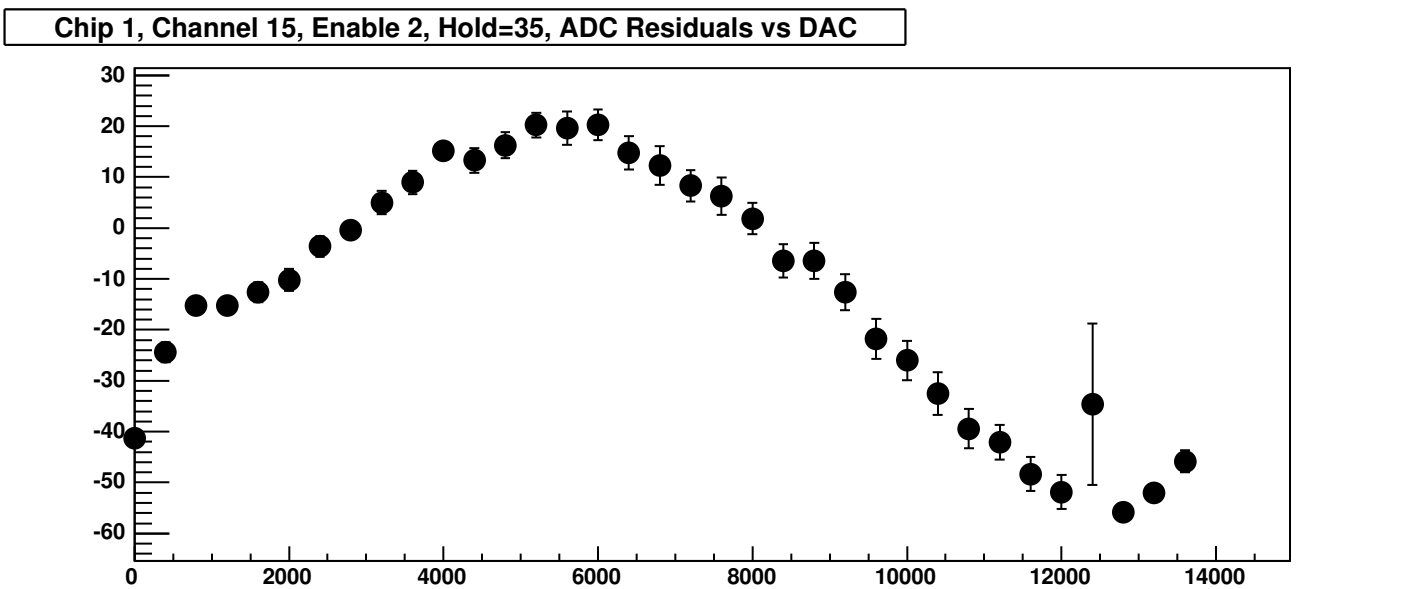

Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.66 / 23
p0 -616.6 ± 0.7468
p1 0.01235 ± 0.0001165
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

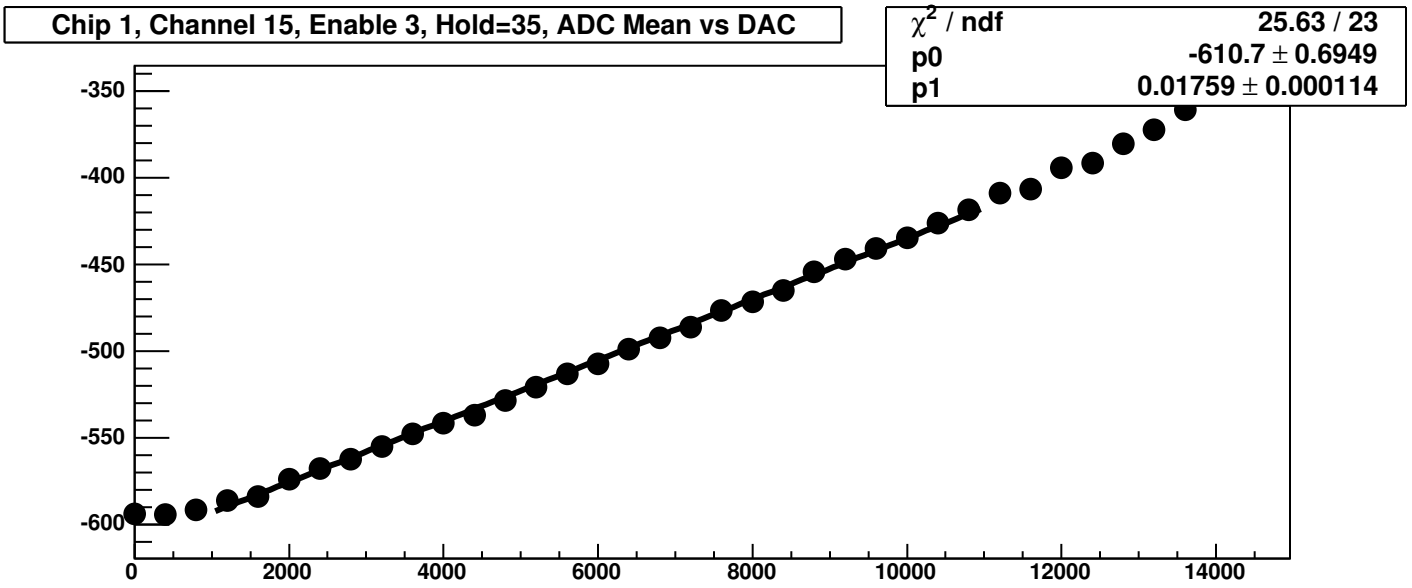

Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

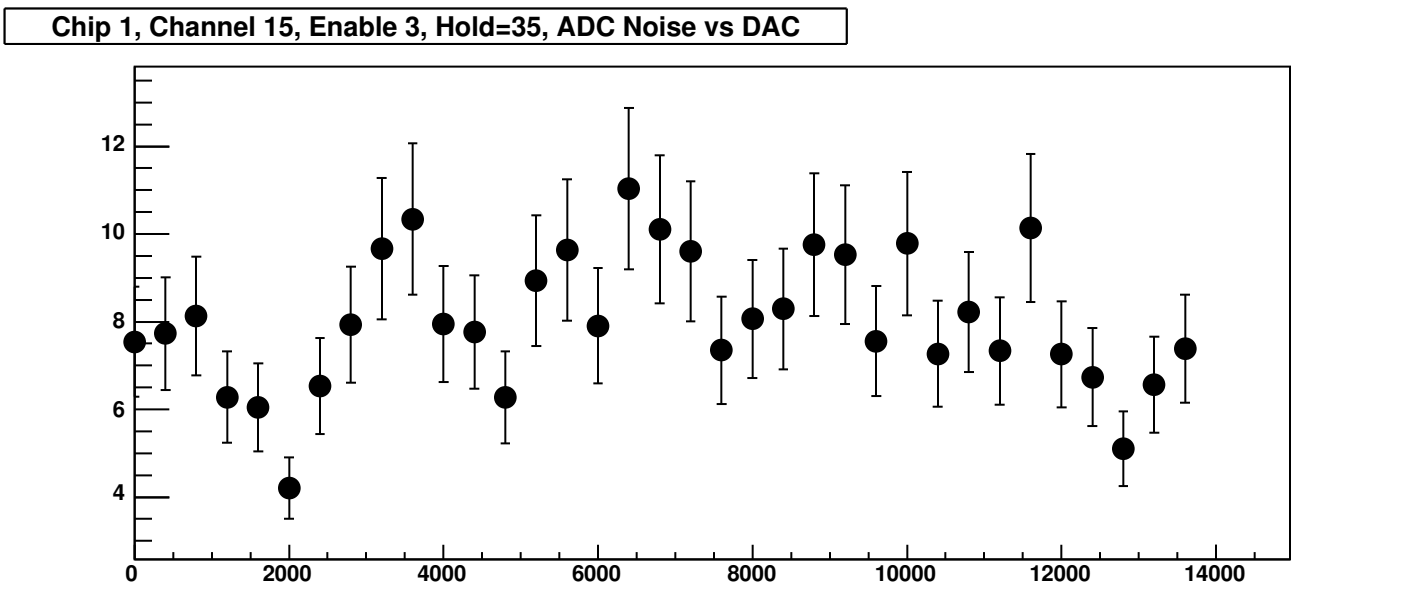

Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

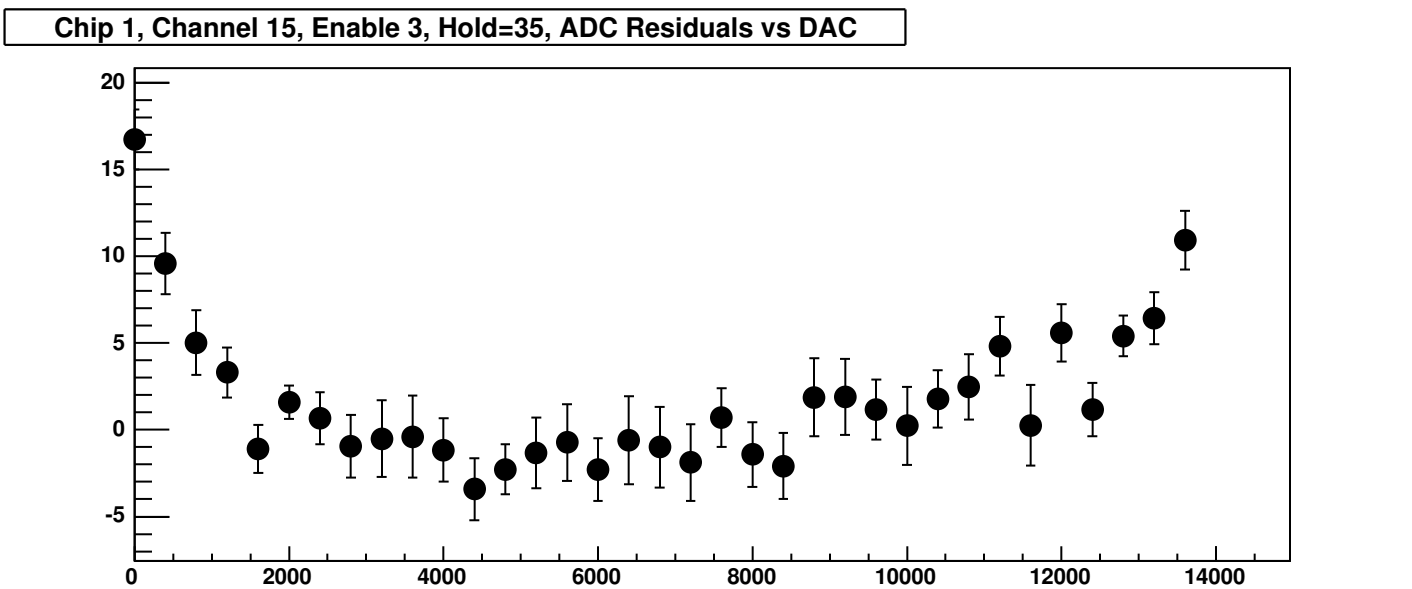

Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 803.4 / 23
p0 -583.2 ± 0.9629
p1 -0.04287 ± 0.0001873
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

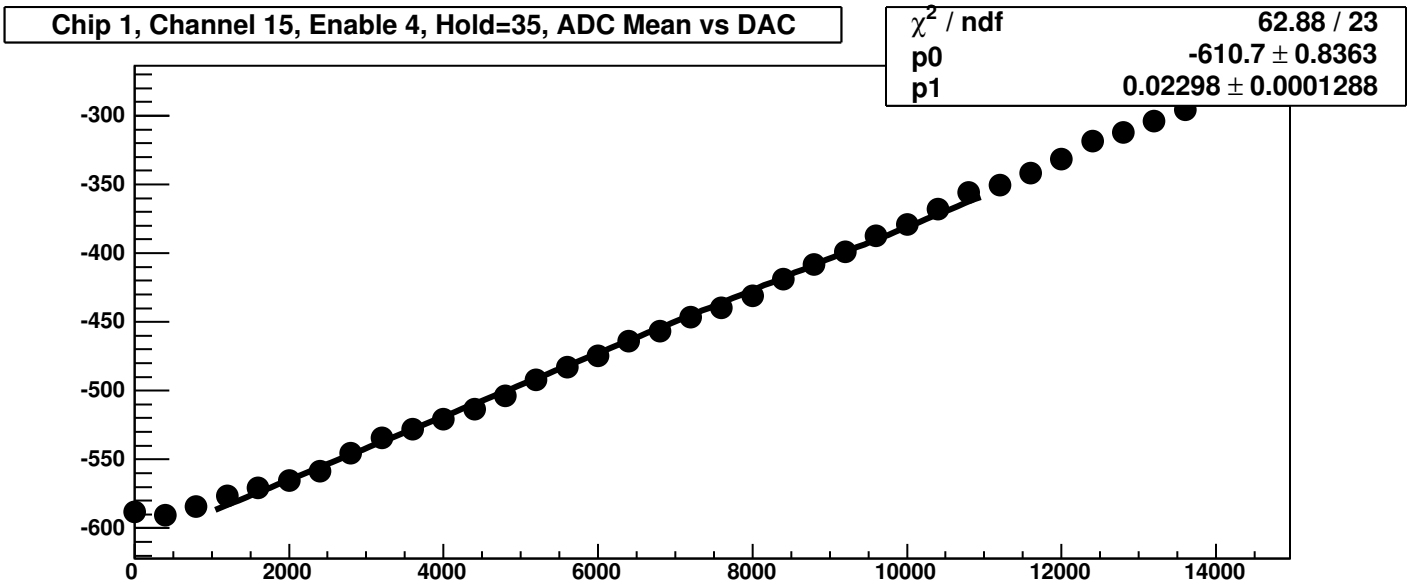

Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

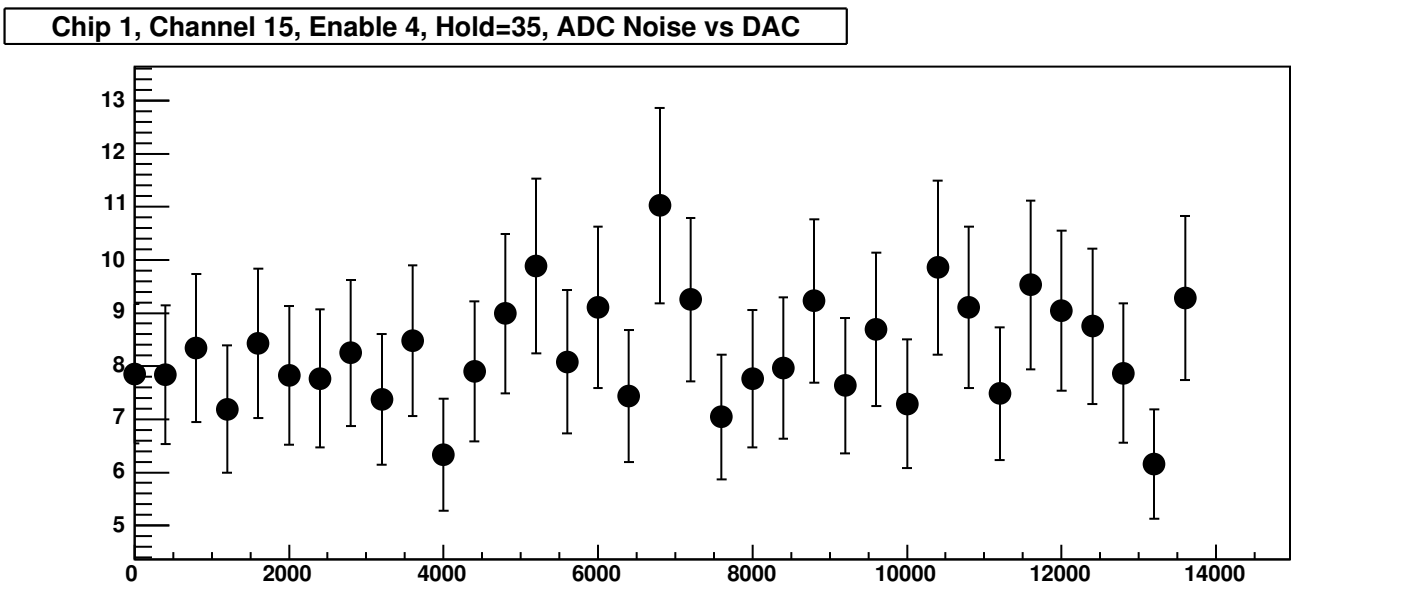

Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000

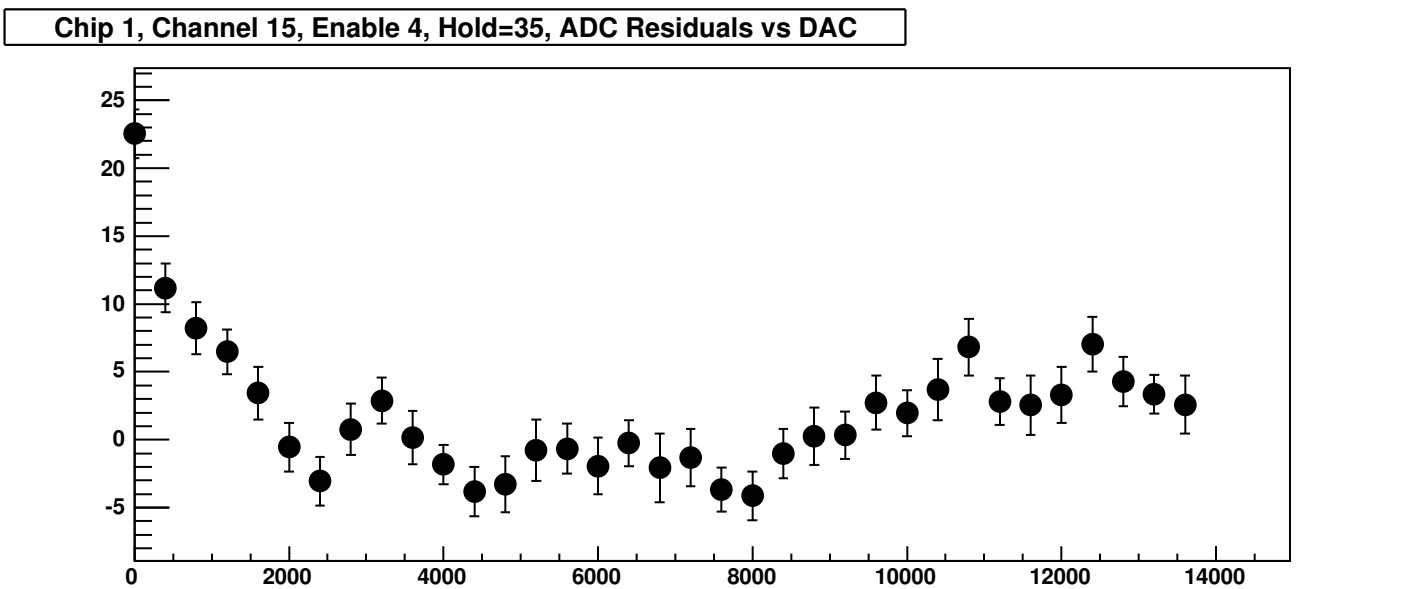

Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.63 / 23
p0 -610.7 ± 0.6949
p1 0.01759 ± 0.000114
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

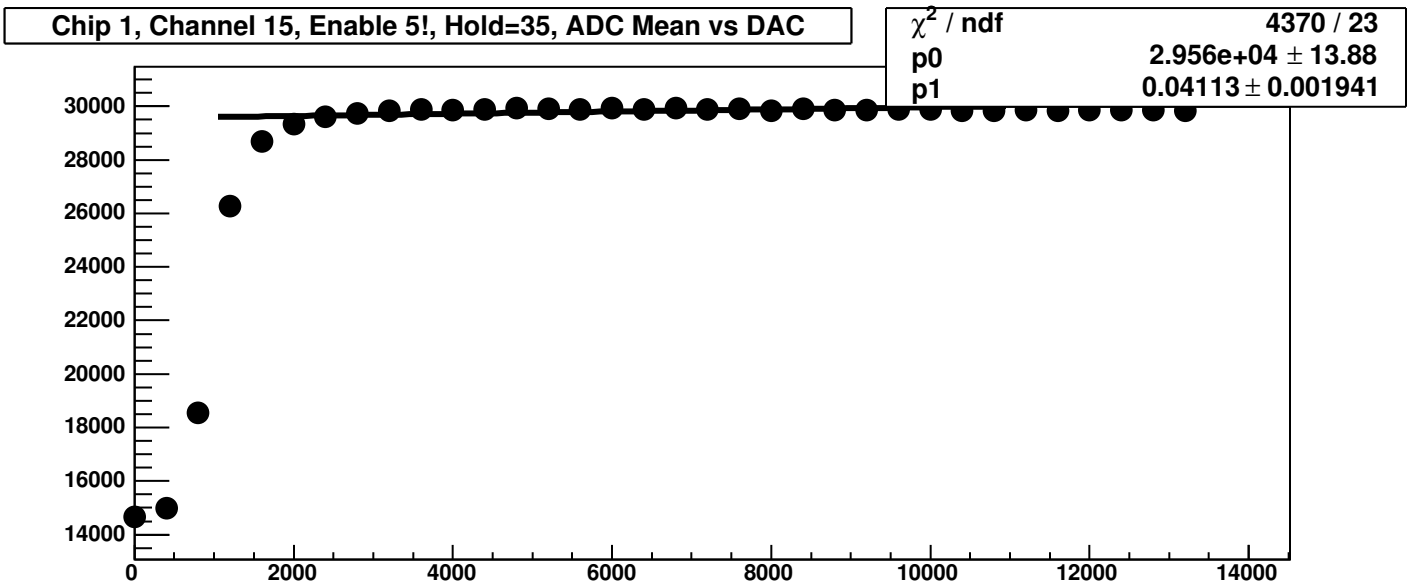

Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

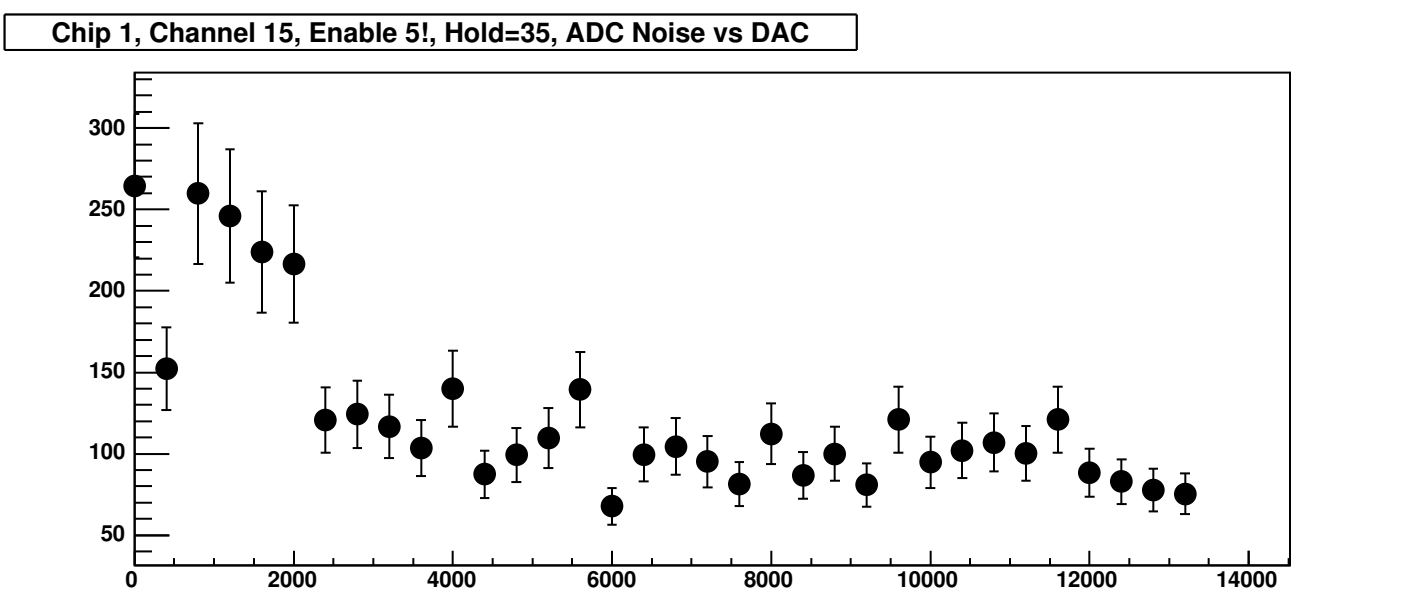

Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

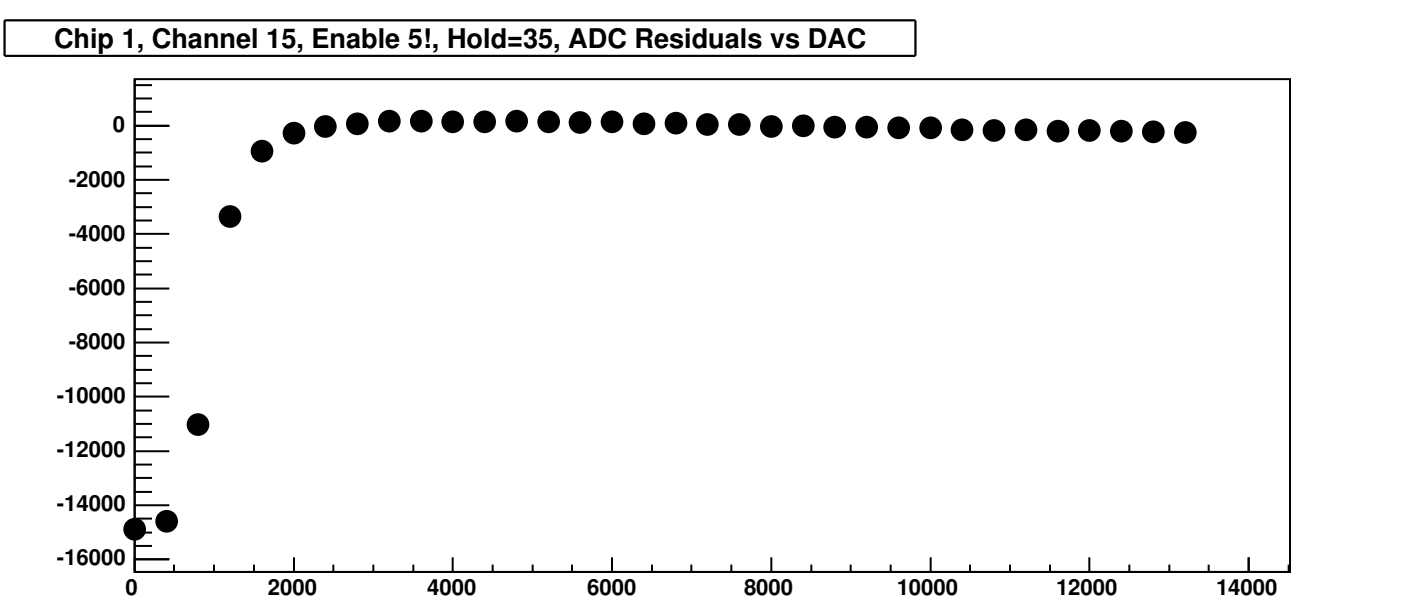

Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 62.88 / 23
p0 -610.7 ± 0.8363
p1 0.02298 ± 0.0001288
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

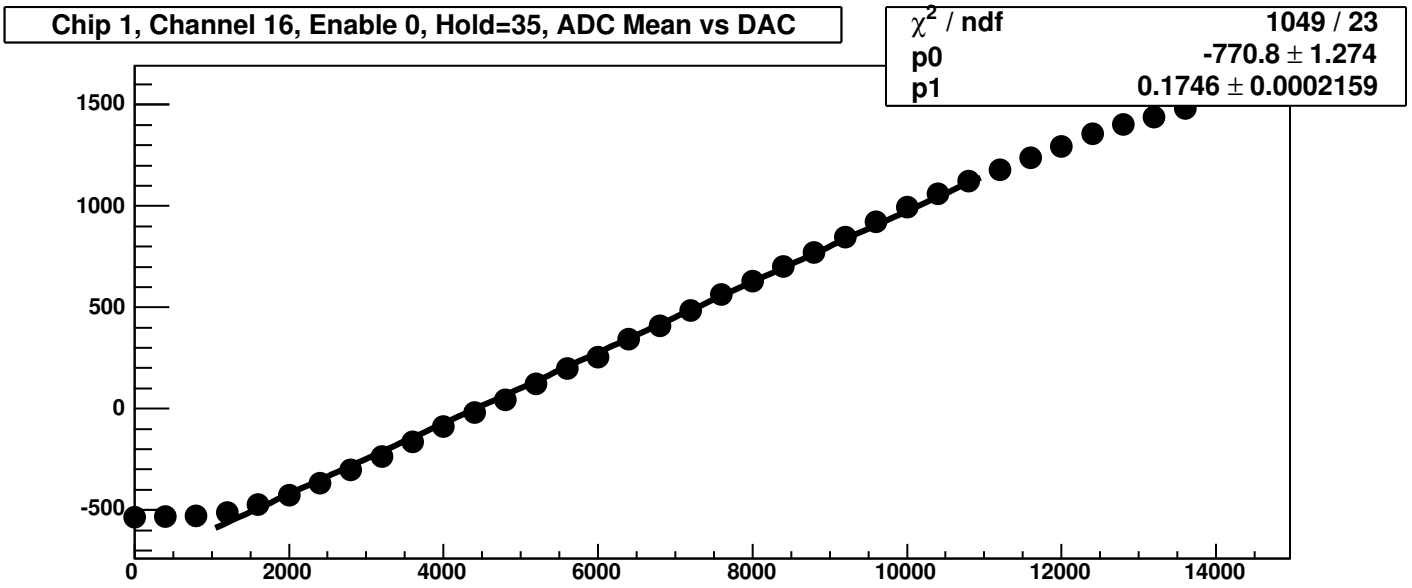

Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

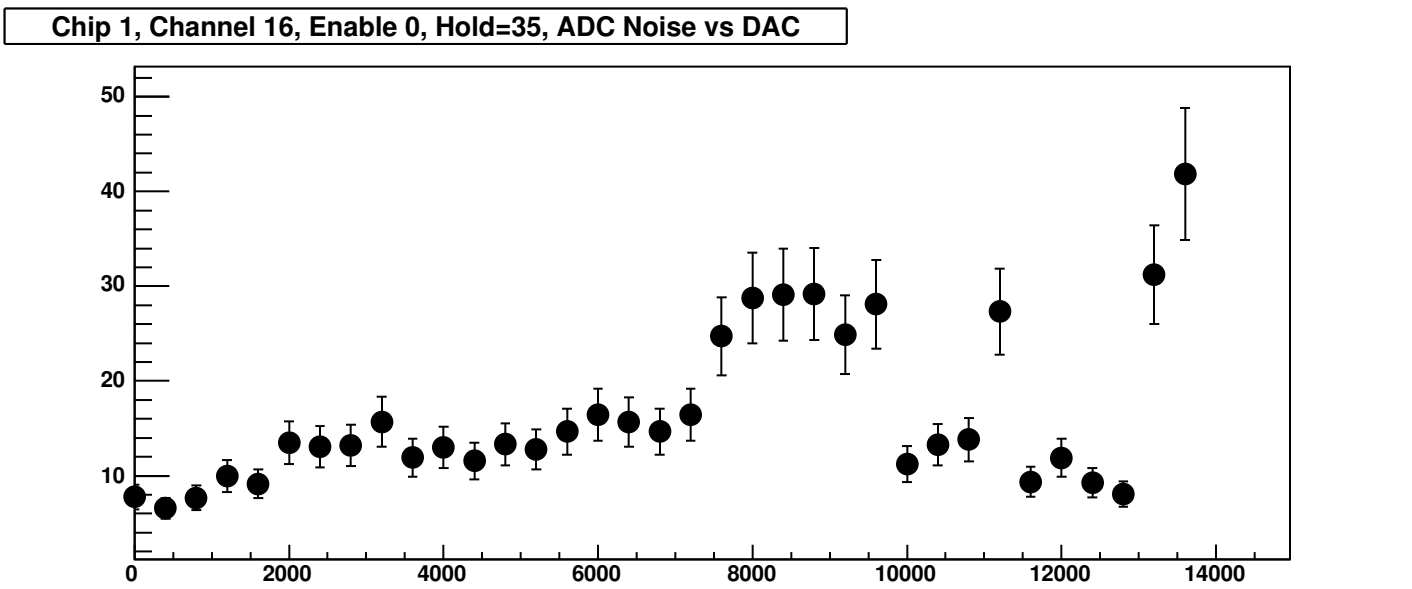

Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

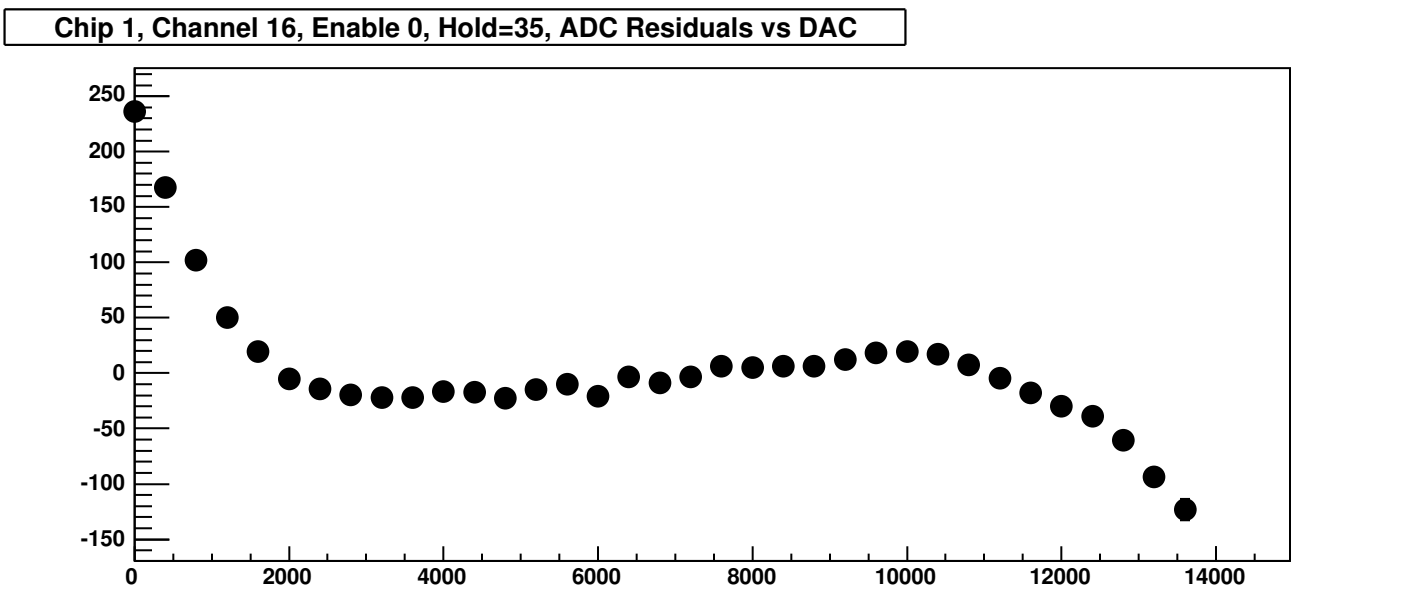

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4370 / 23
p0 2.956e+04 ± 13.88
p1 0.04113 ± 0.001941
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

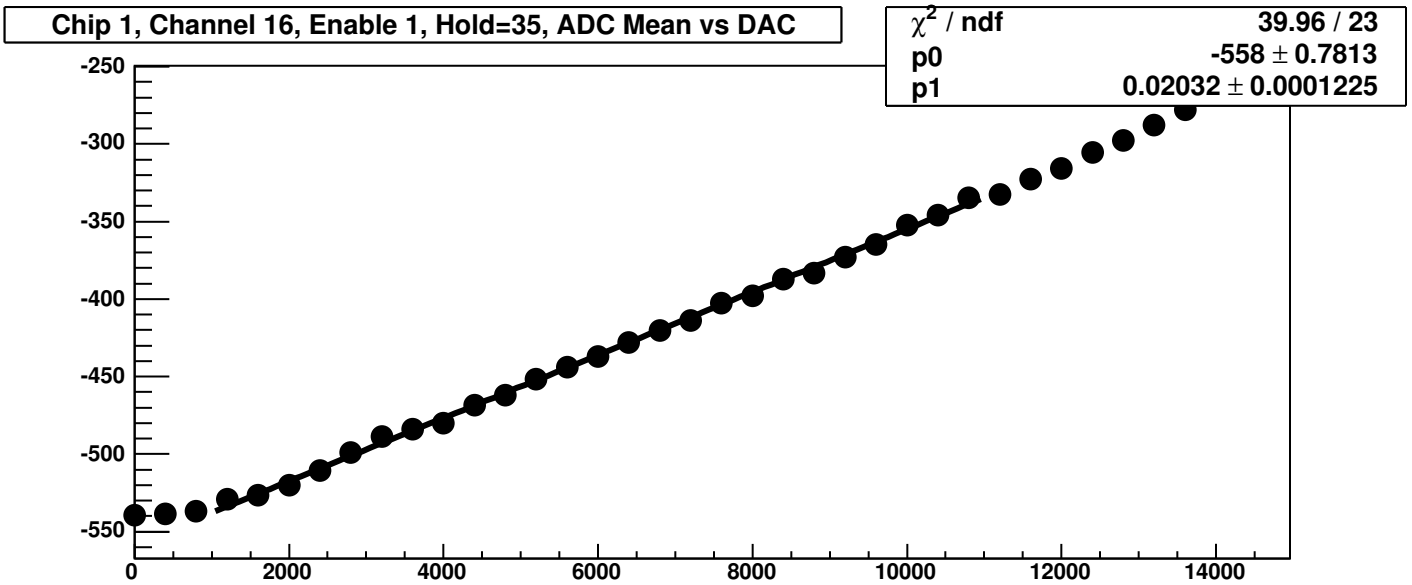

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

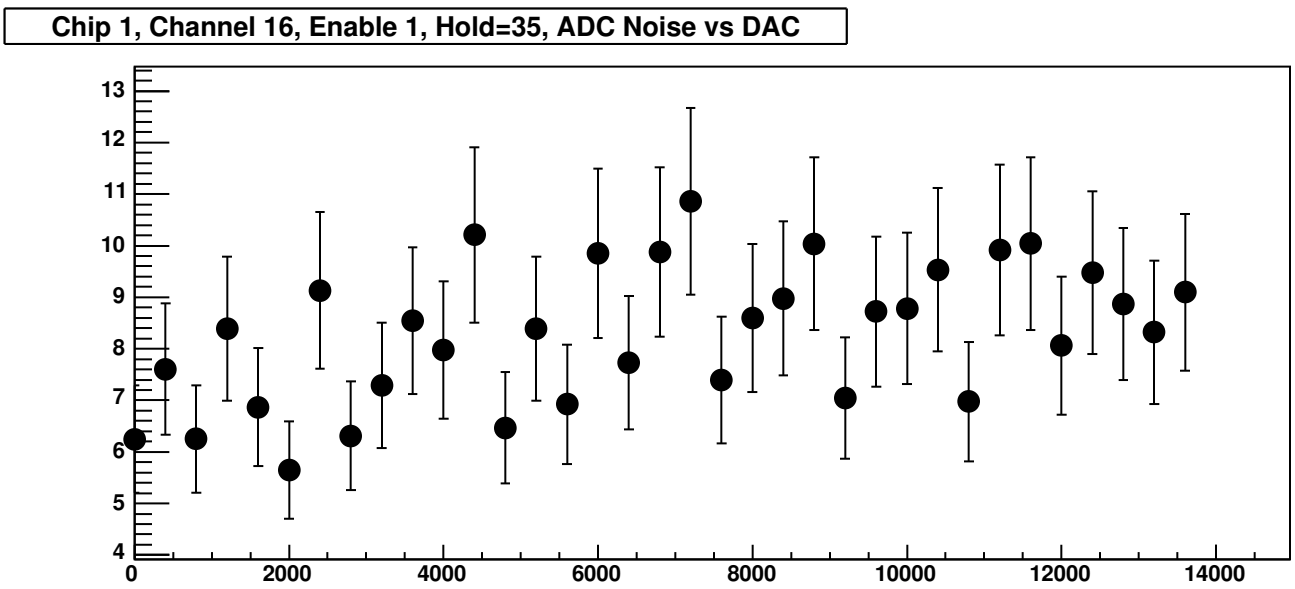

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

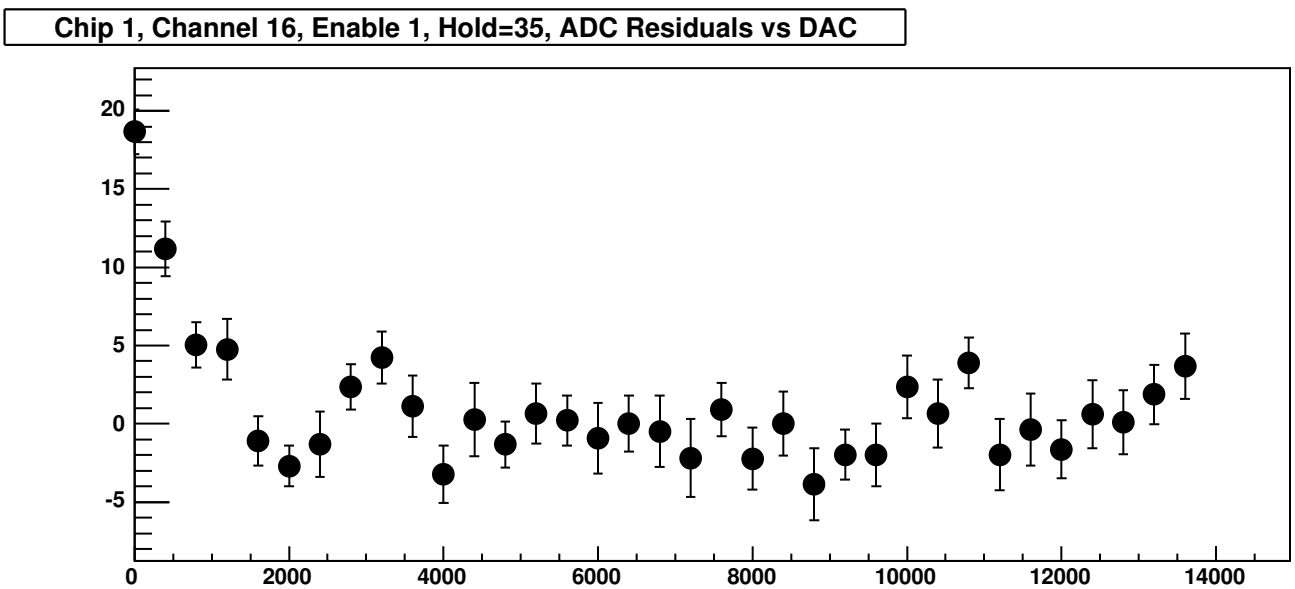

Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1049 / 23
p0 -770.8 ± 1.274
p1 0.1746 ± 0.0002159
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

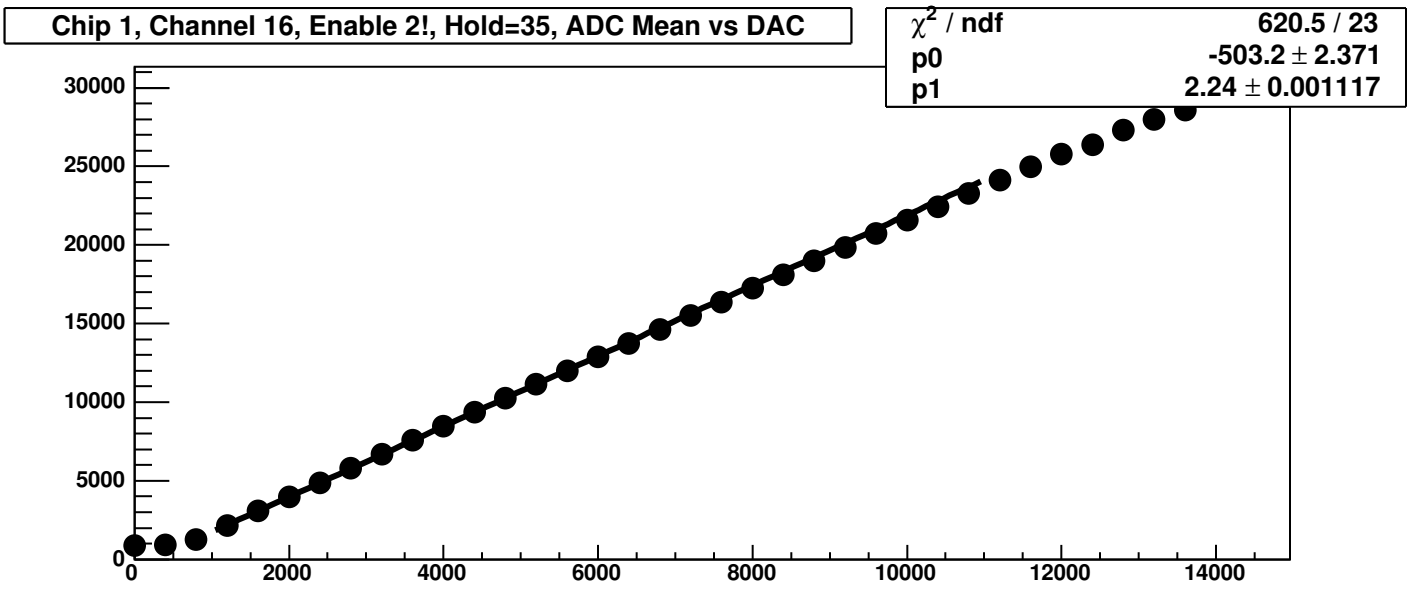

Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

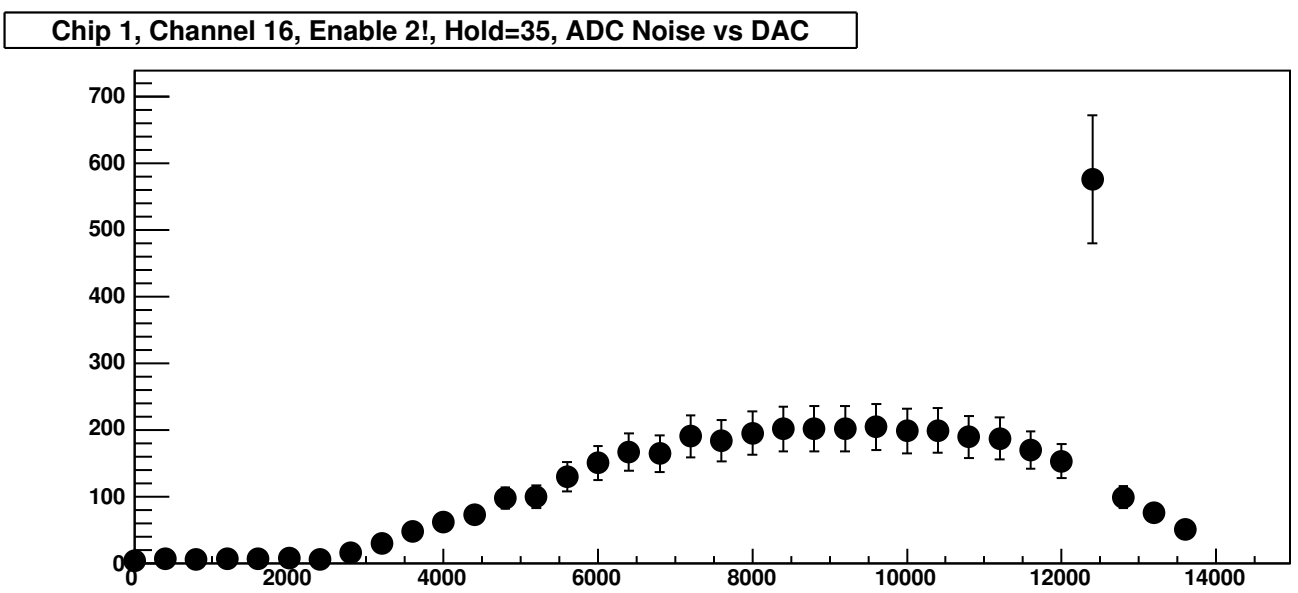

Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

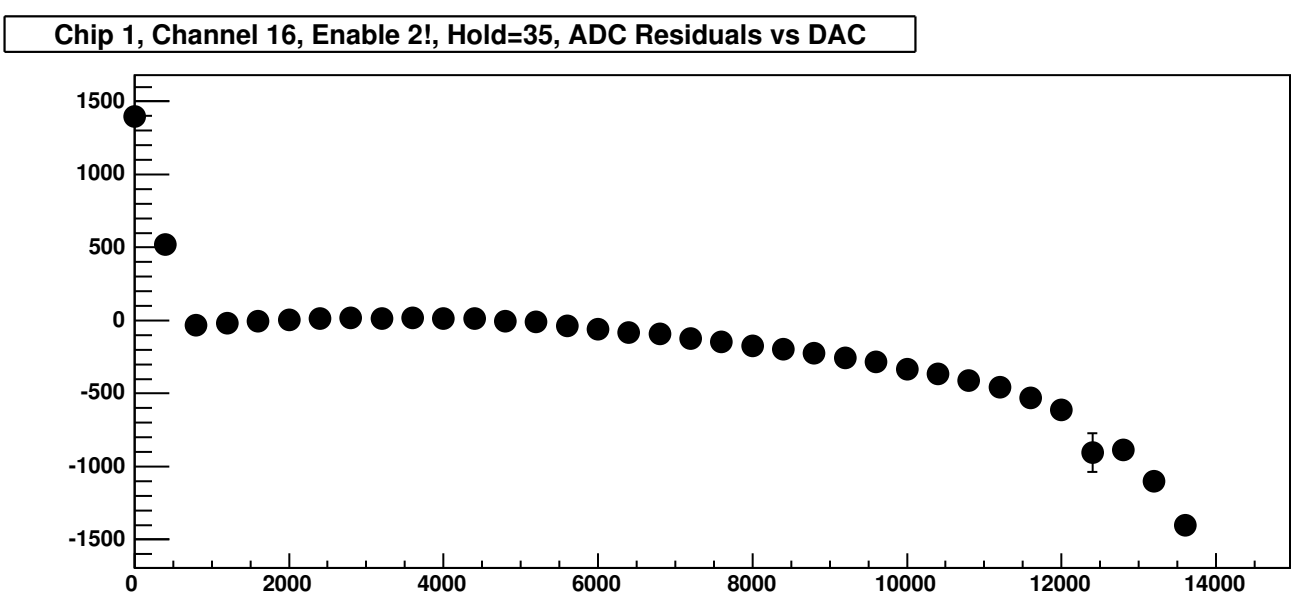

Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.96 / 23
p0 -558 ± 0.7813
p1 0.02032 ± 0.0001225
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

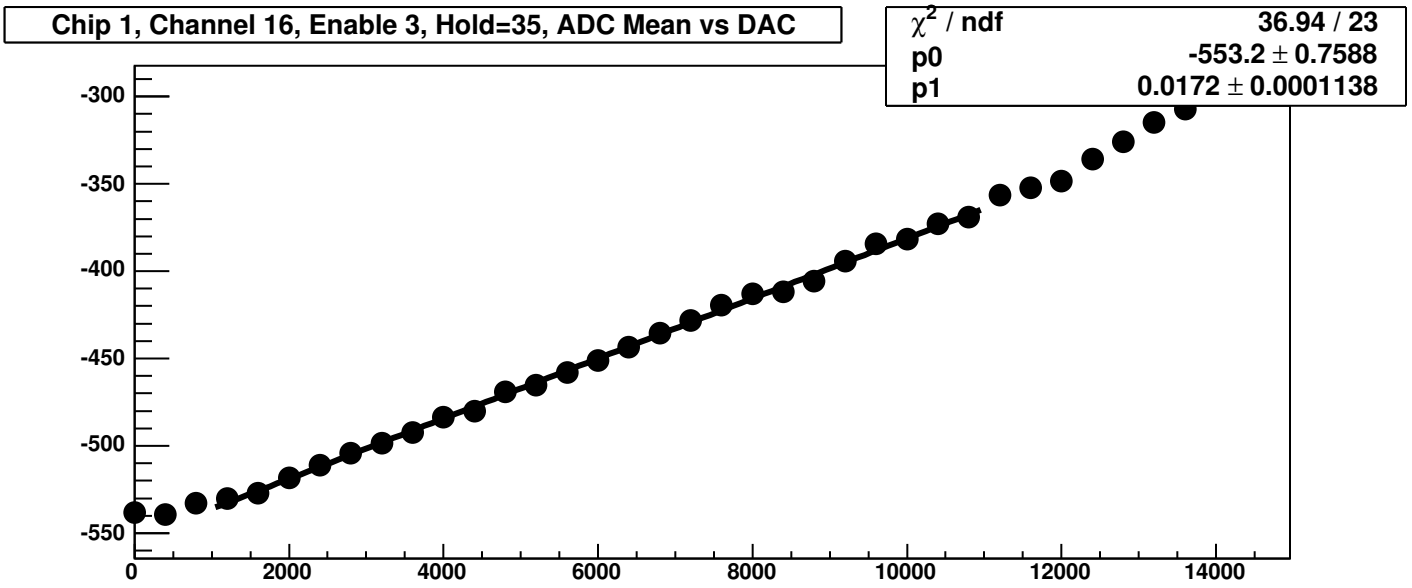

Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

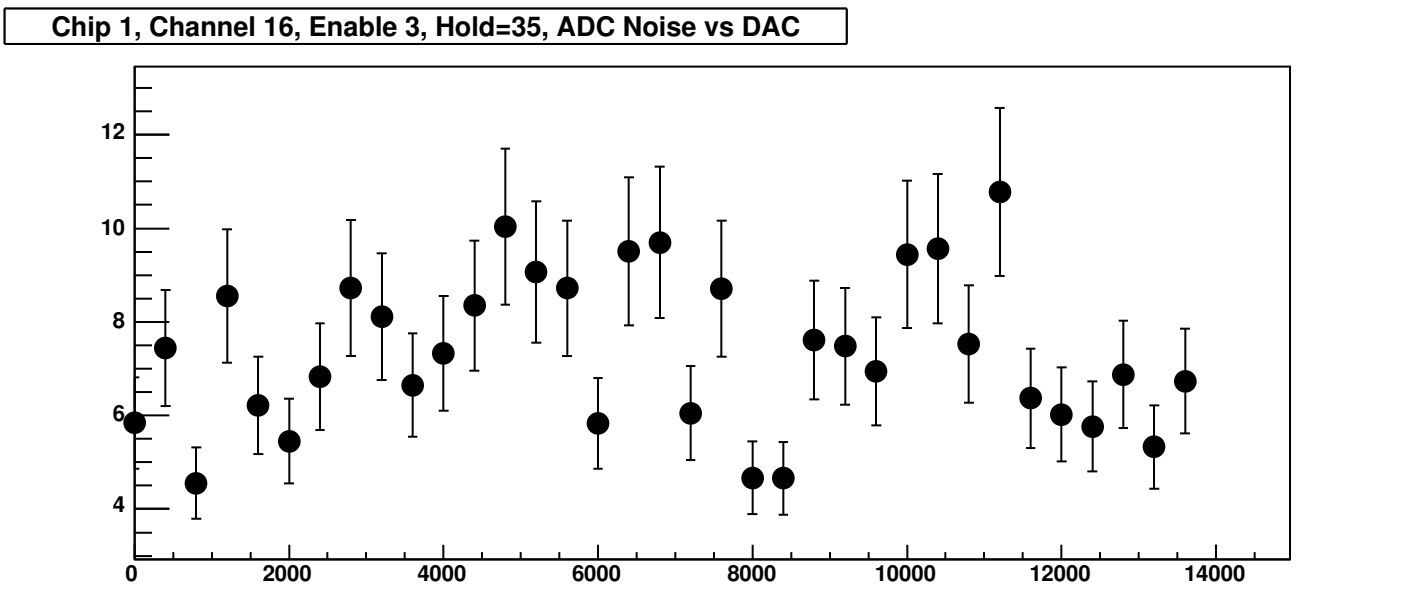

Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

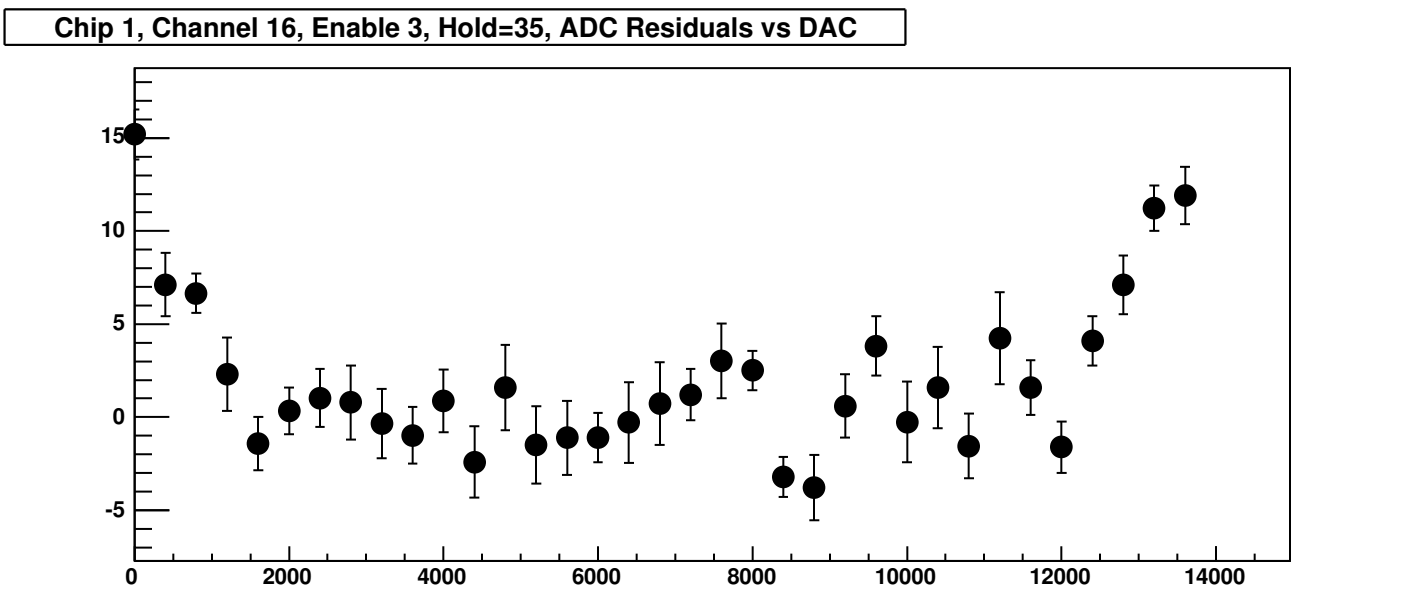

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 620.5 / 23
p0 -503.2 ± 2.371
p1 2.24 ± 0.001117
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

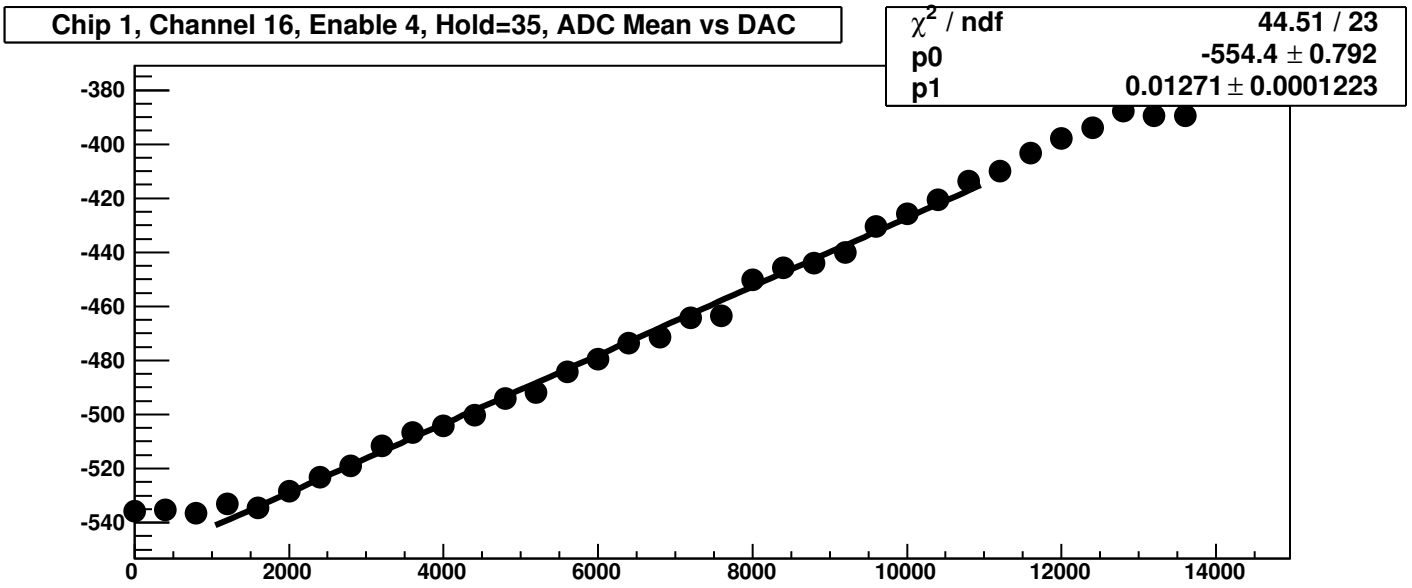

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

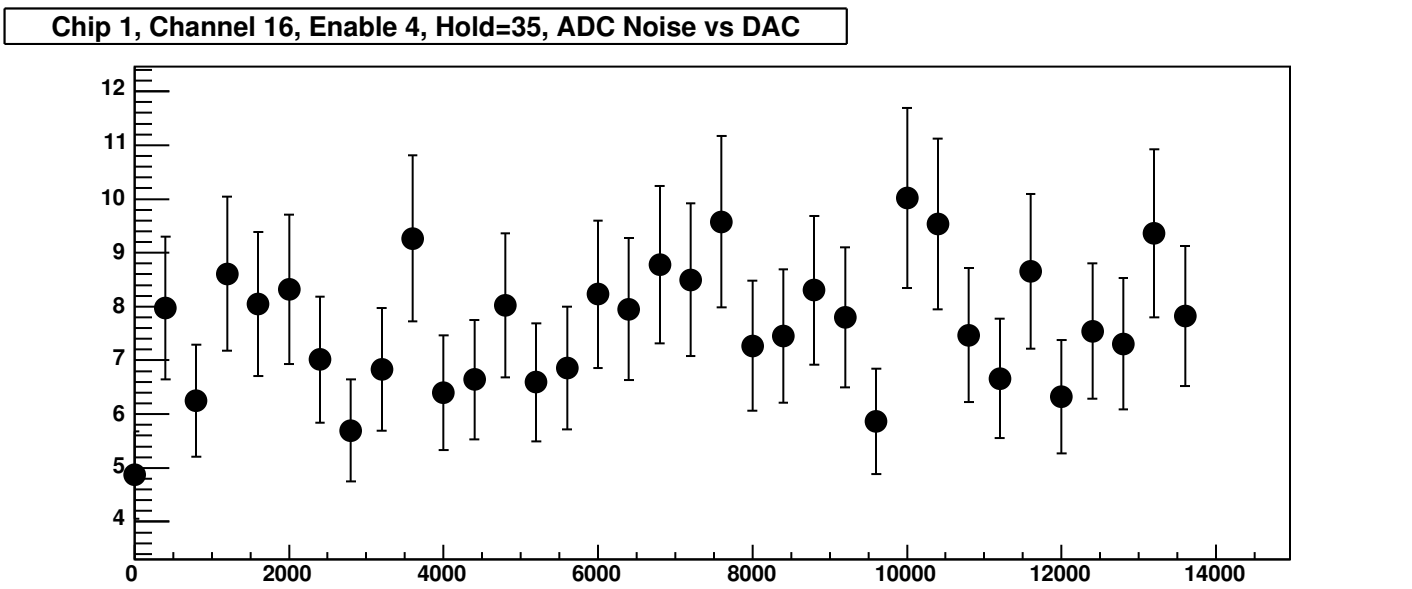

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

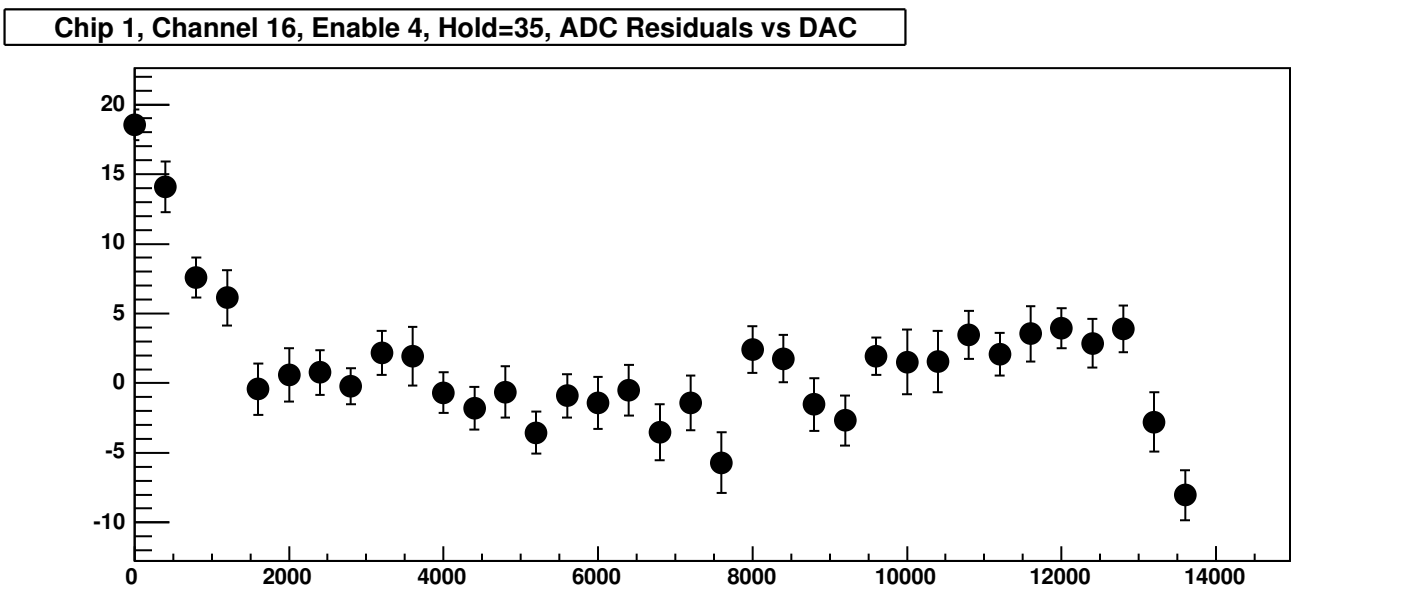

Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.94 / 23
p0 -553.2 ± 0.7588
p1 0.0172 ± 0.0001138
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

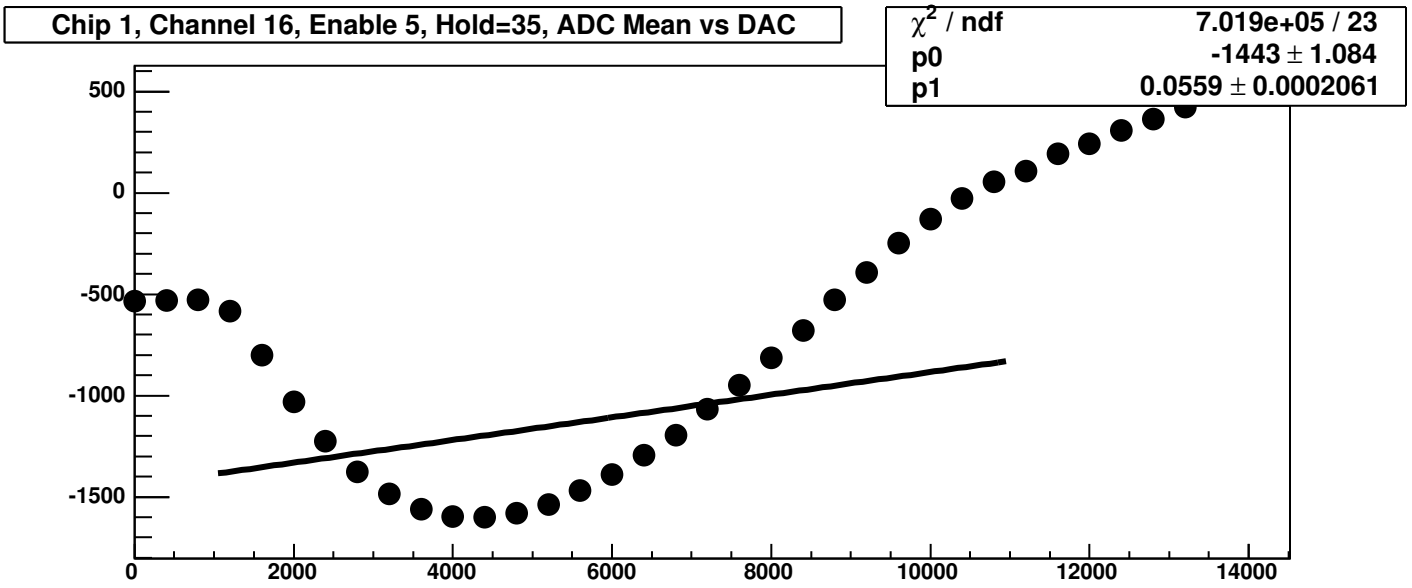

Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

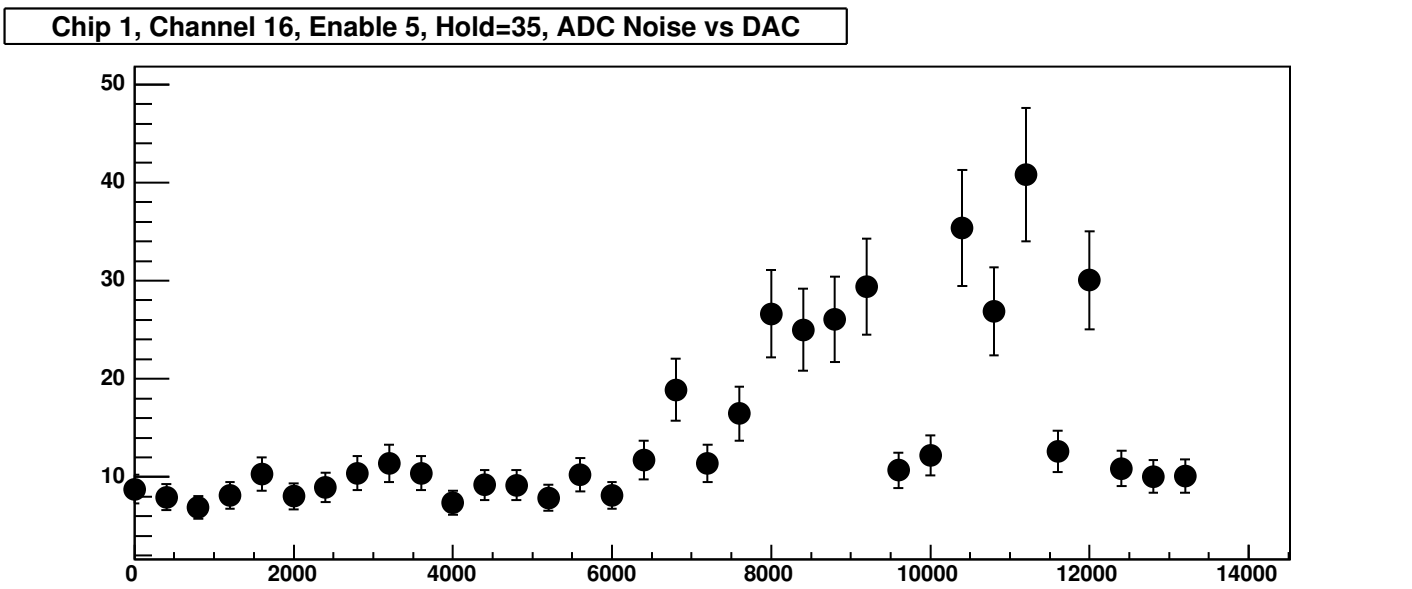

Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

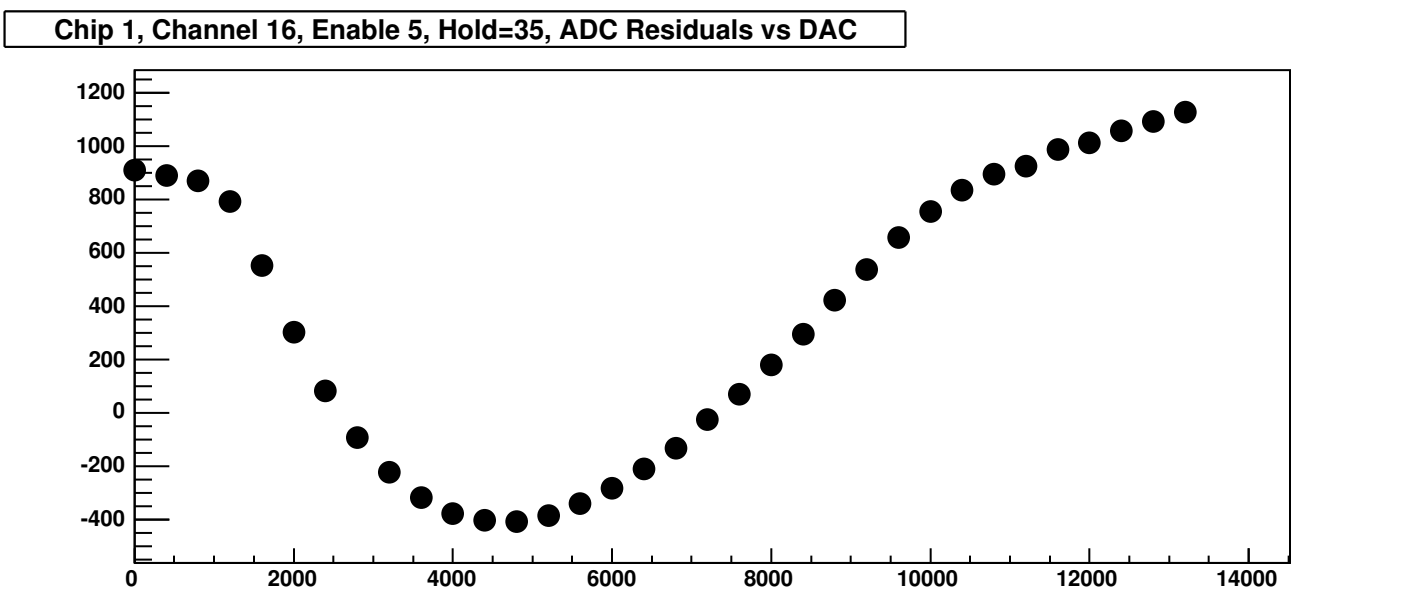

Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 44.51 / 23
p0 -554.4 ± 0.792
p1 0.01271 ± 0.0001223
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

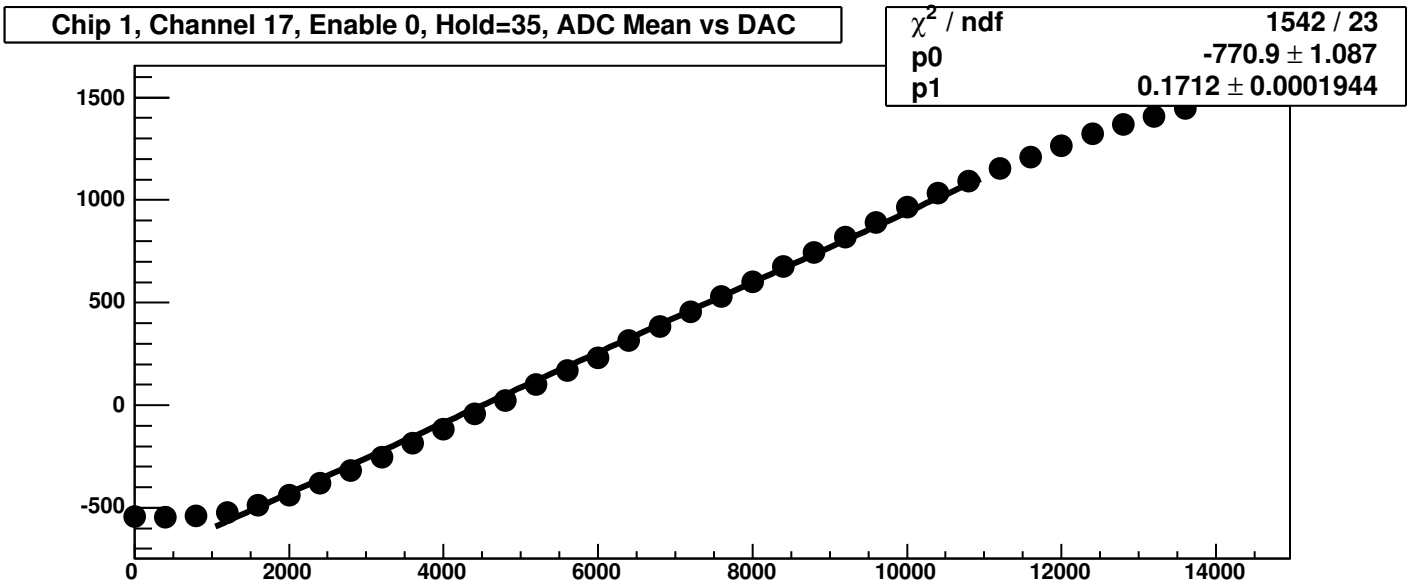

Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

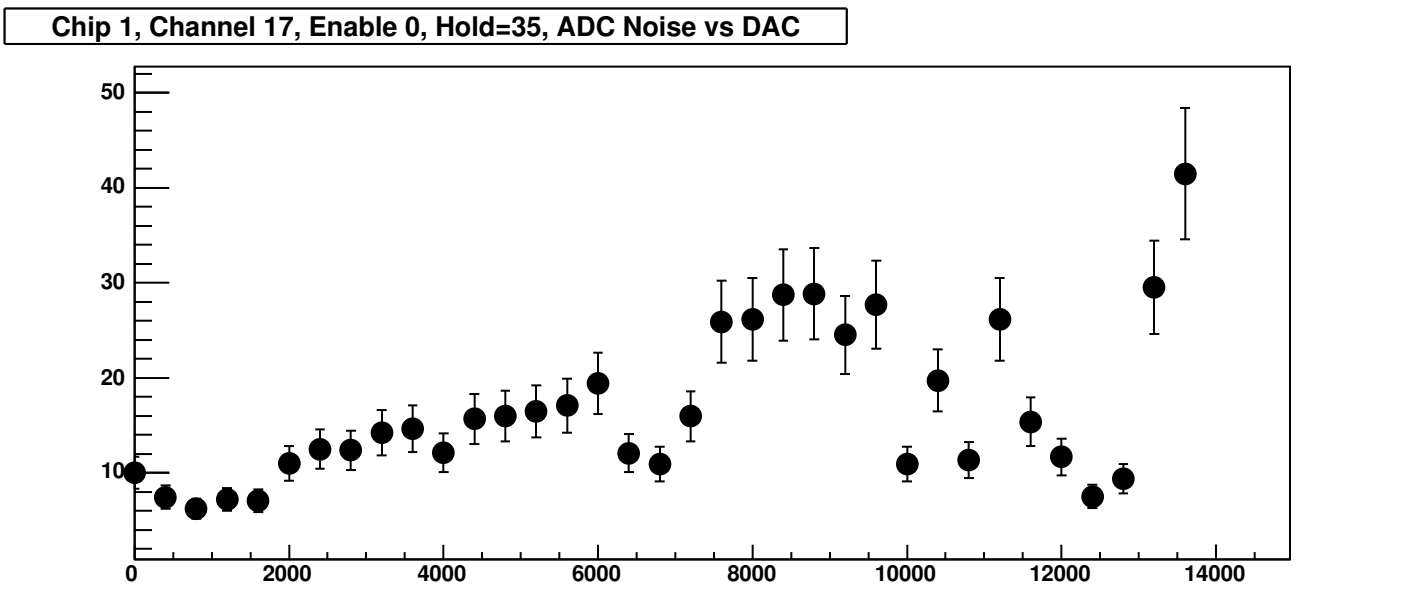

Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

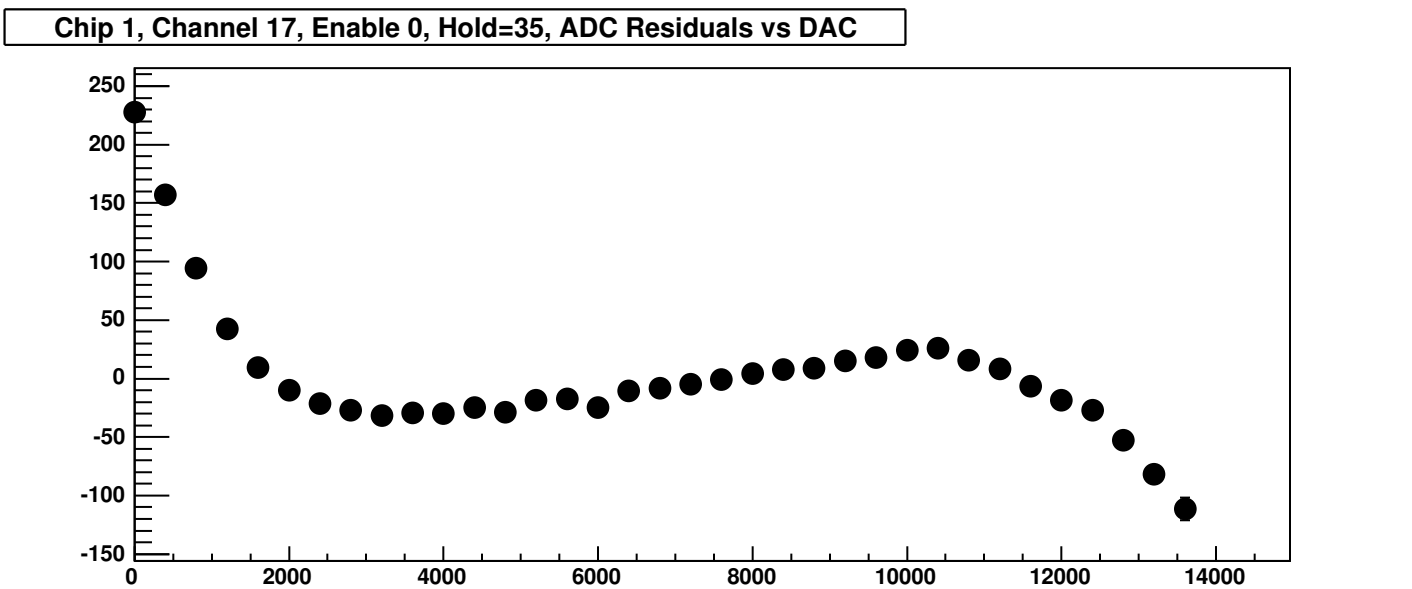

Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.019e+05 / 23
p0 -1443 ± 1.084
p1 0.0559 ± 0.0002061
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

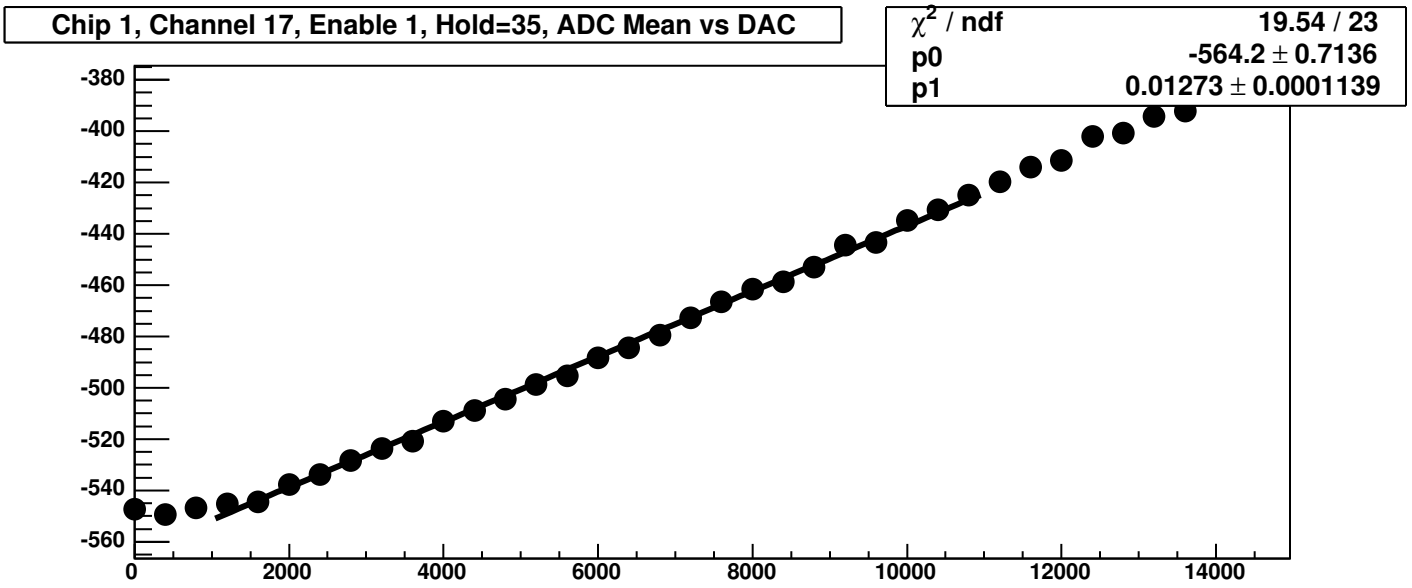

Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

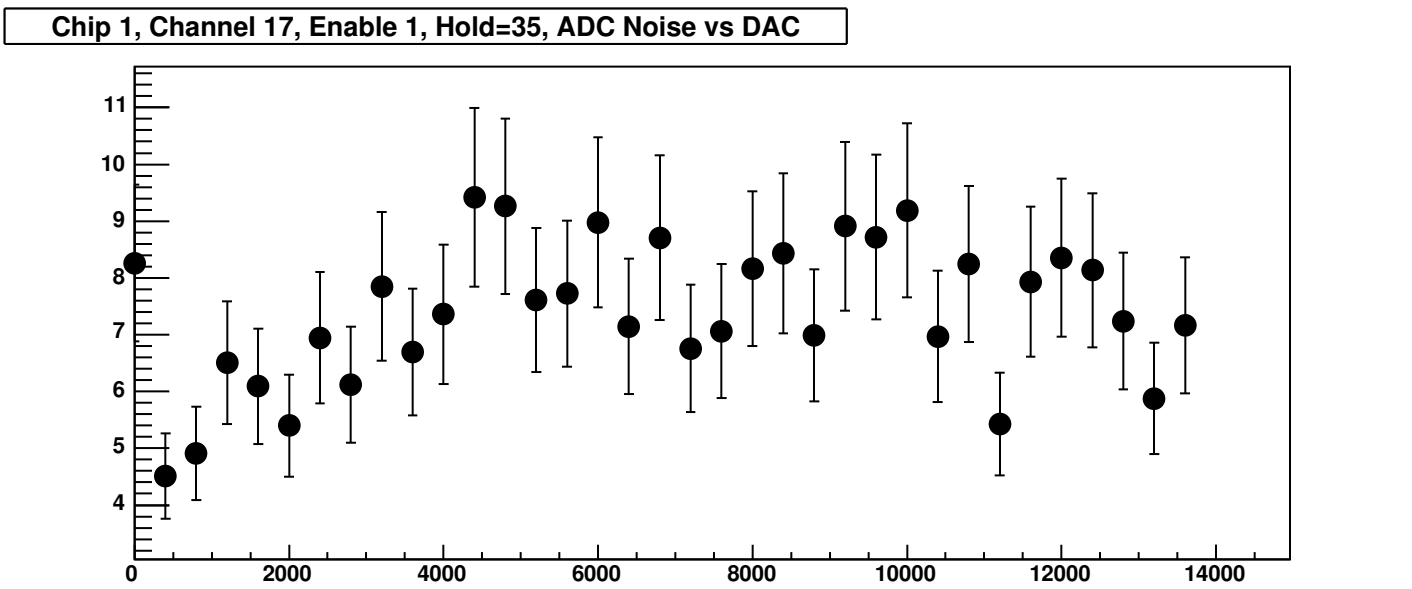

Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

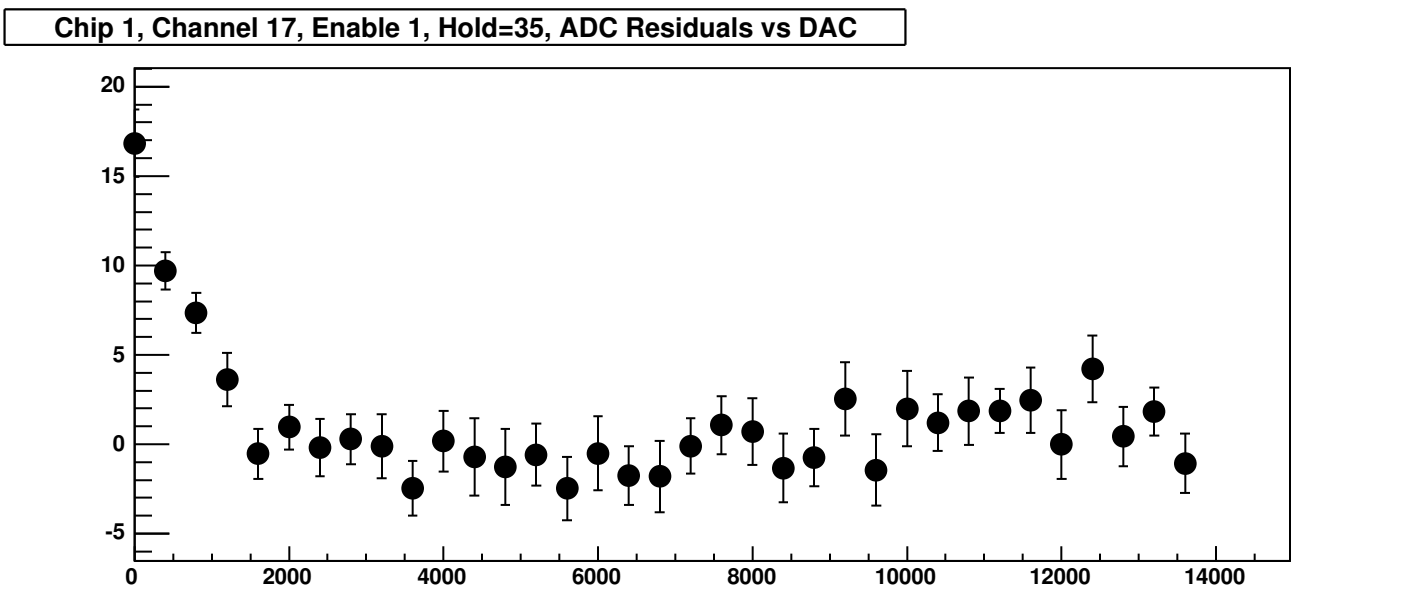

Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1542 / 23
p0 -770.9 ± 1.087
p1 0.1712 ± 0.0001944
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

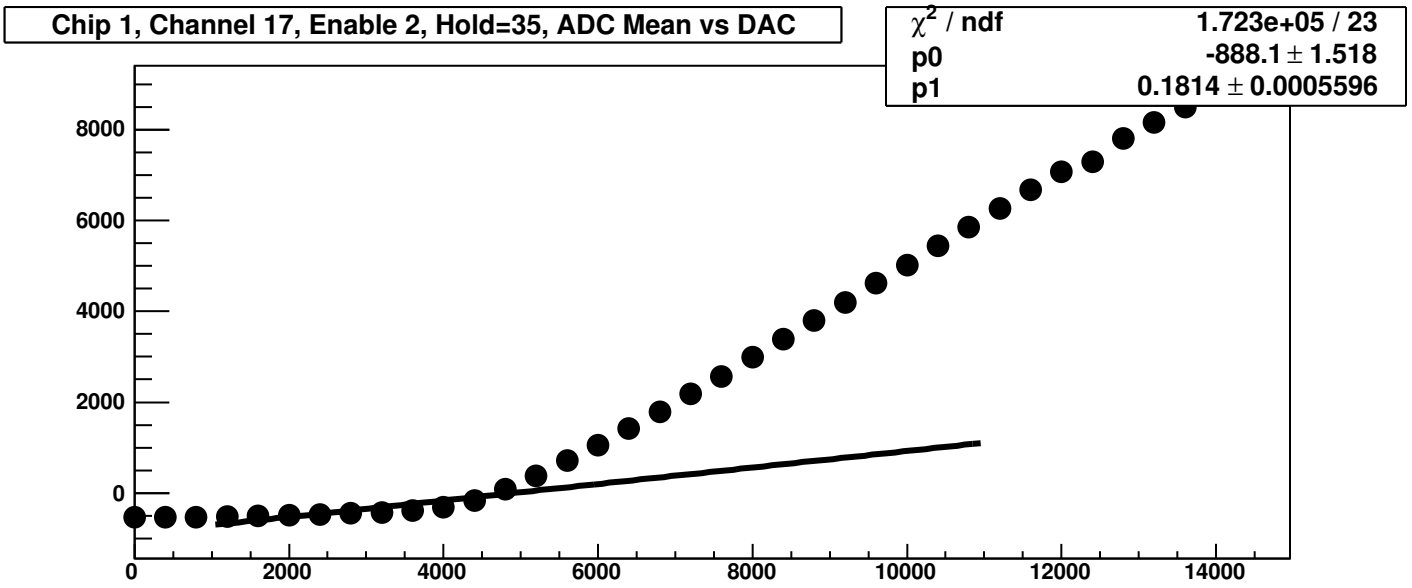

Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

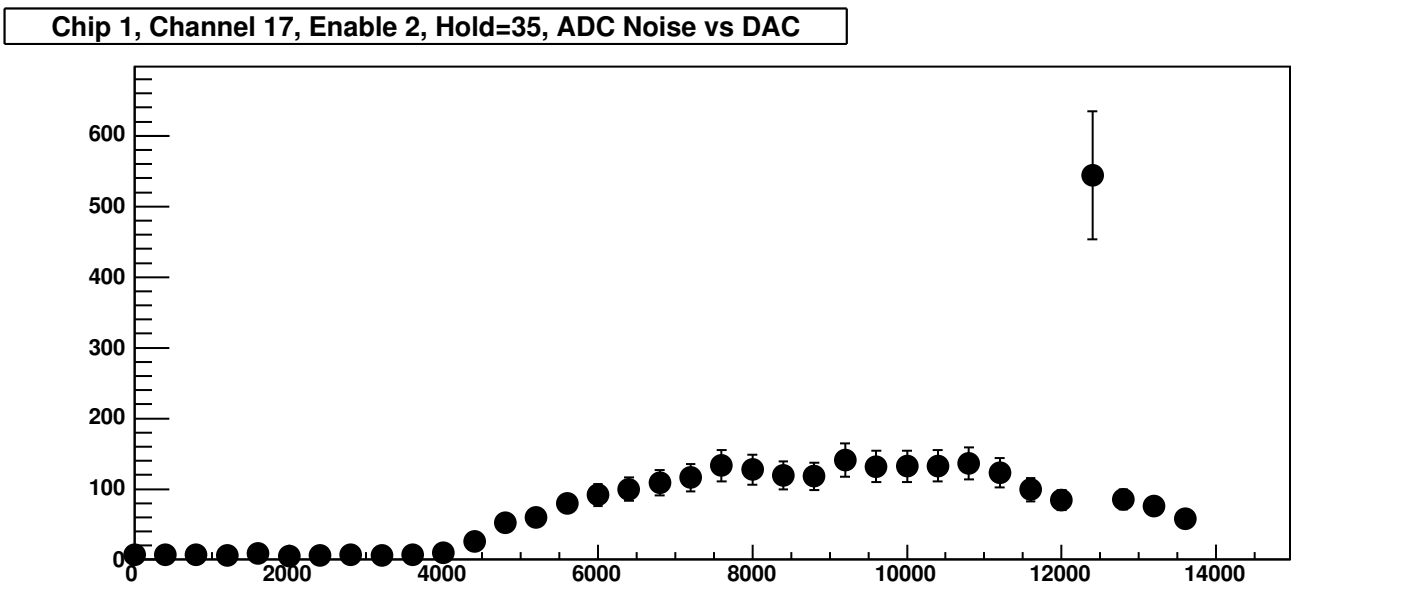

Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

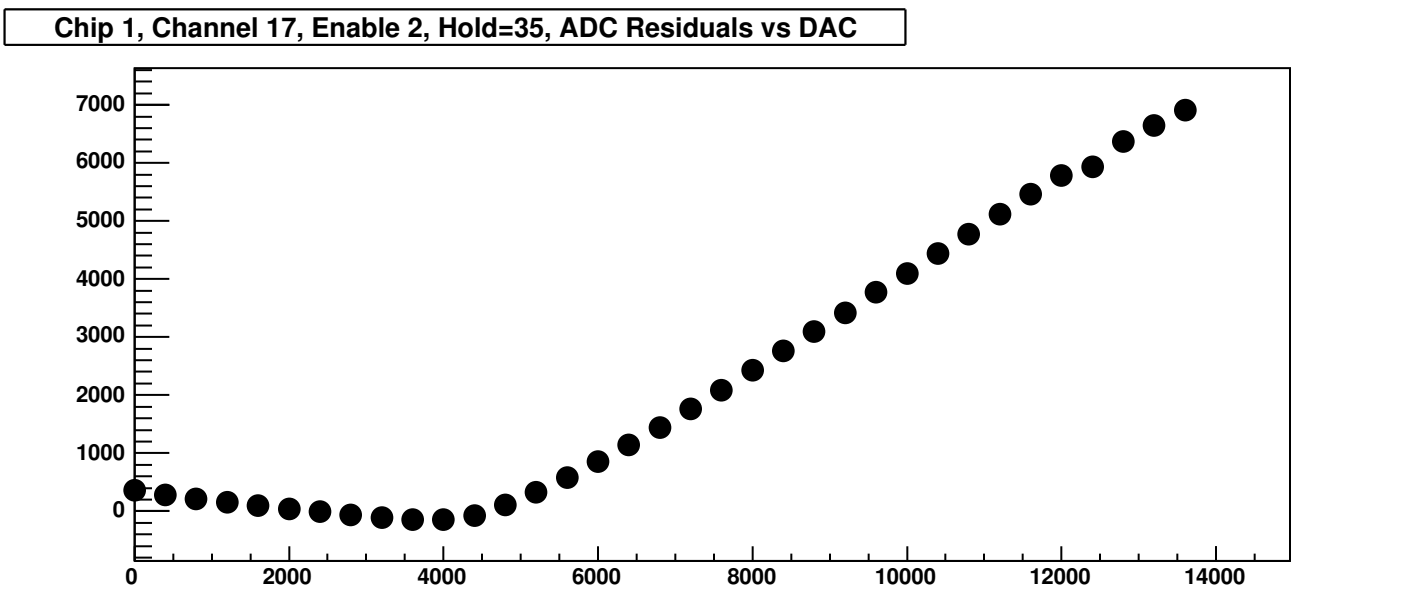

Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.54 / 23
p0 -564.2 ± 0.7136
p1 0.01273 ± 0.0001139
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

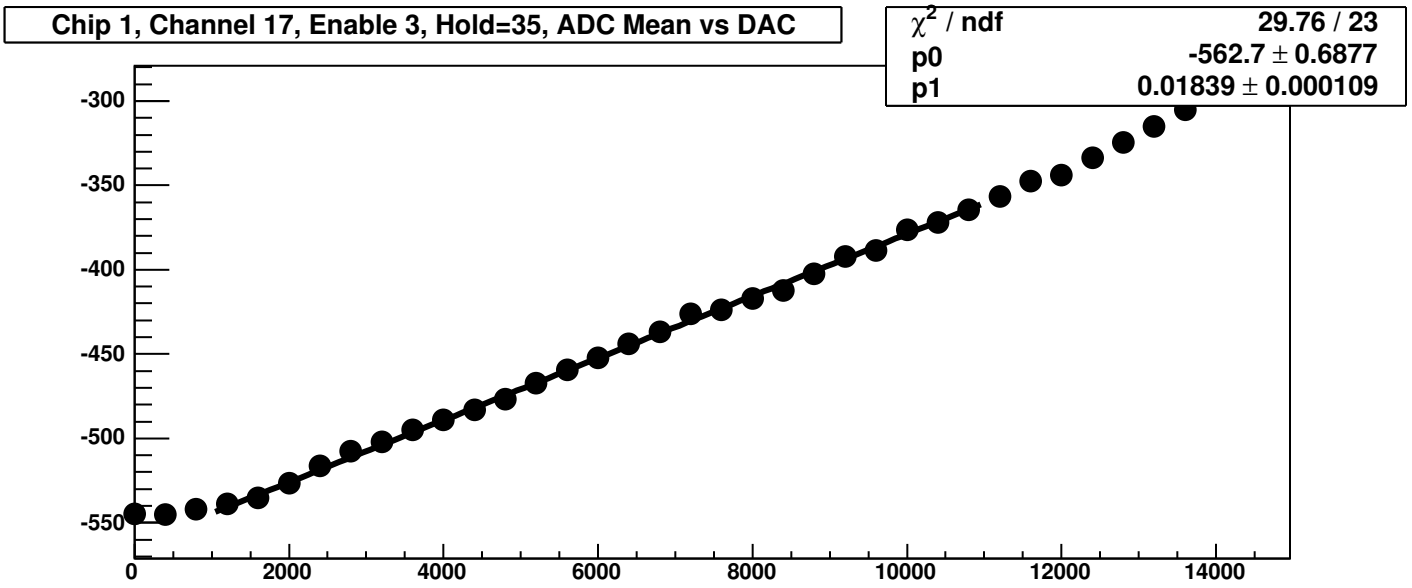

Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

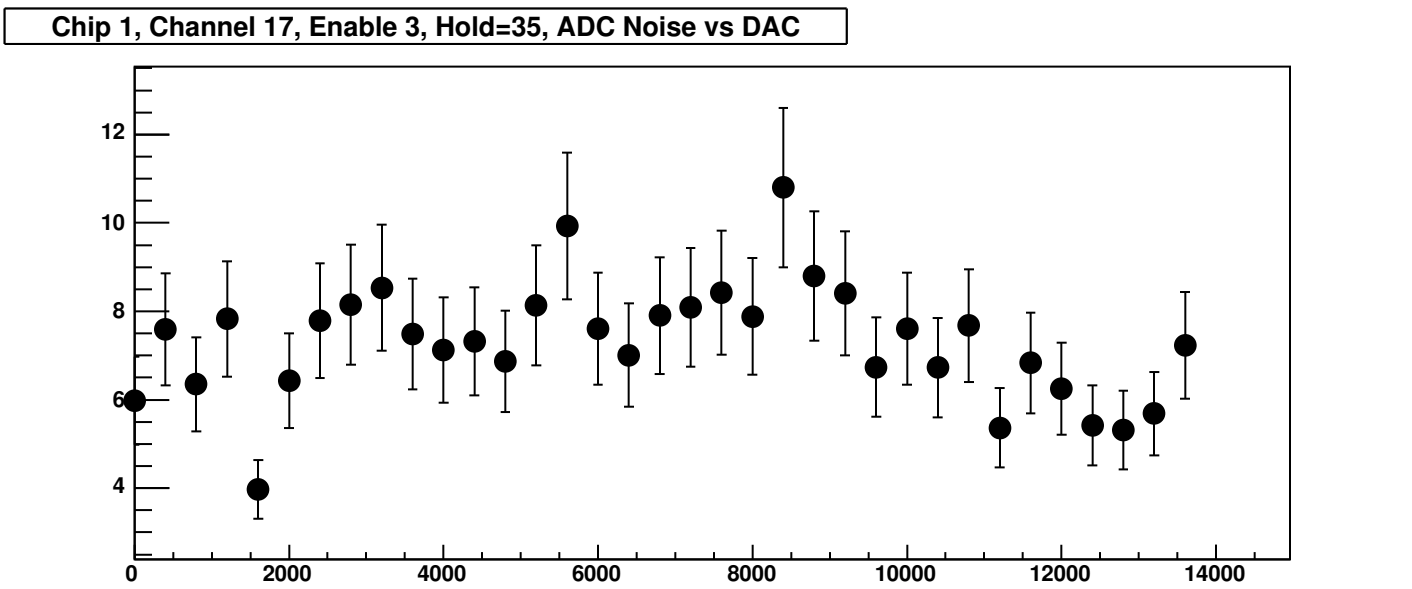

Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

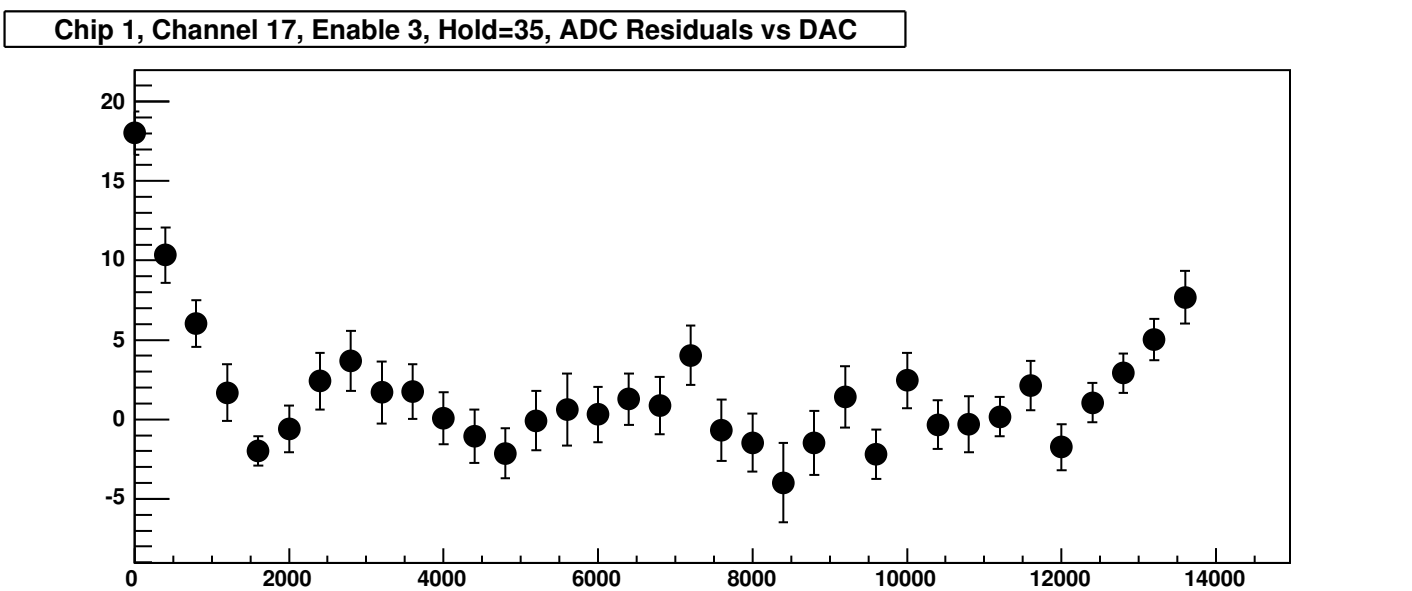

Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.723e+05 / 23
p0 -888.1 ± 1.518
p1 0.1814 ± 0.0005596
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

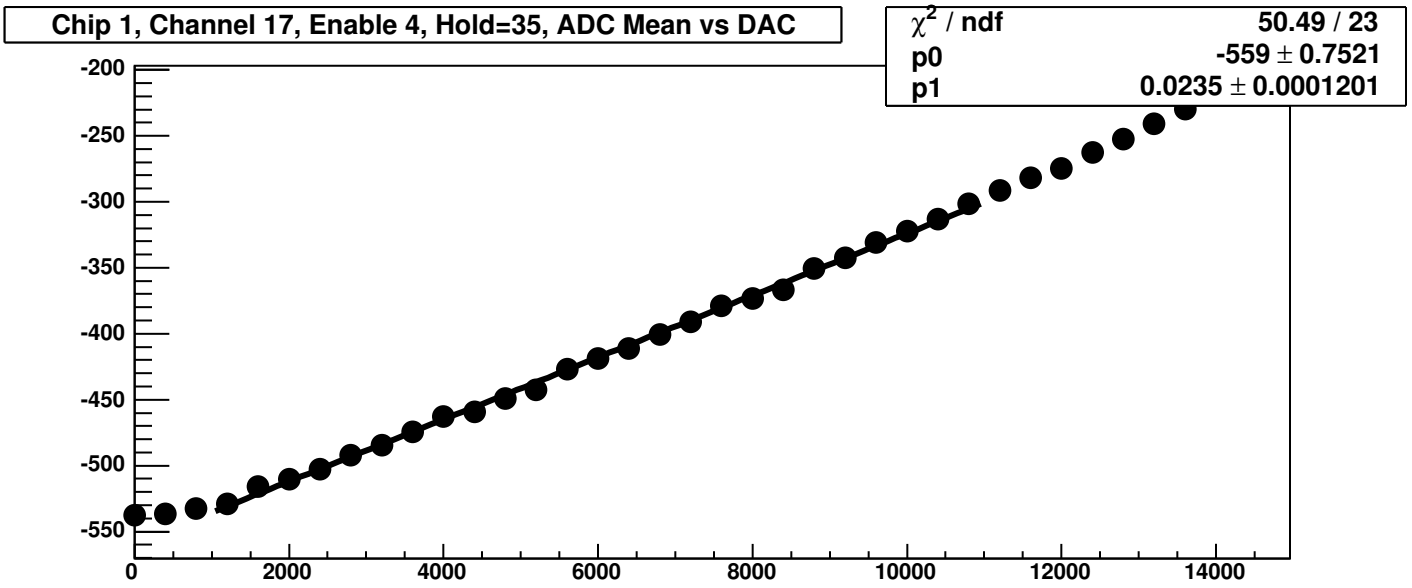

Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

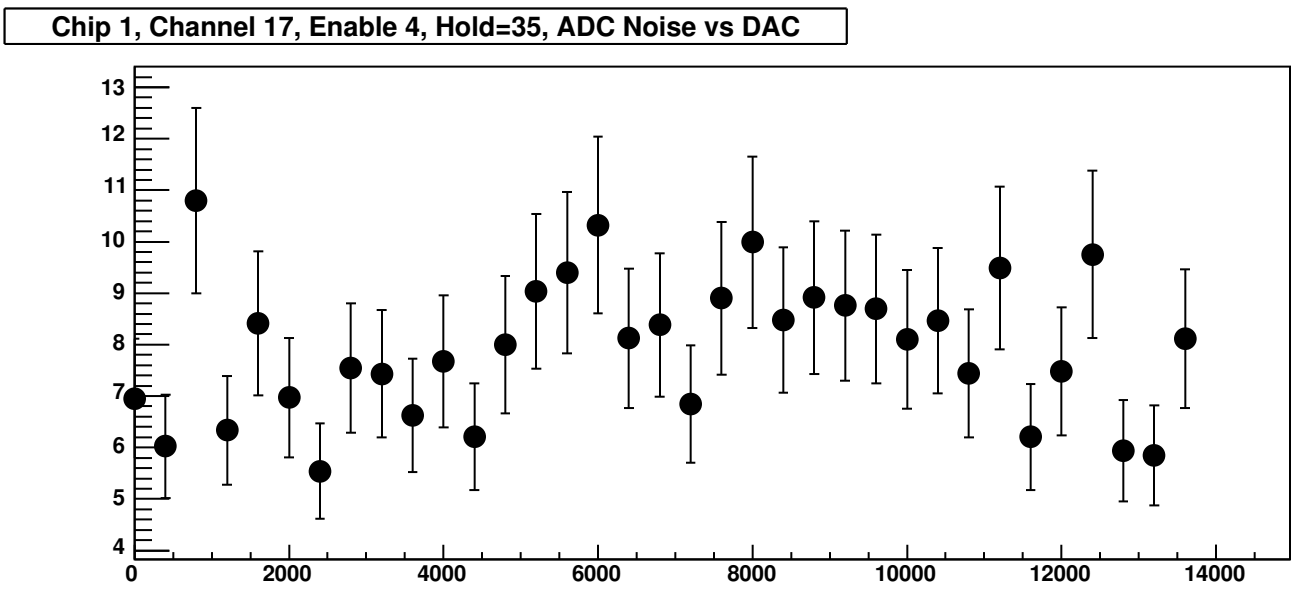

Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

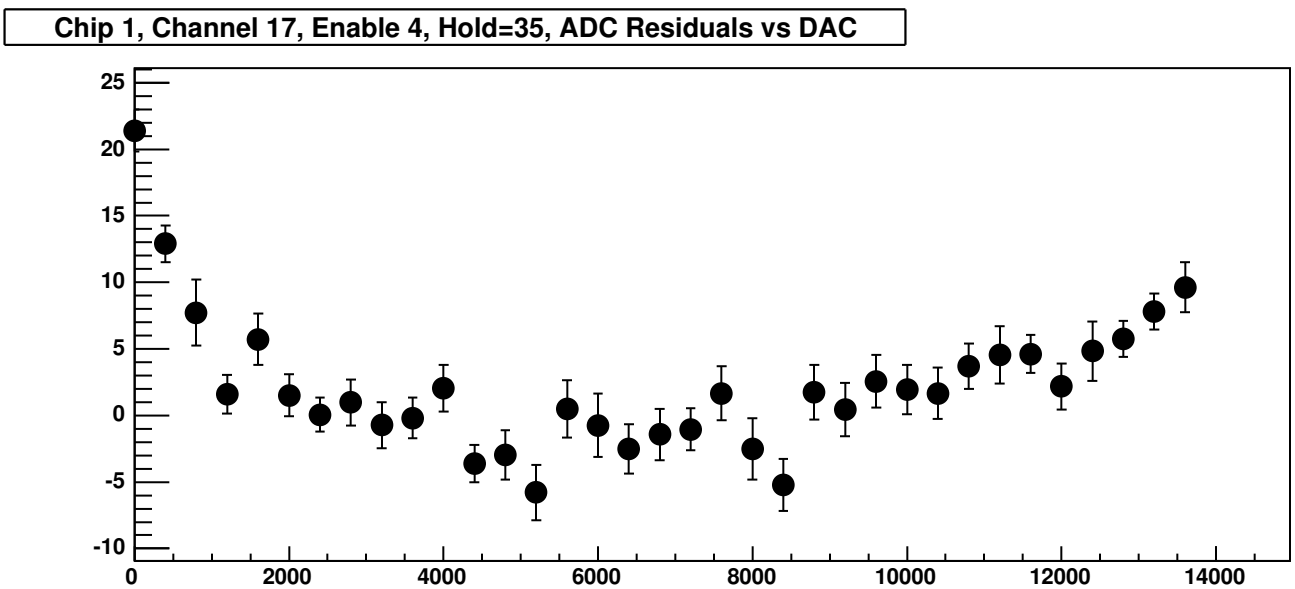

Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.76 / 23
p0 -562.7 ± 0.6877
p1 0.01839 ± 0.000109
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

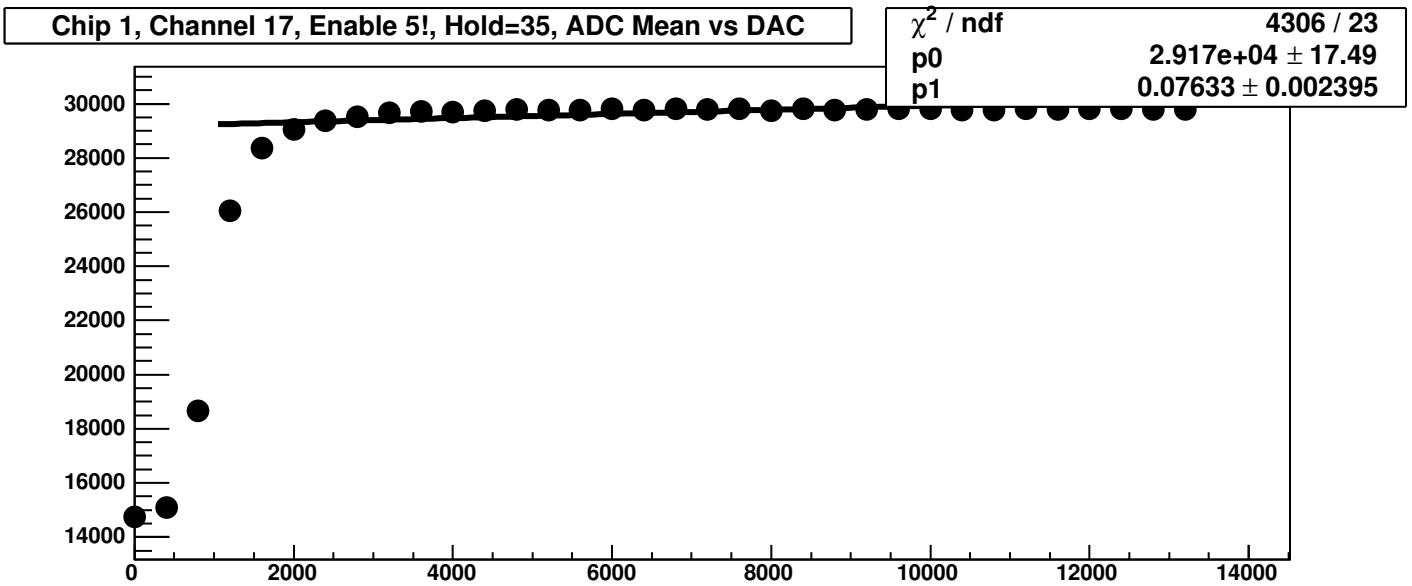

Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

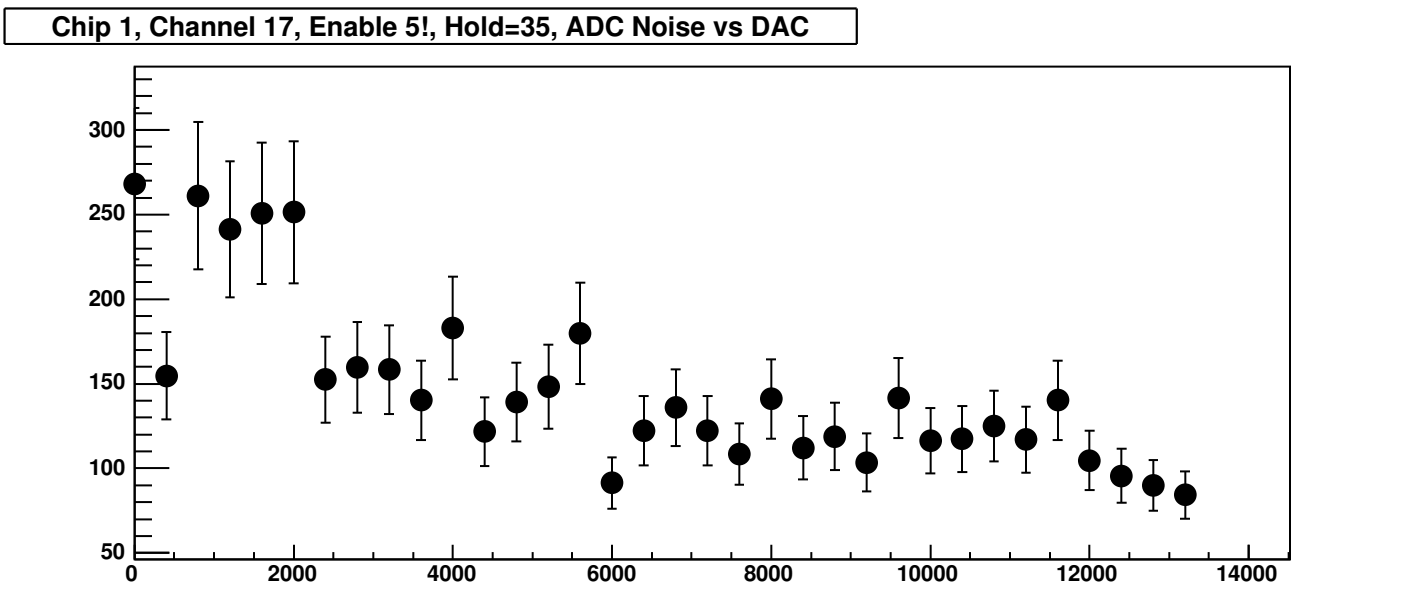

Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

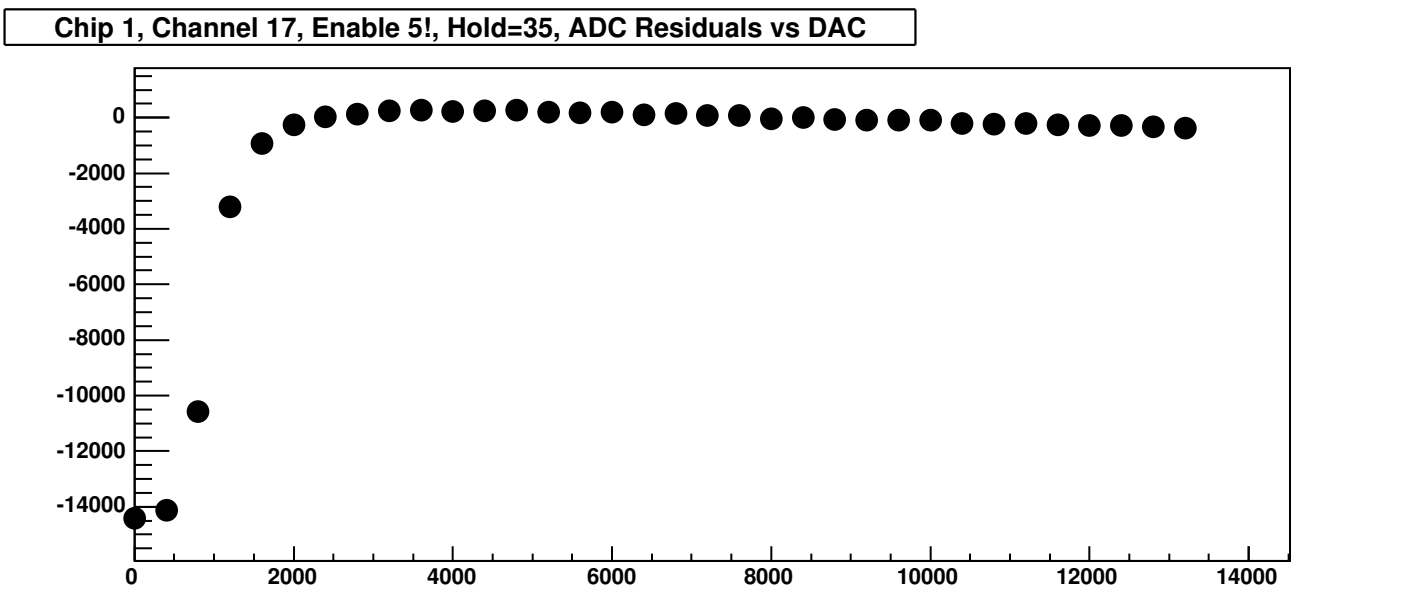

Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.49 / 23
p0 -559 ± 0.7521
p1 0.0235 ± 0.0001201
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

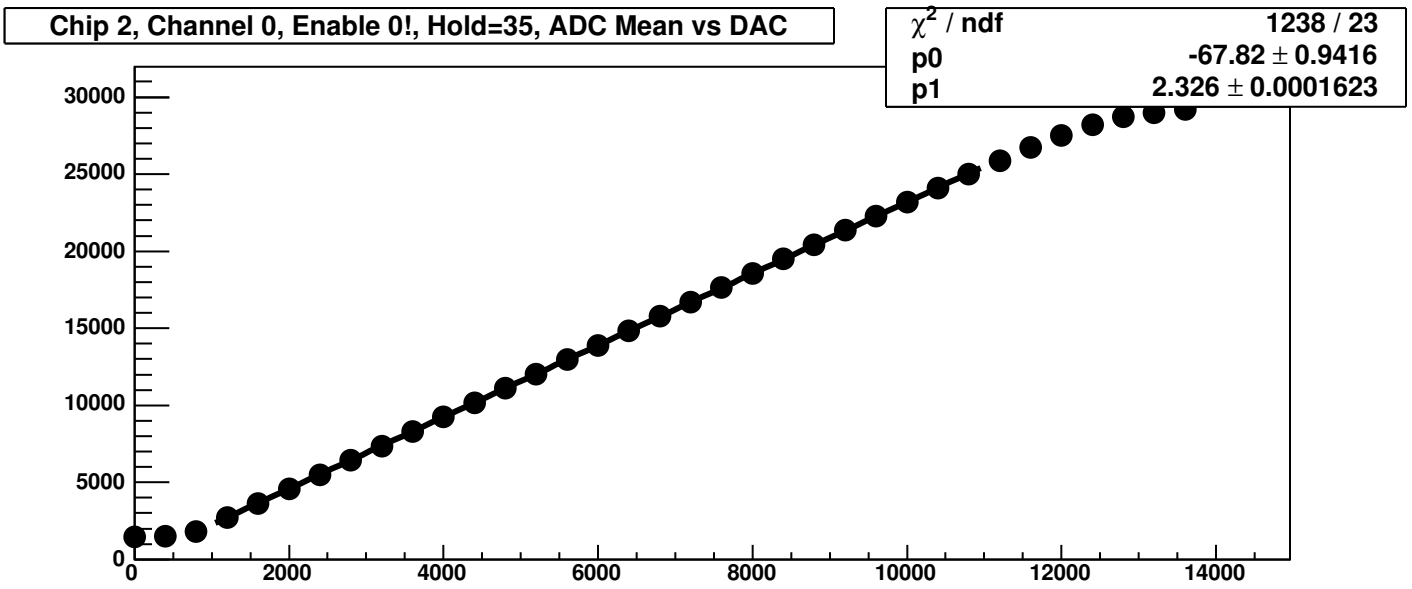

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

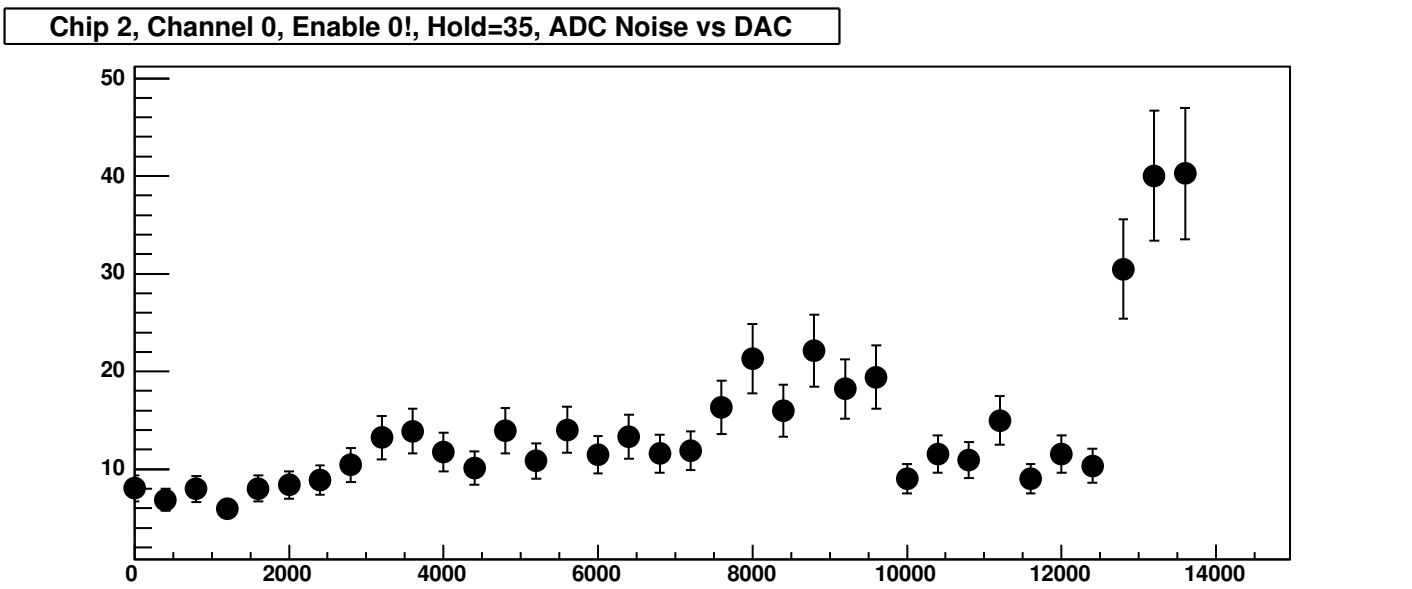

Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

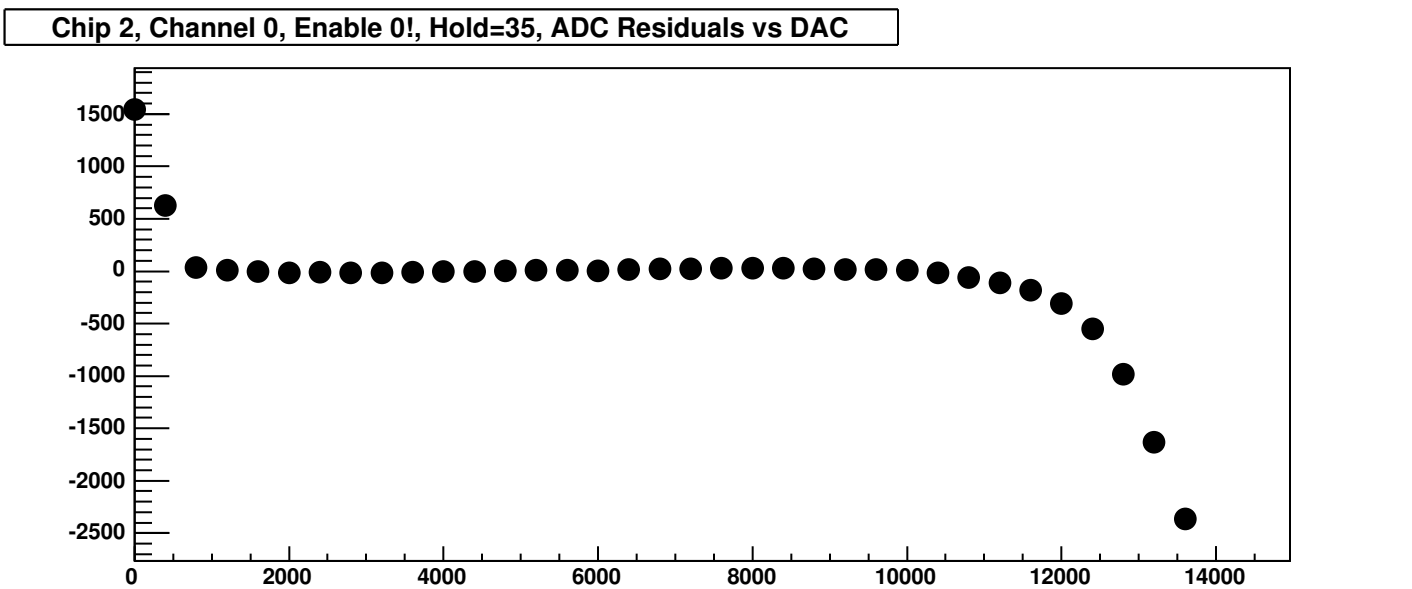

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4306 / 23
p0 2.917e+04 ± 17.49
p1 0.07633 ± 0.002395
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

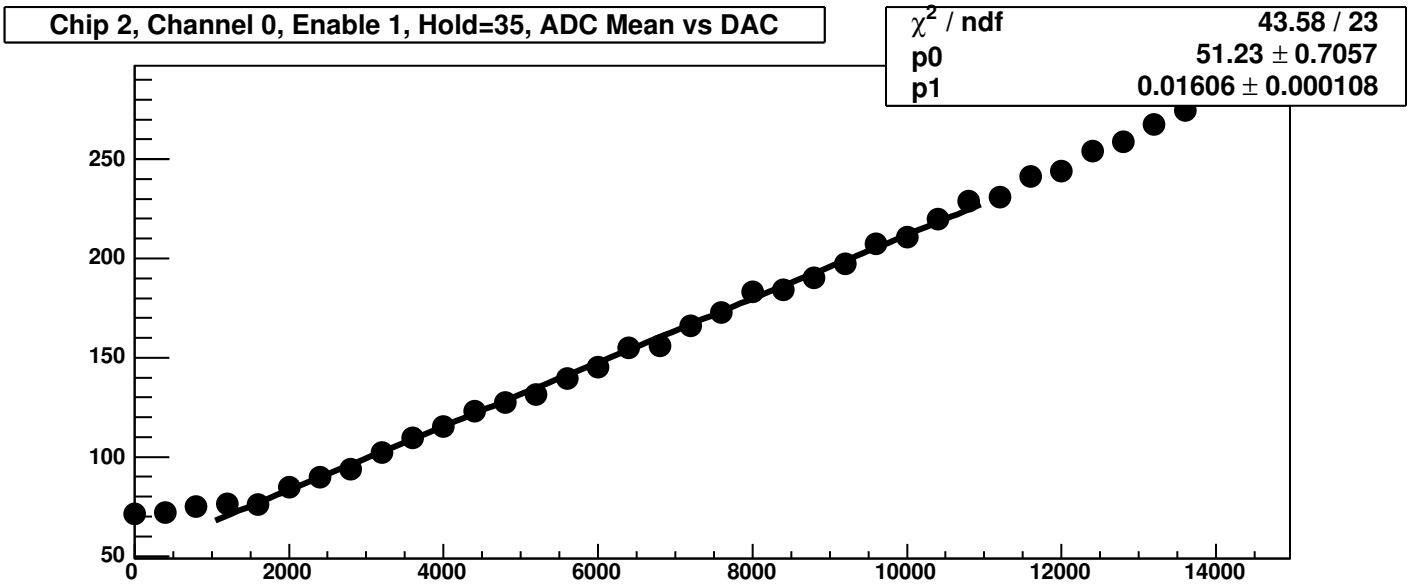

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

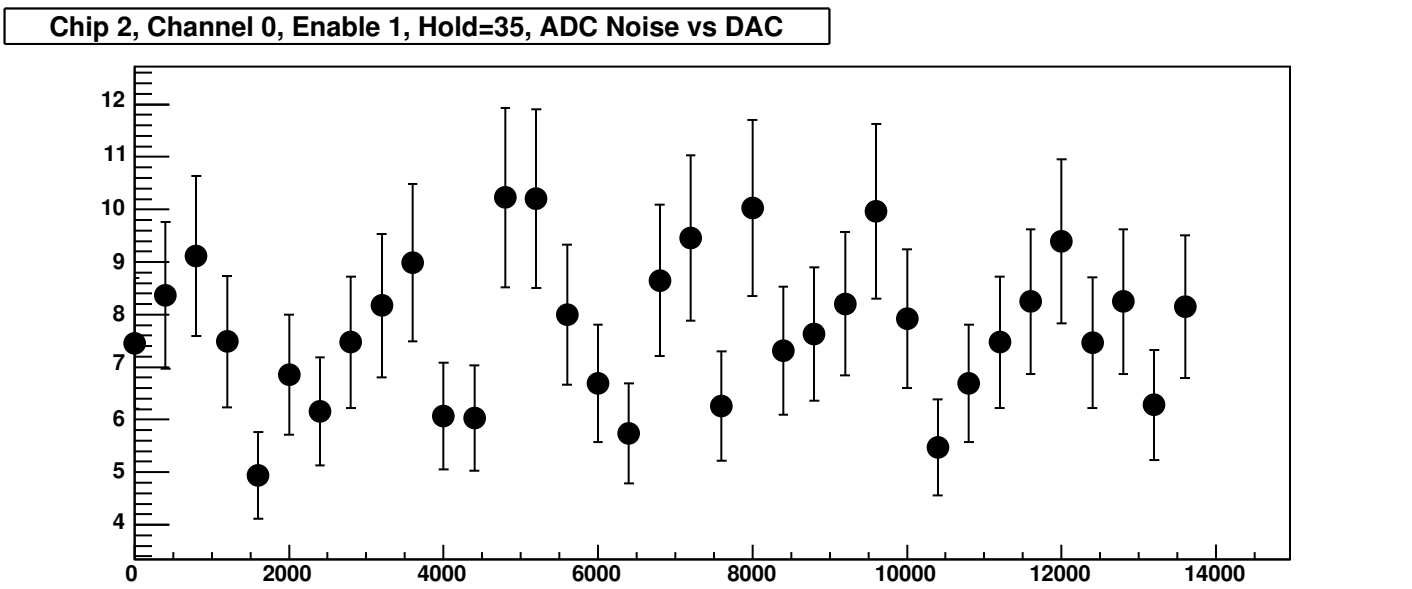

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

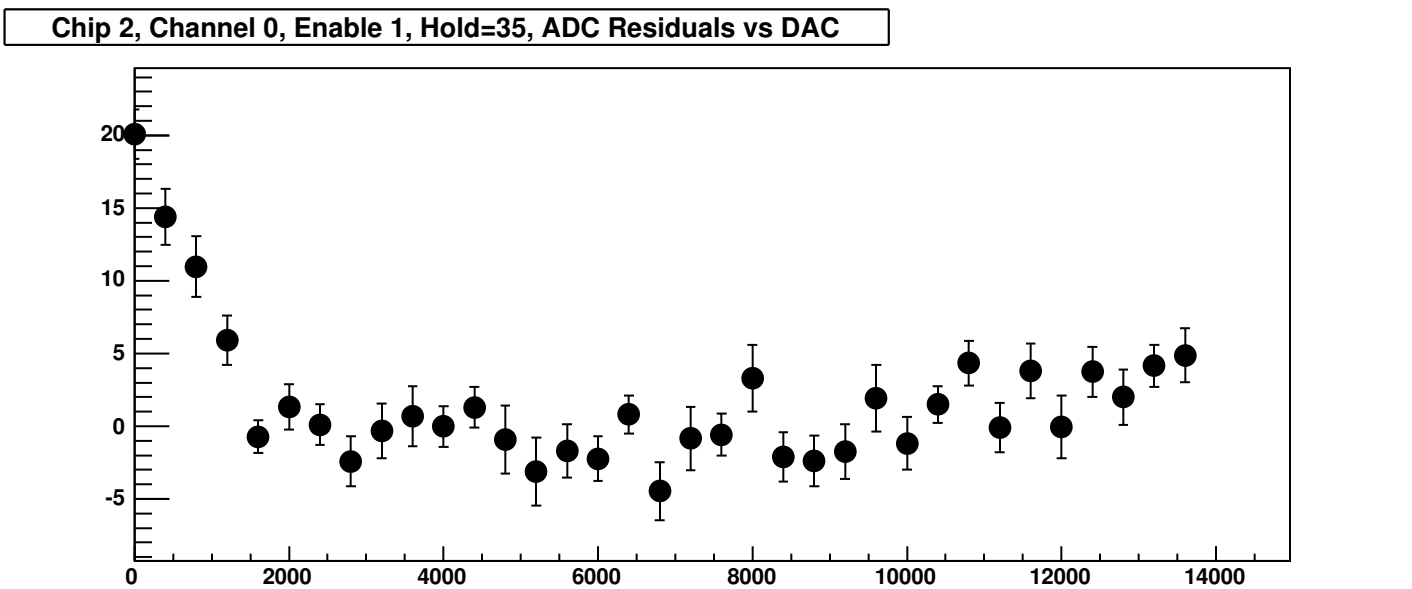

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1238 / 23
p0 -67.82 ± 0.9416
p1 2.326 ± 0.0001623
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

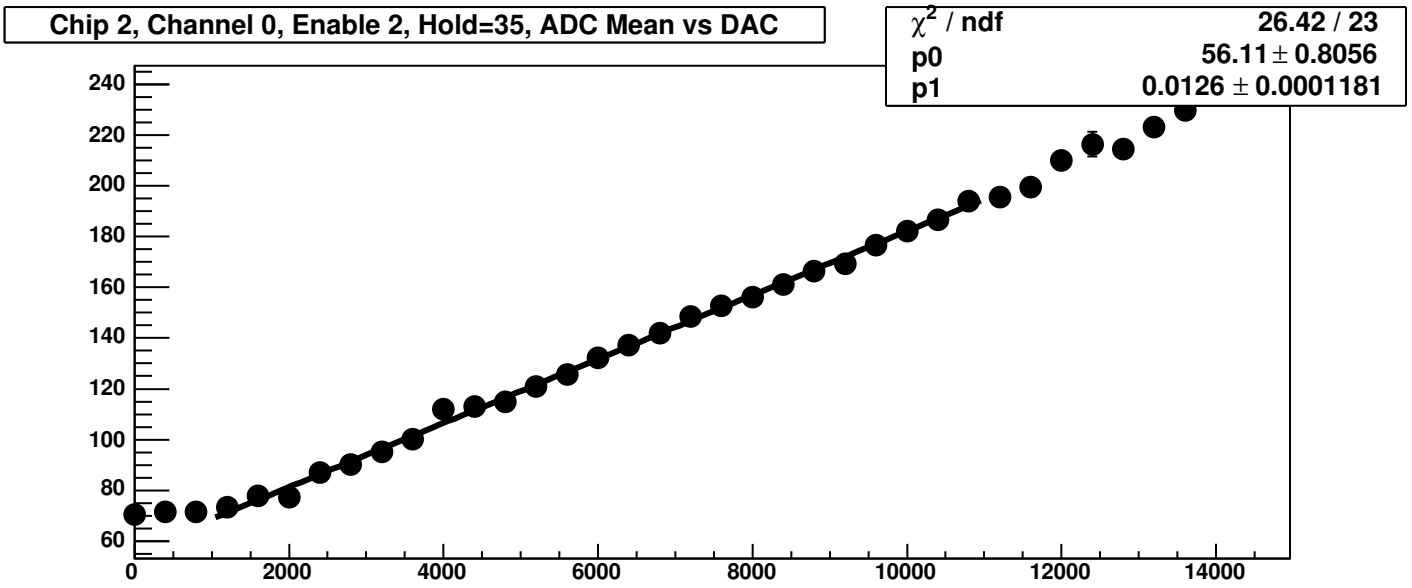

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

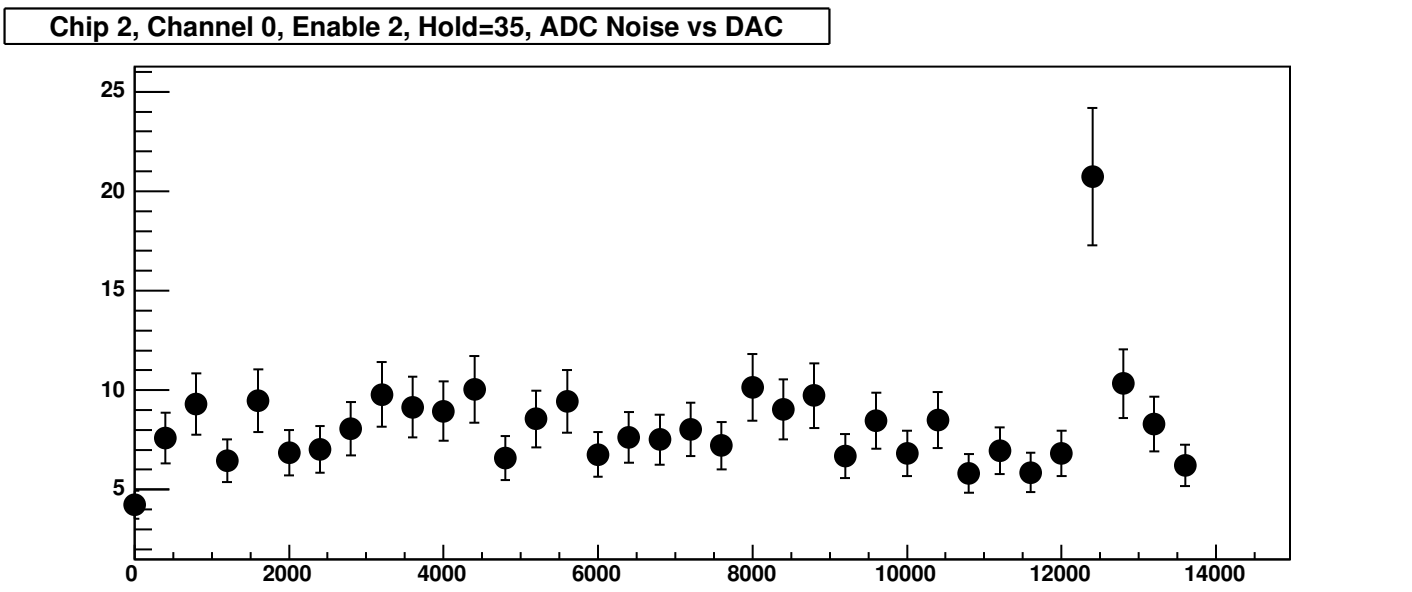

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

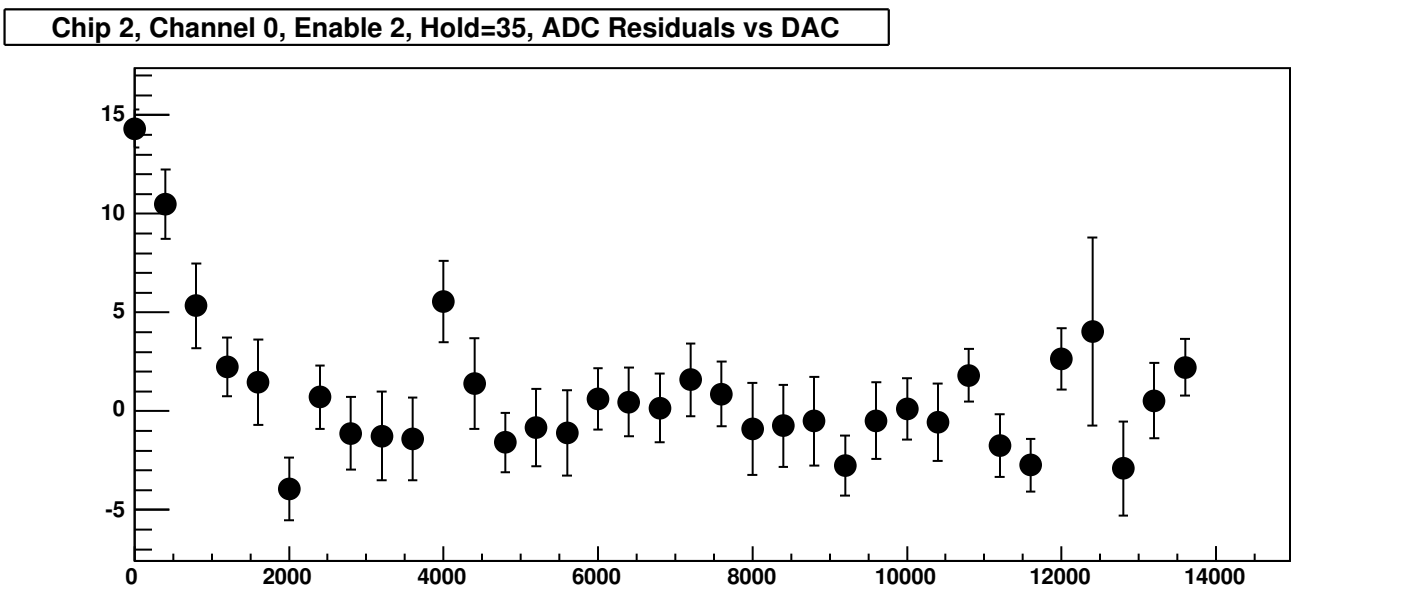

Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.58 / 23
p0 51.23 ± 0.7057
p1 0.01606 ± 0.000108
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

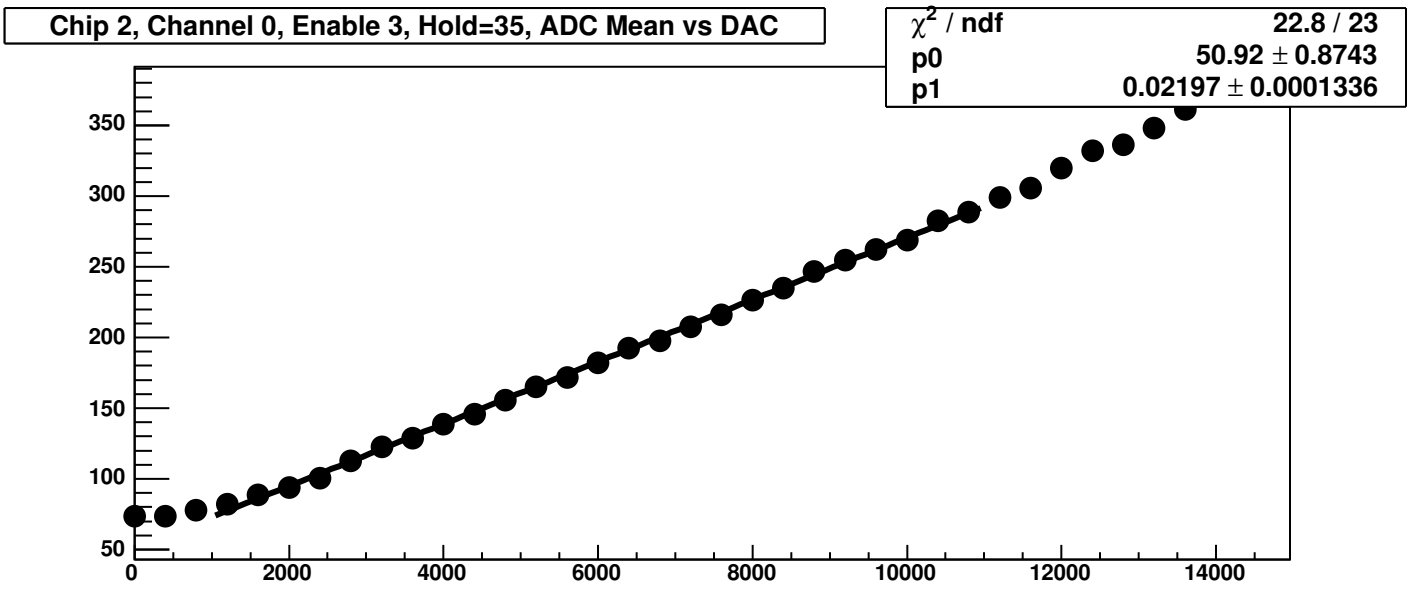

Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

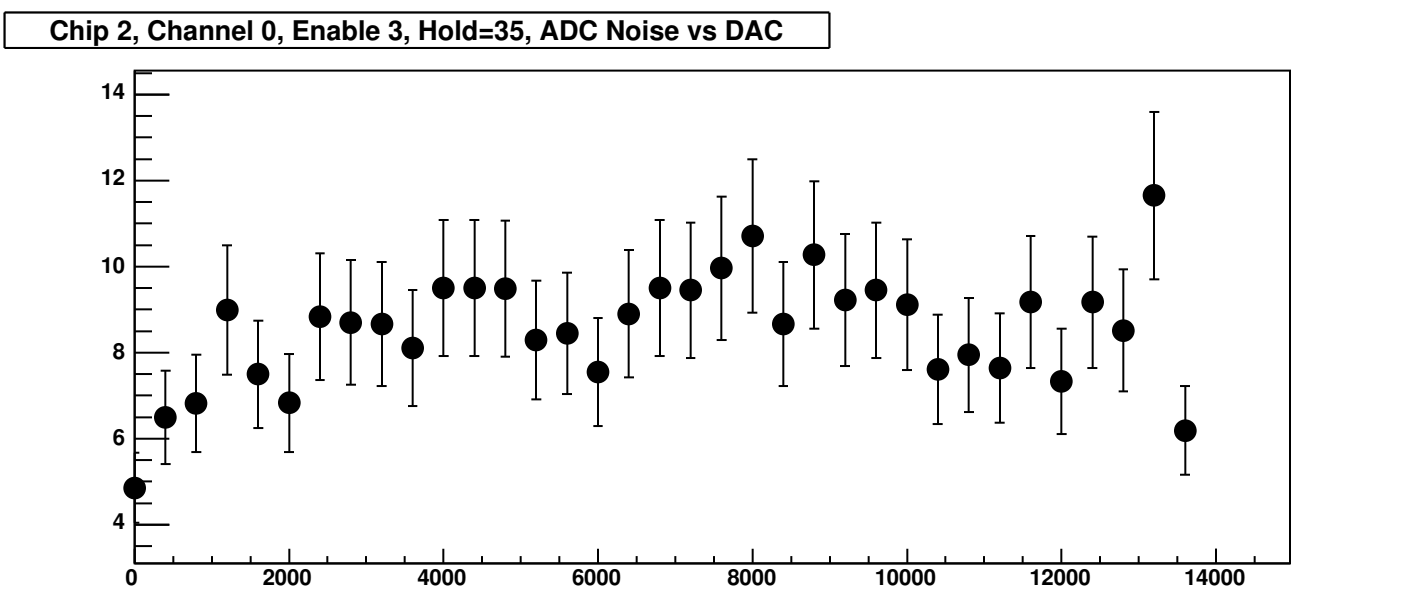

Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

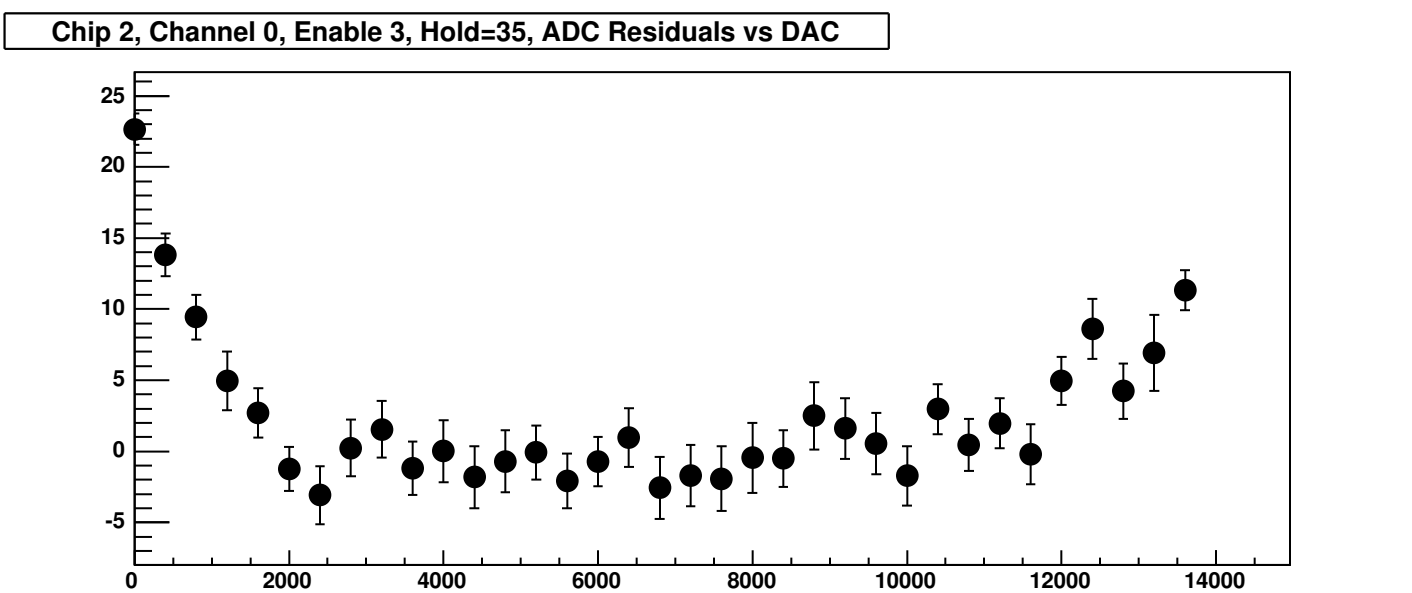

Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.42 / 23
p0 56.11 ± 0.8056
p1 0.0126 ± 0.0001181
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

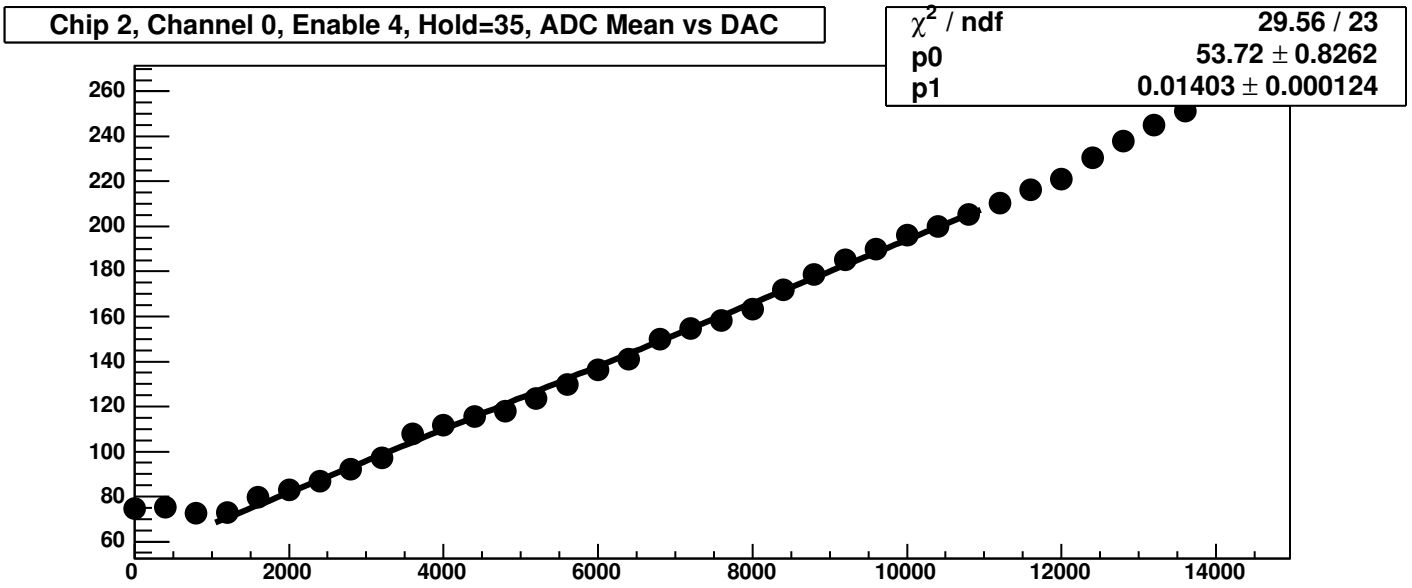

Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

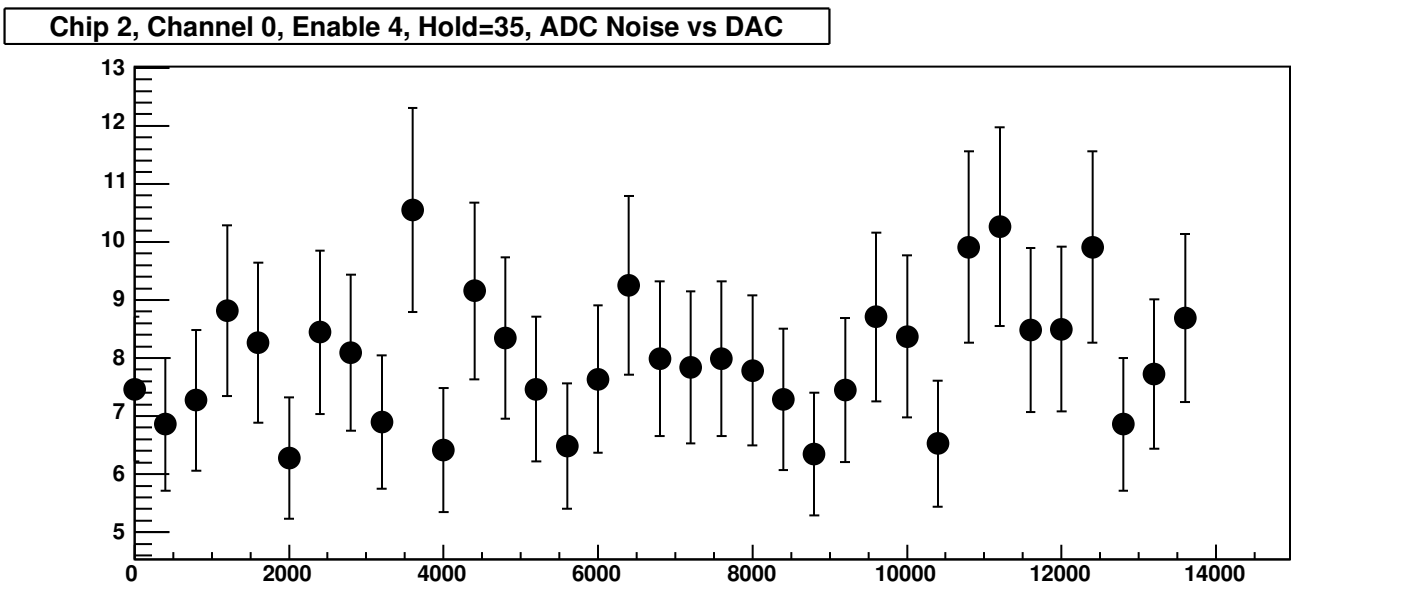

Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

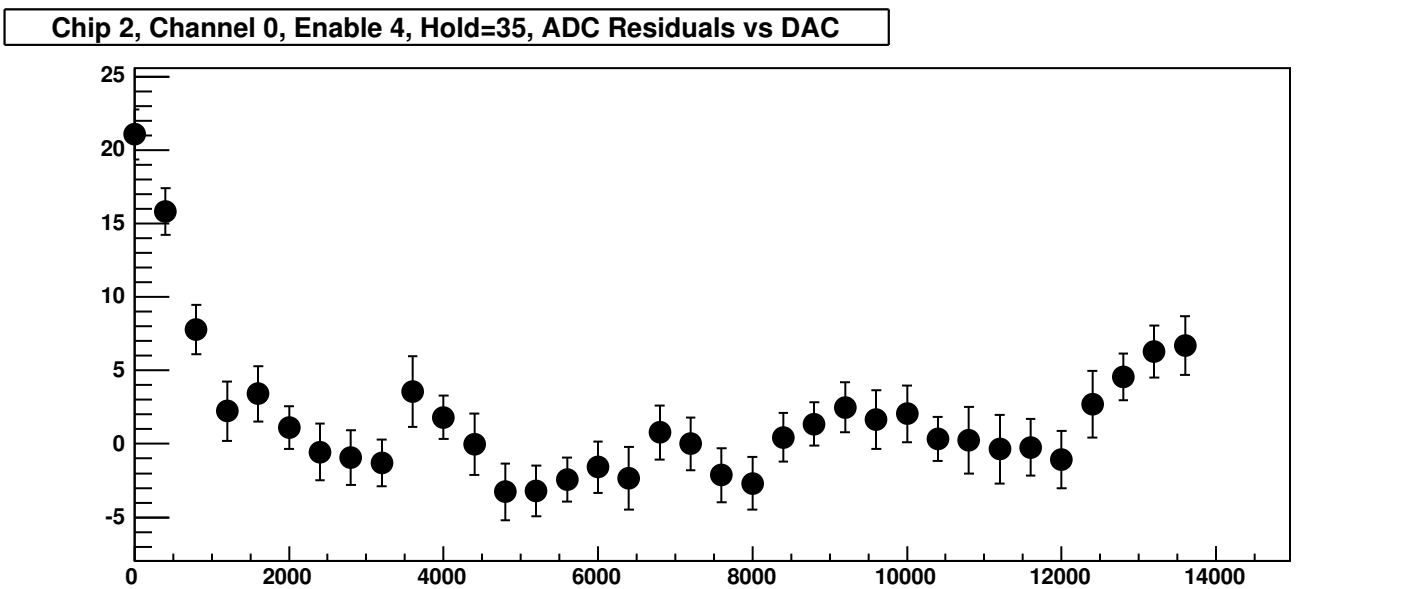

Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.8 / 23
p0 50.92 ± 0.8743
p1 0.02197 ± 0.0001336
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

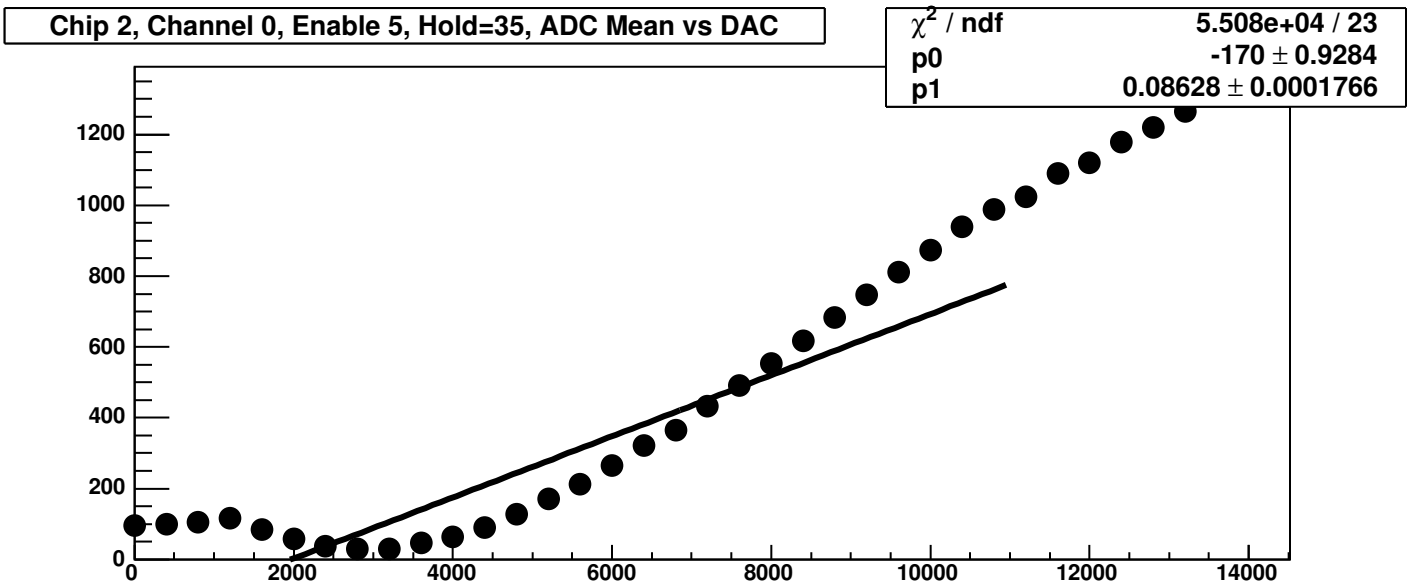

Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

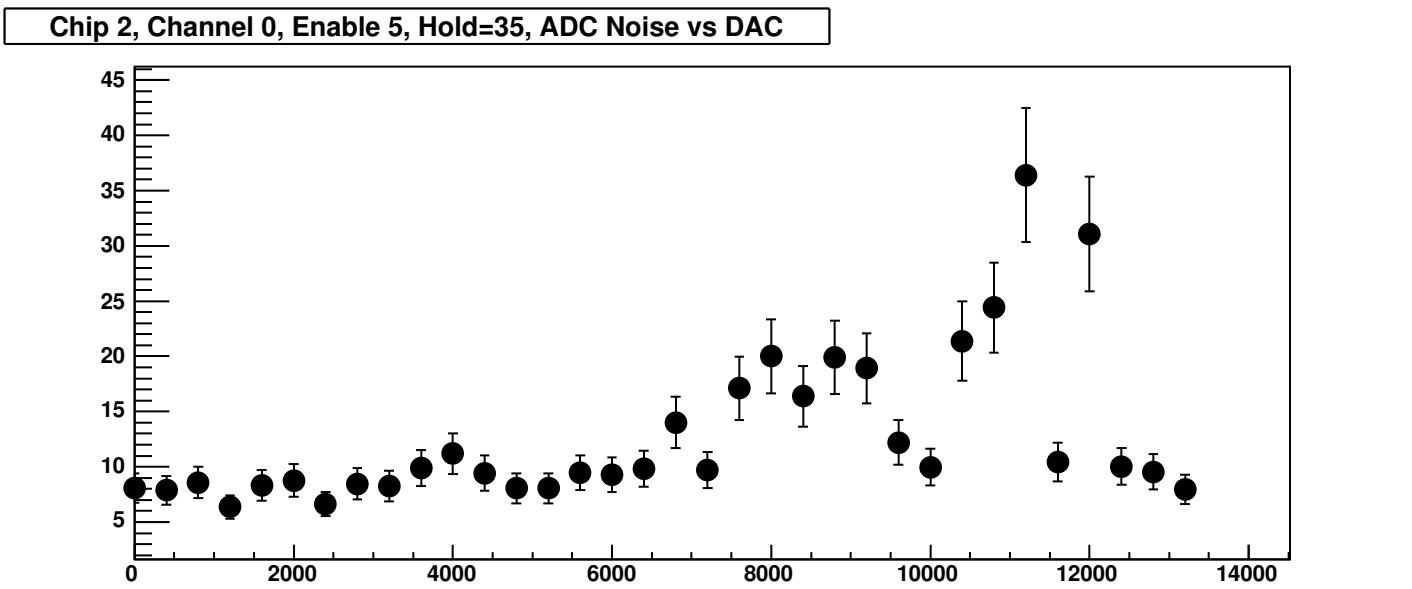

Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

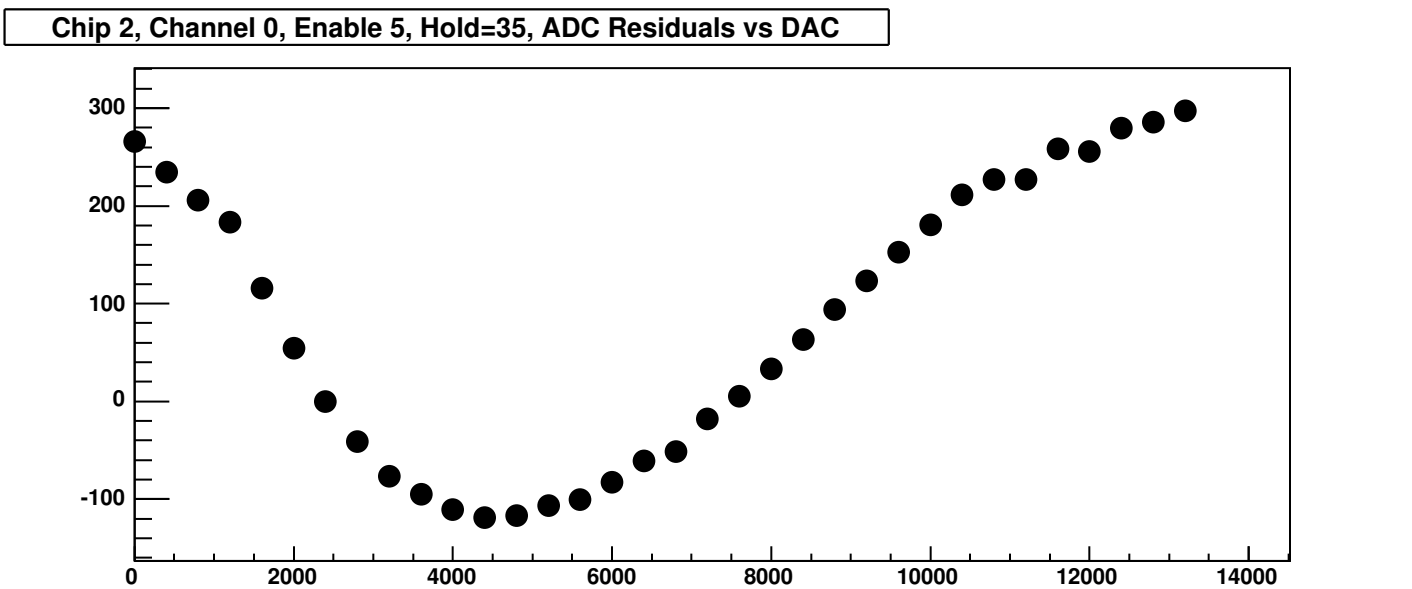

Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.56 / 23
p0 53.72 ± 0.8262
p1 0.01403 ± 0.000124
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

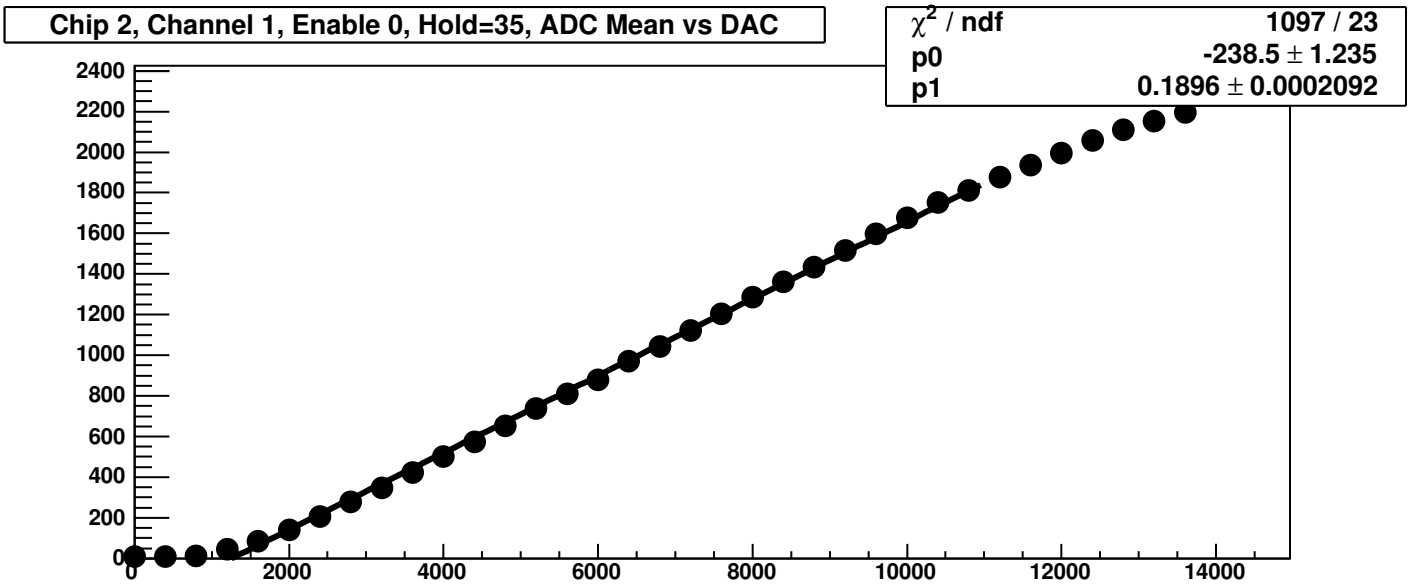

Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

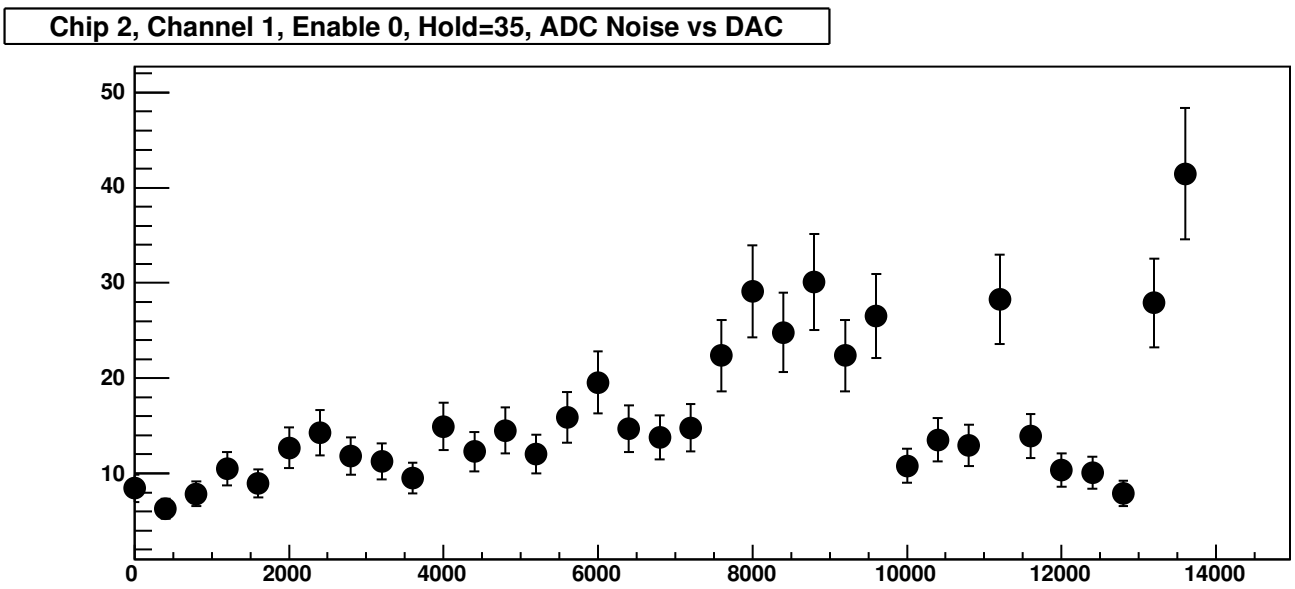

Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

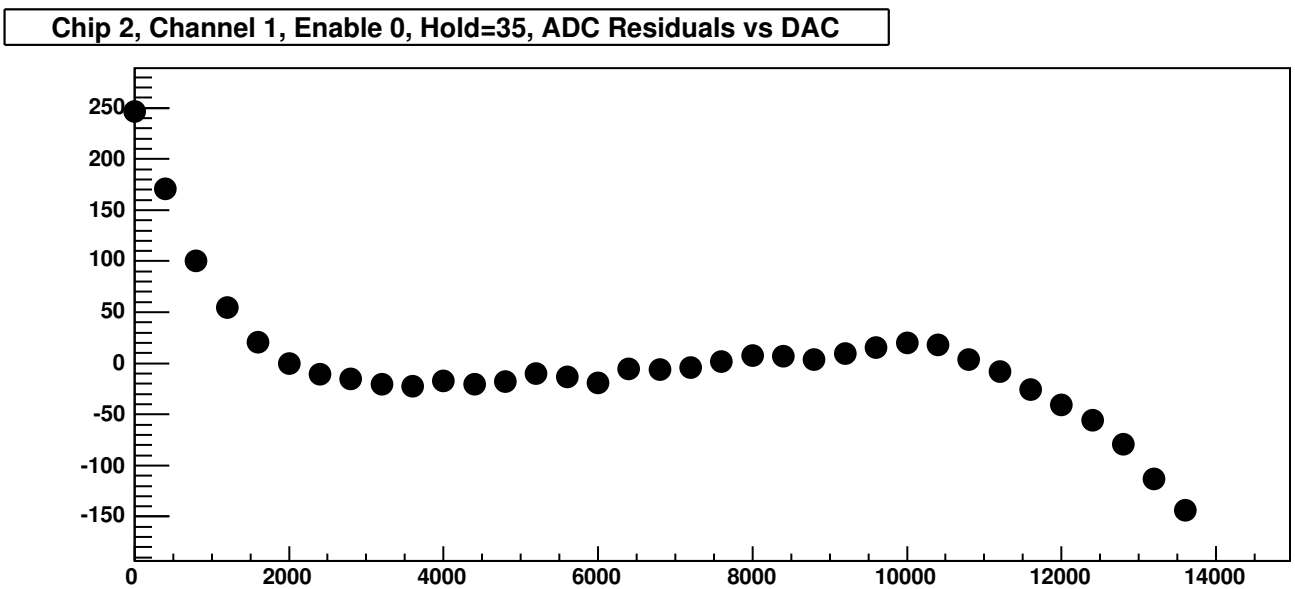

Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.508e+04 / 23
p0 -170 ± 0.9284
p1 0.08628 ± 0.0001766
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

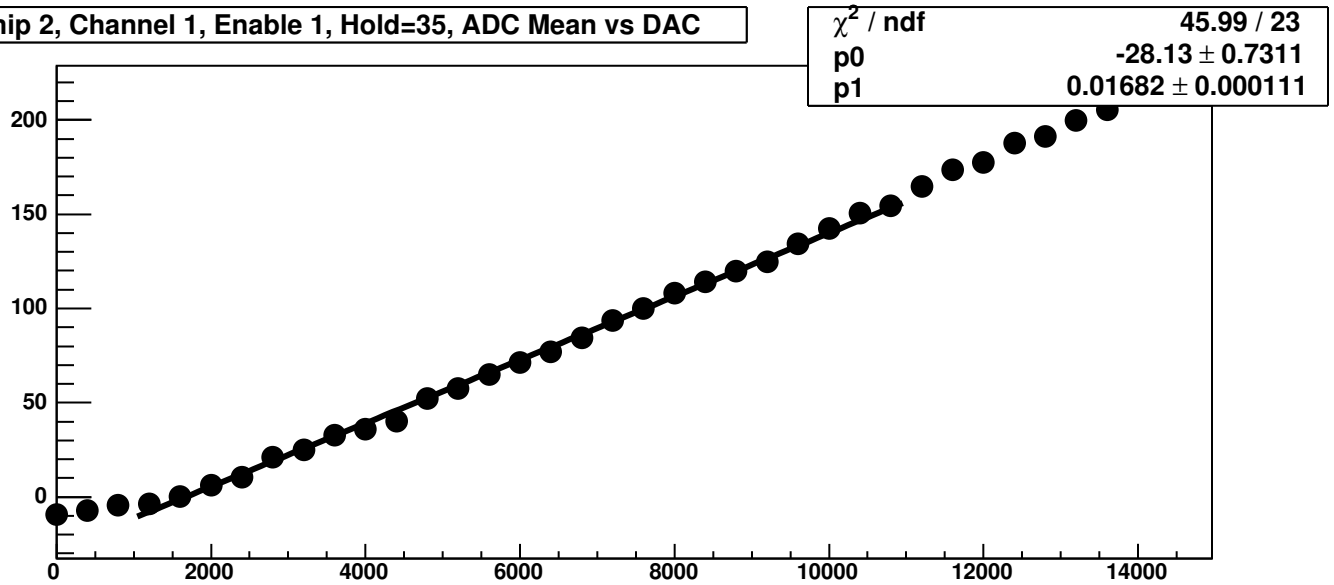

Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1097 / 23
p0 -238.5 ± 1.235
p1 0.1896 ± 0.0002092
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

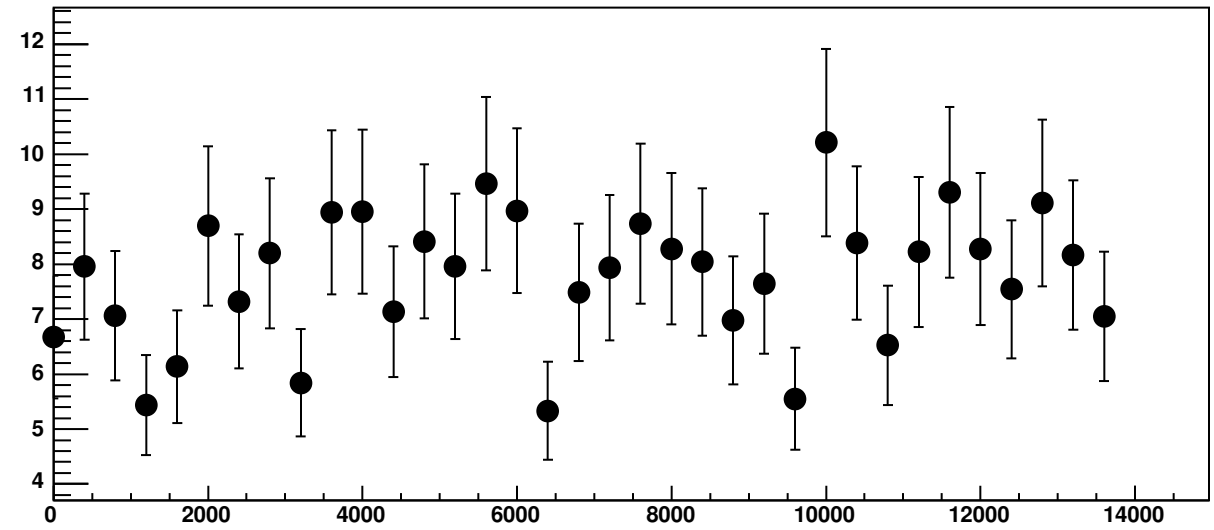

Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

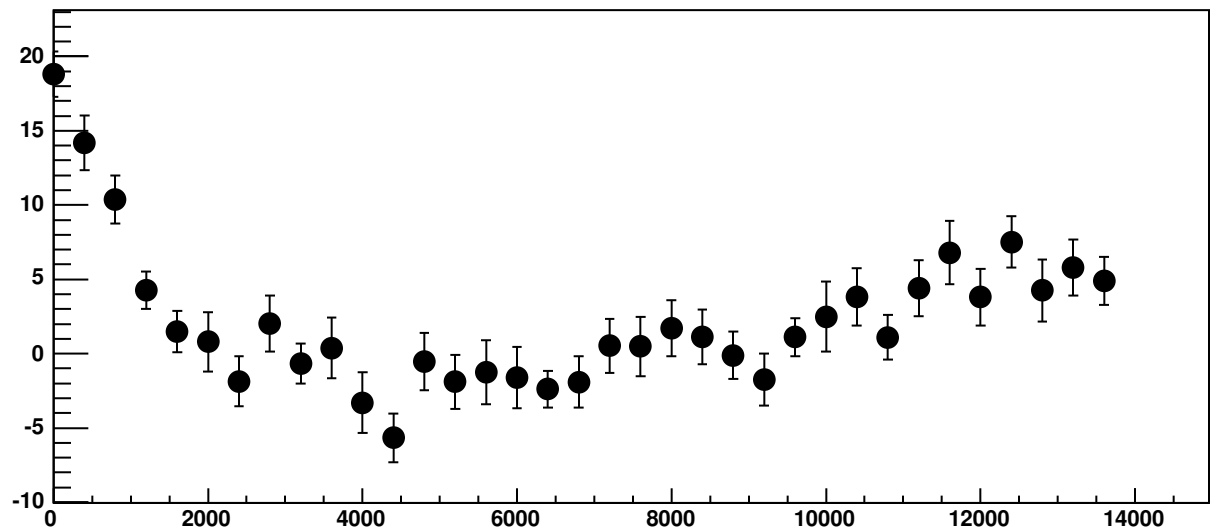
Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

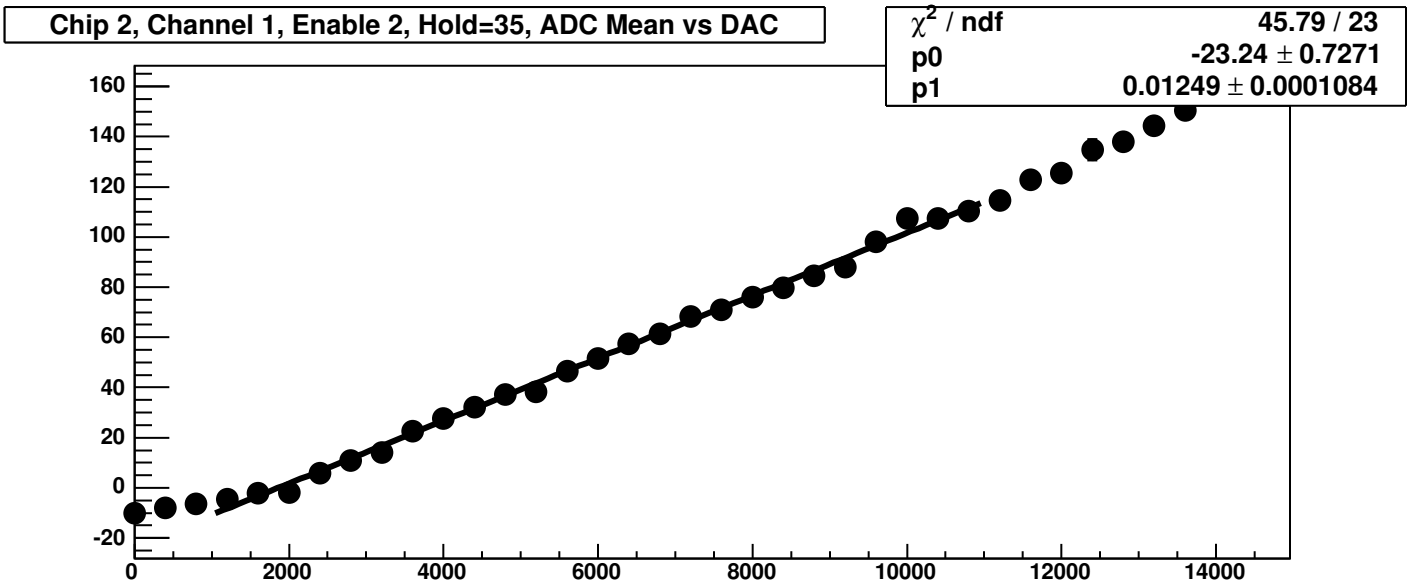


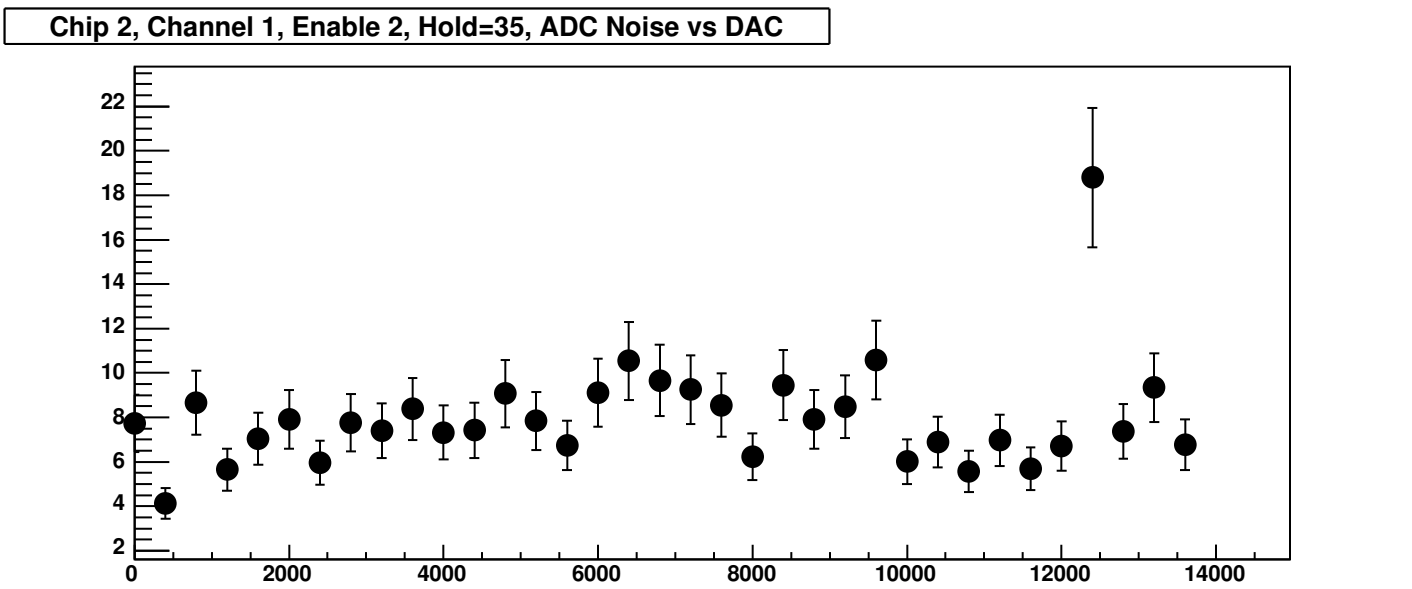
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

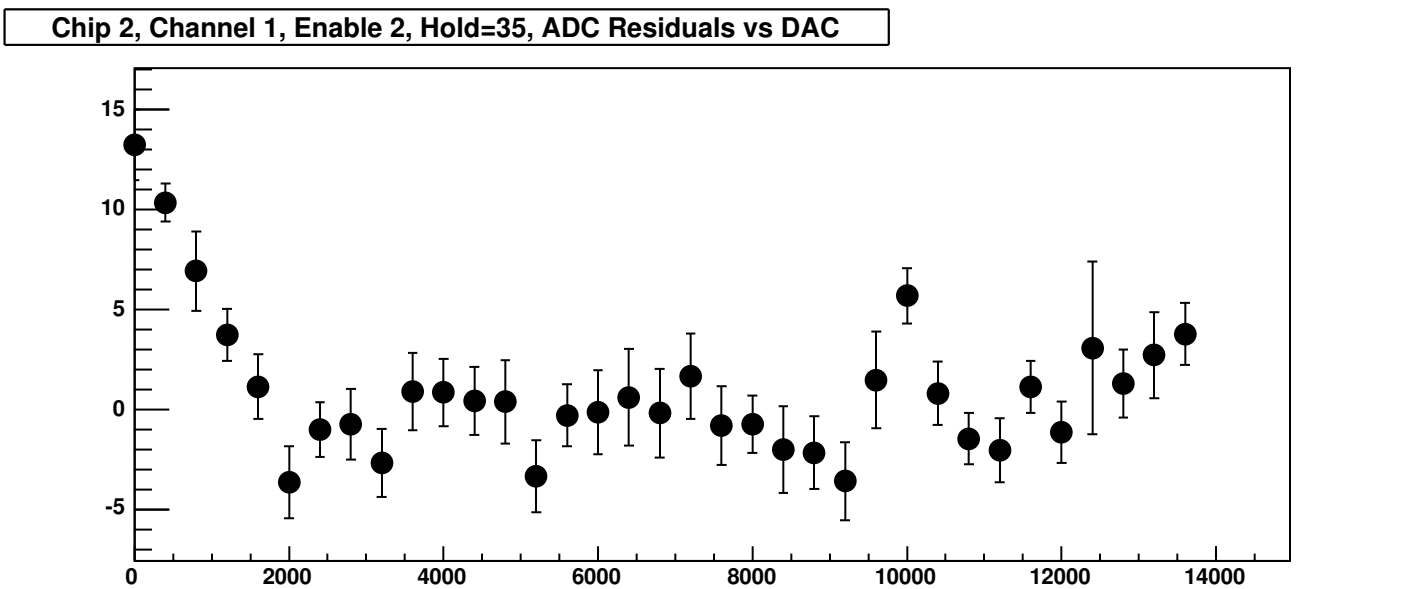


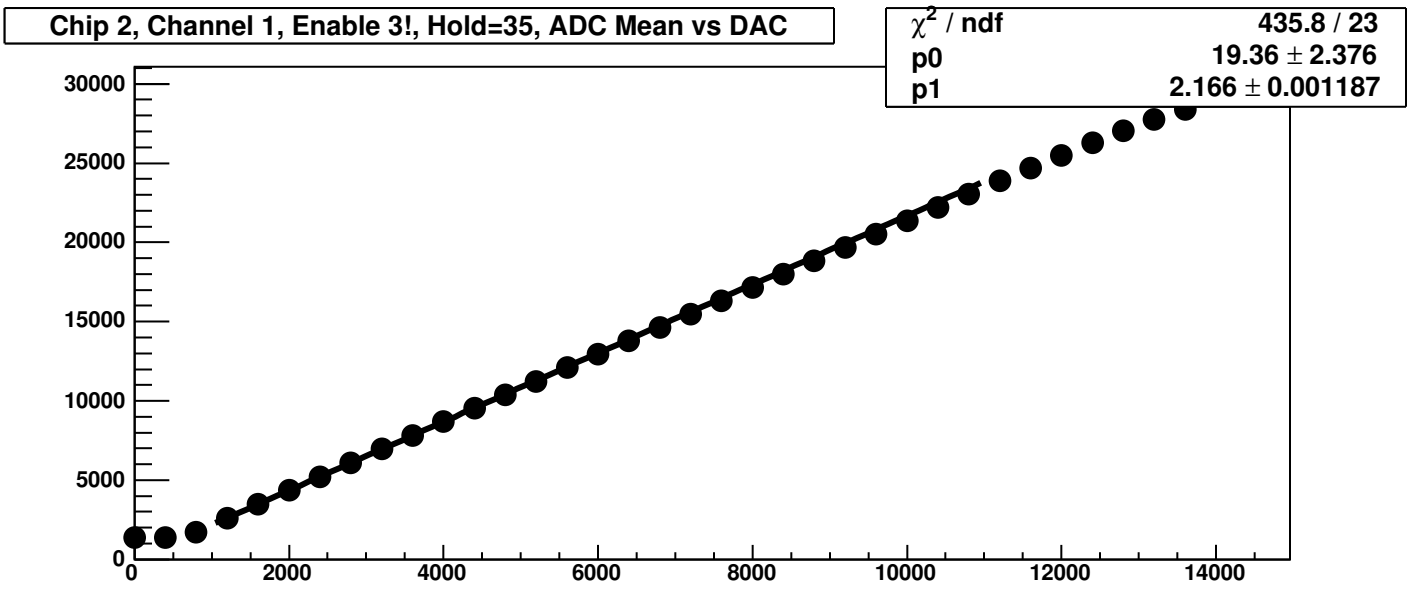
Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

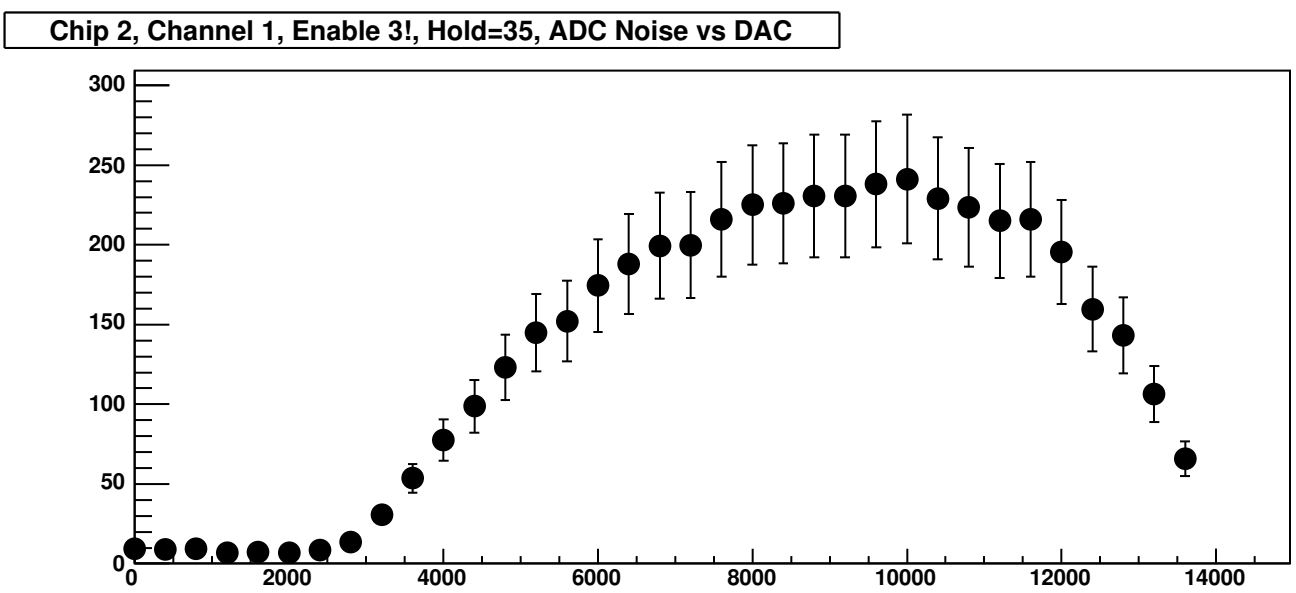


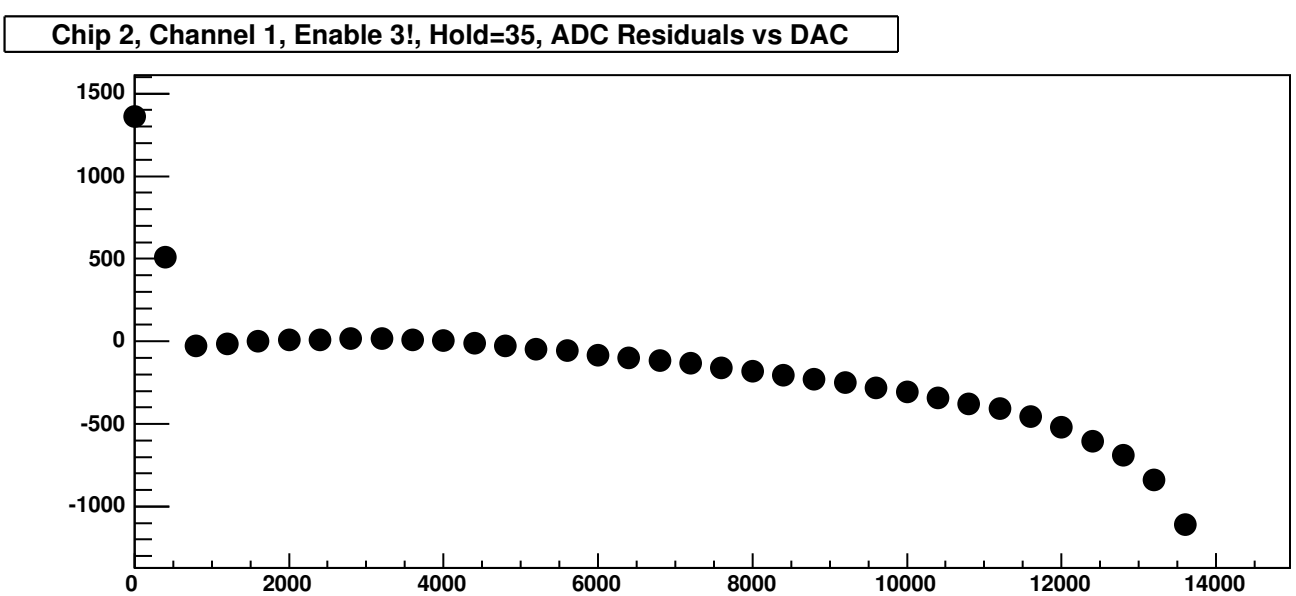

Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.79 / 23
p0 -23.24 ± 0.7271
p1 0.01249 ± 0.0001084
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

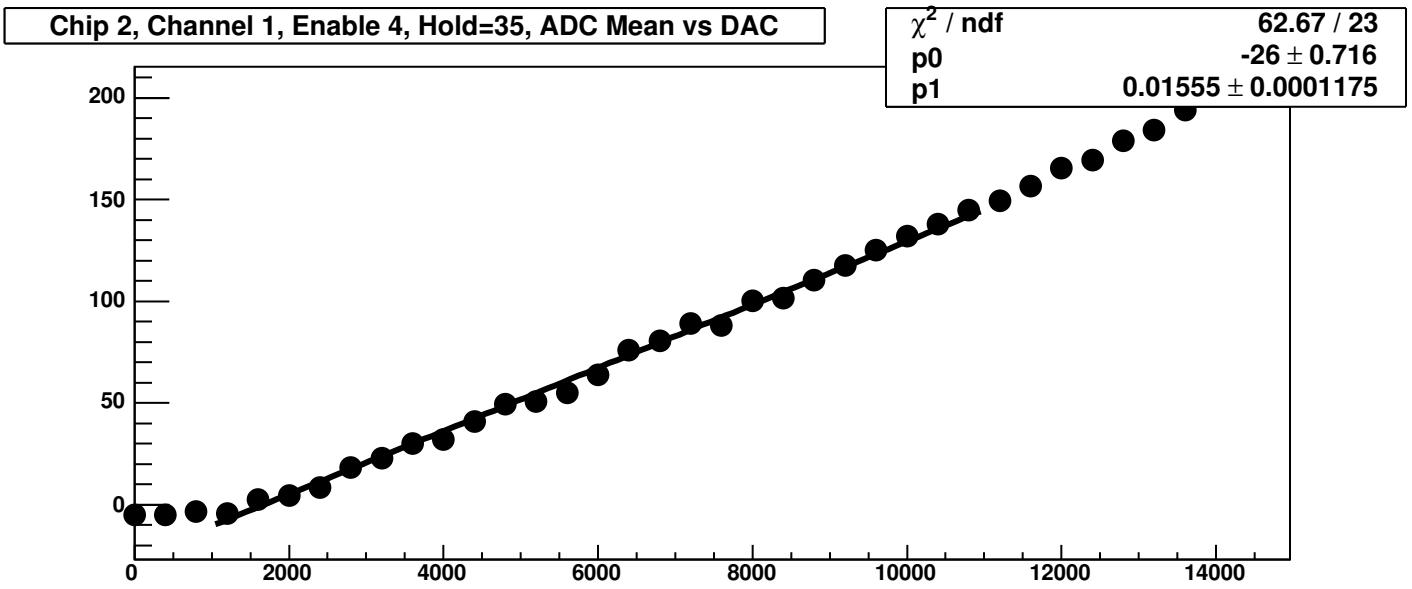

Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

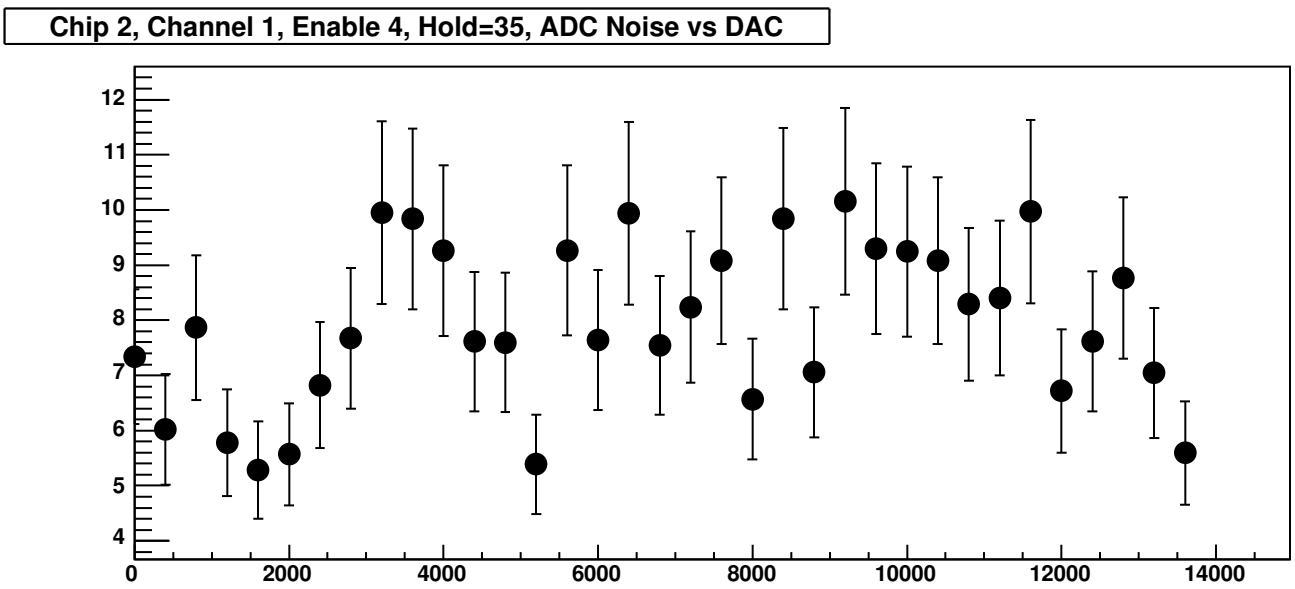

Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

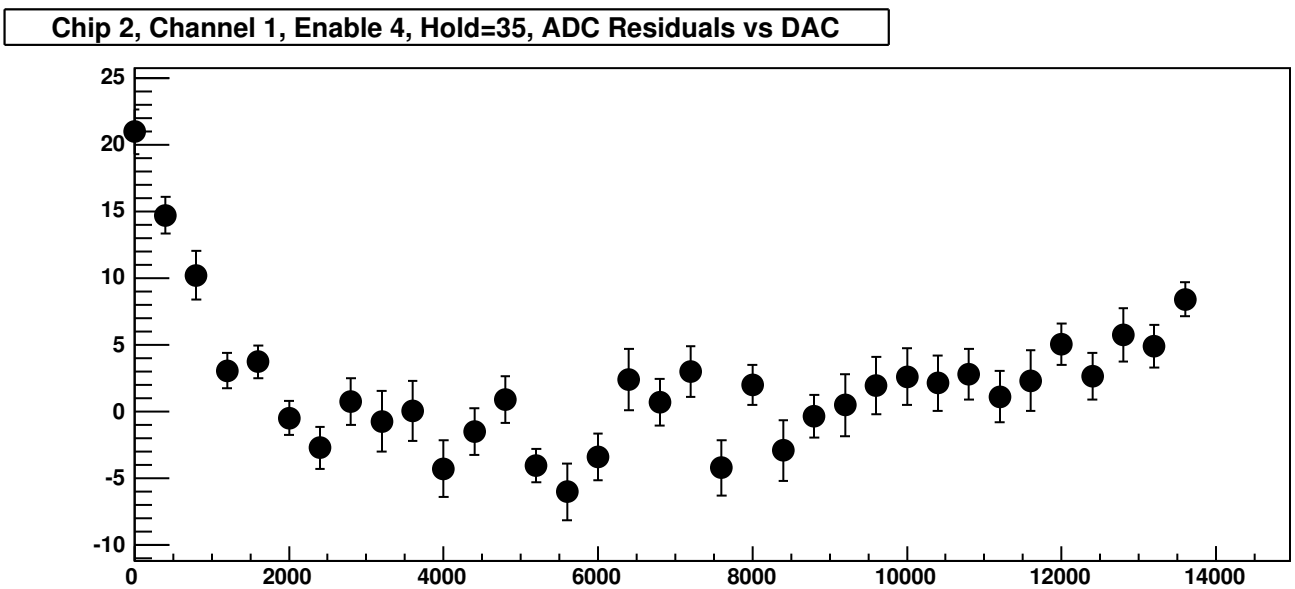

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 435.8 / 23
p0 19.36 ± 2.376
p1 2.166 ± 0.001187
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

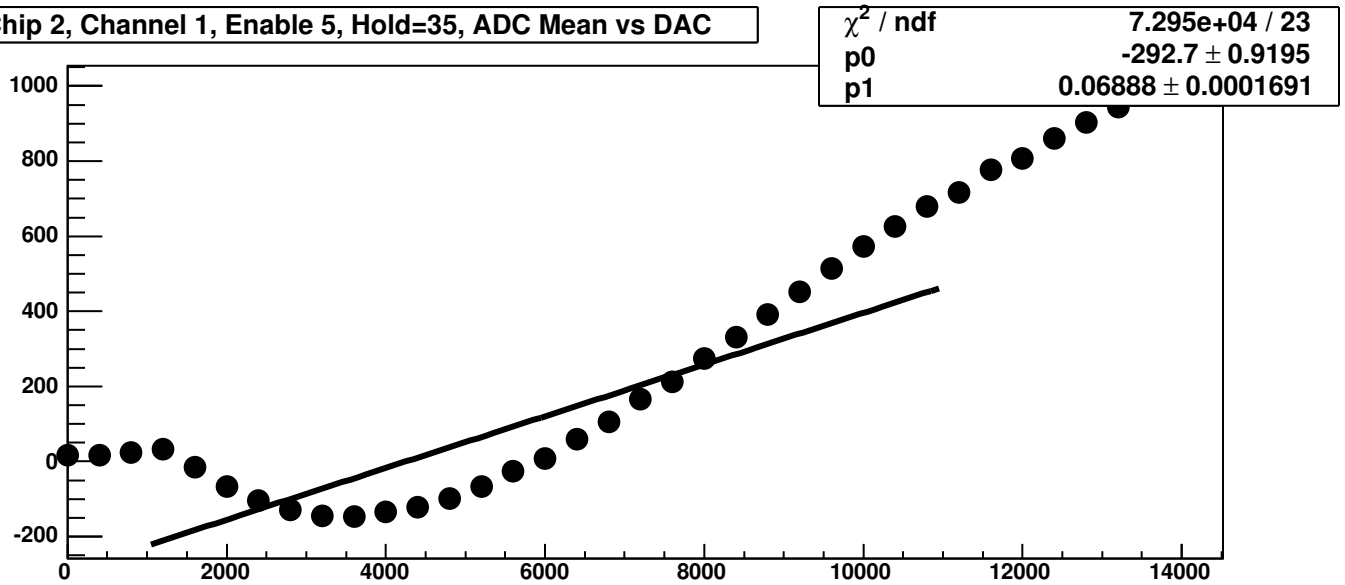

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 62.67 / 23
p0 -26 ± 0.716
p1 0.01555 ± 0.0001175
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

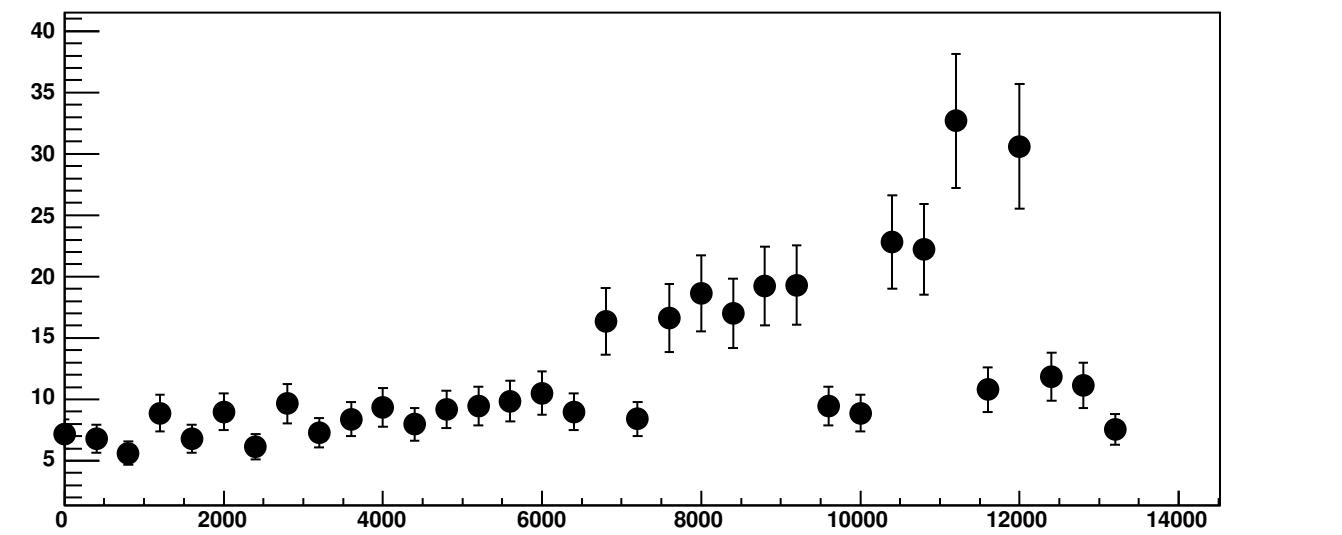

Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

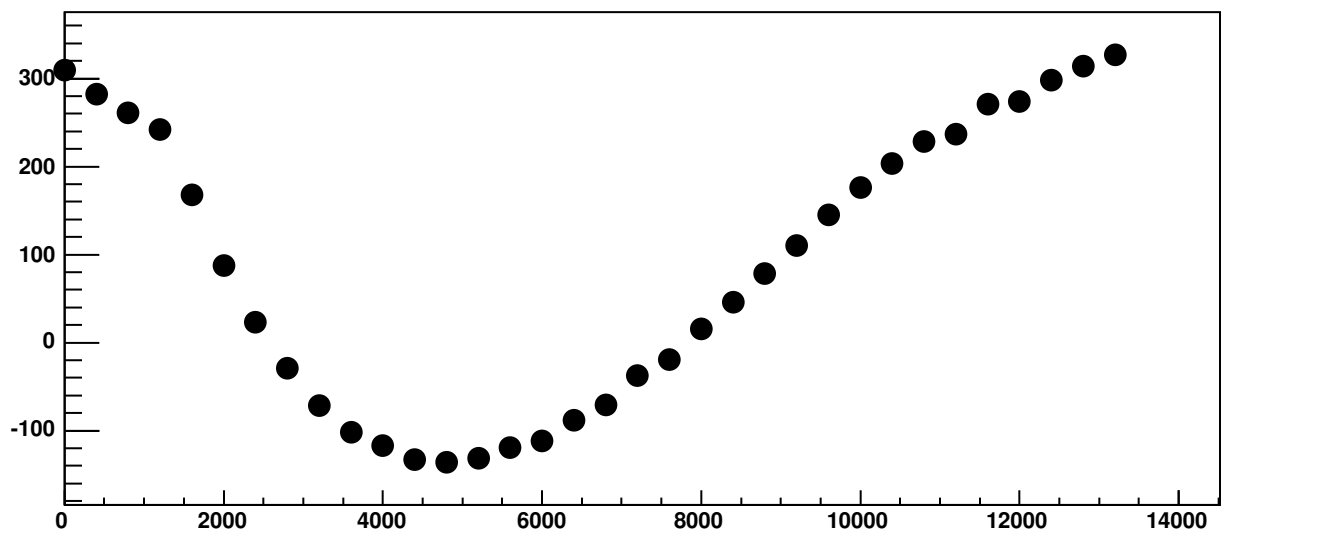
Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

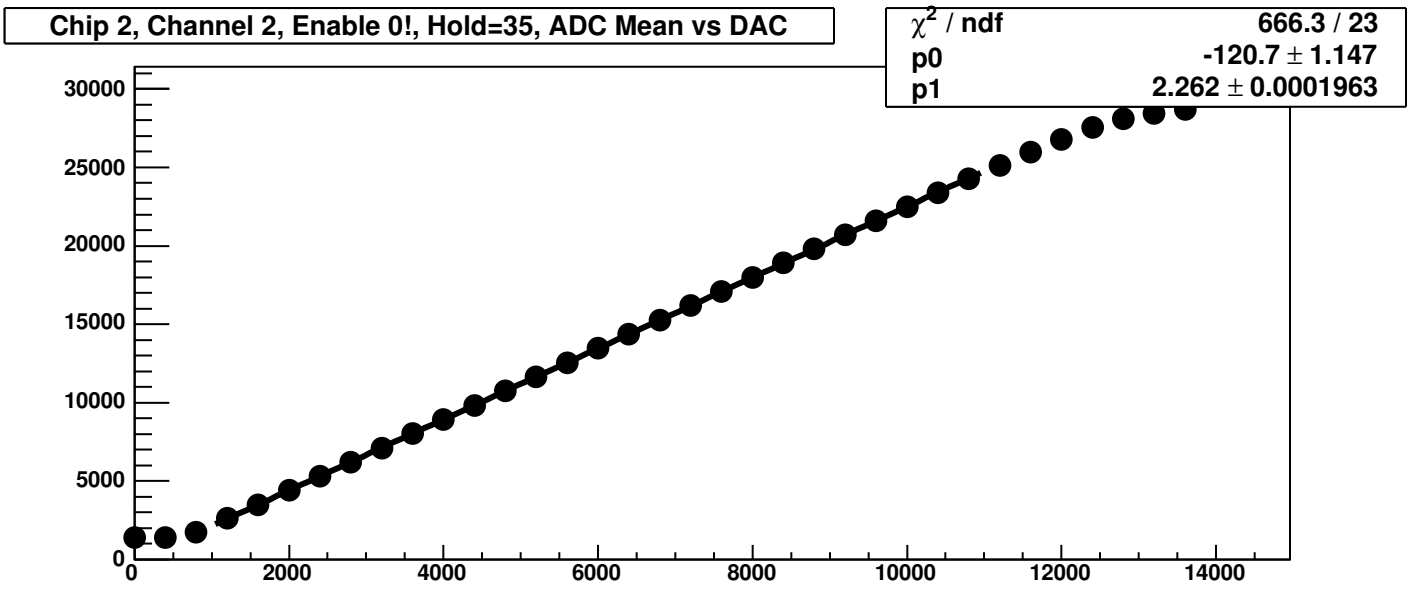


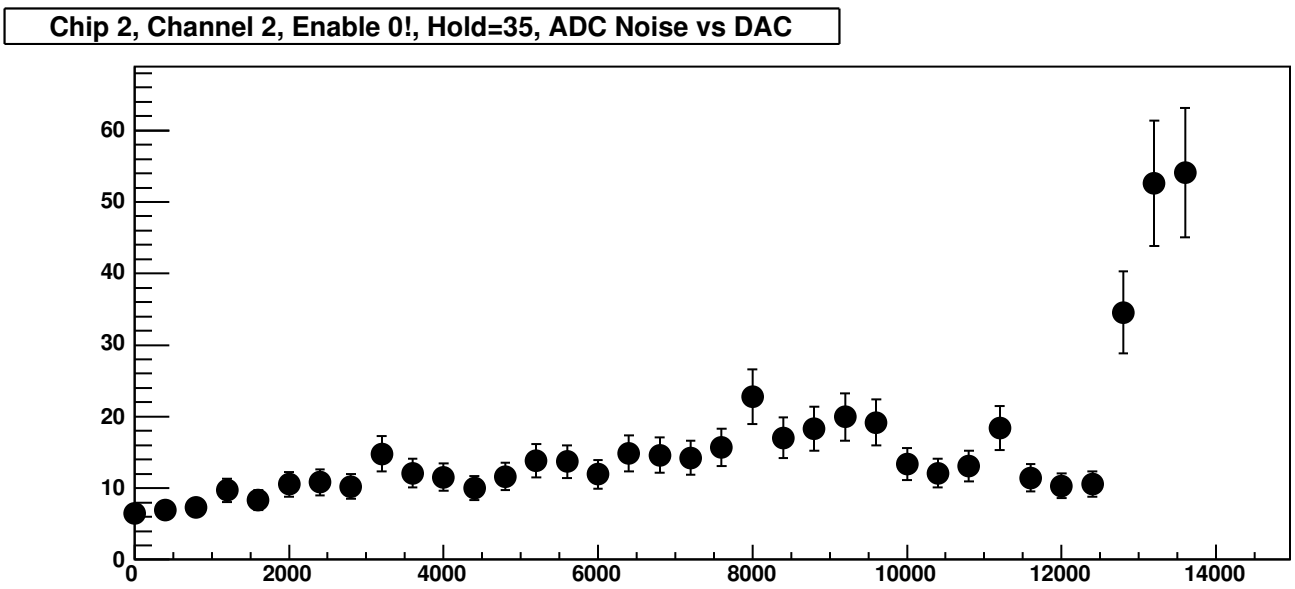
Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

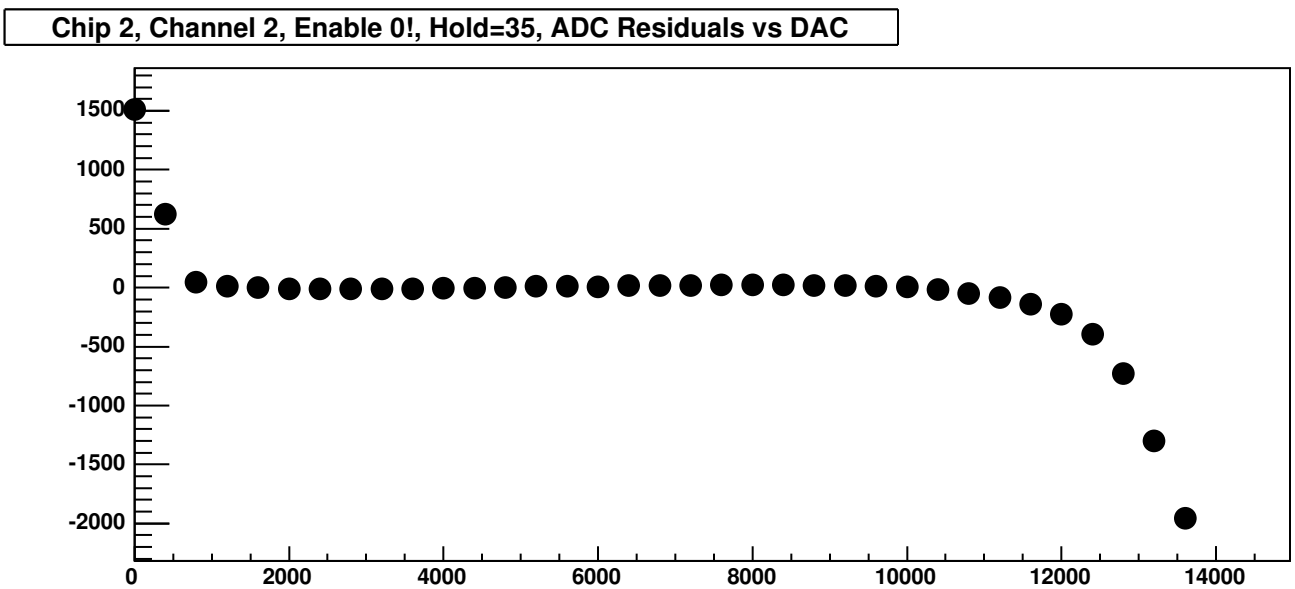


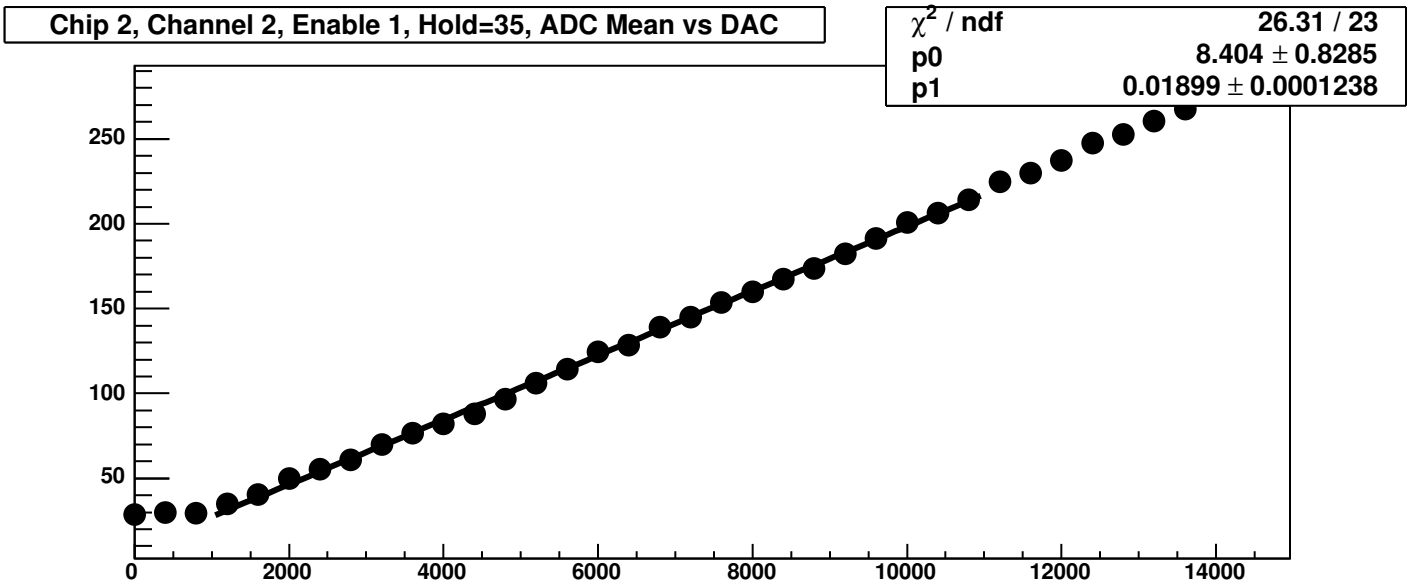
Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

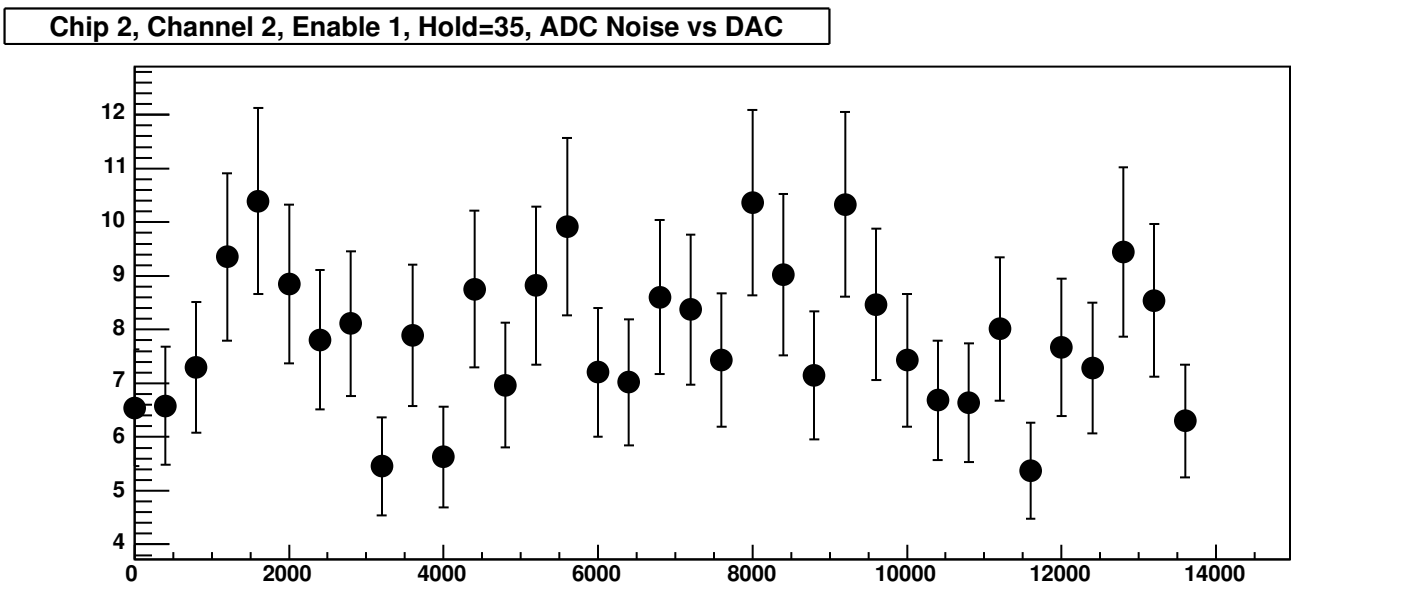


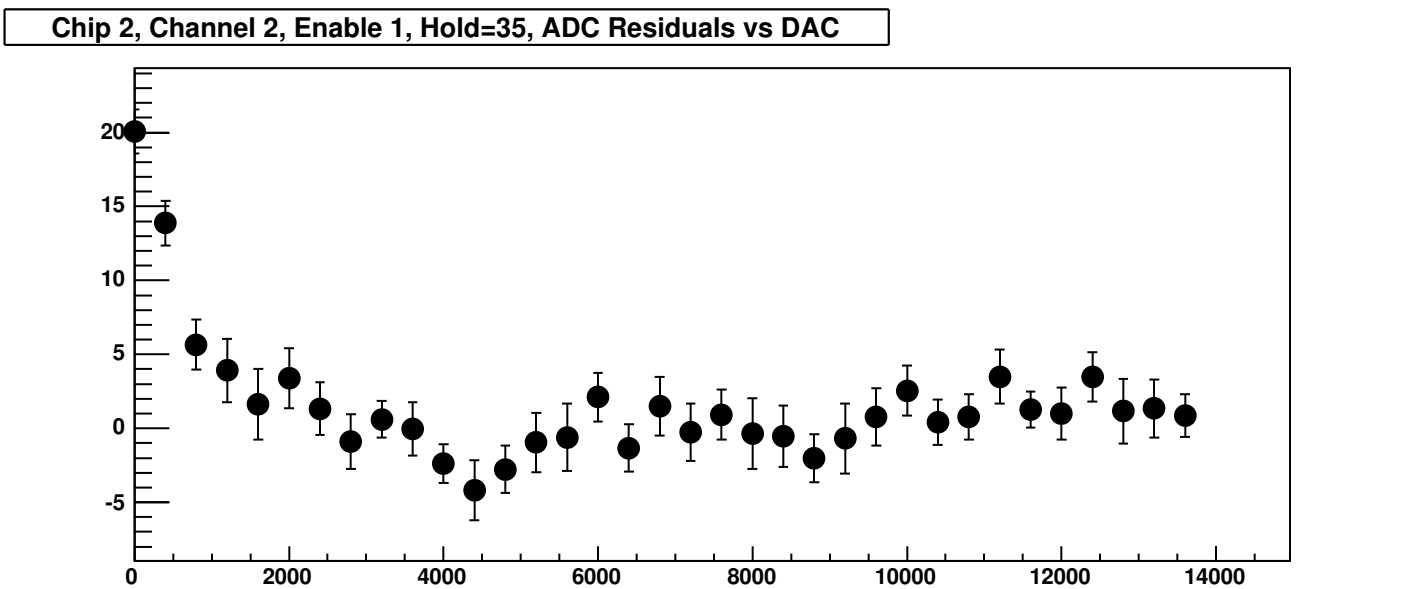

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 666.3 / 23
p0 -120.7 ± 1.147
p1 2.262 ± 0.0001963
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

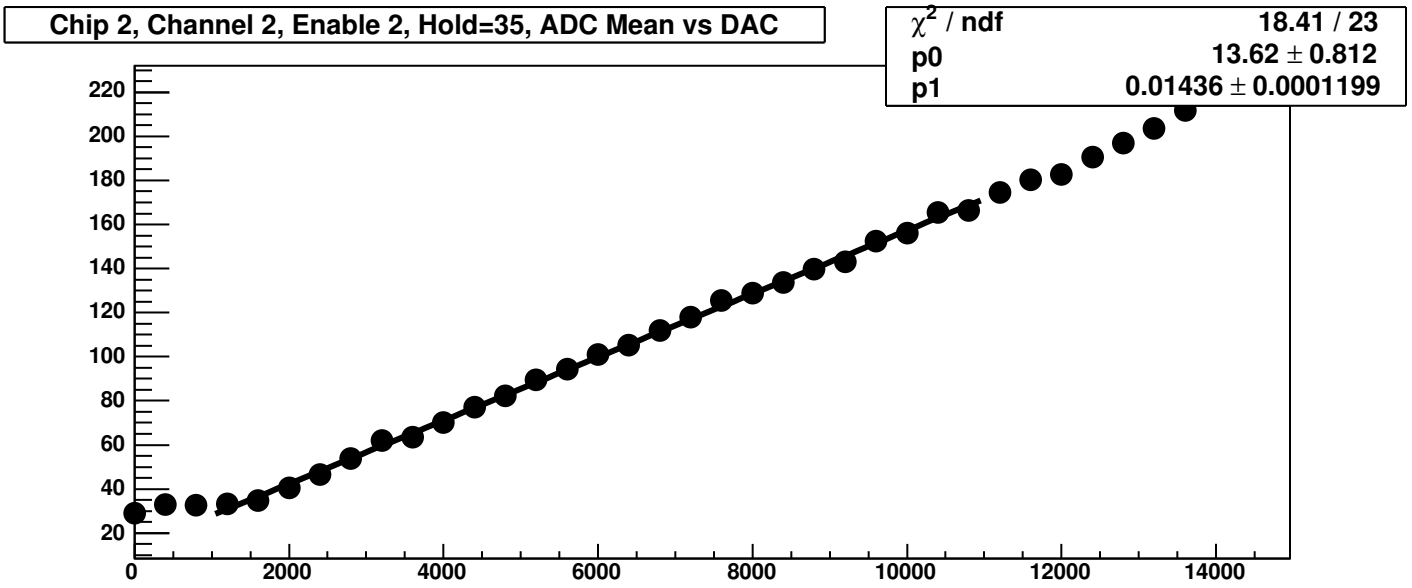

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

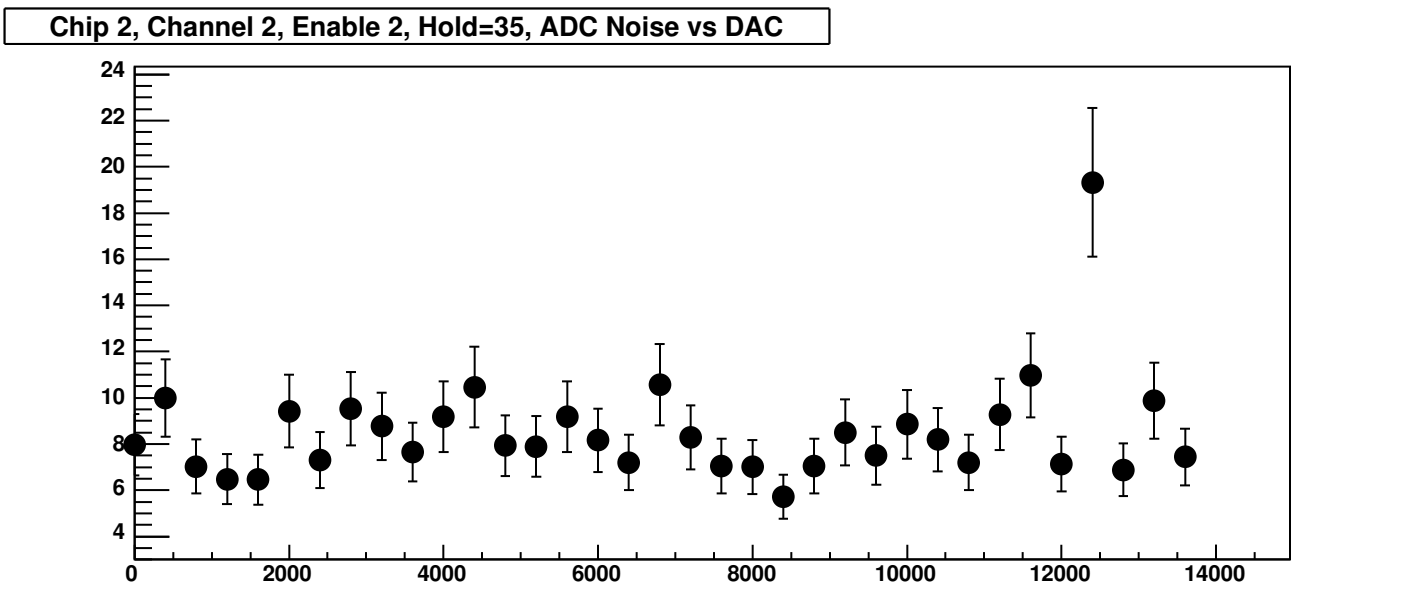

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

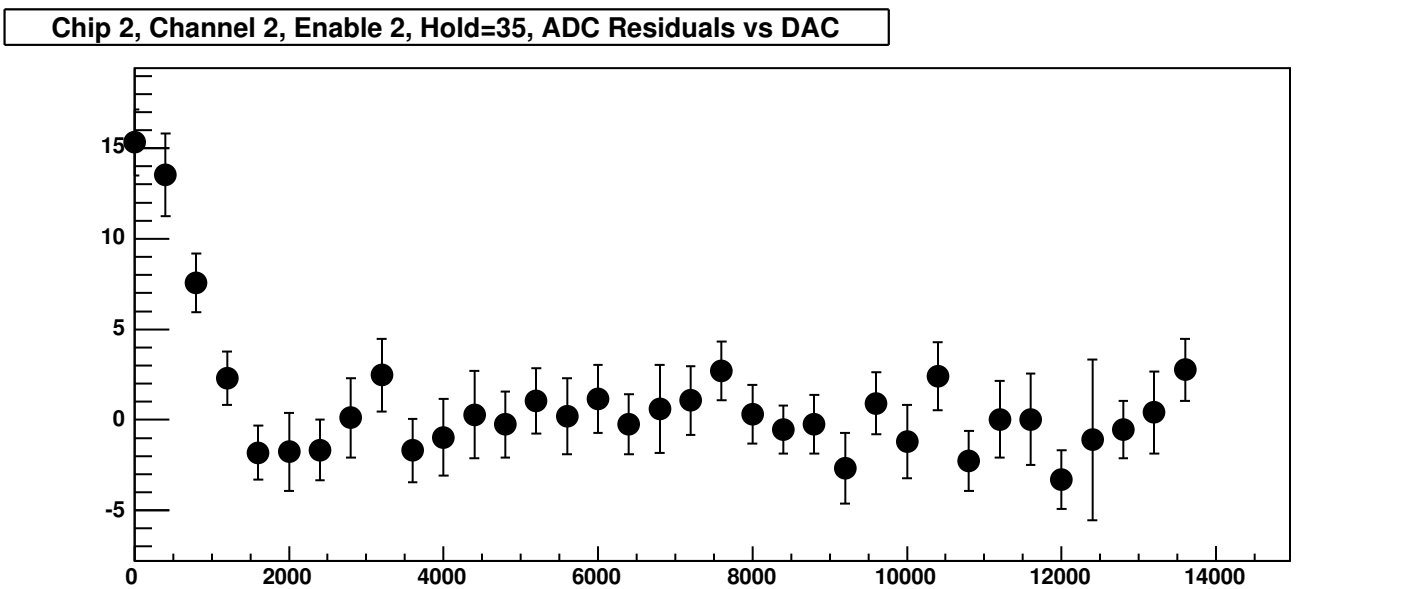

Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.31 / 23
p0 8.404 ± 0.8285
p1 0.01899 ± 0.0001238
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

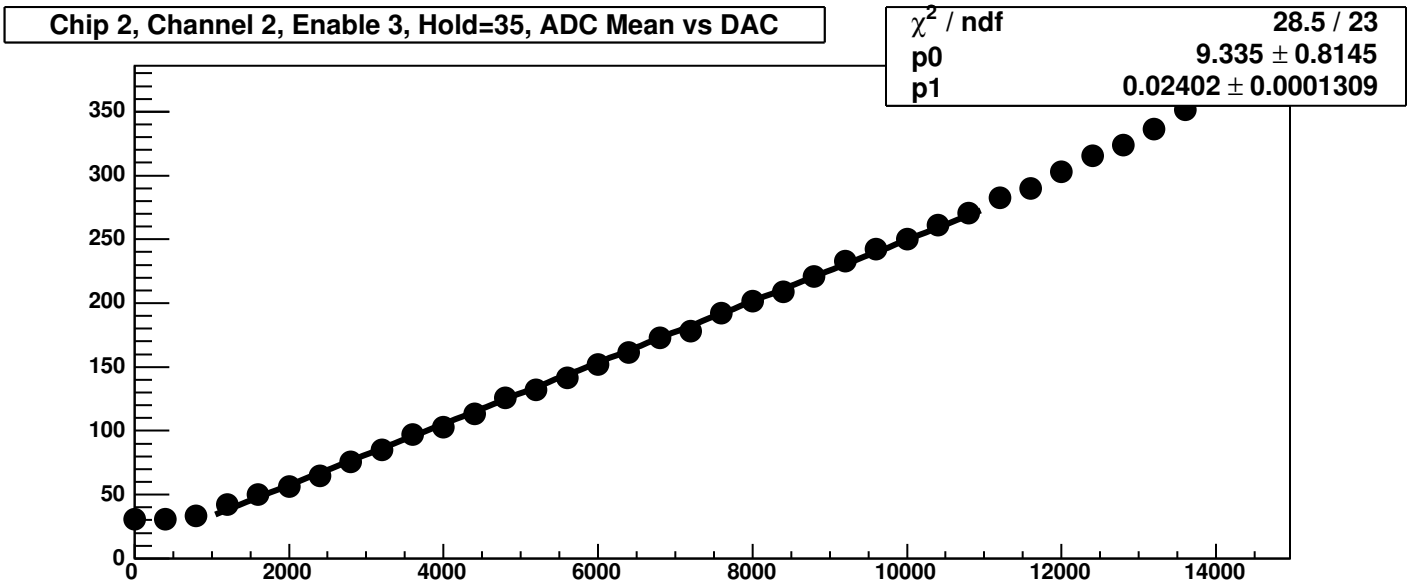

Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

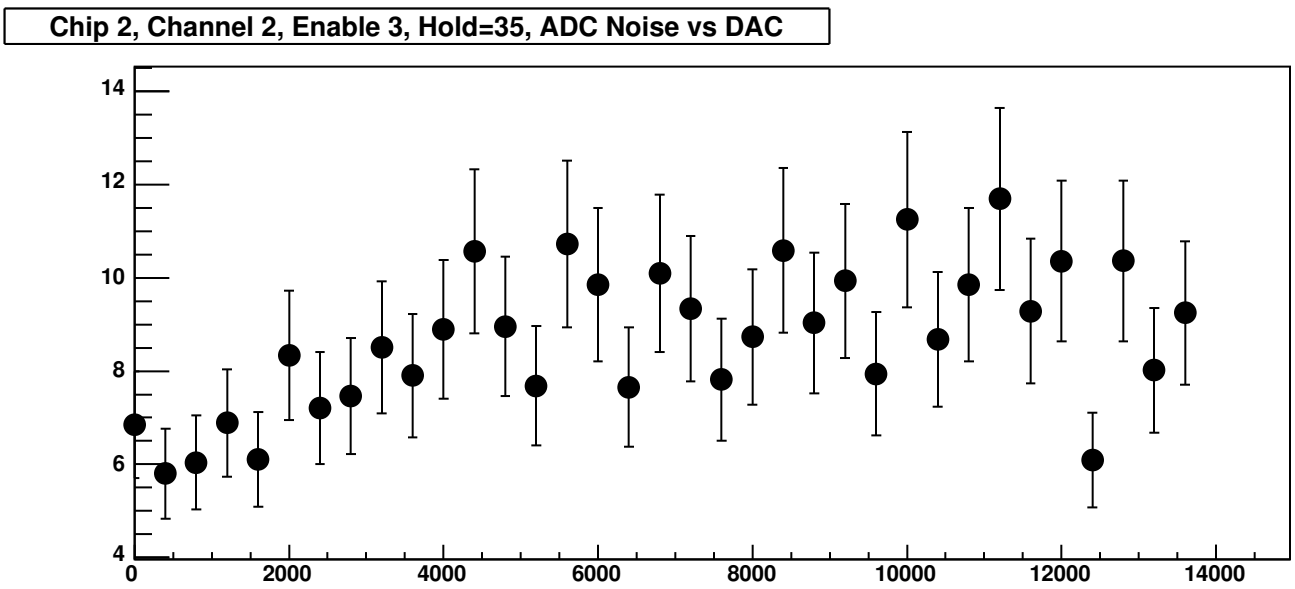

Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

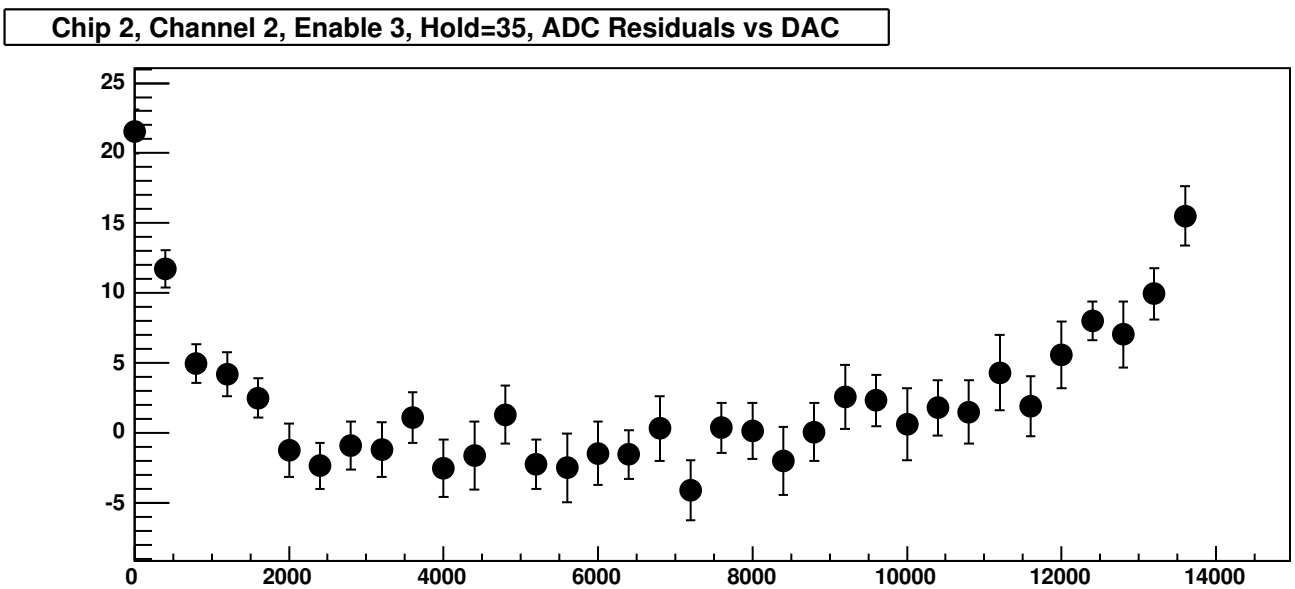

Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.41 / 23
p0 13.62 ± 0.812
p1 0.01436 ± 0.0001199
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

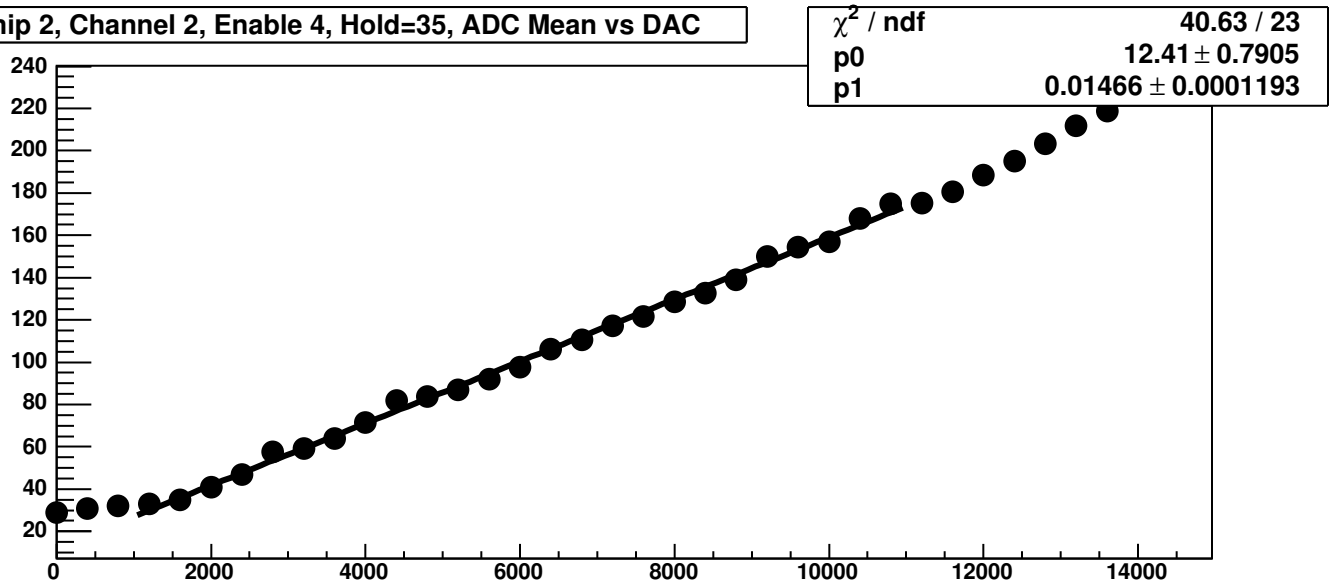

Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.5 / 23
p0 9.335 ± 0.8145
p1 0.02402 ± 0.0001309
0 50 100 150 200 250 300 350
0 2000 4000 6000 8000 10000 12000 14000

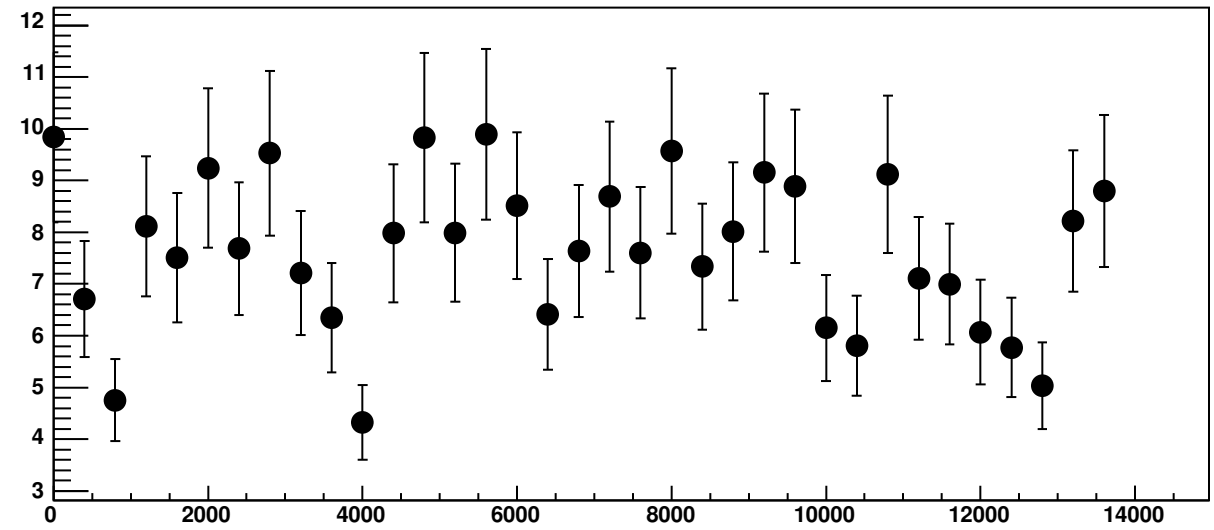

Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
4 6 8 10 12 14
0 2000 4000 6000 8000 10000 12000 14000


Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
-5 0 5 10 15 20 25
0 2000 4000 6000 8000 10000 12000 14000

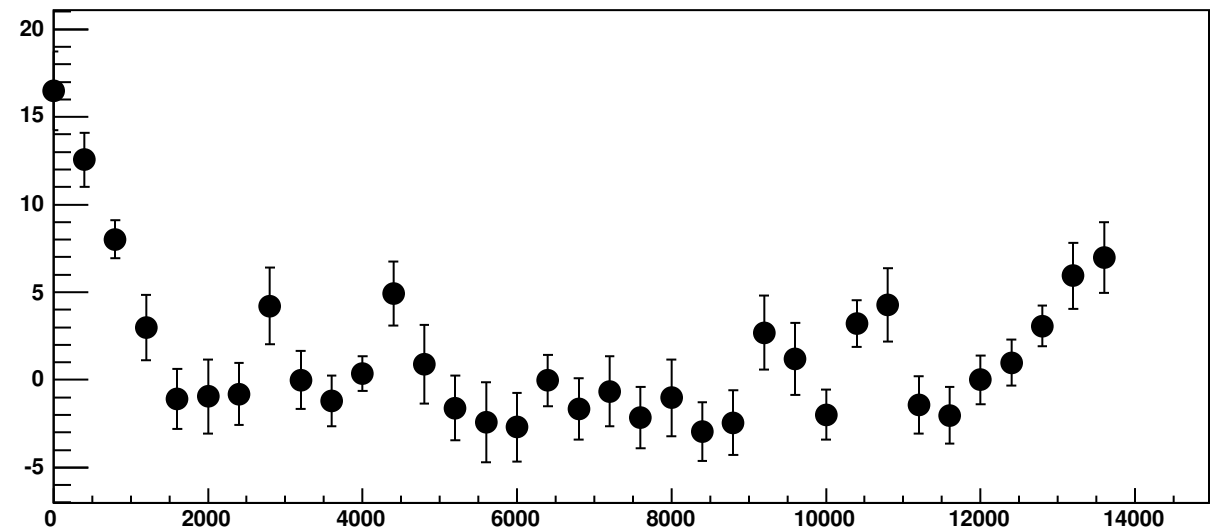
Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

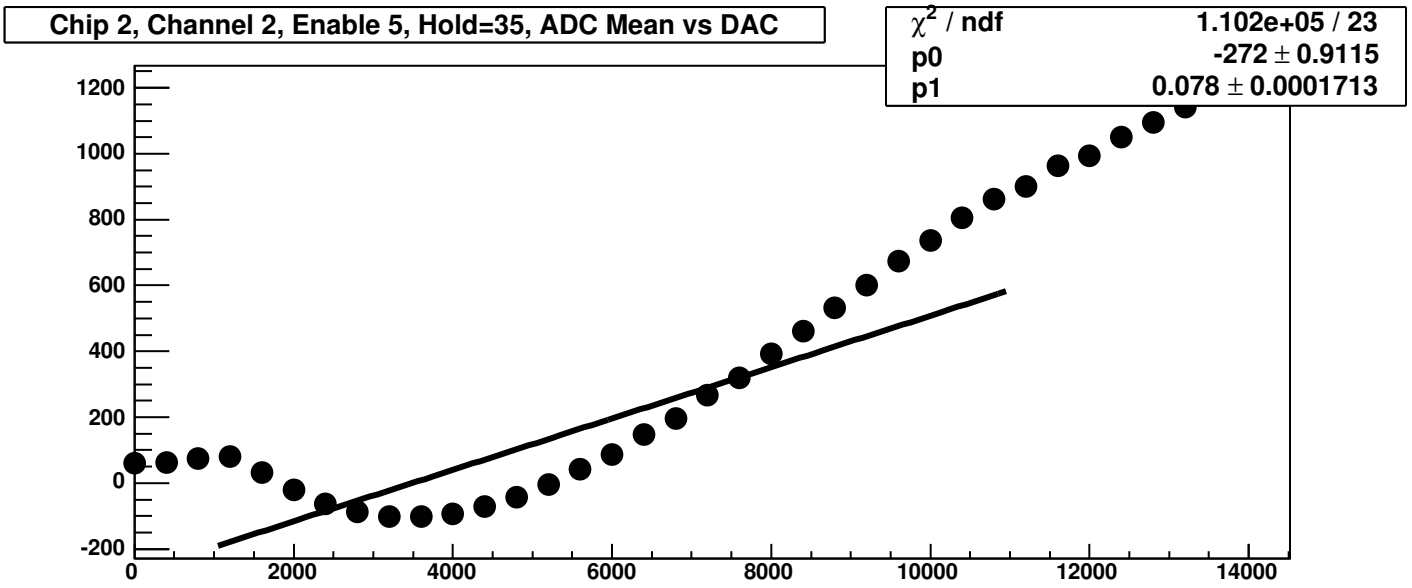


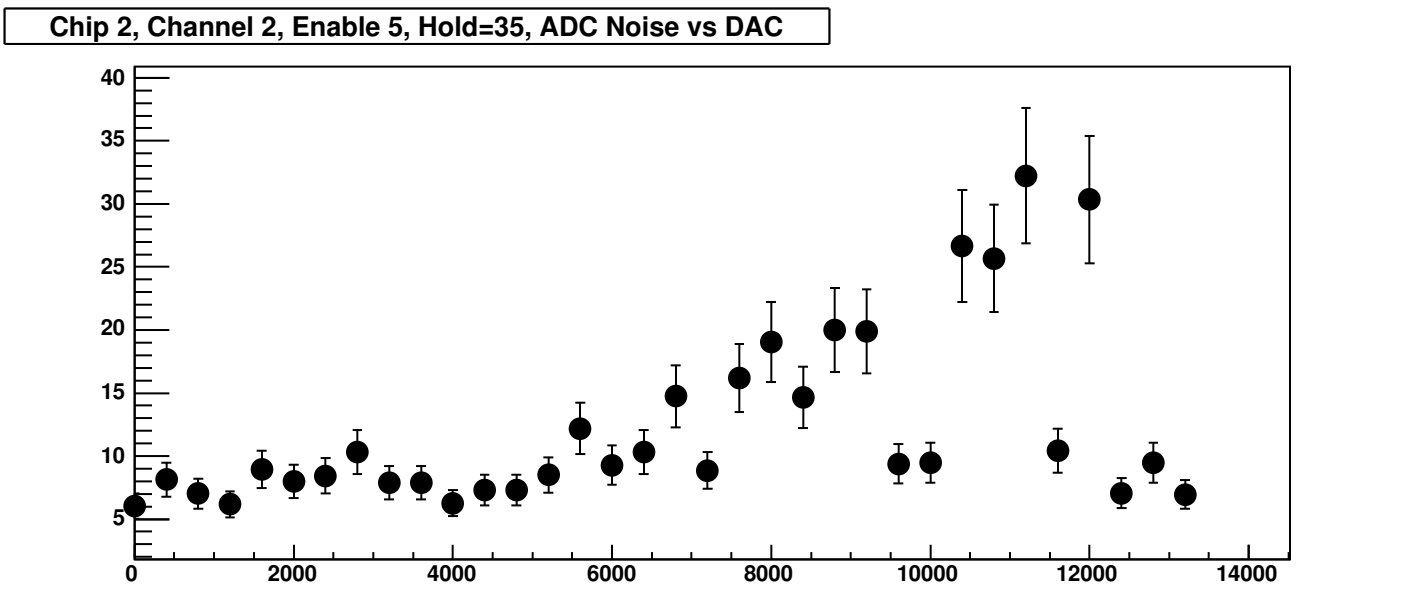
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

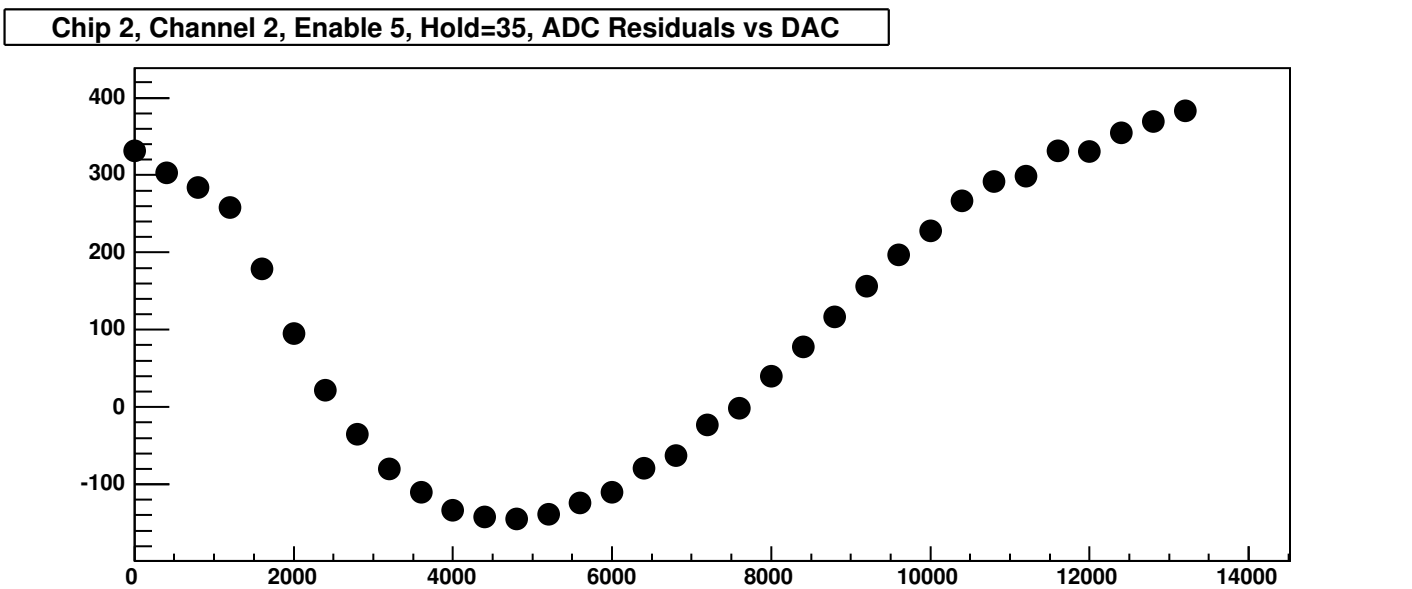


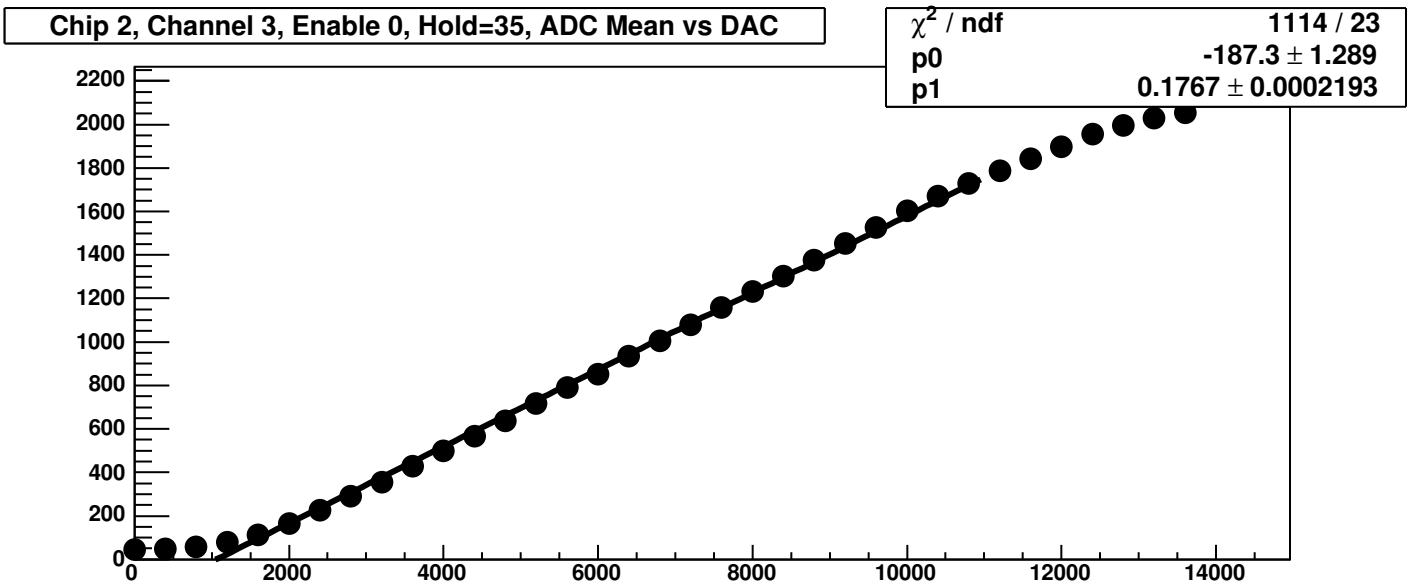
Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

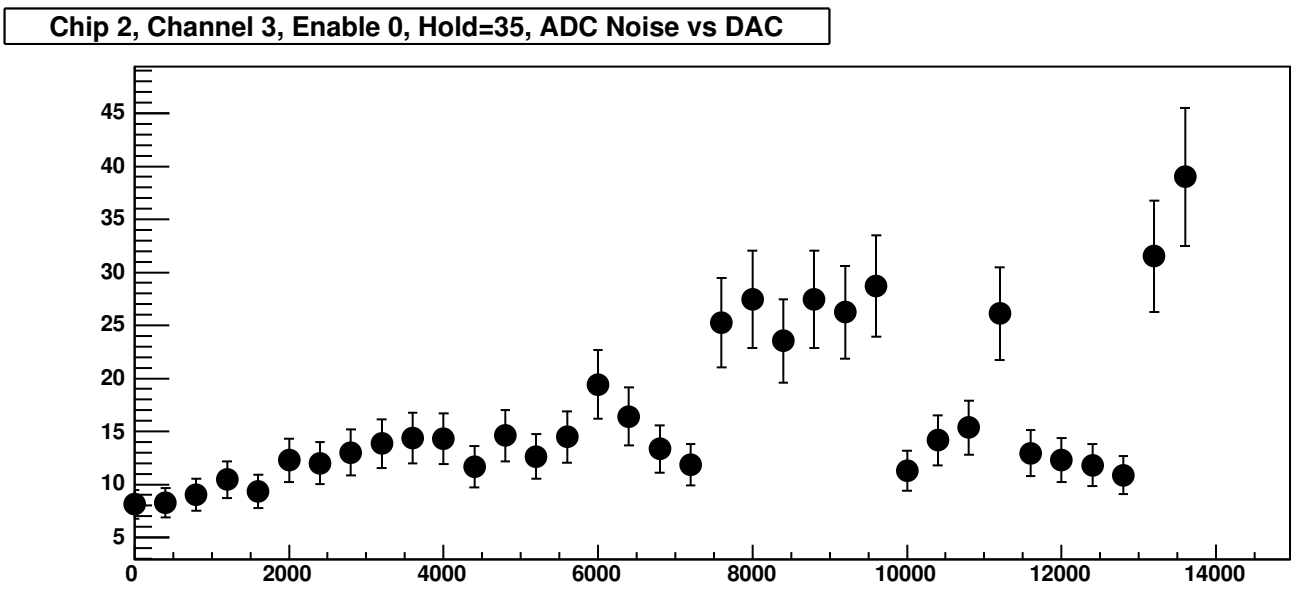


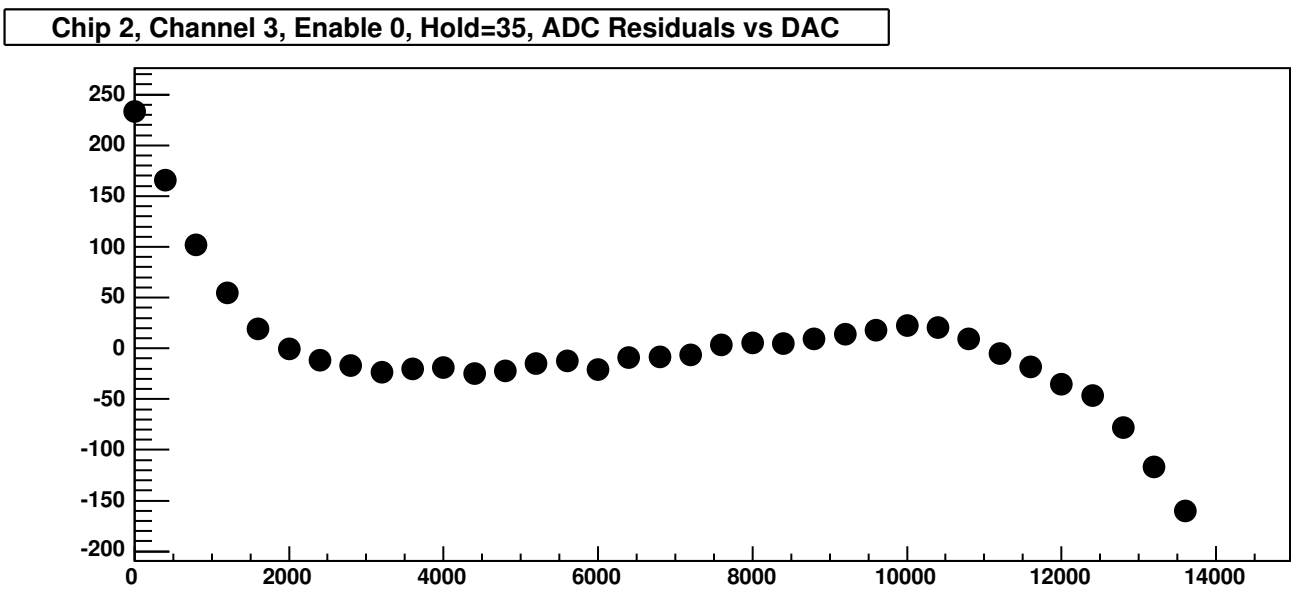

Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.102e+05 / 23
p0 -272 ± 0.9115
p1 0.078 ± 0.0001713
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

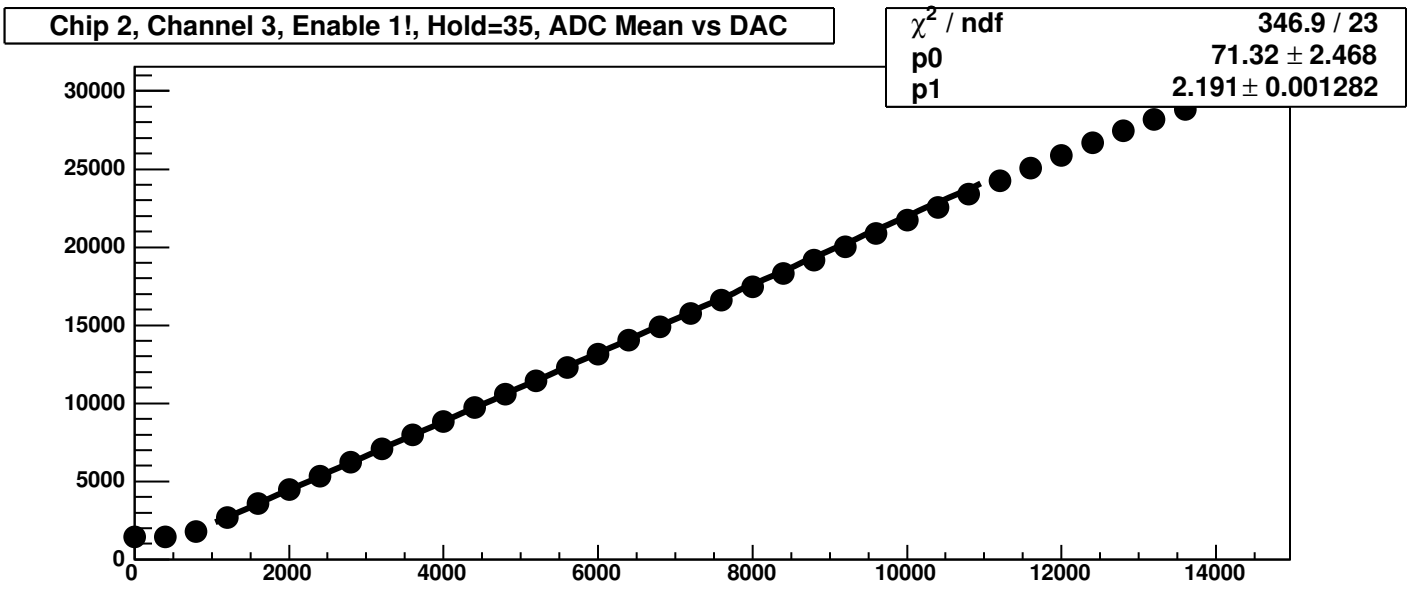

Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

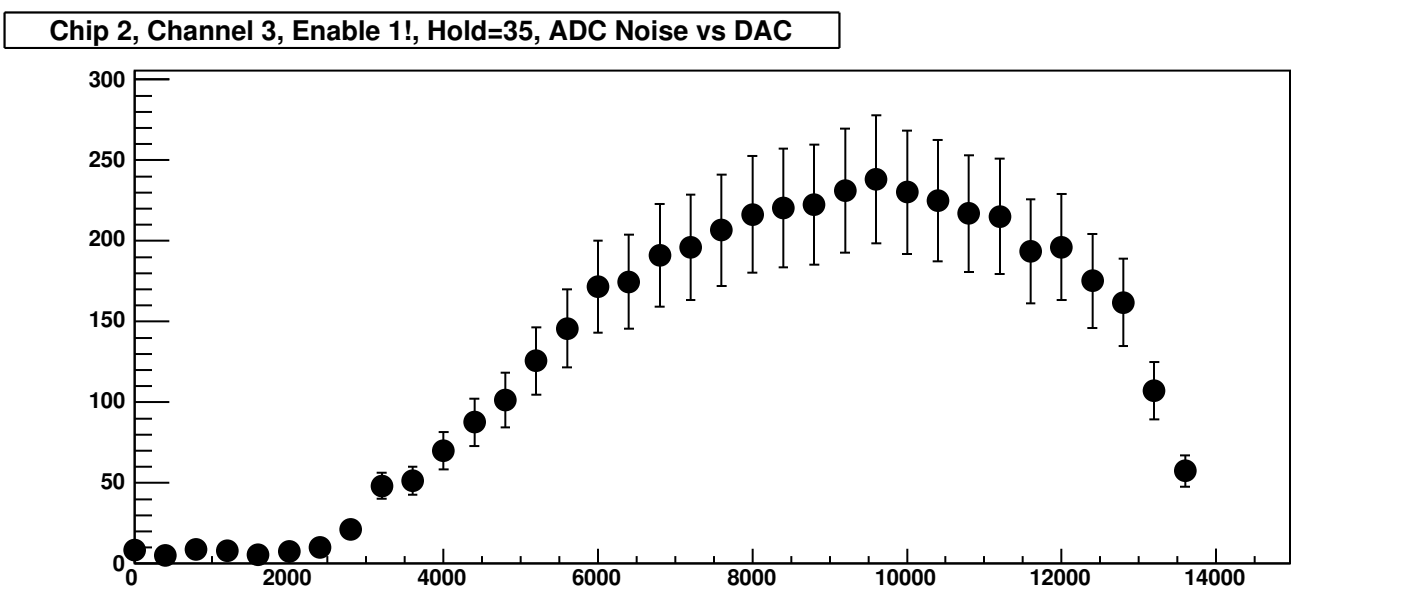

Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

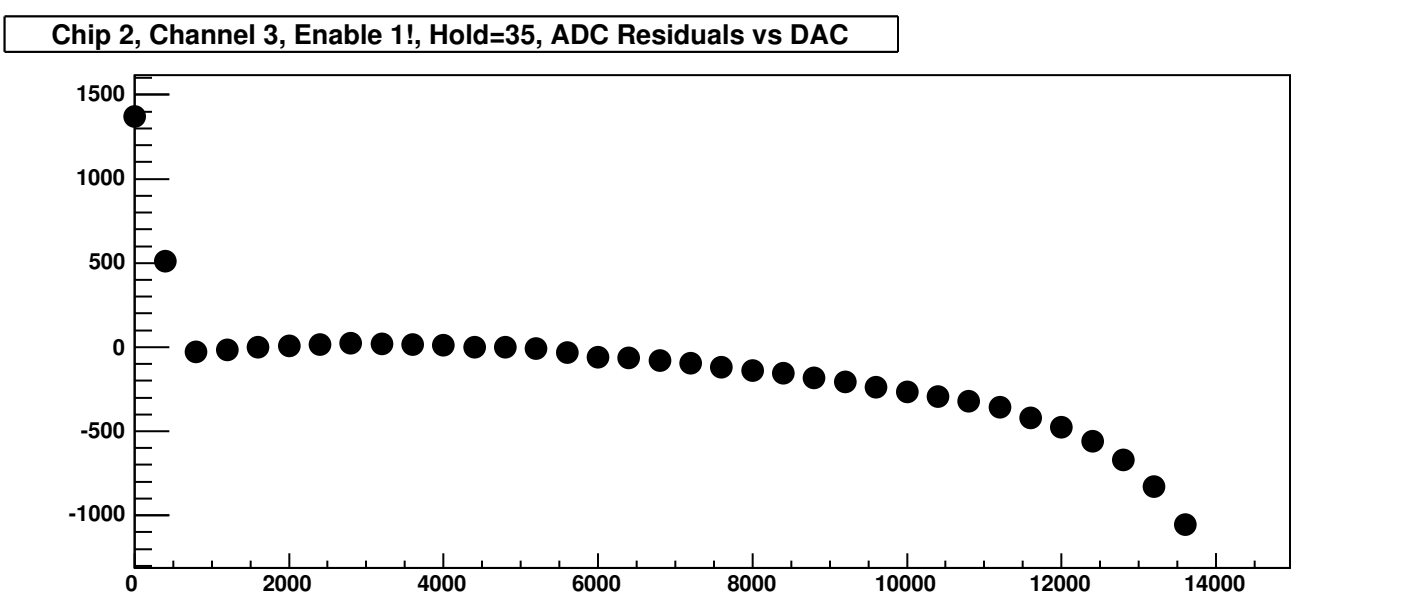

Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1114 / 23
p0 -187.3 ± 1.289
p1 0.1767 ± 0.0002193
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

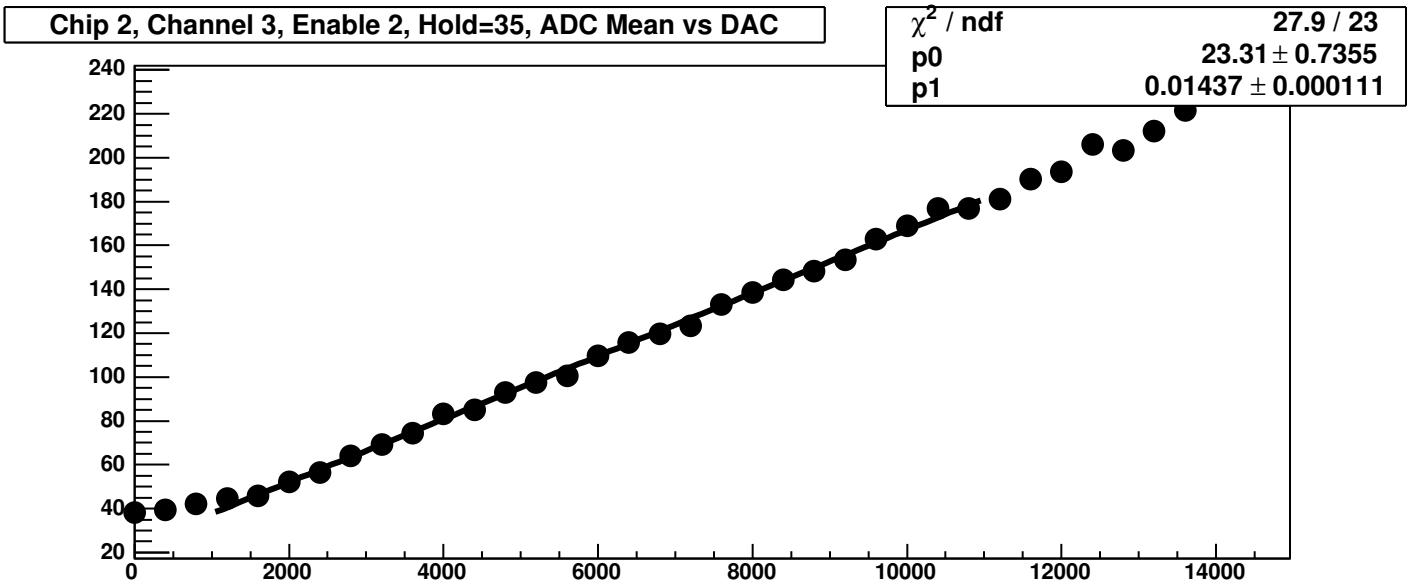

Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

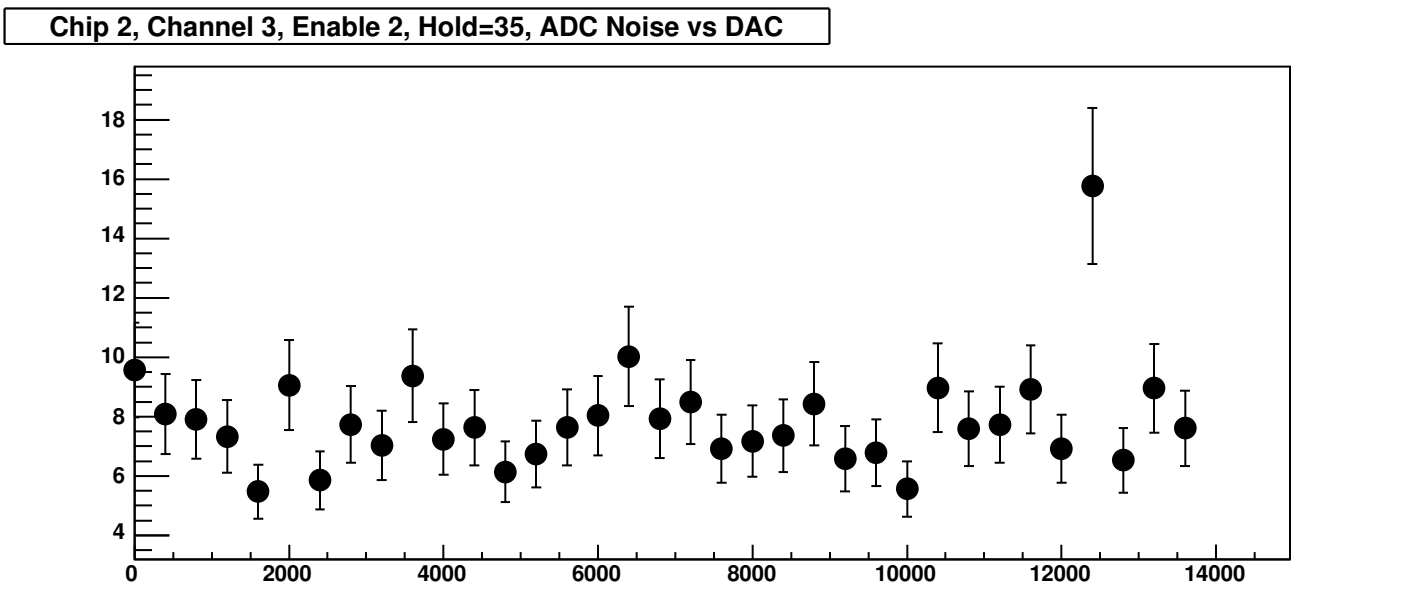

Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

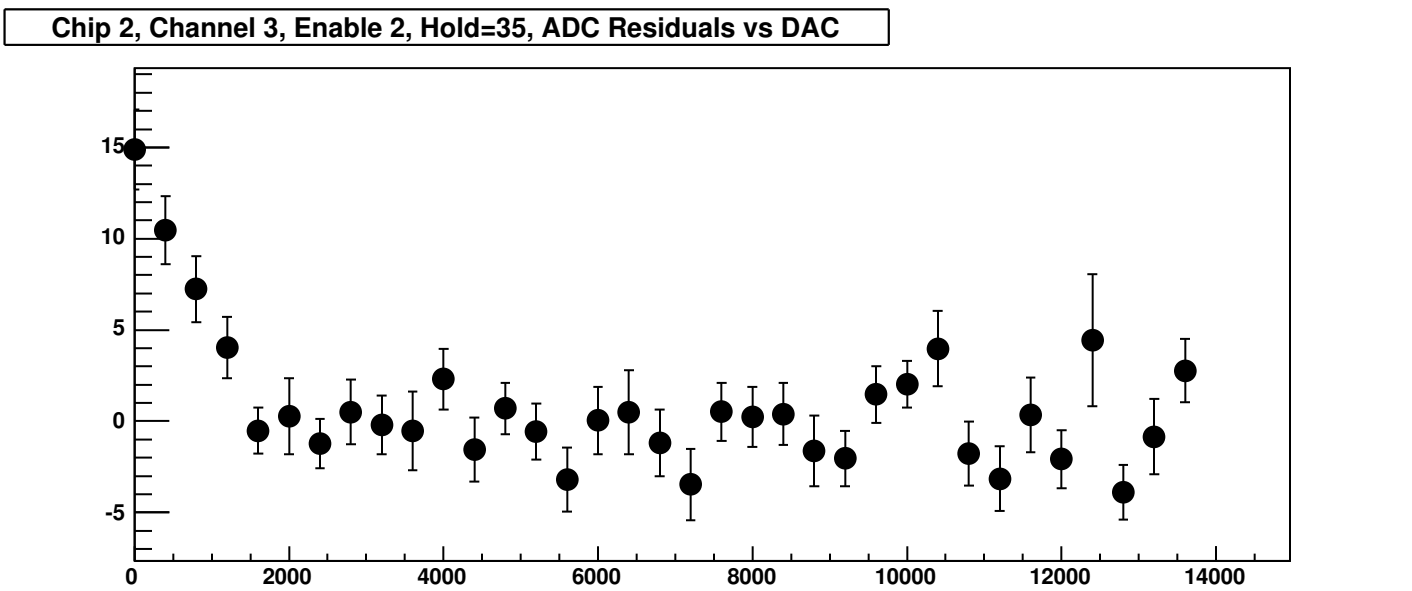

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 346.9 / 23
p0 71.32 ± 2.468
p1 2.191 ± 0.001282
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

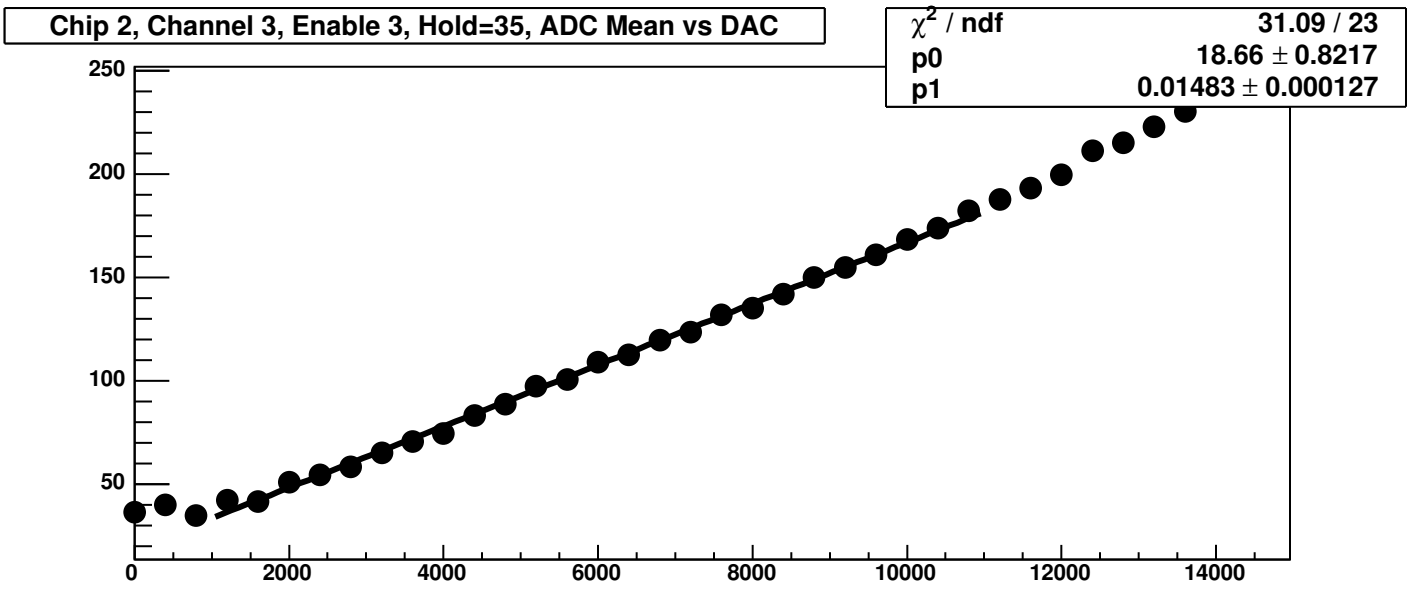

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

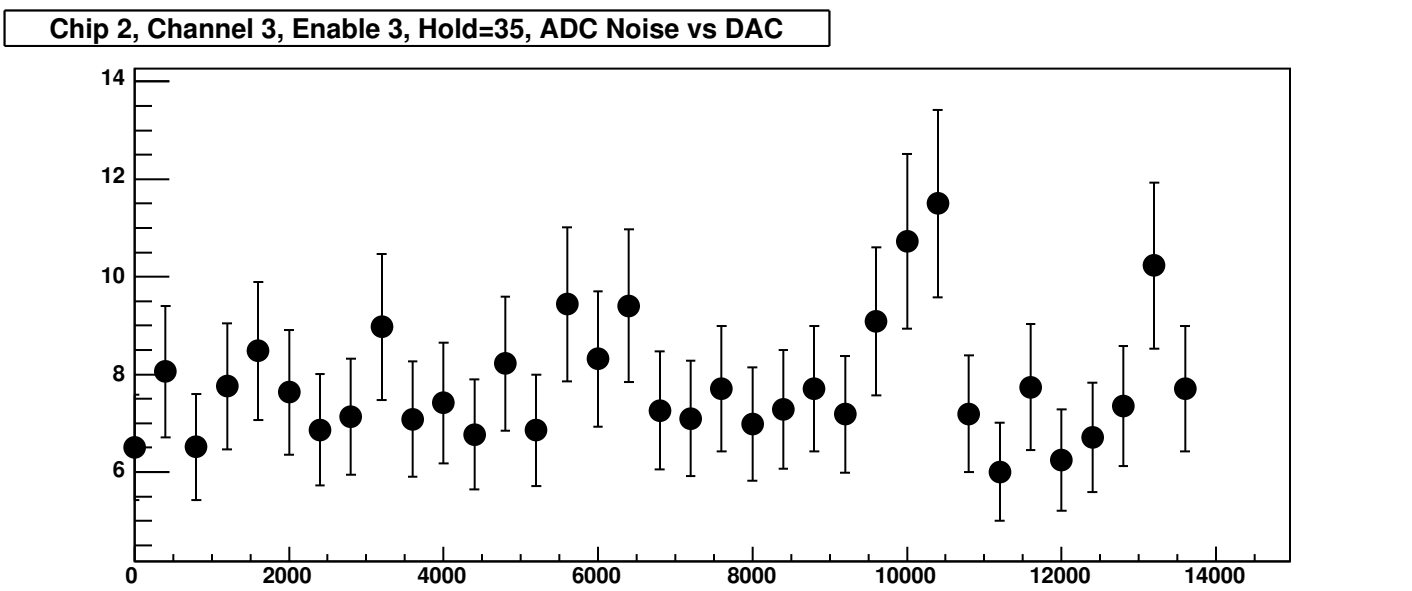

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

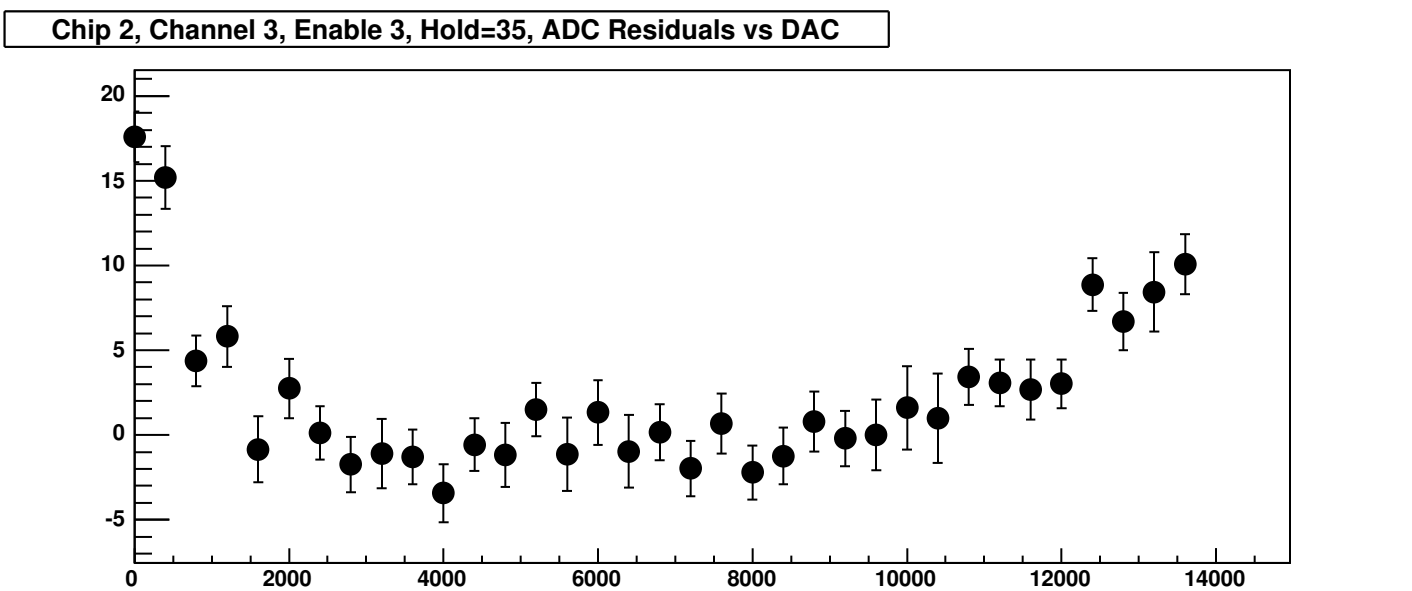

Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.9 / 23
p0 23.31 ± 0.7355
p1 0.01437 ± 0.000111
240
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

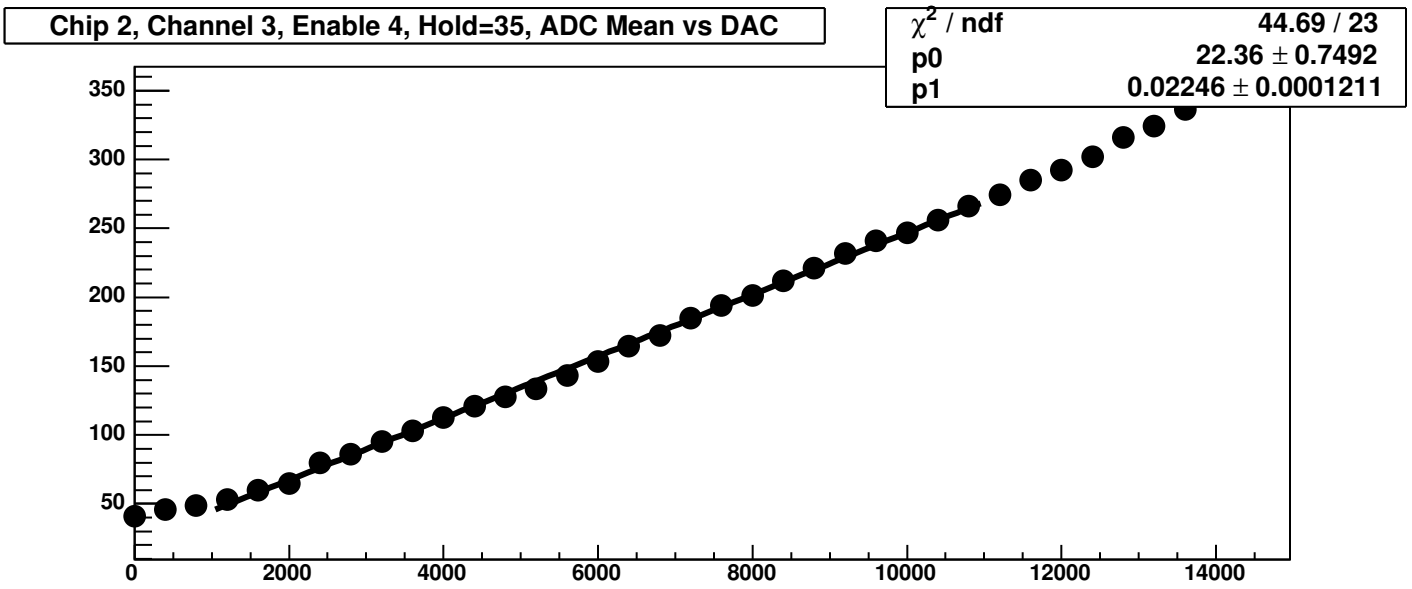

Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

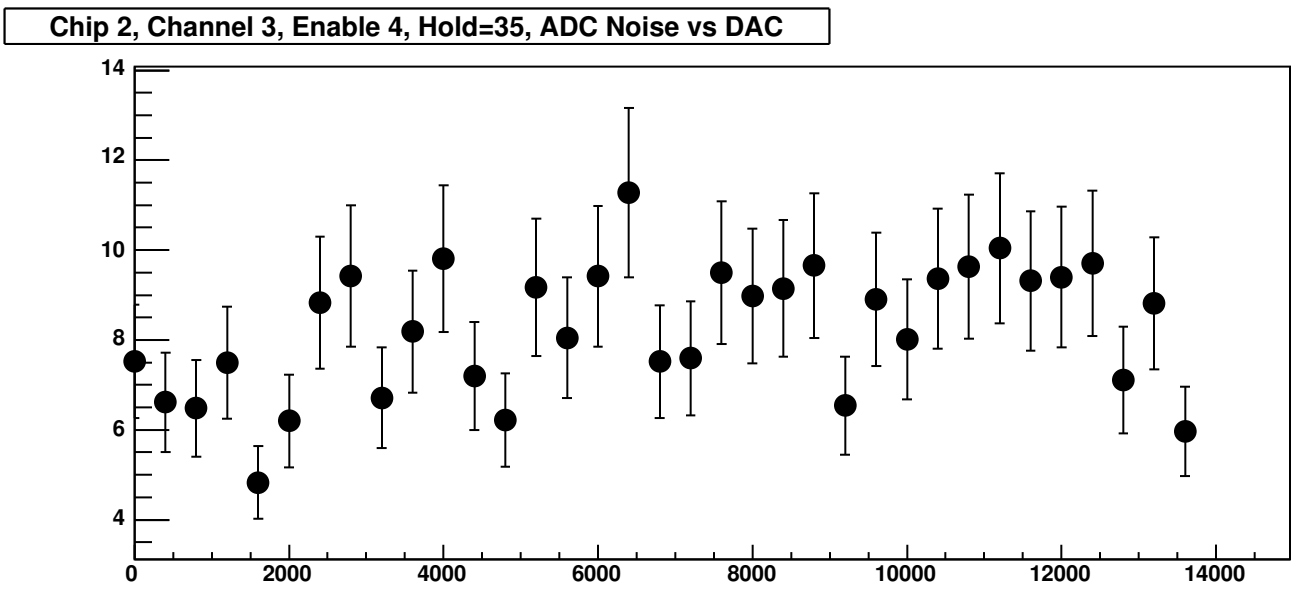

Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

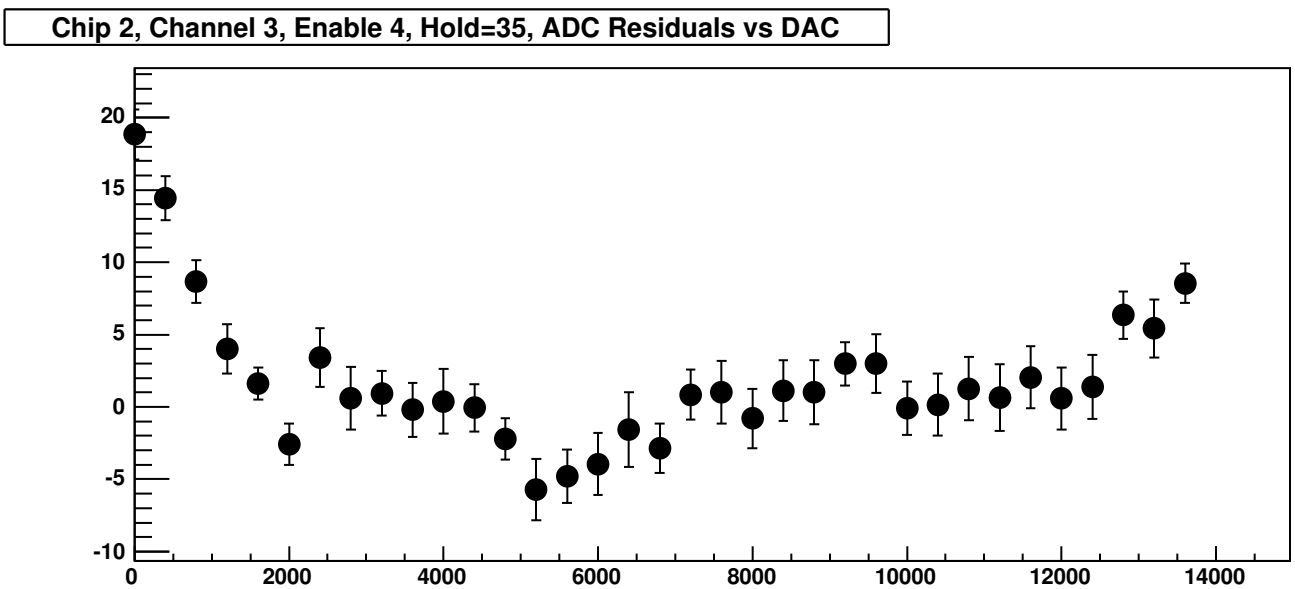

Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.09 / 23
p0 18.66 ± 0.8217
p1 0.01483 ± 0.000127
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

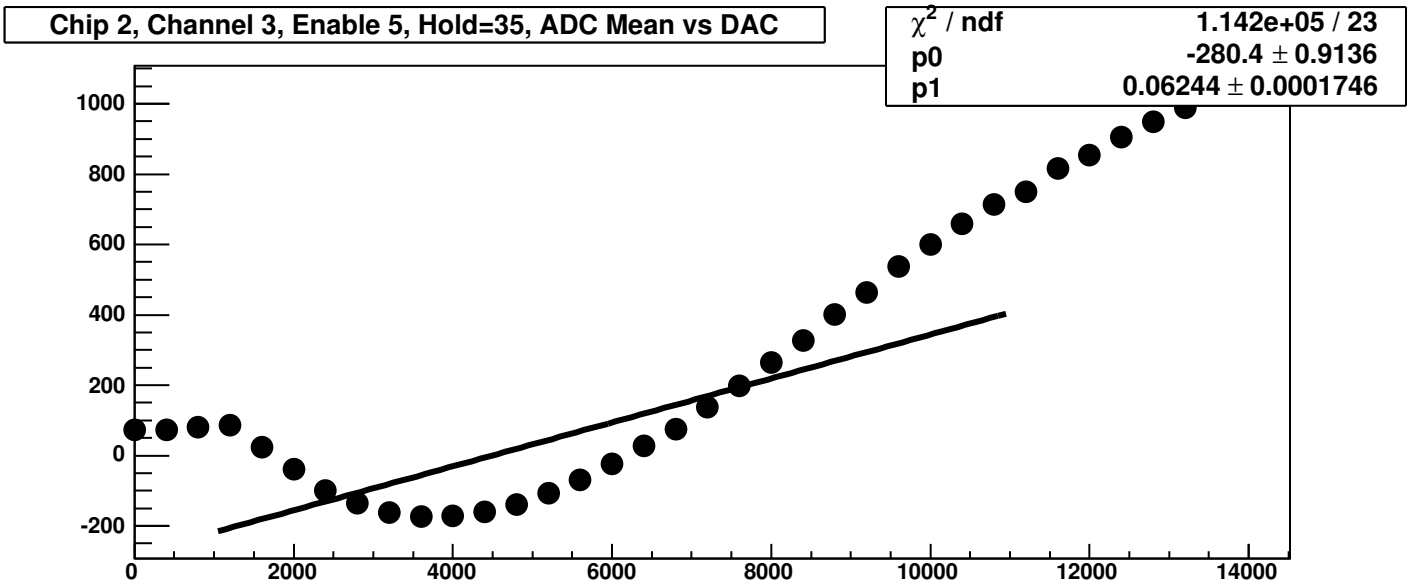

Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

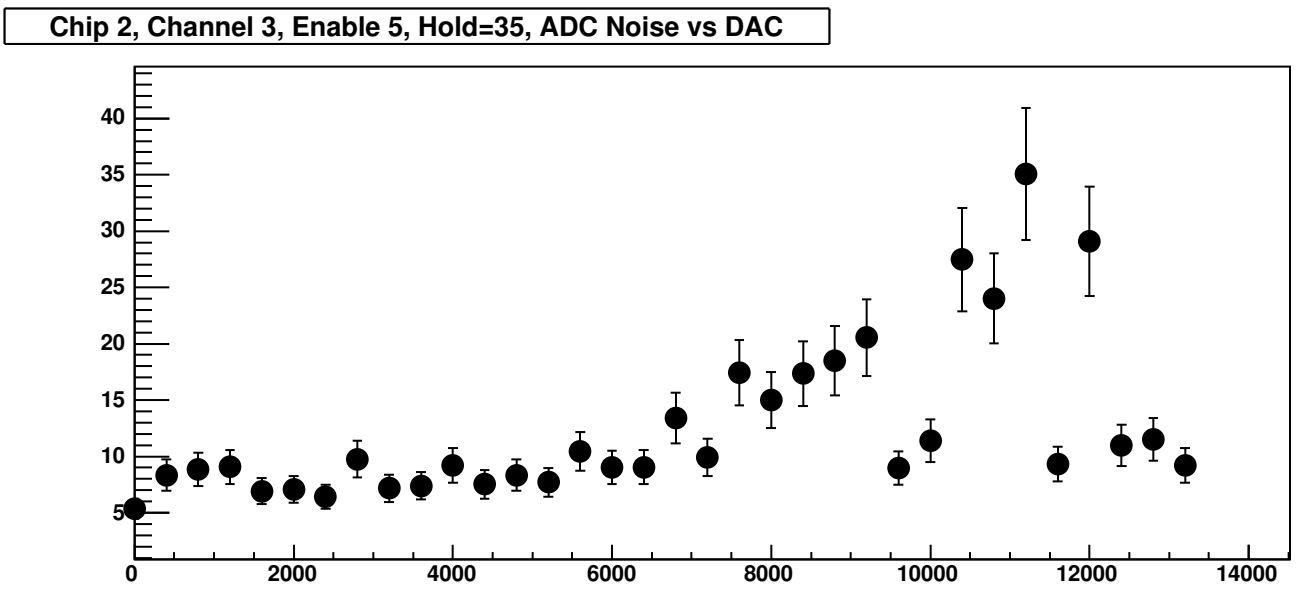

Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

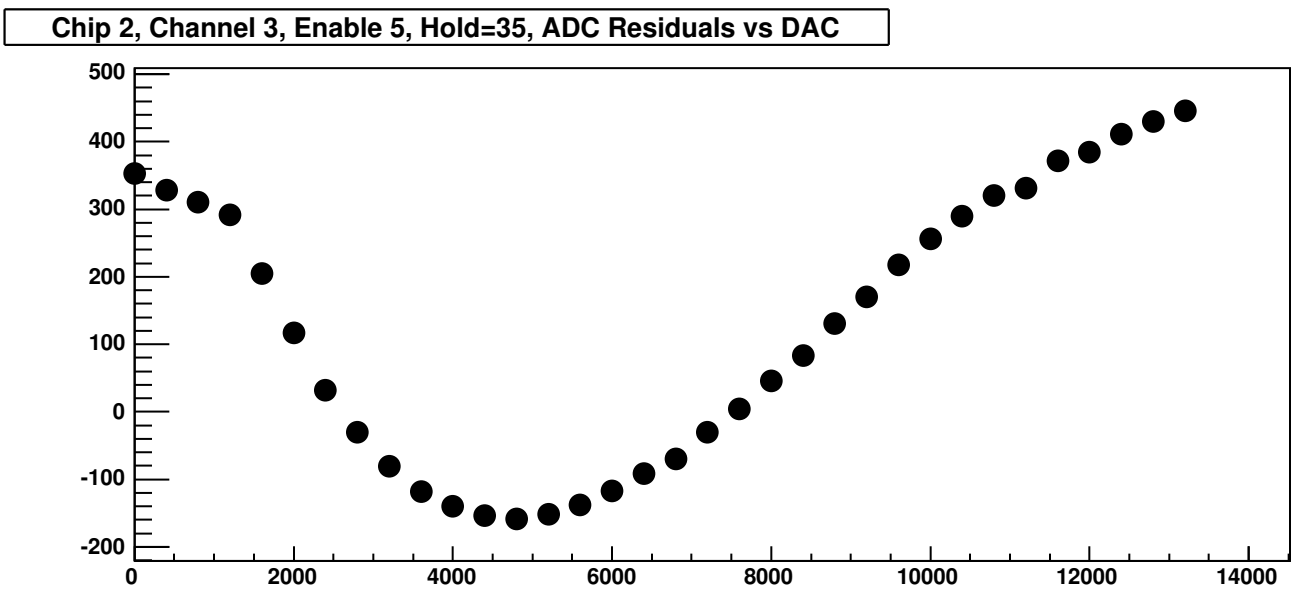

Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 44.69 / 23
p0 22.36 ± 0.7492
p1 0.02246 ± 0.0001211
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

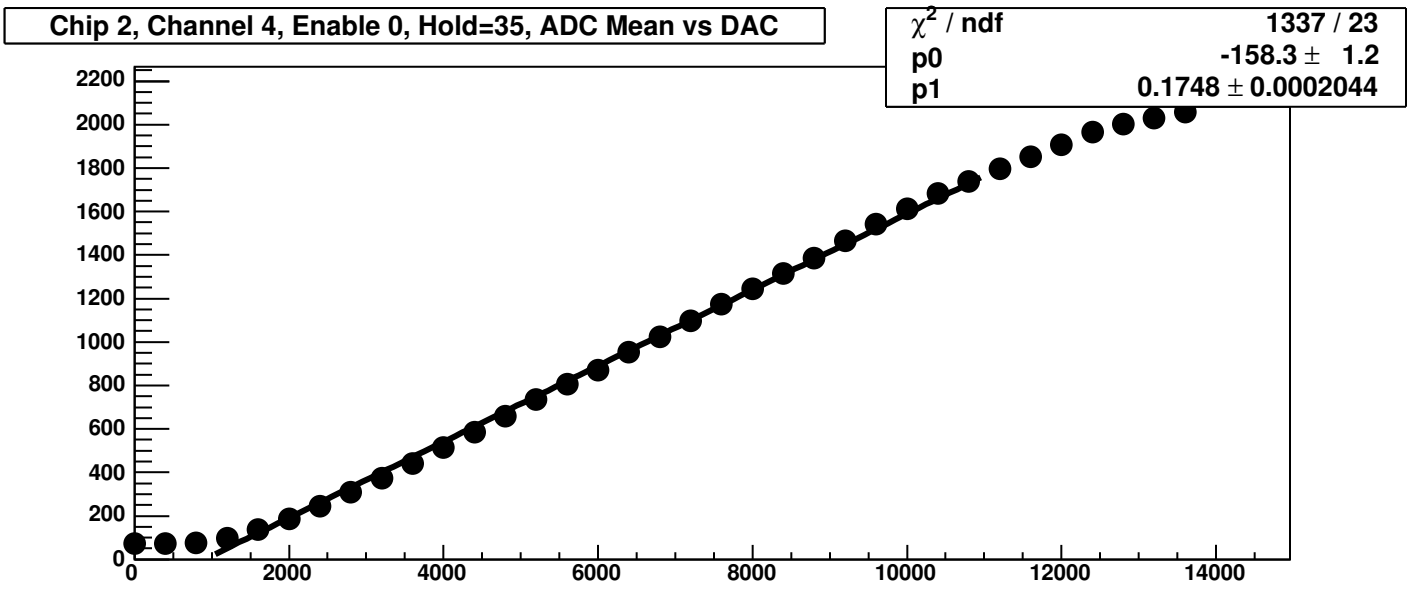

Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

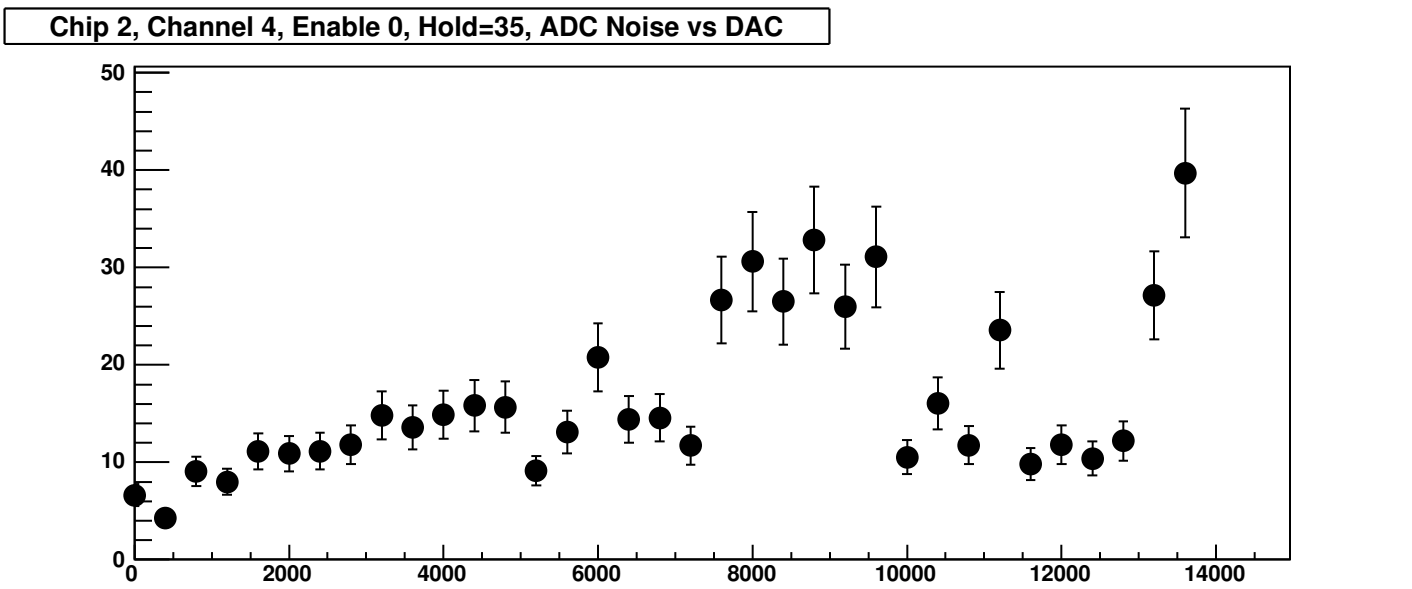

Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

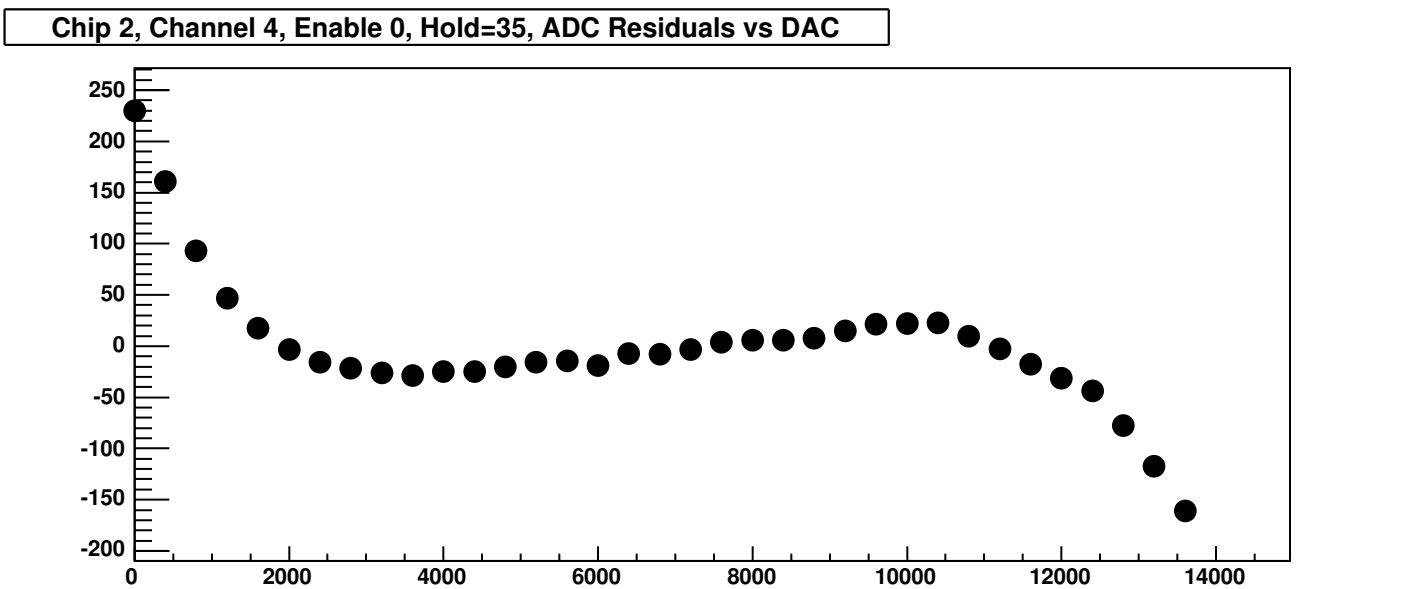

Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.142e+05 / 23
p0 -280.4 ± 0.9136
p1 0.06244 ± 0.0001746
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

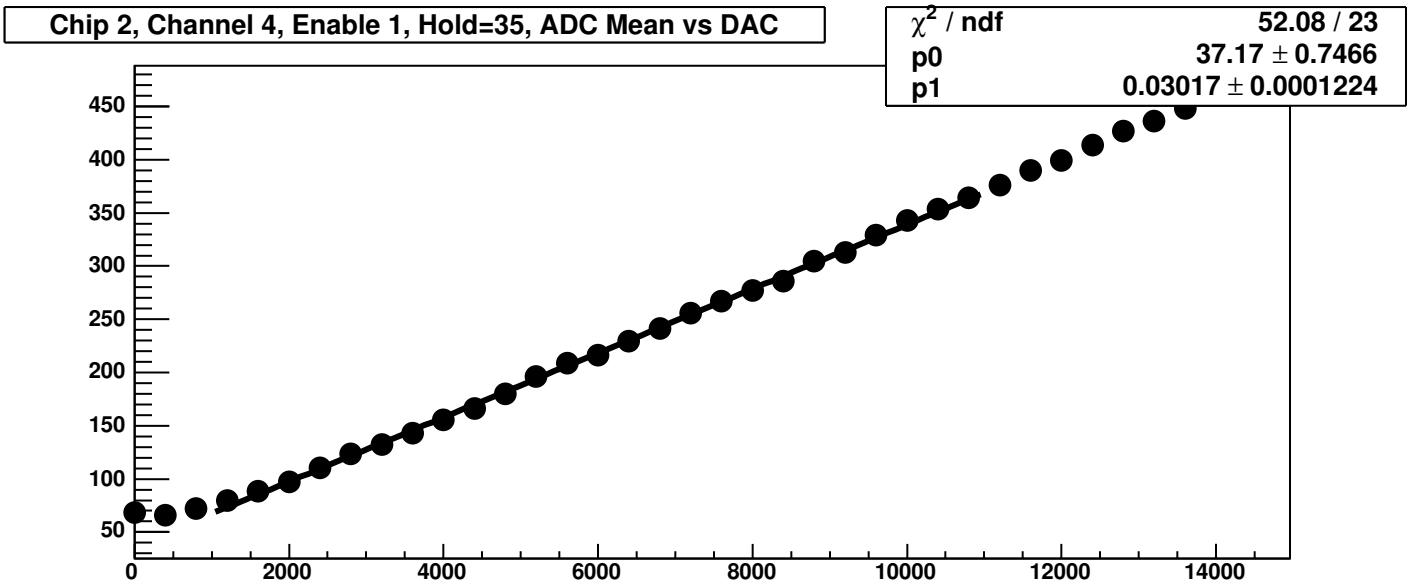

Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

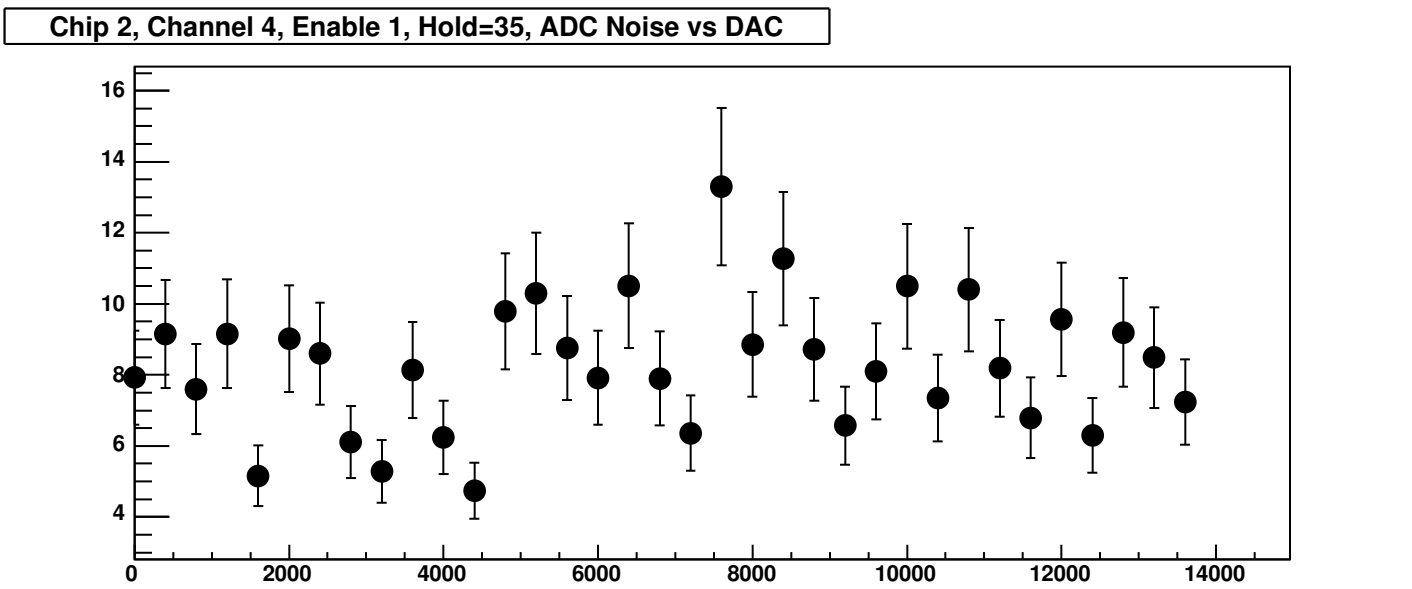

Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

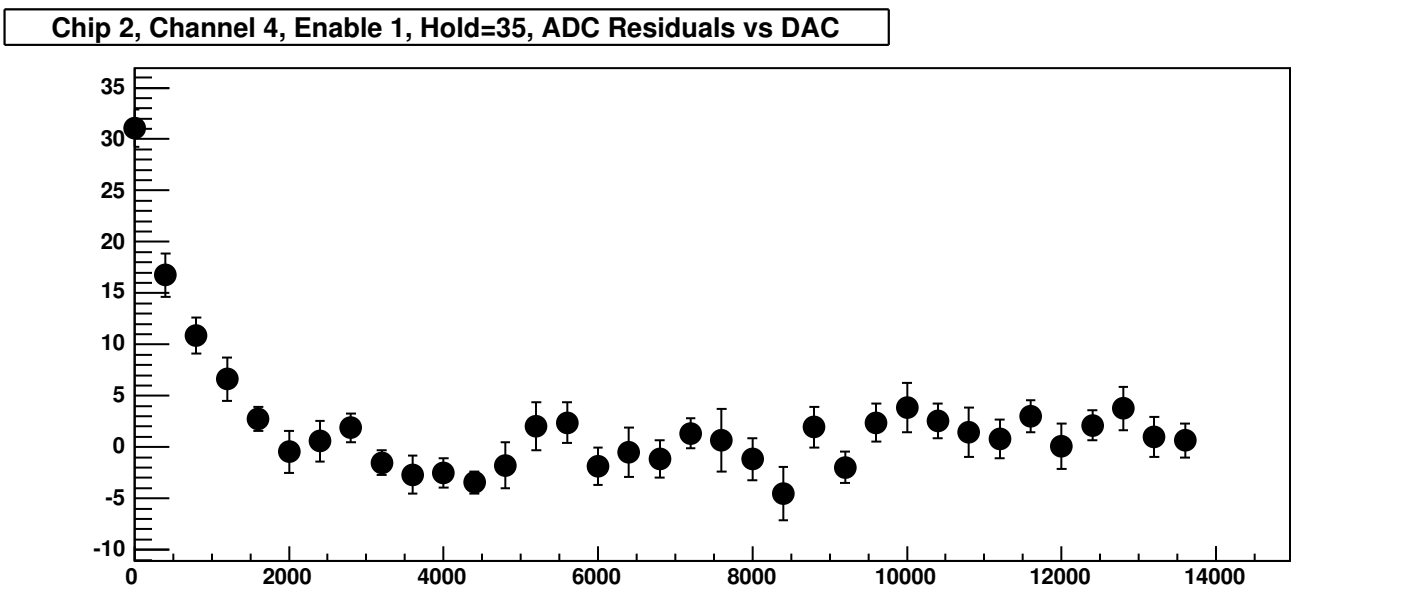

Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1337 / 23
p0 -158.3 ± 1.2
p1 0.1748 ± 0.0002044
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

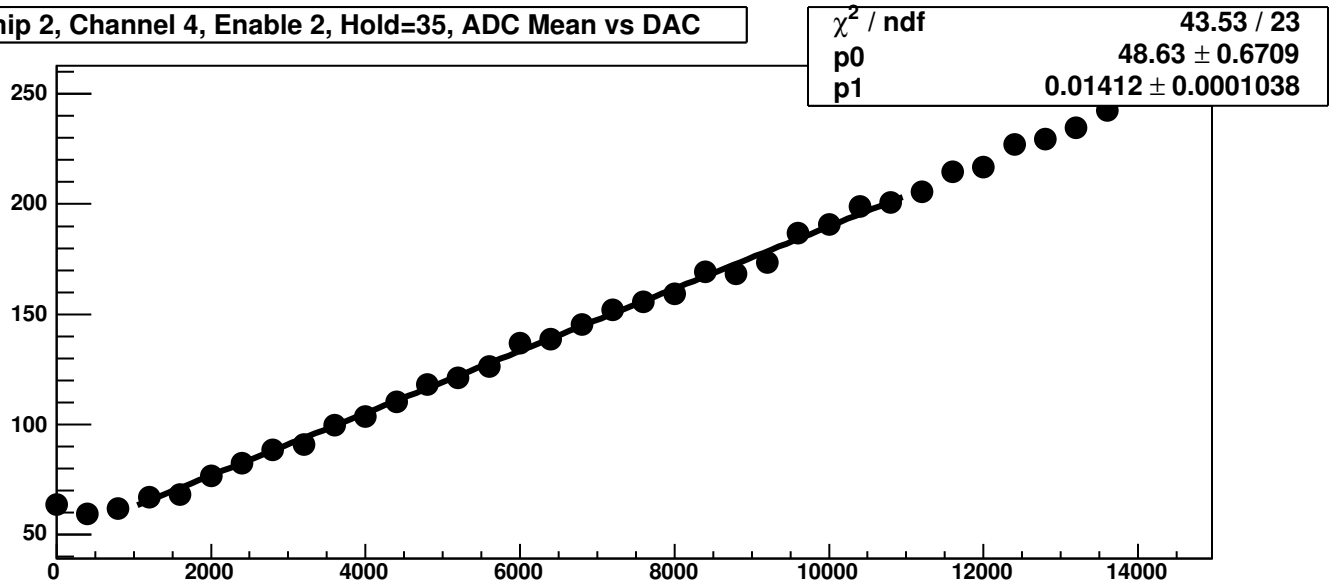

Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.08 / 23
p0 37.17 ± 0.7466
p1 0.03017 ± 0.0001224
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

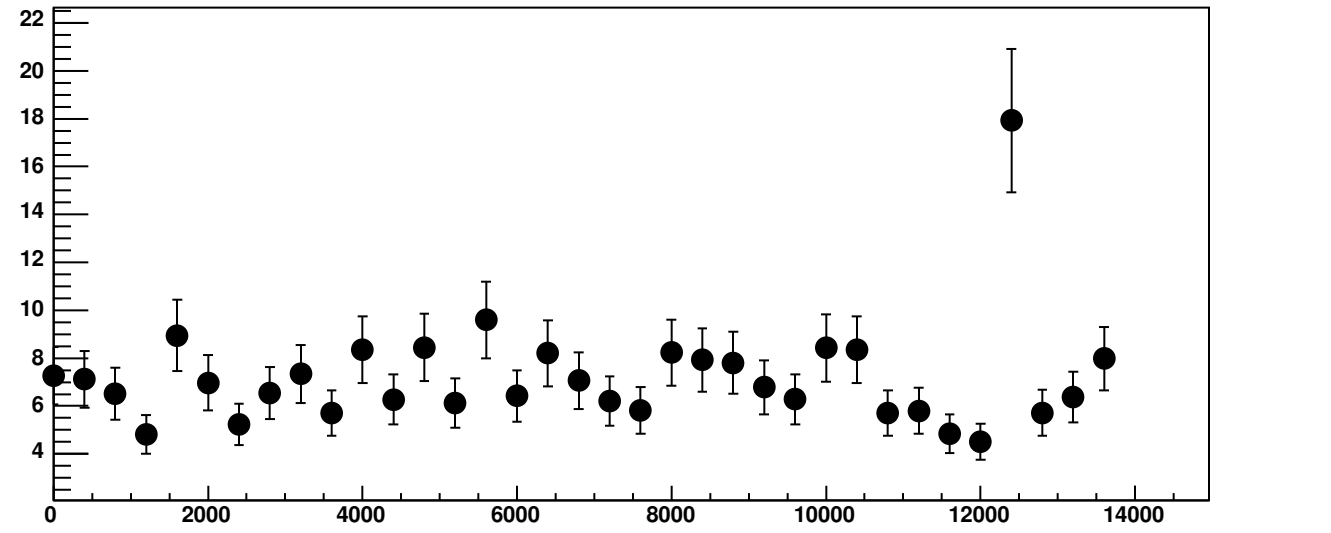

Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

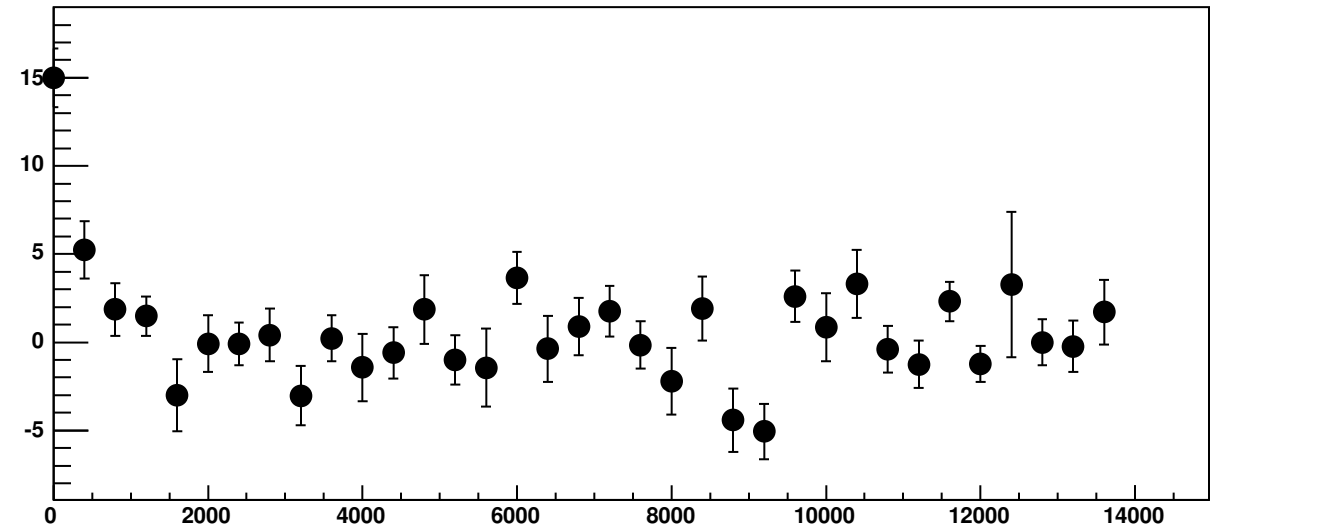
Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



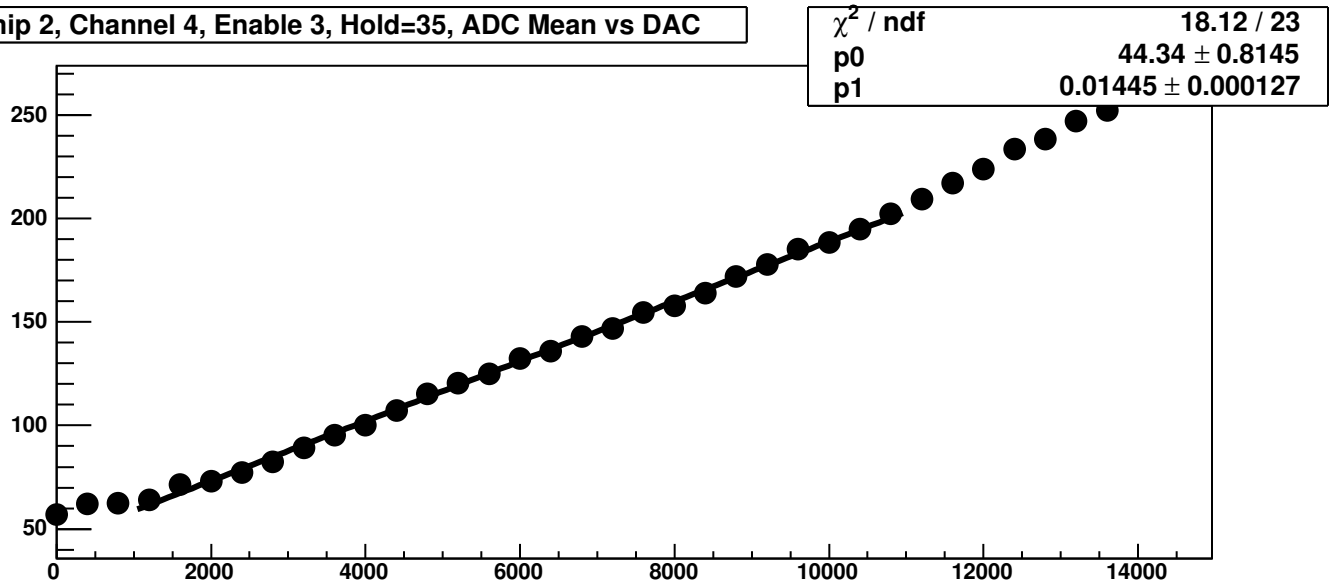
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



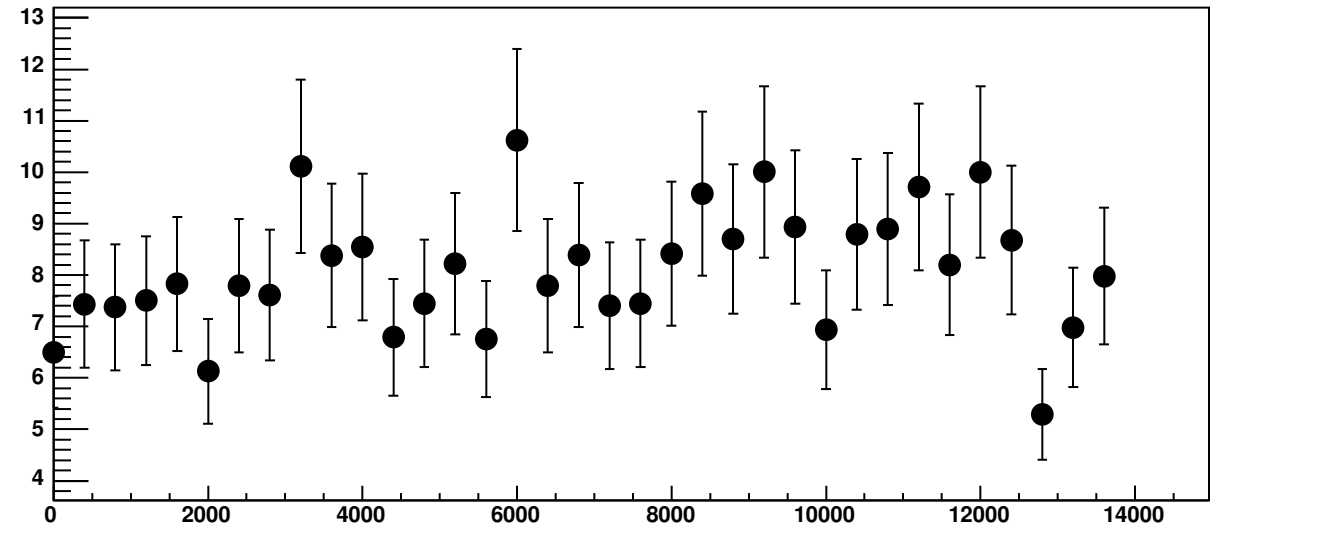
Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



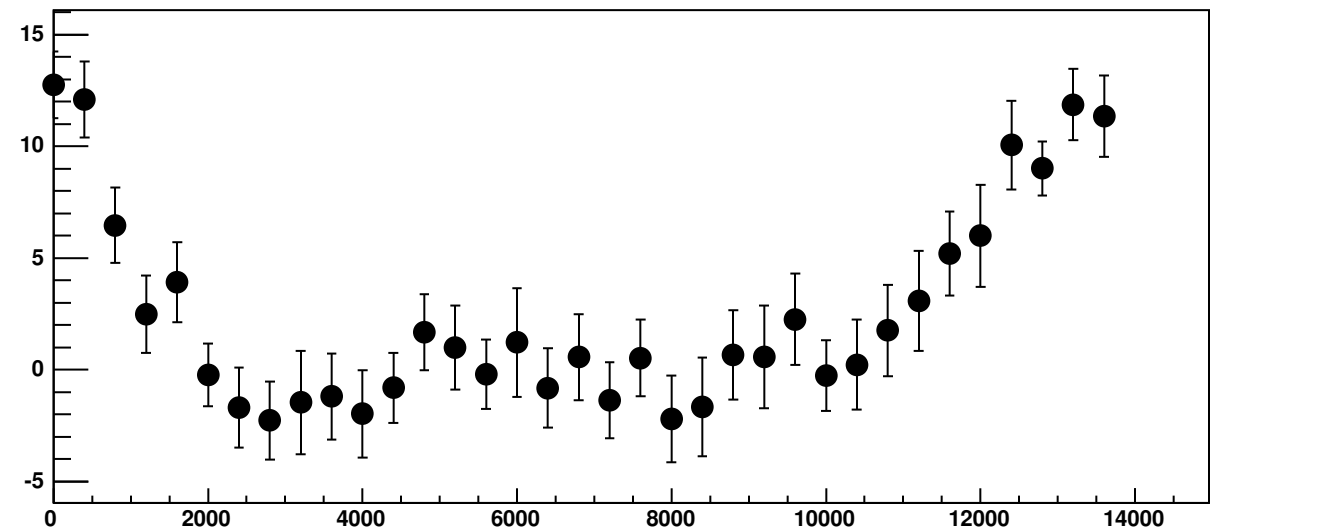
Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

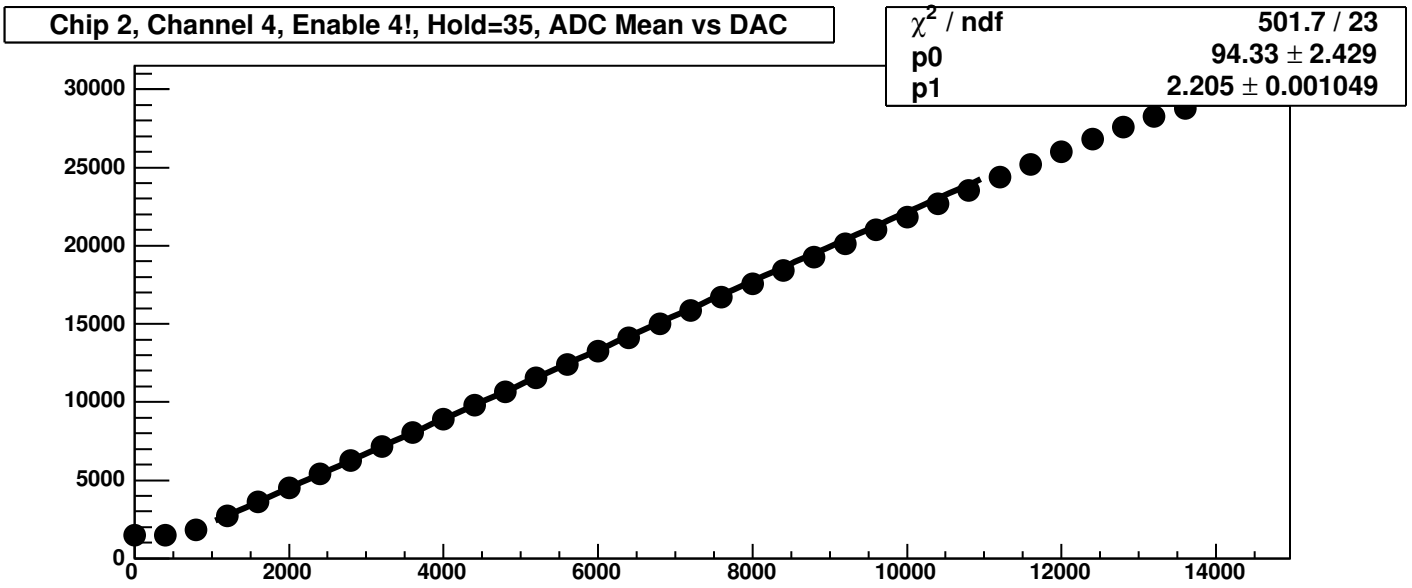


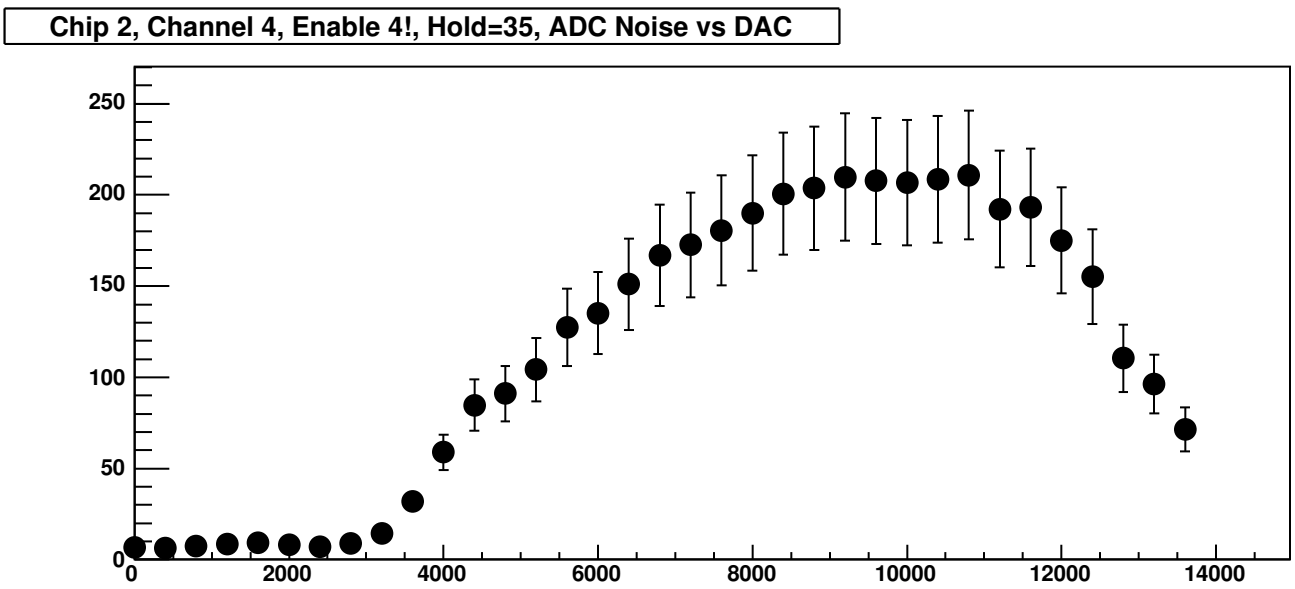
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

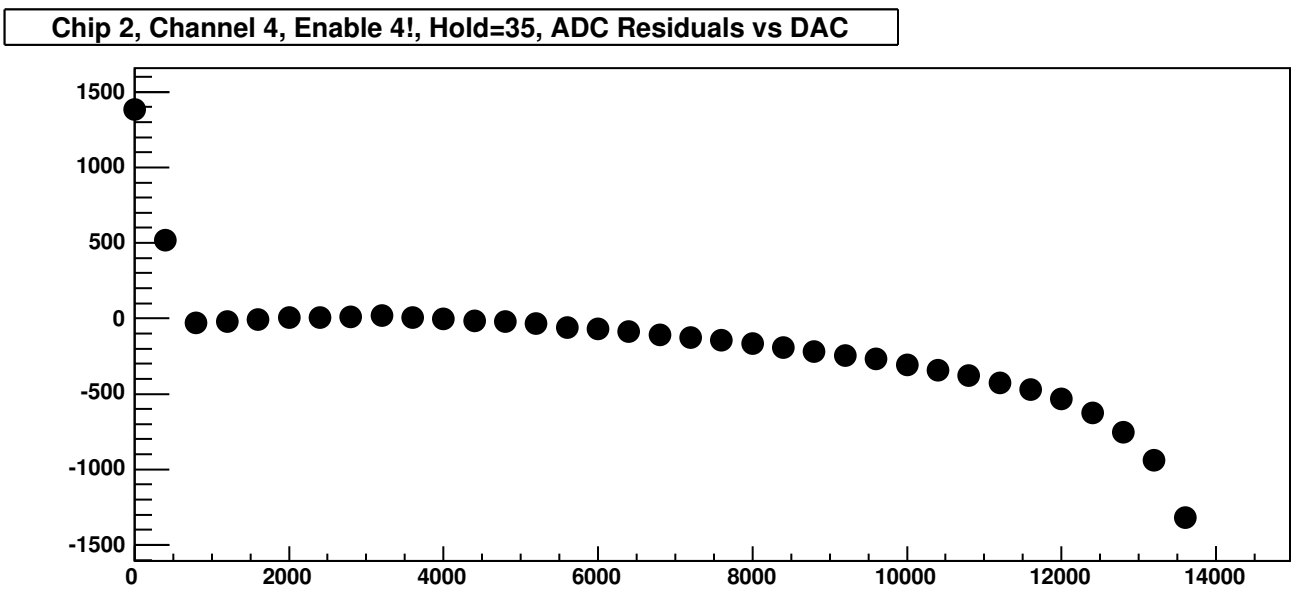


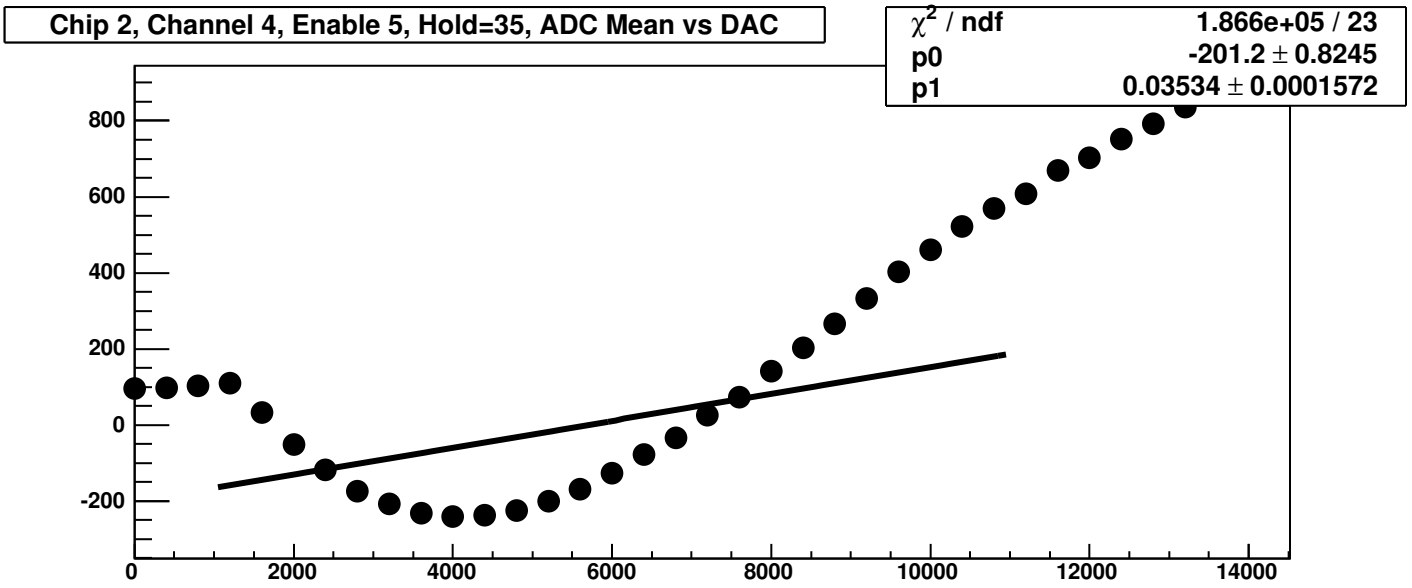
Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

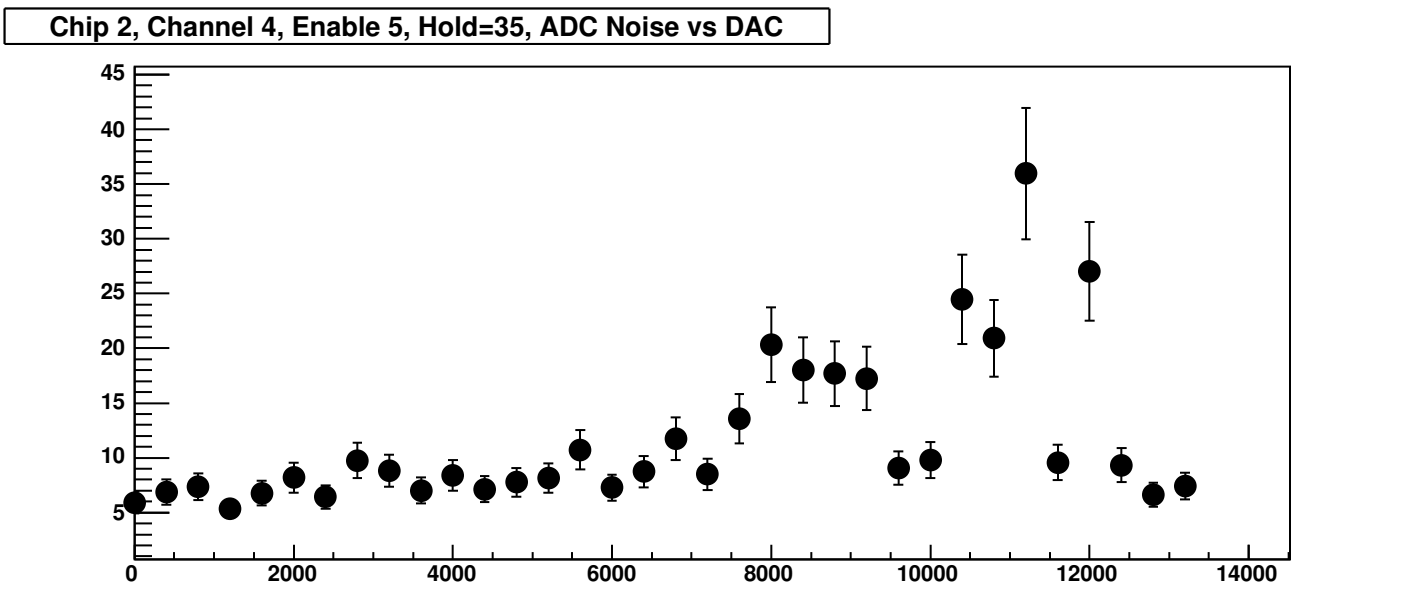


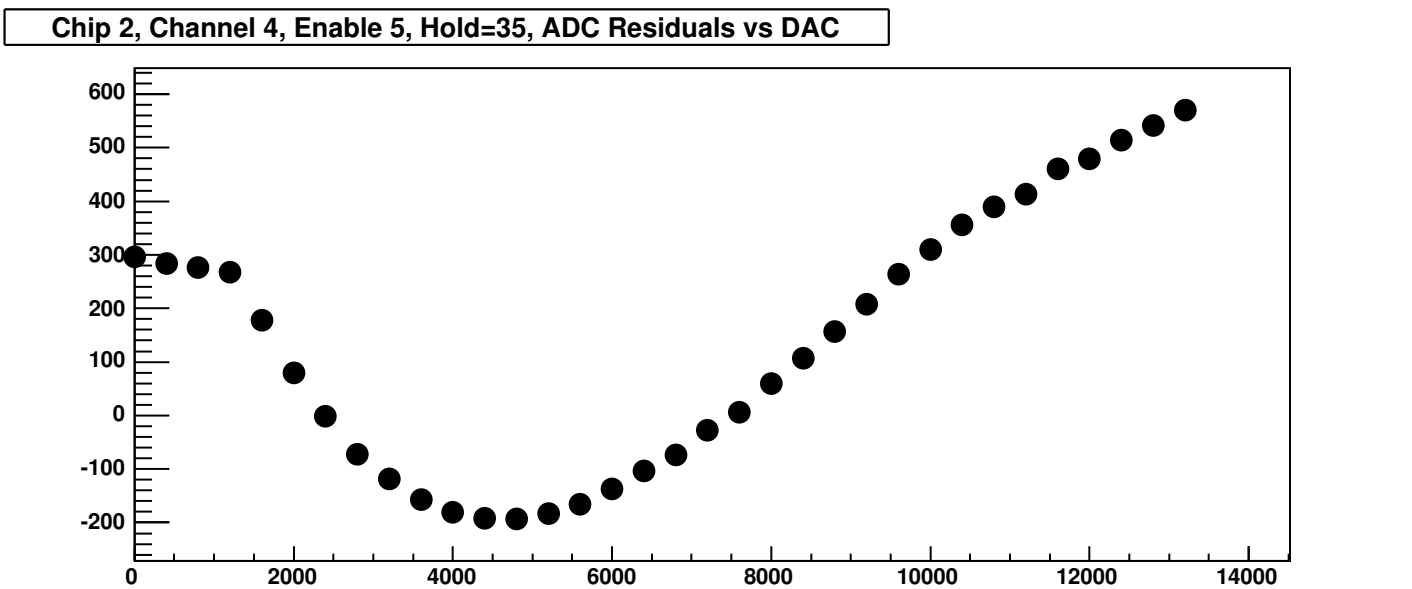

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 501.7 / 23
p0 94.33 ± 2.429
p1 2.205 ± 0.001049
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

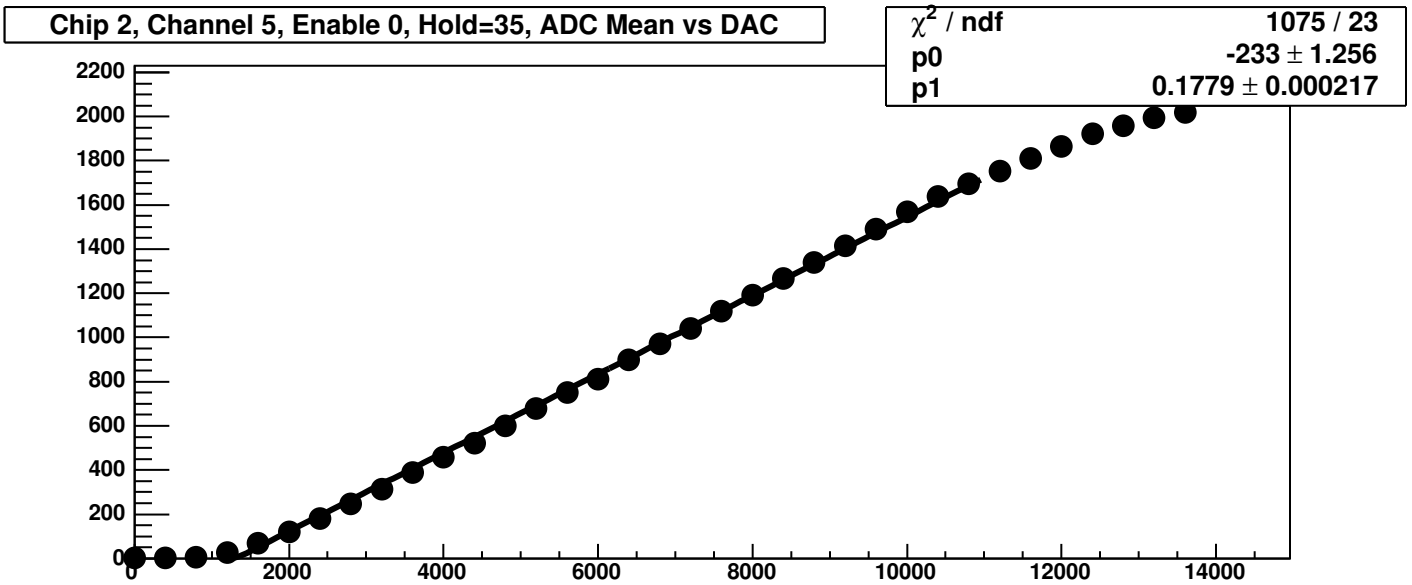

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

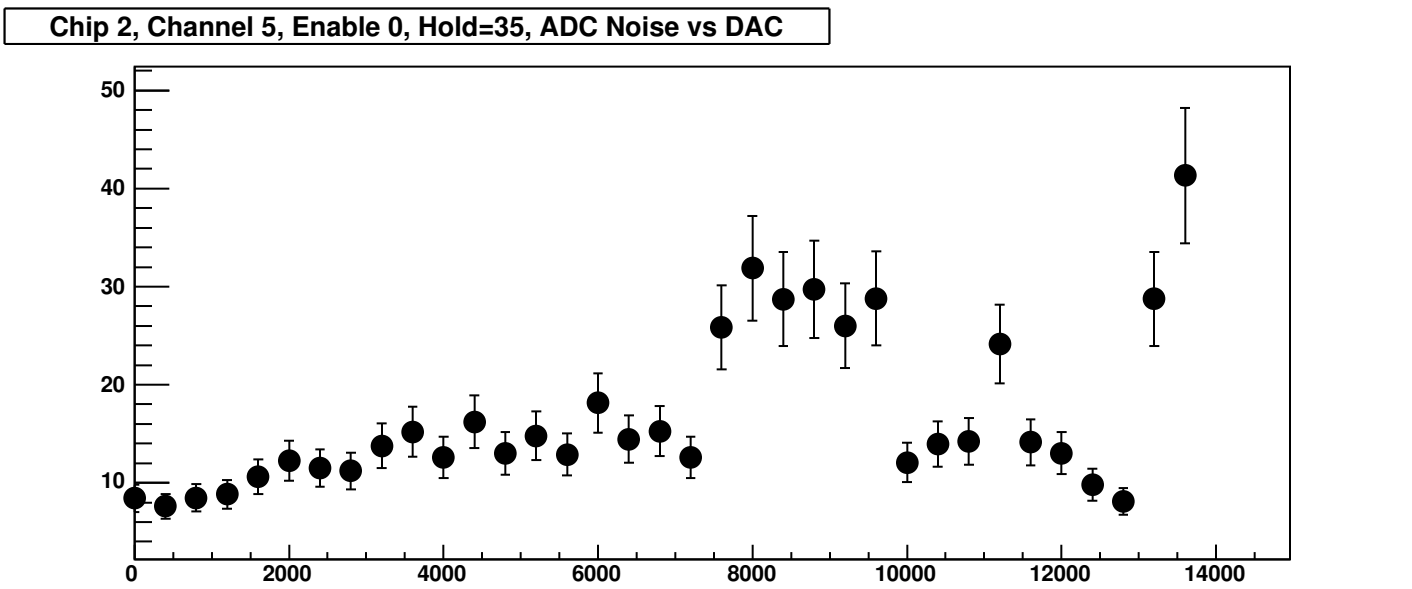

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

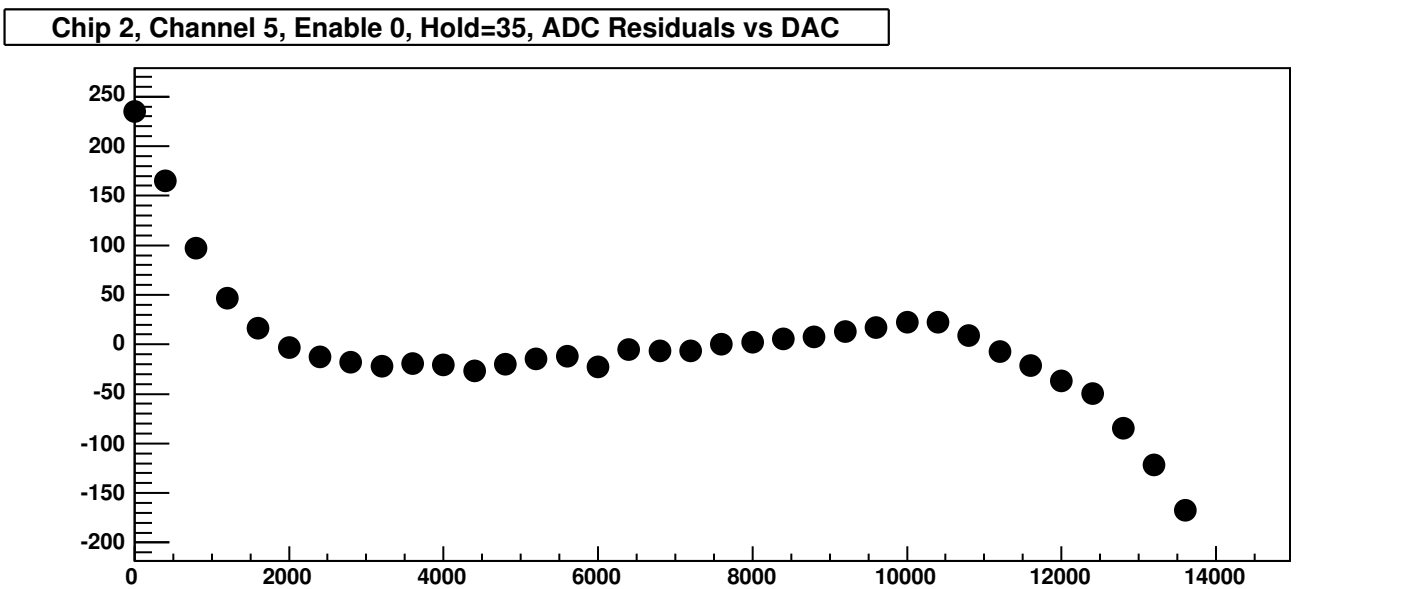

Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.866e+05 / 23
p0 -201.2 ± 0.8245
p1 0.03534 ± 0.0001572
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

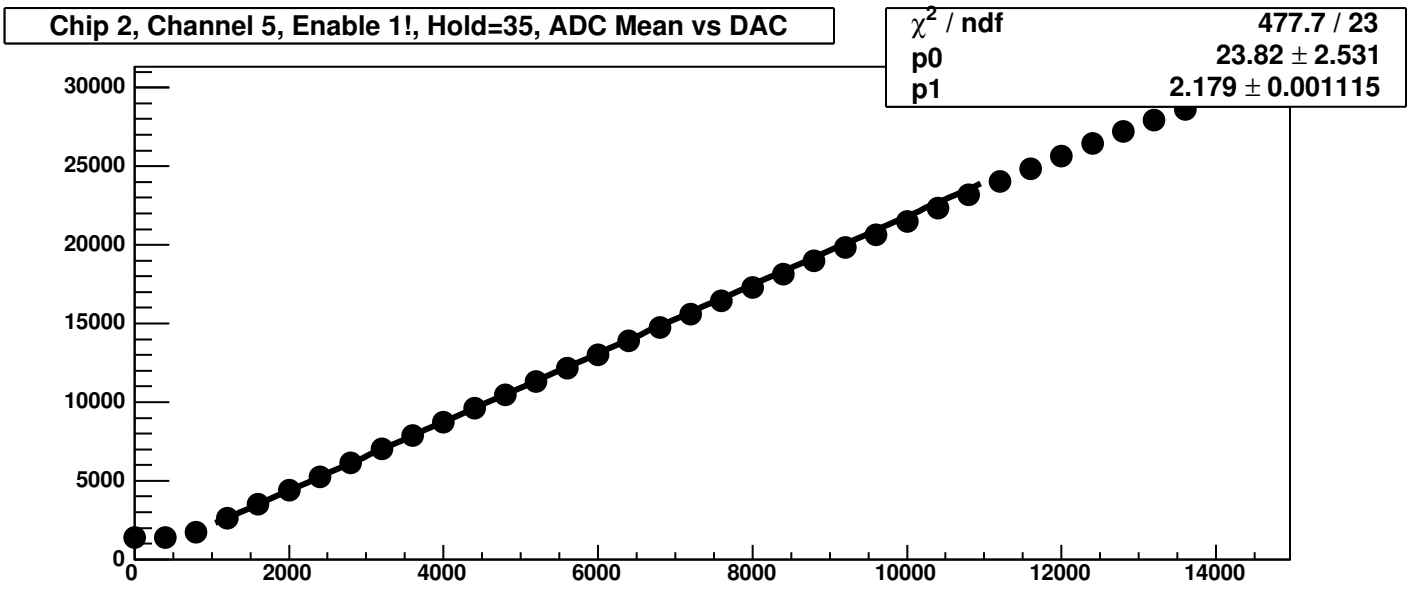

Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

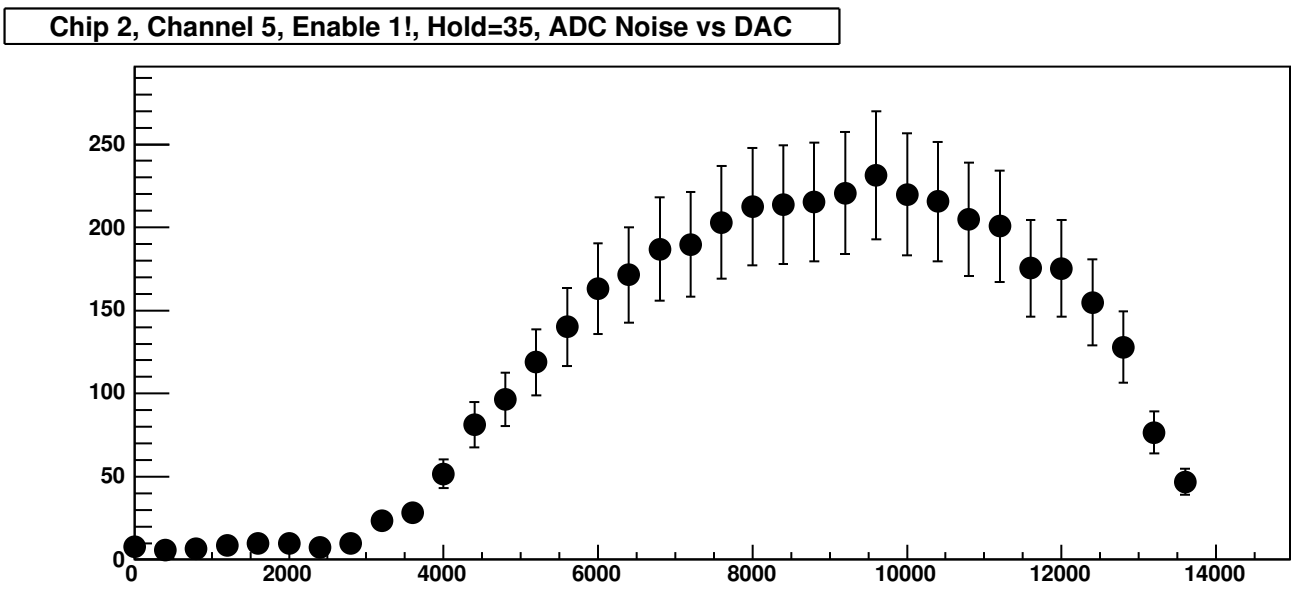

Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

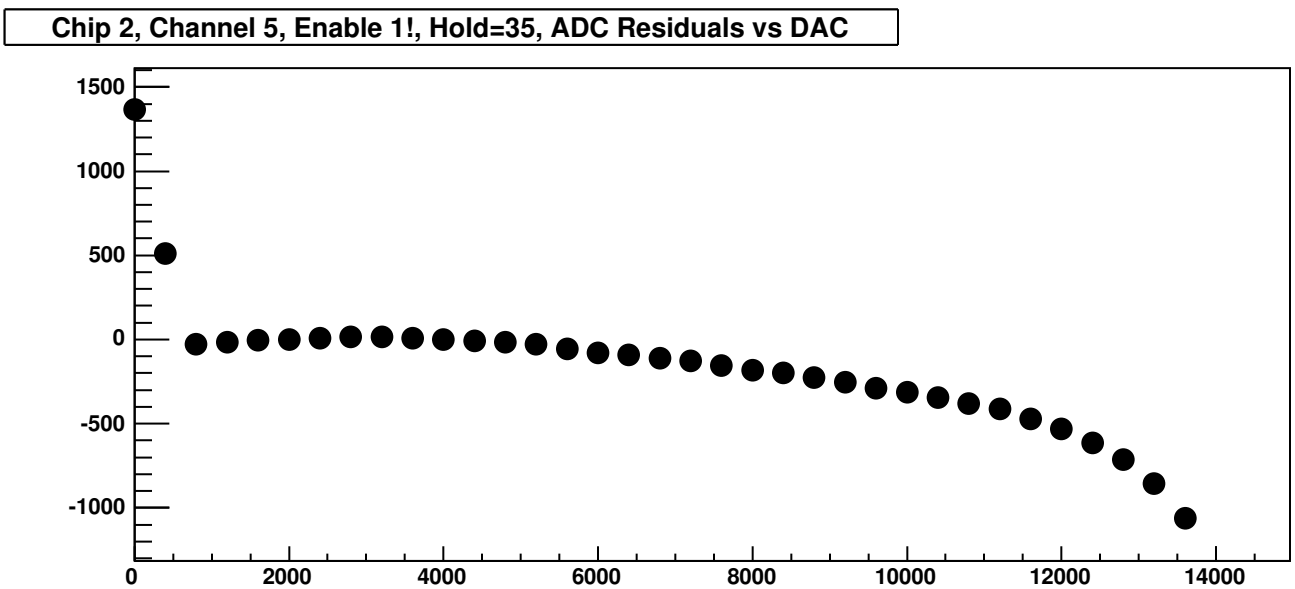

Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1075 / 23
p0 -233 ± 1.256
p1 0.1779 ± 0.000217
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

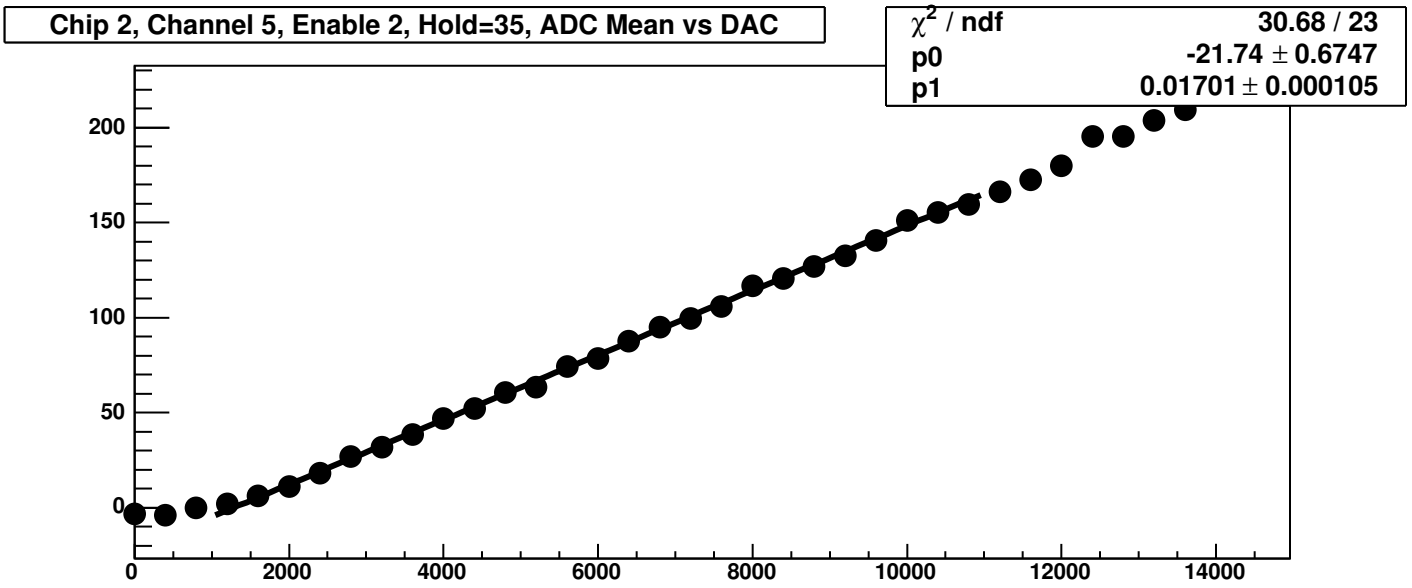

Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

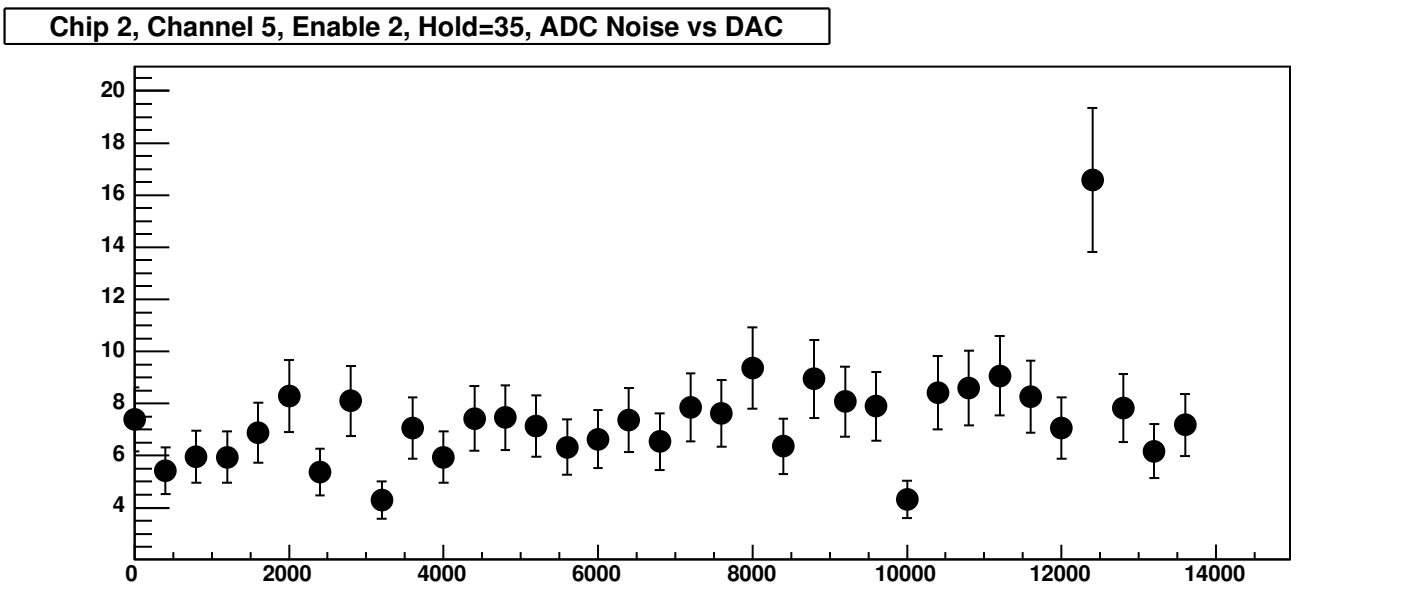

Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

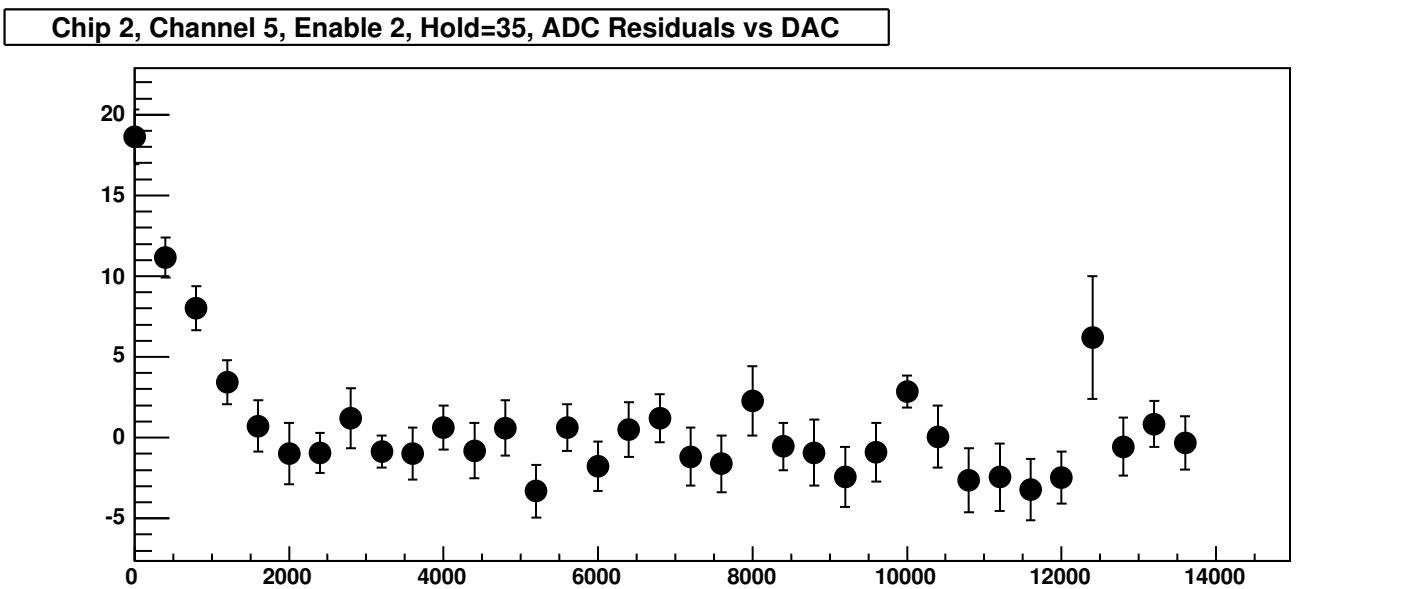

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 477.7 / 23
p0 23.82 ± 2.531
p1 2.179 ± 0.001115
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

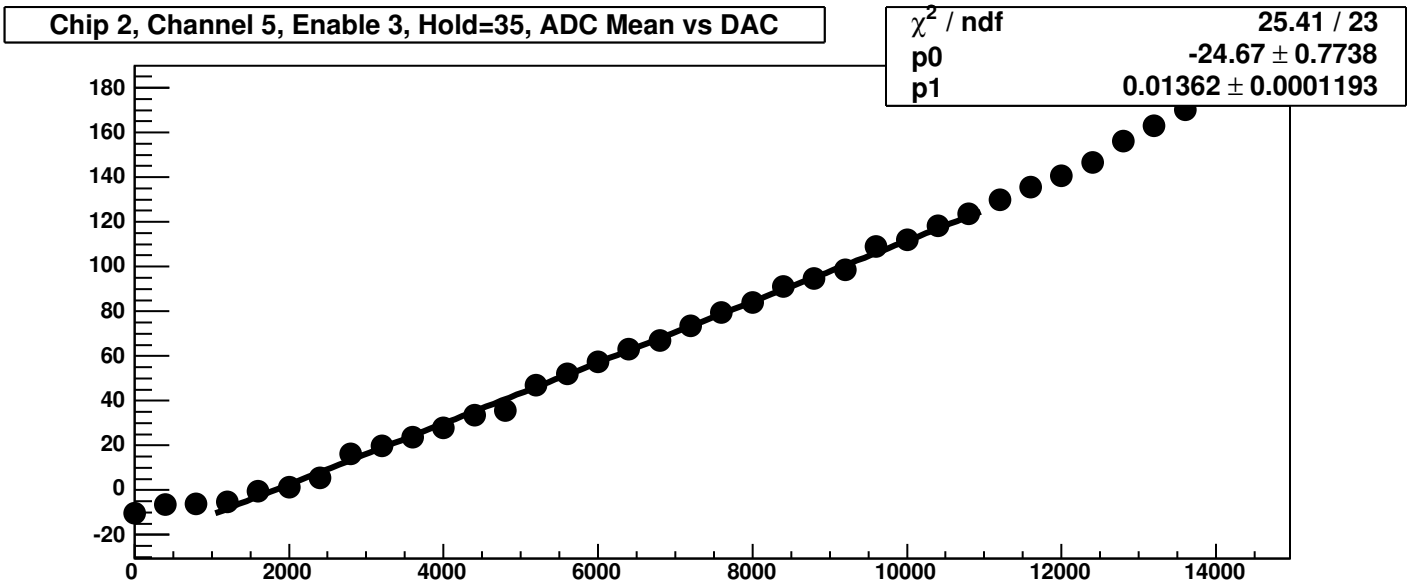

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

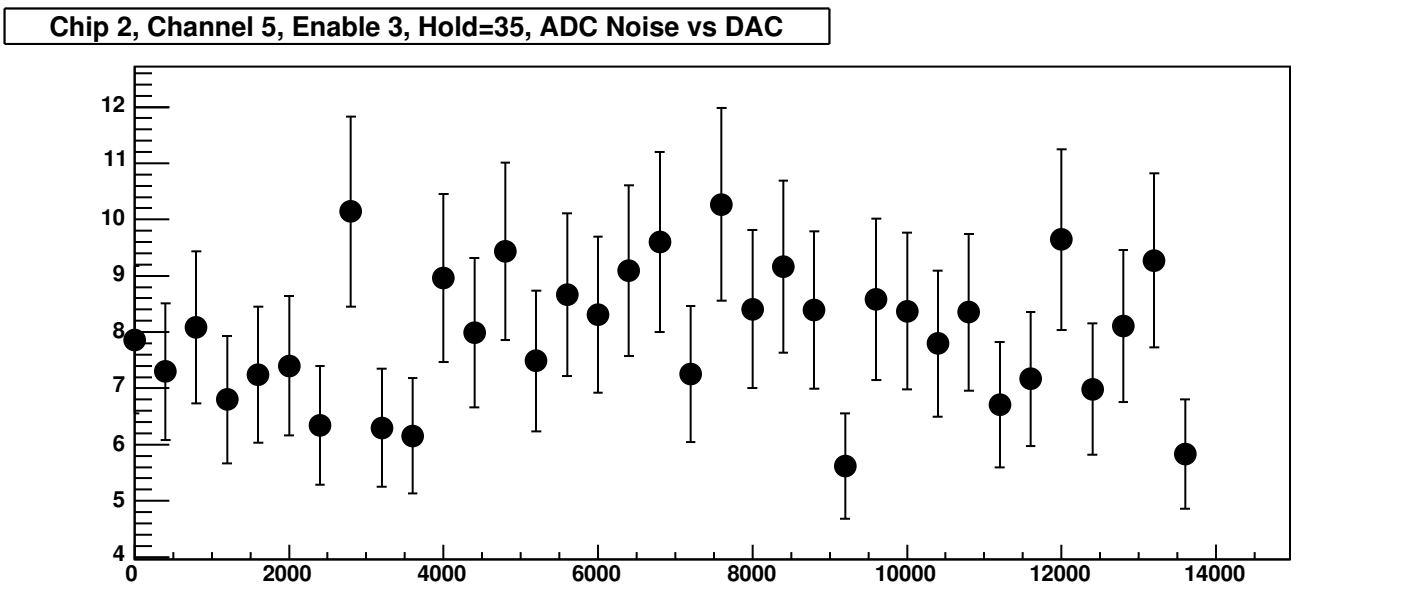

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

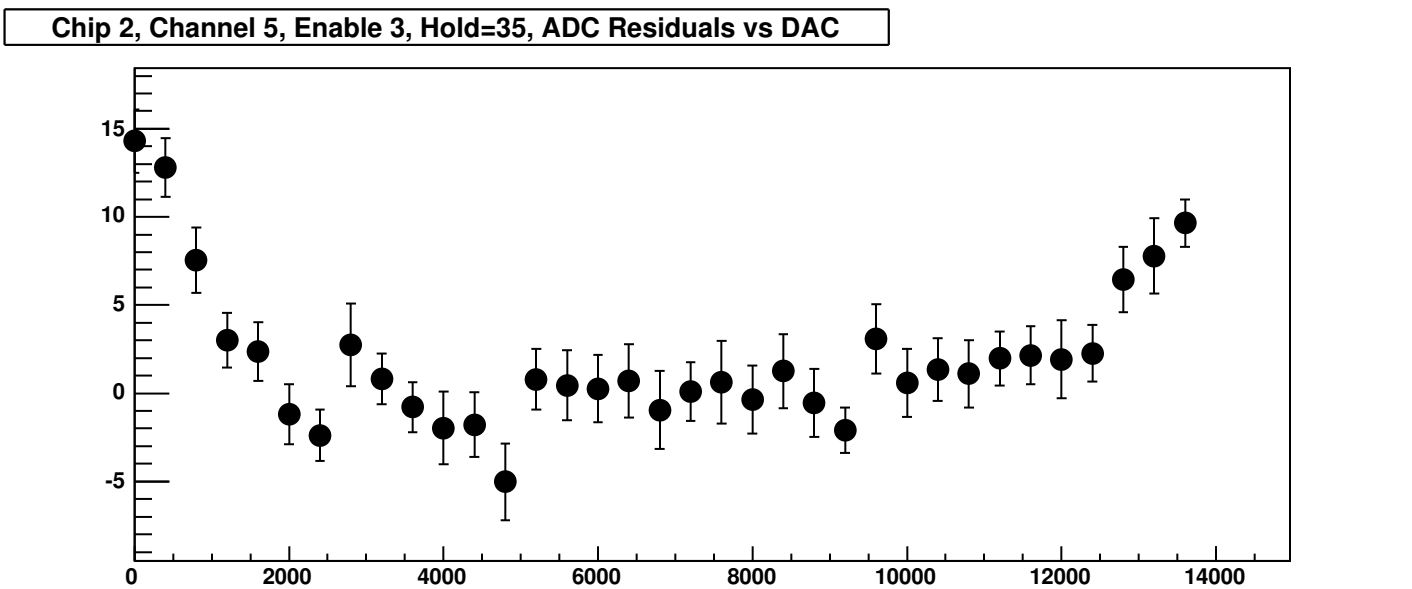

Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.68 / 23
p0 -21.74 ± 0.6747
p1 0.01701 ± 0.000105
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

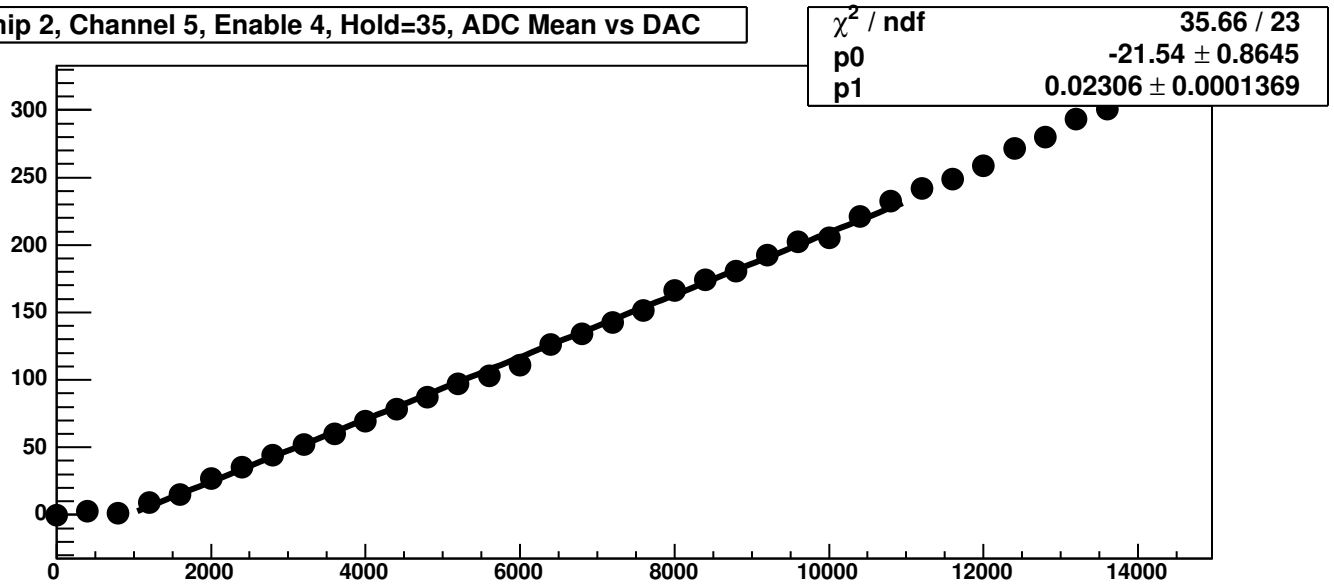

Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.41 / 23
p0 -24.67 ± 0.7738
p1 0.01362 ± 0.0001193
180
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

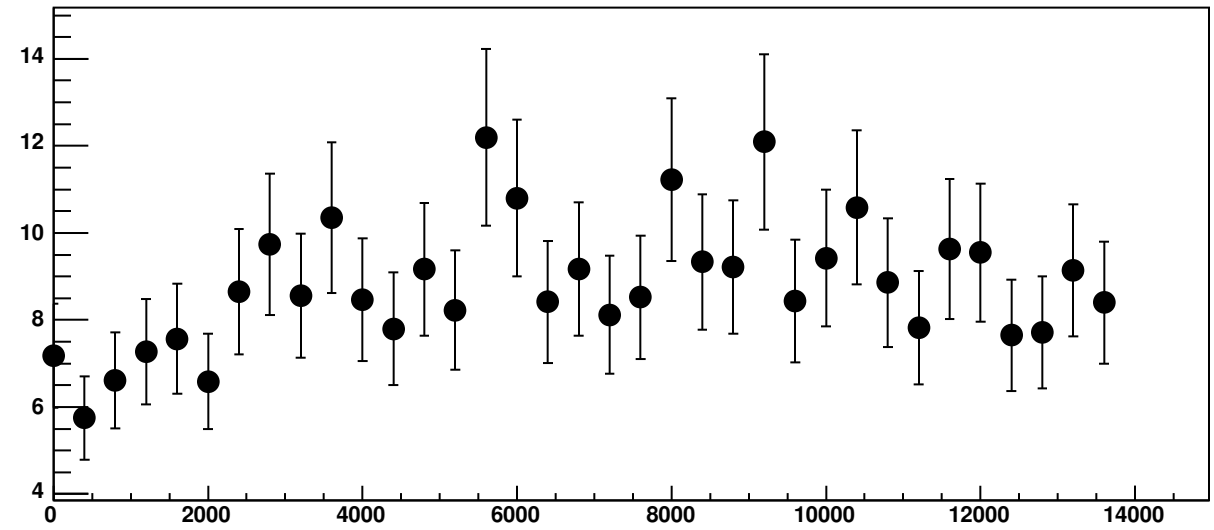

Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

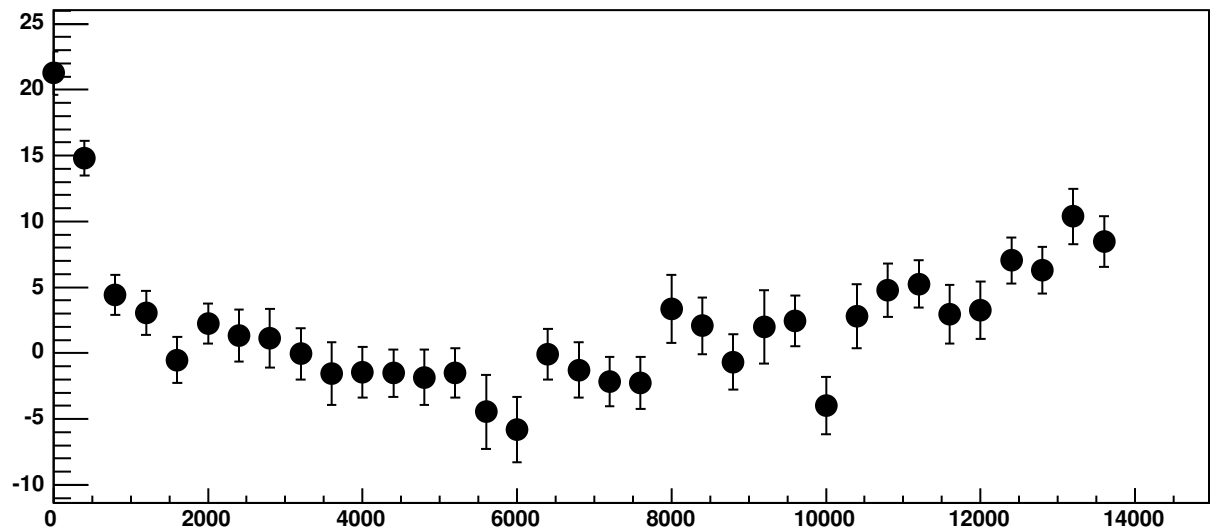
Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

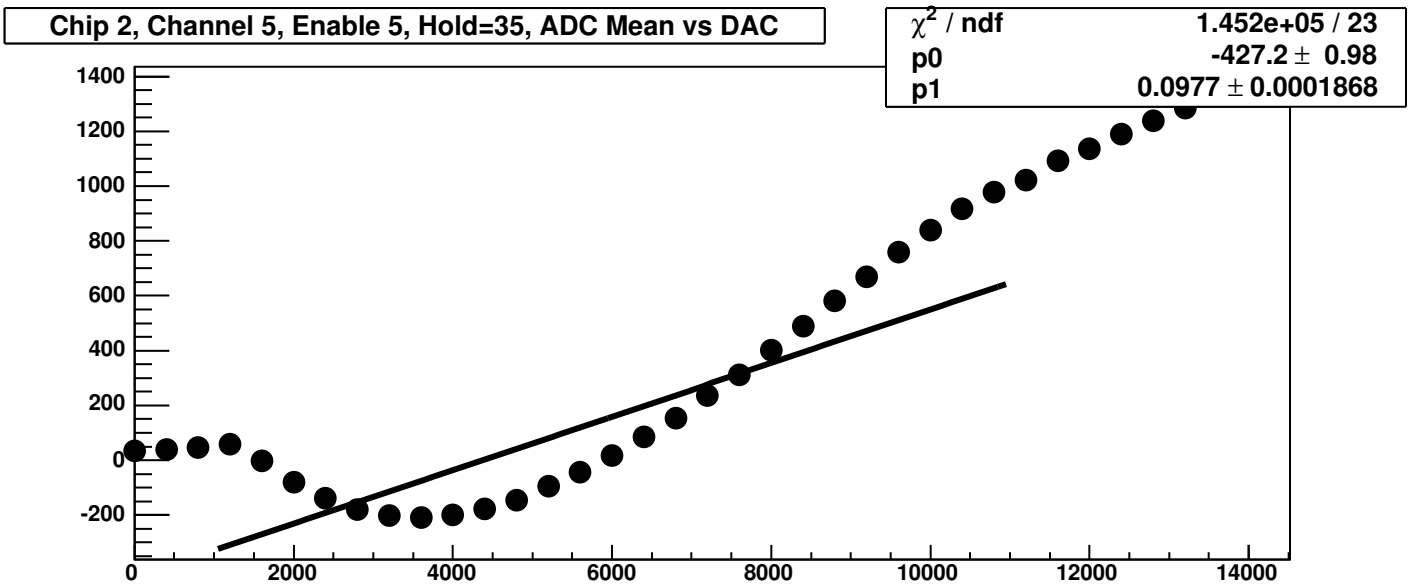


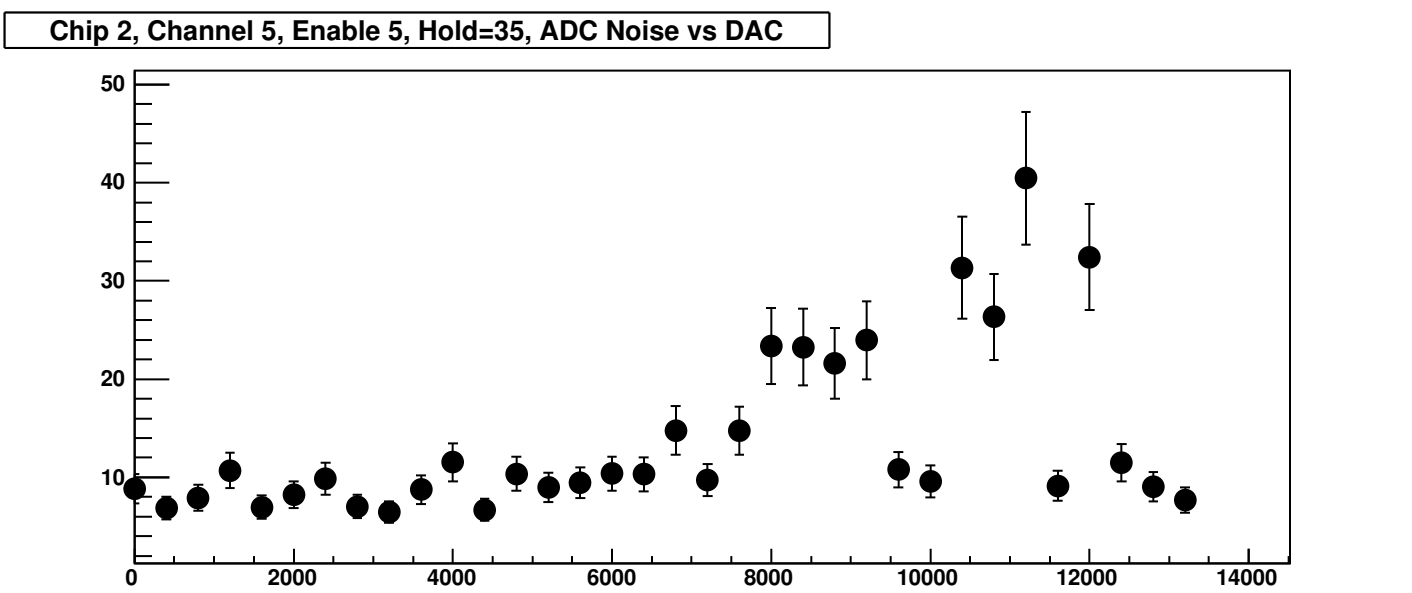
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC

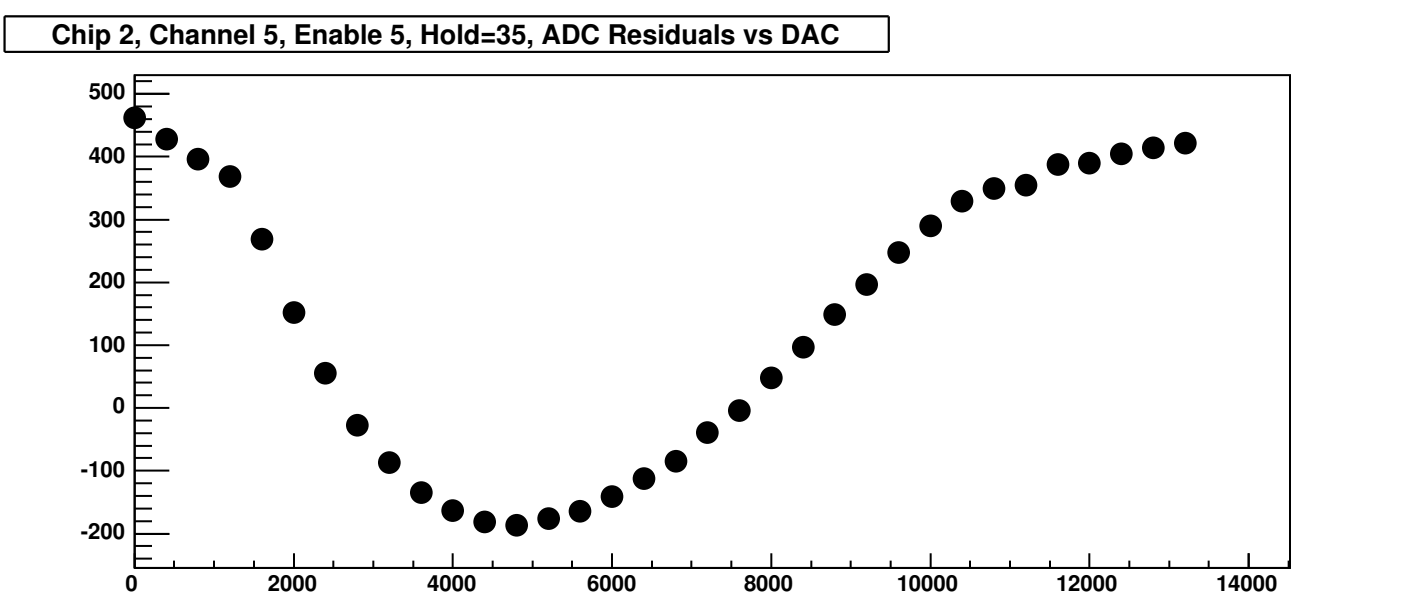


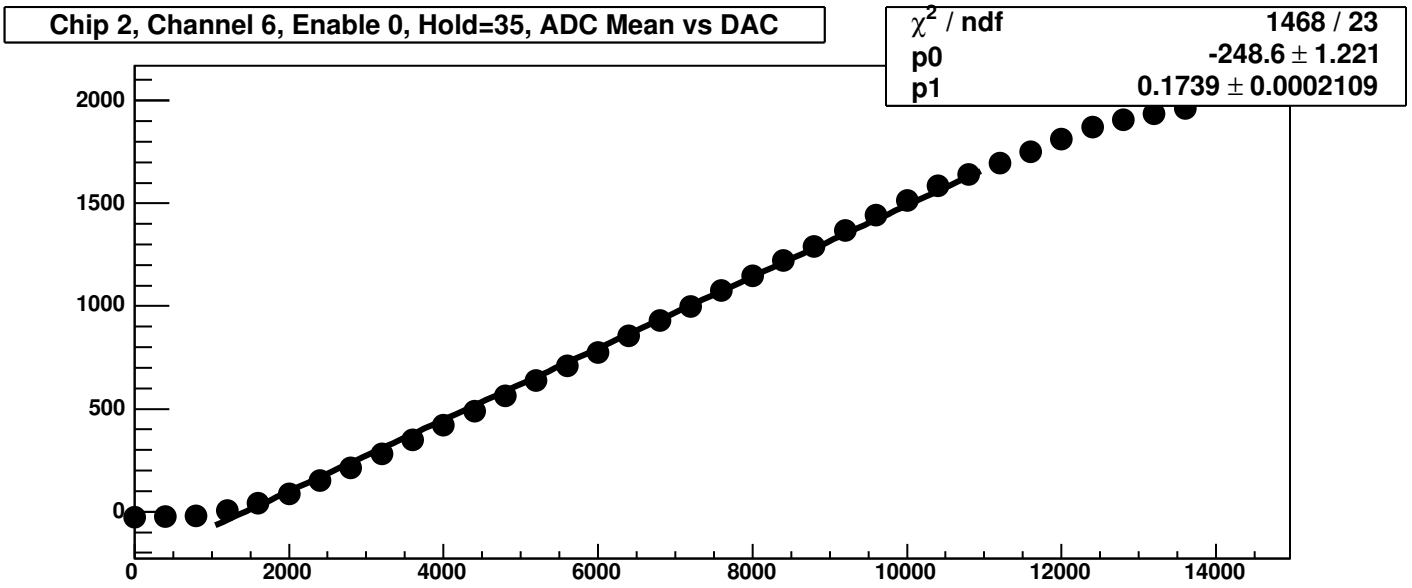
Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

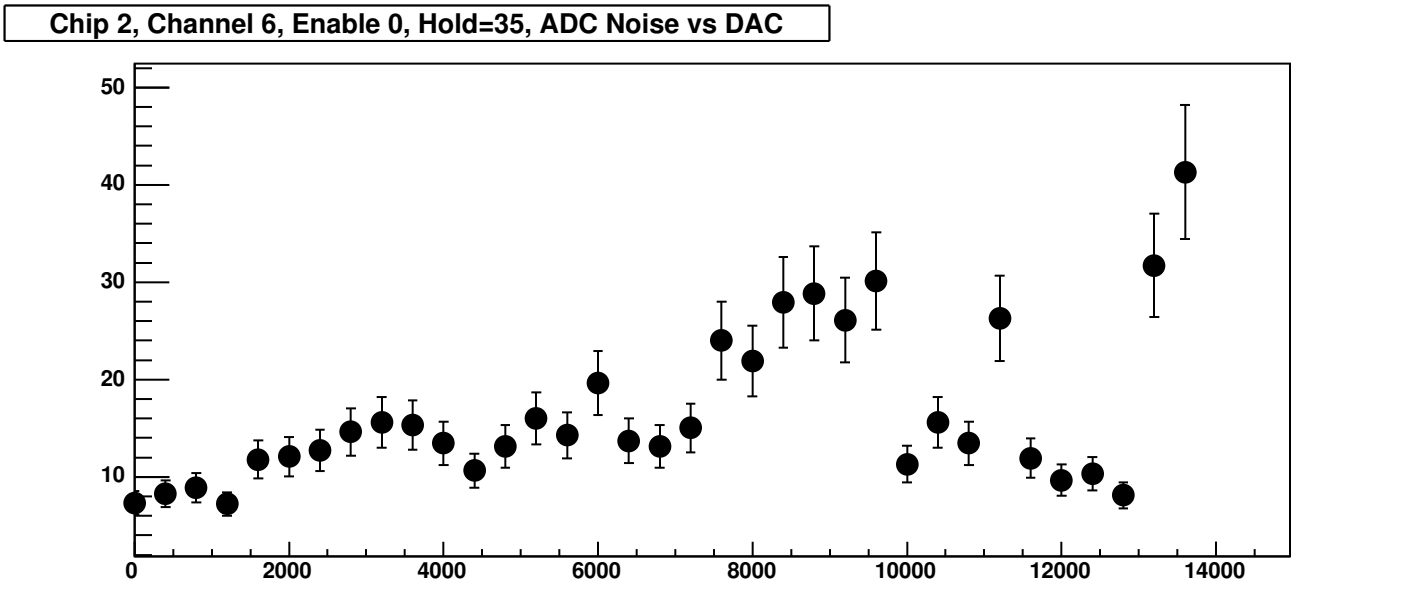


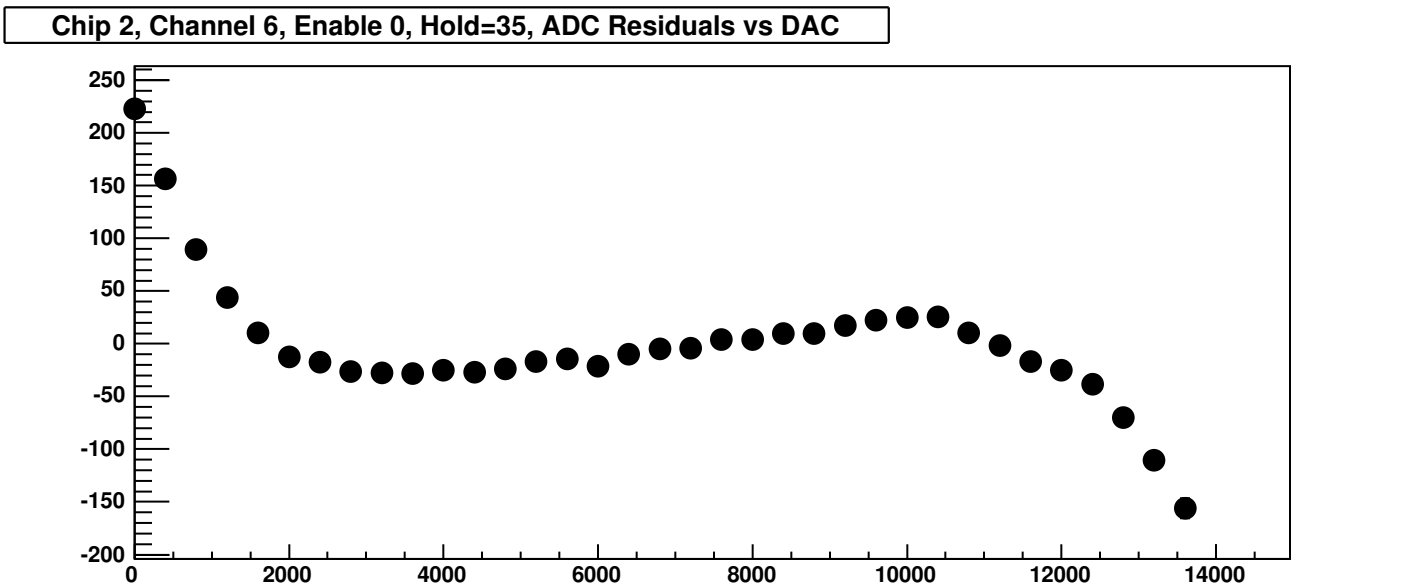

Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.452e+05 / 23
p0 -427.2 ± 0.98
p1 0.0977 ± 0.0001868
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

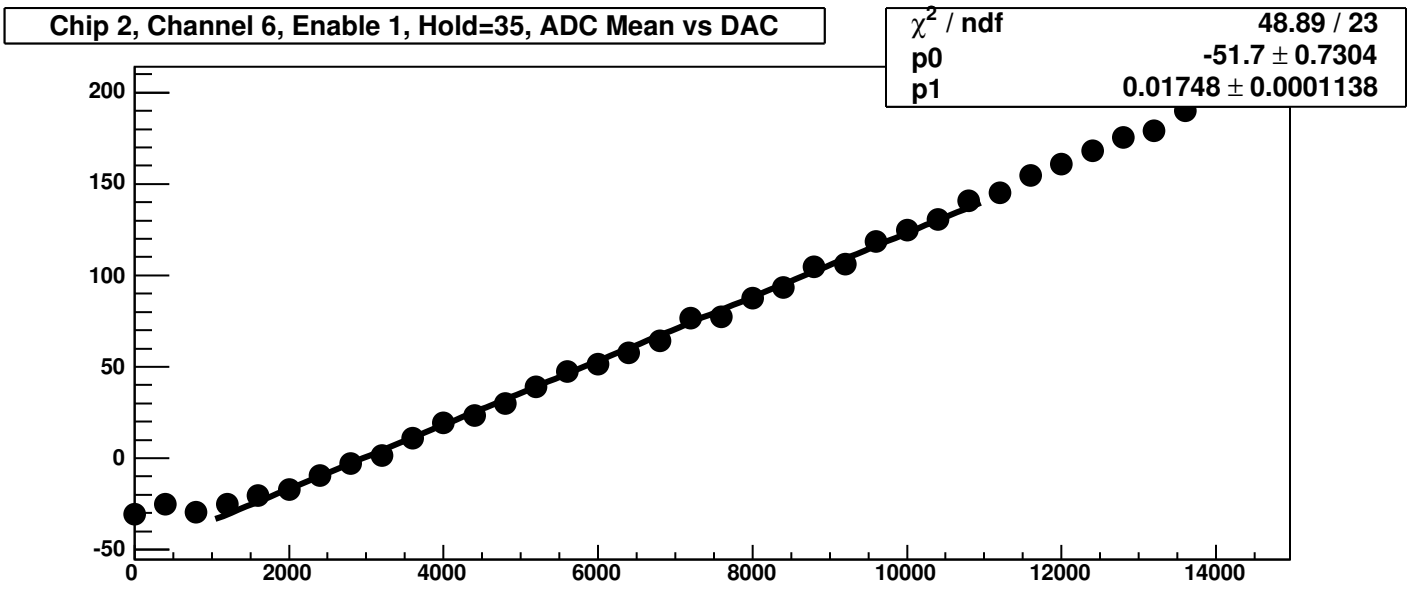

Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

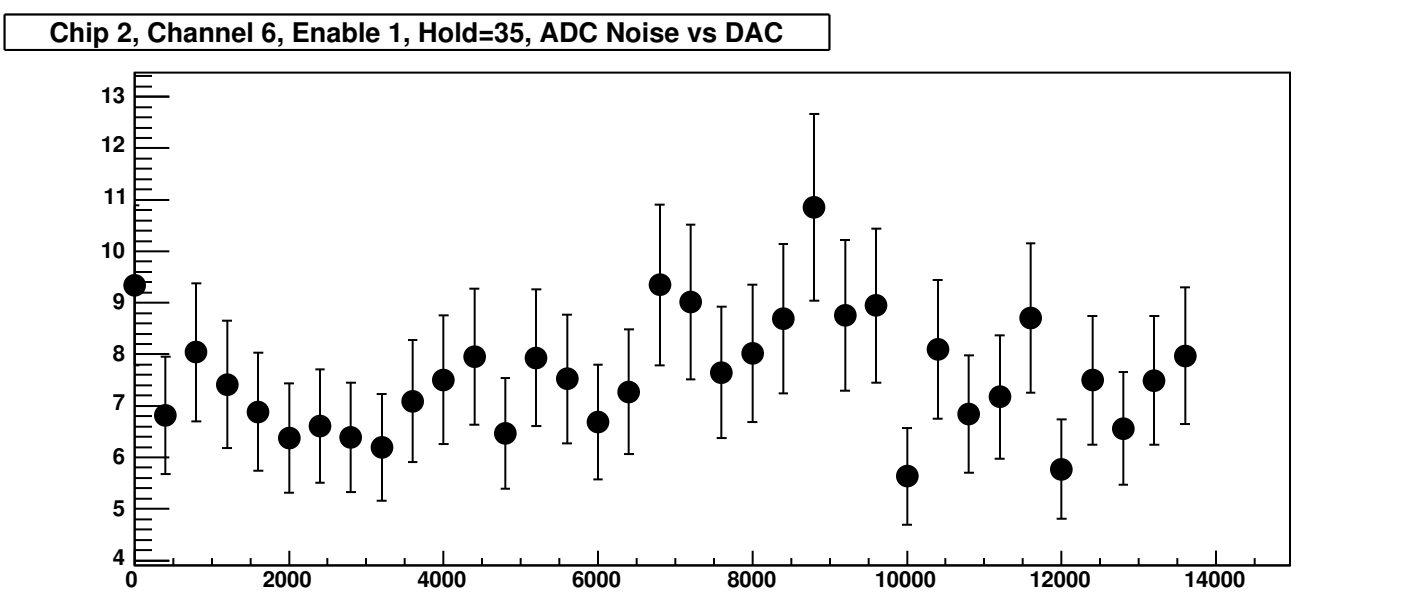

Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

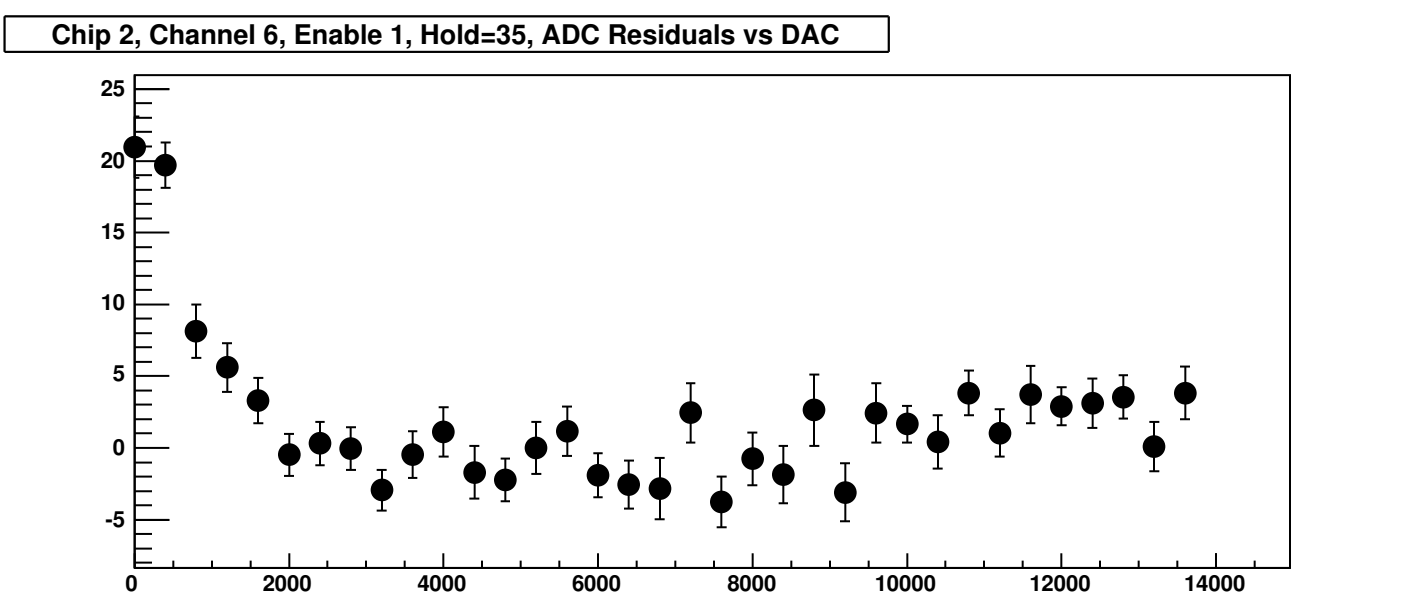

Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1468 / 23
p0 -248.6 ± 1.221
p1 0.1739 ± 0.0002109
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

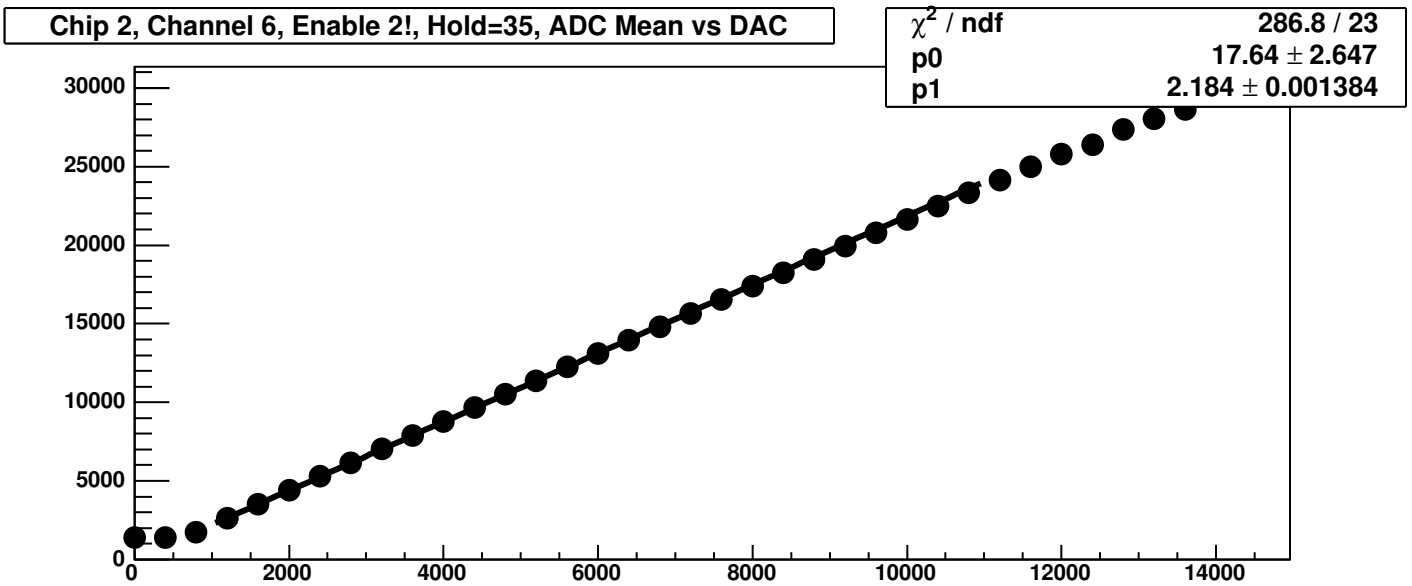

Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

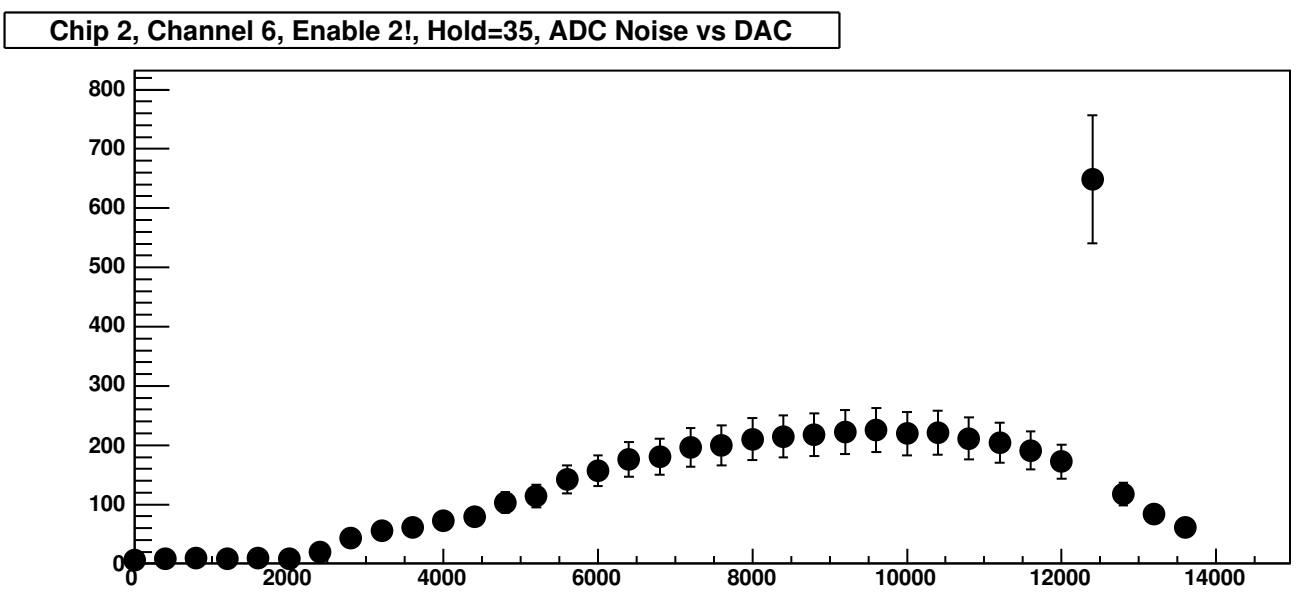

Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

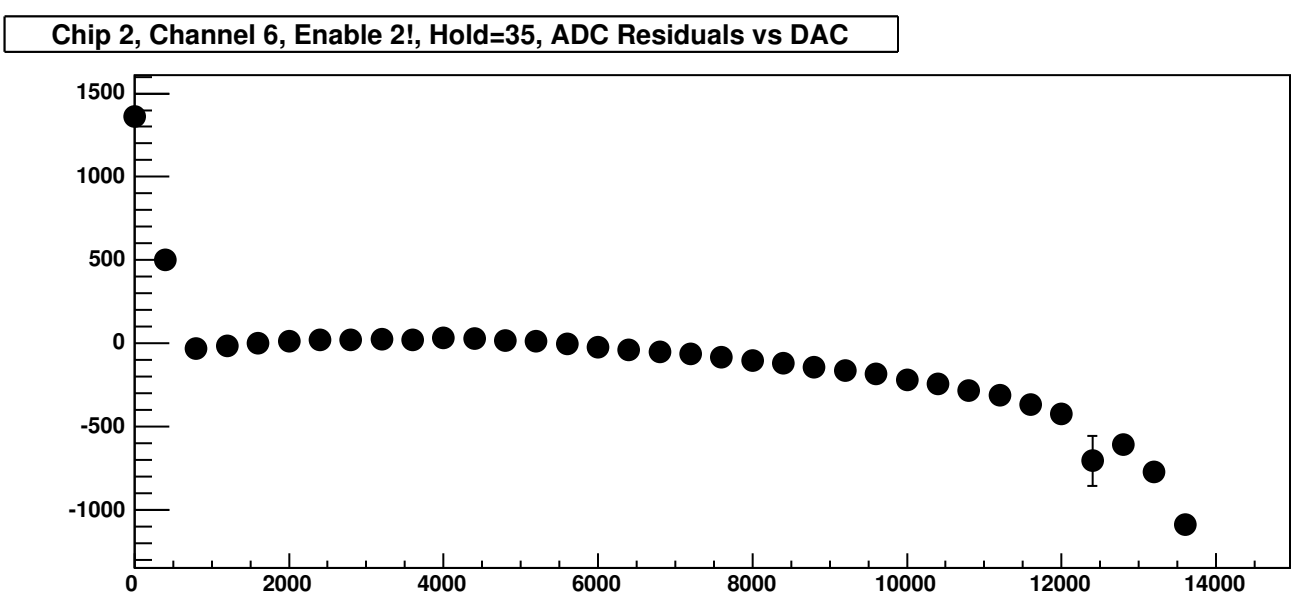

Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 48.89 / 23
p0 -51.7 ± 0.7304
p1 0.01748 ± 0.0001138
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

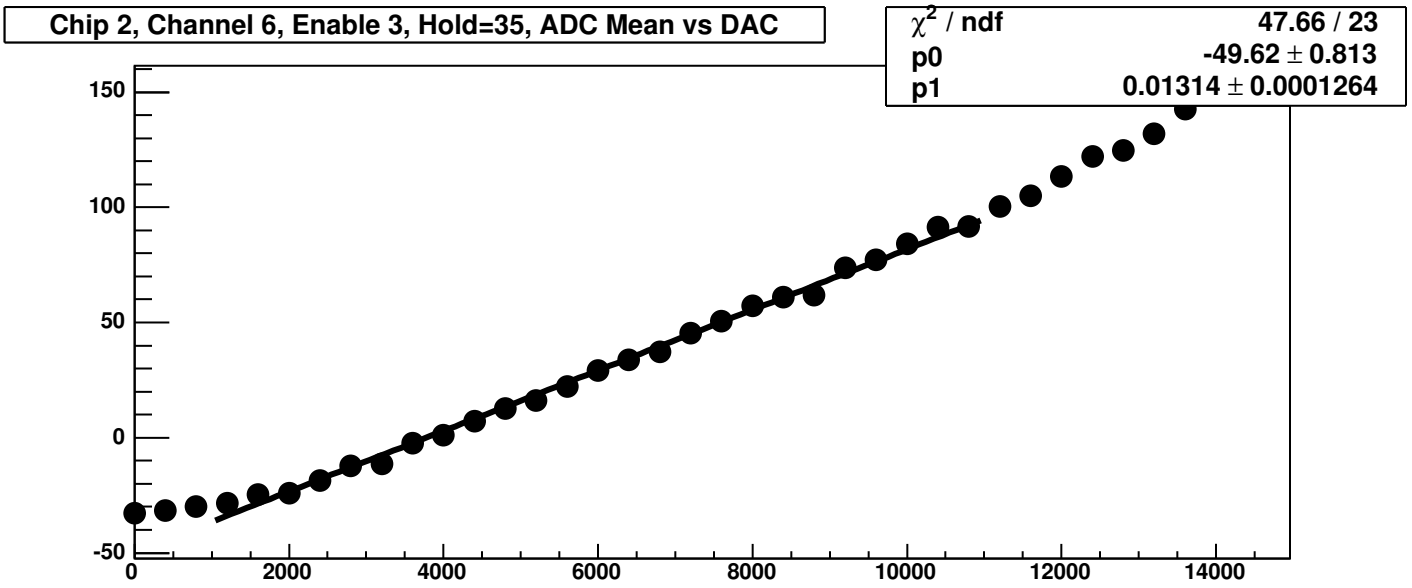

Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

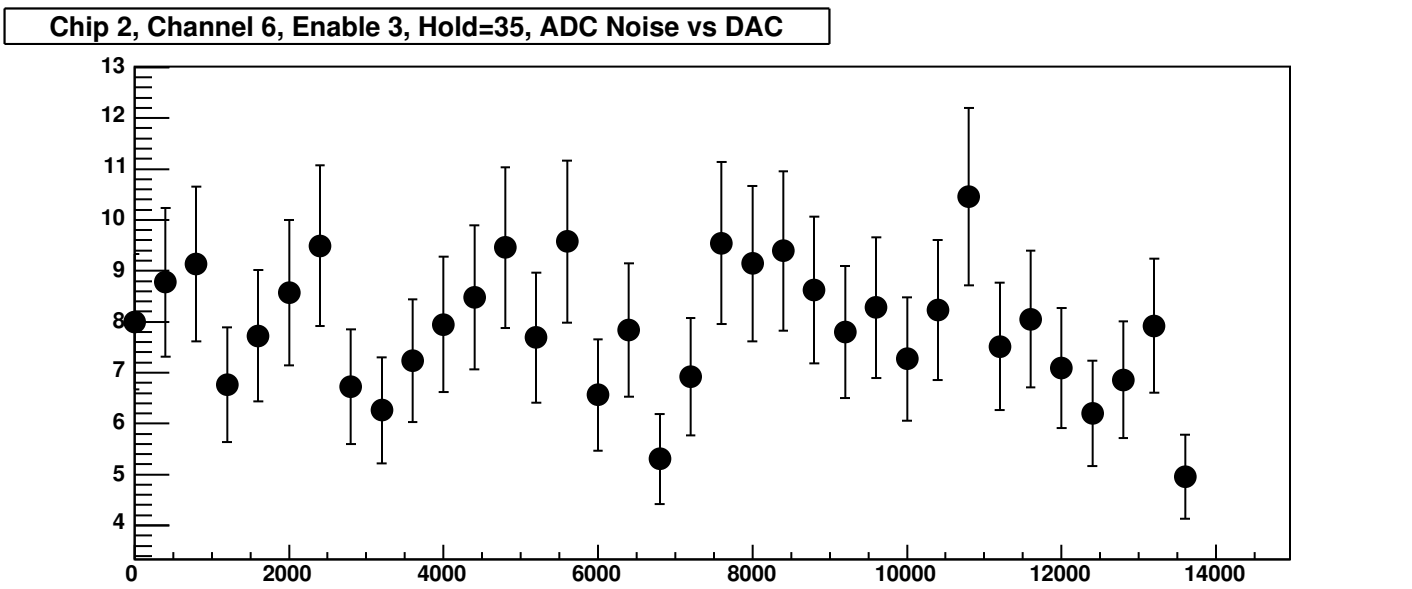

Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

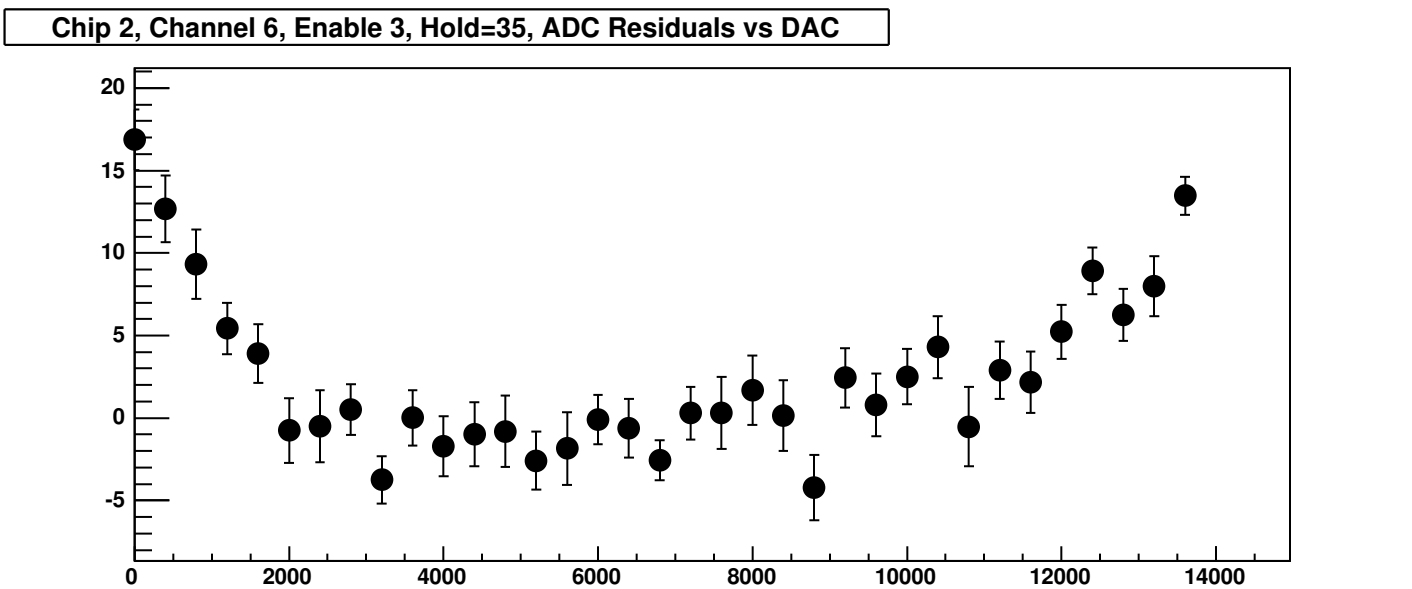

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 286.8 / 23
p0 17.64 ± 2.647
p1 2.184 ± 0.001384
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

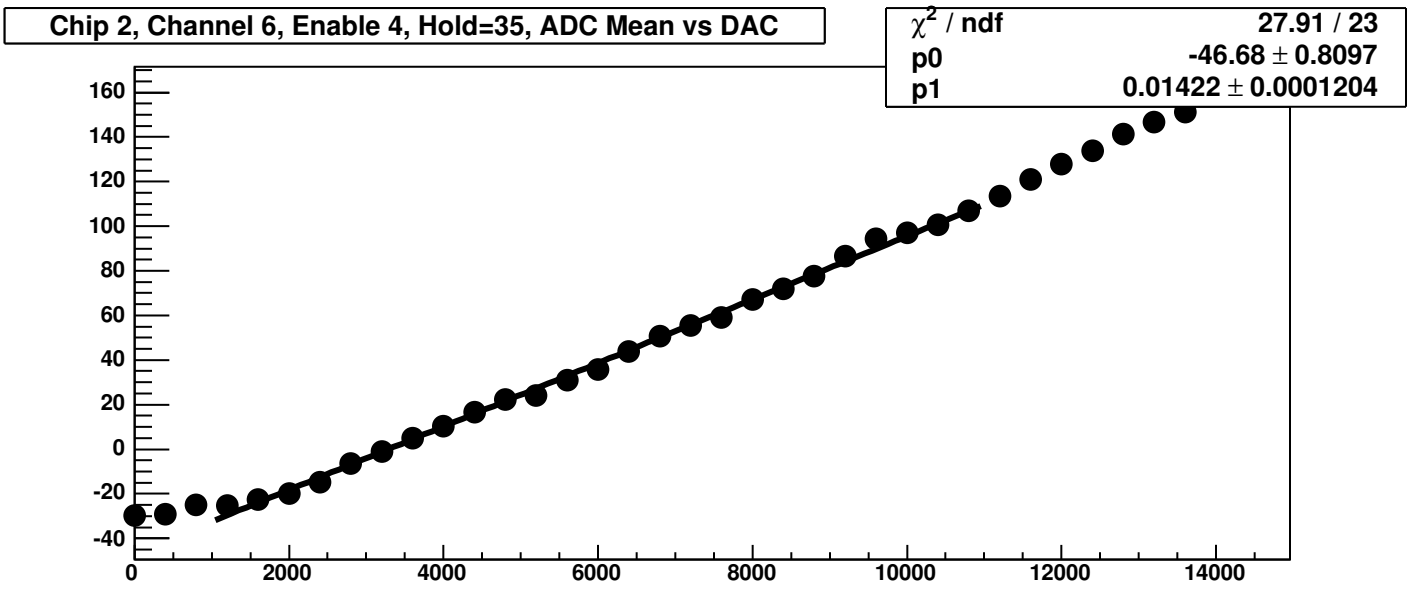

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

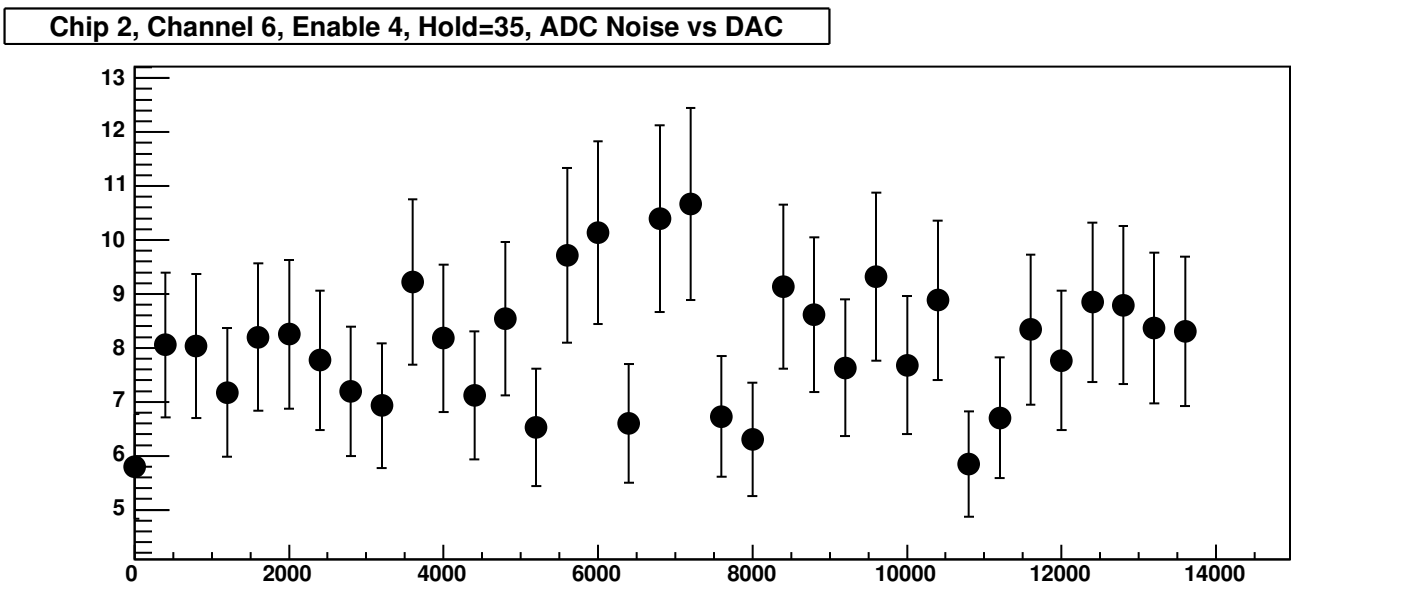

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

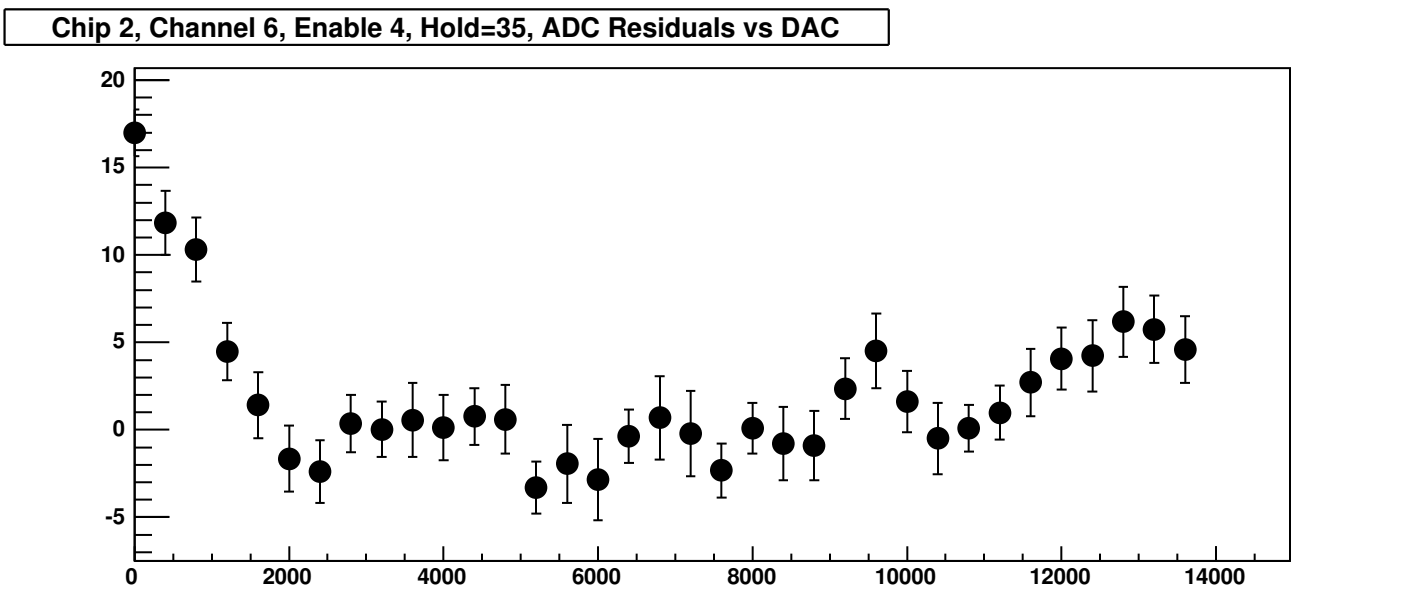

Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.66 / 23
p0 -49.62 ± 0.813
p1 0.01314 ± 0.0001264
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

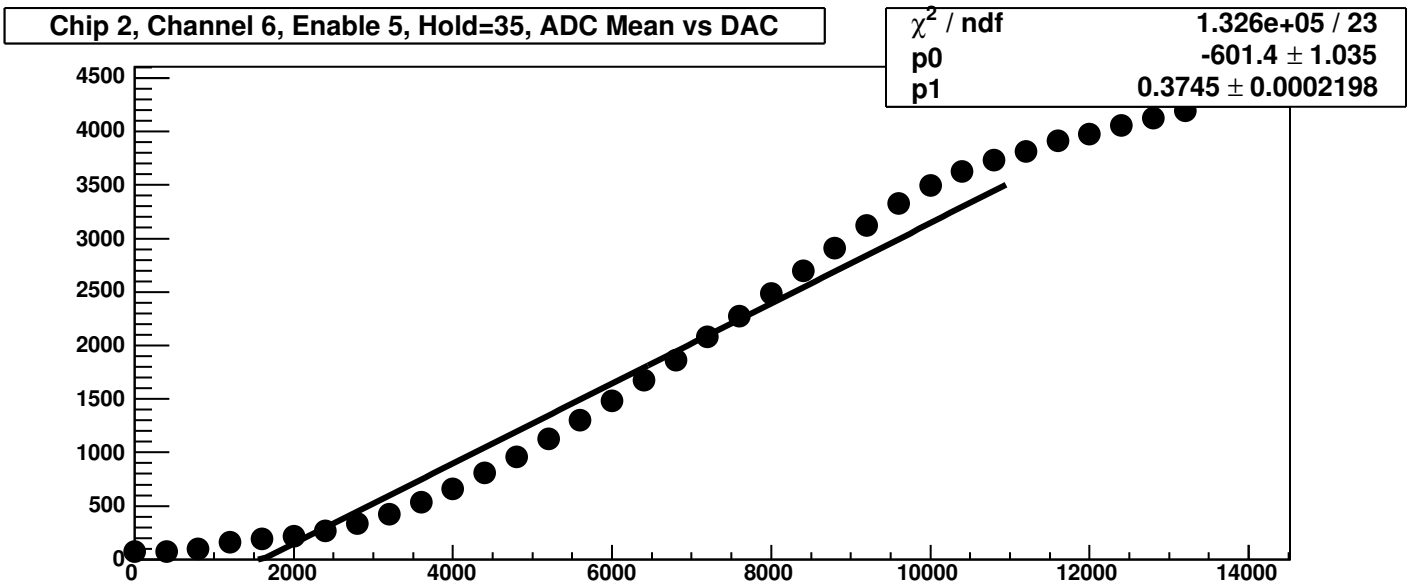

Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

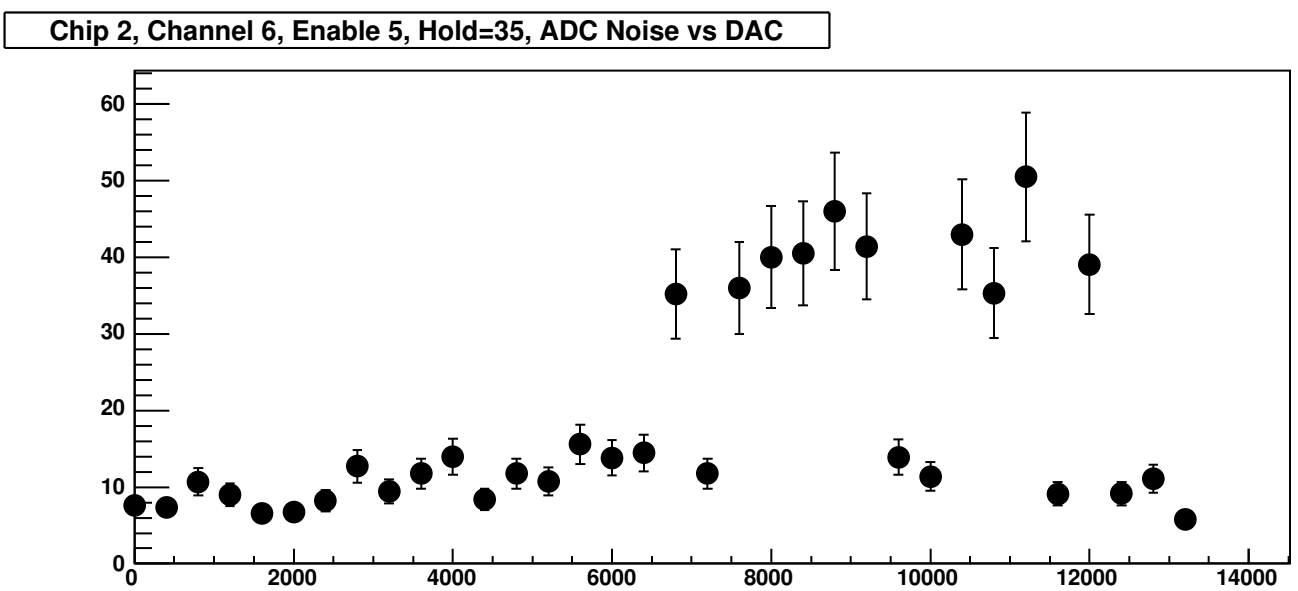

Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

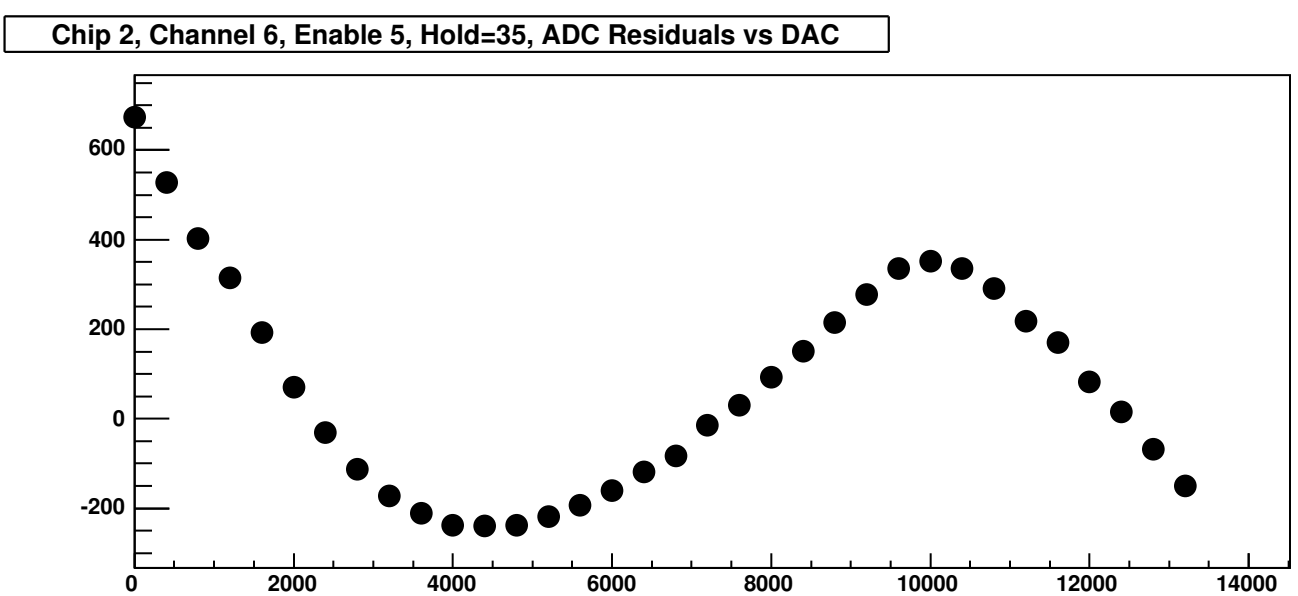

Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.91 / 23
p0 -46.68 ± 0.8097
p1 0.01422 ± 0.0001204
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

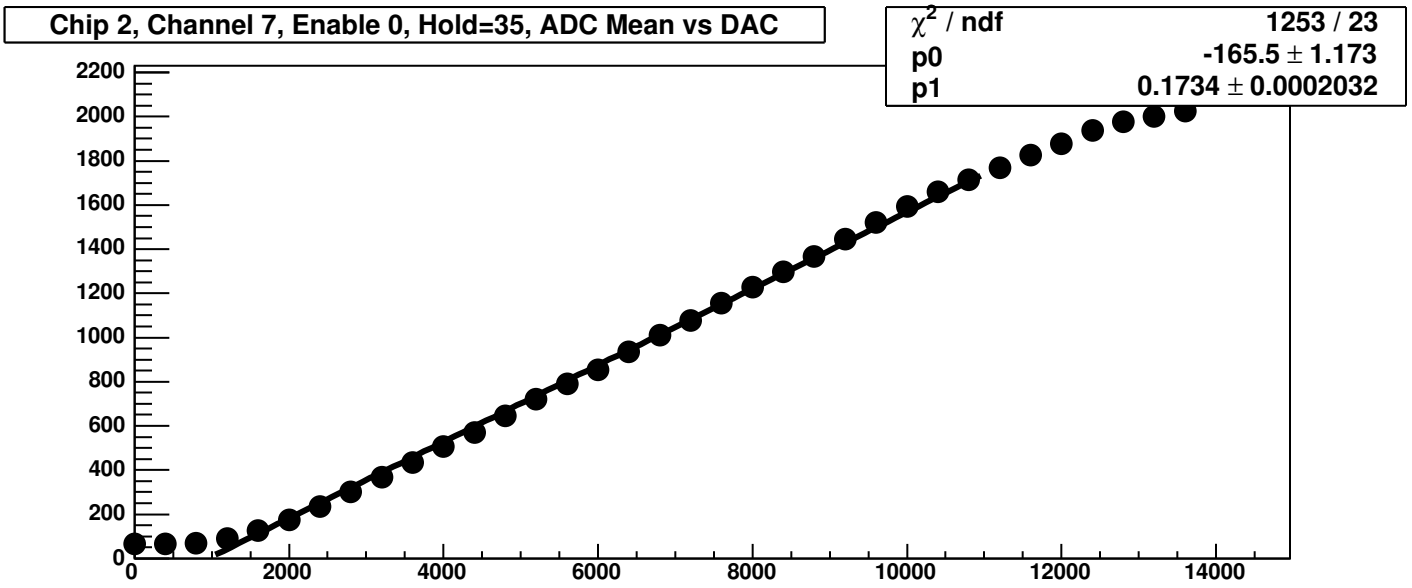

Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

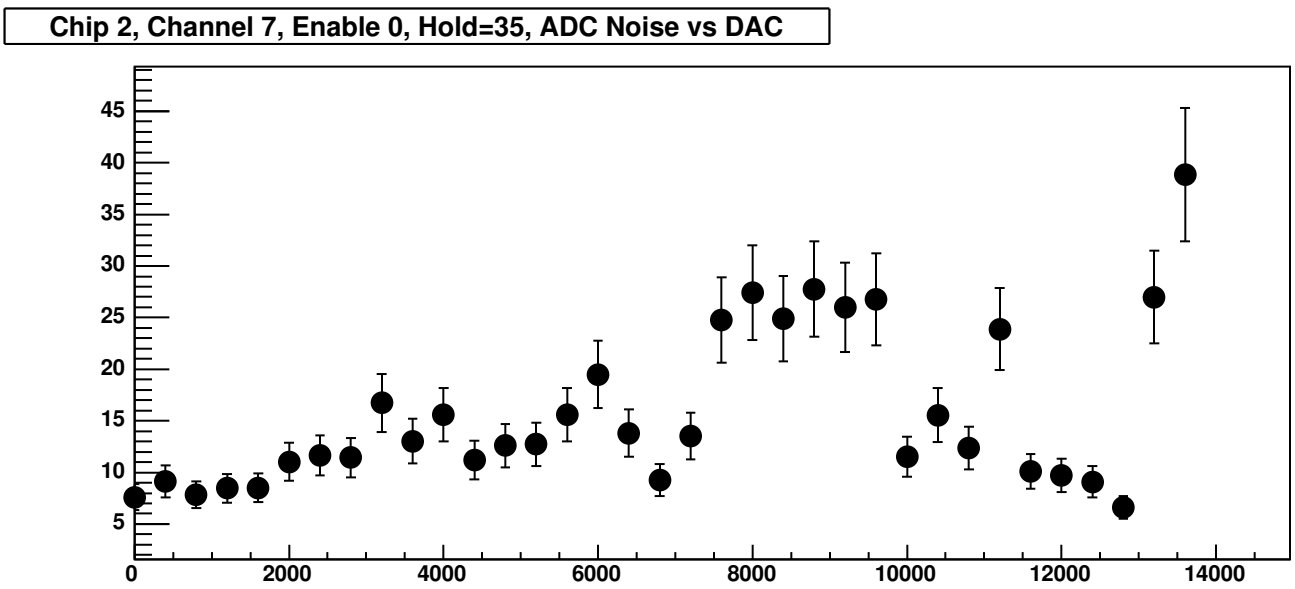

Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

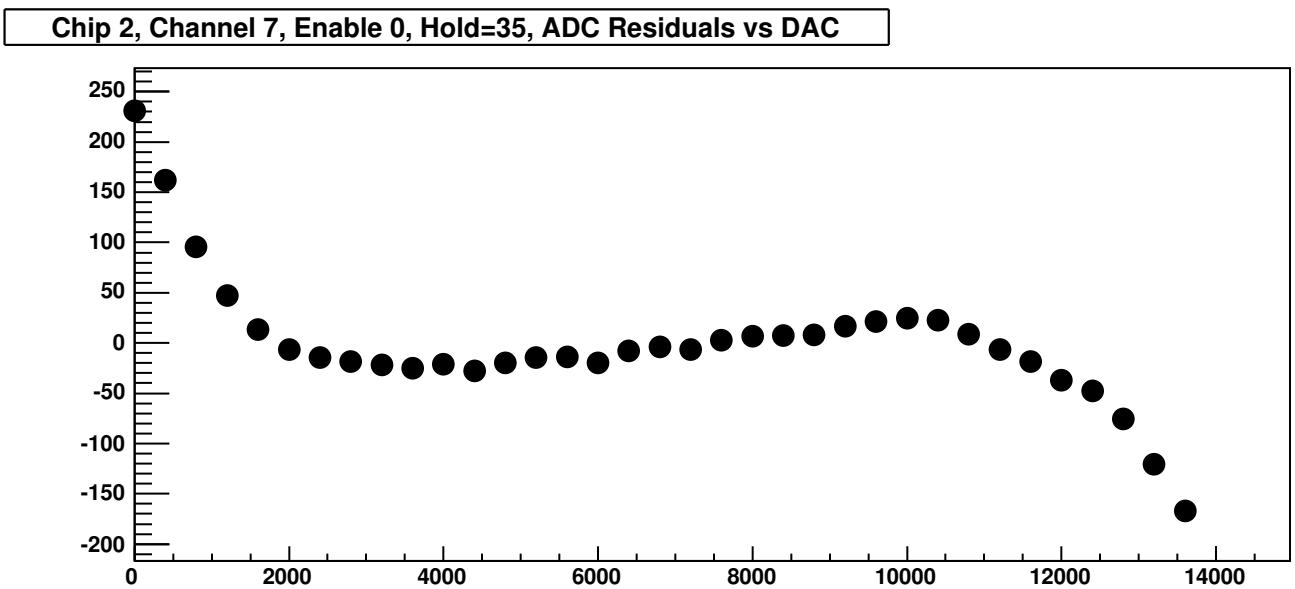

Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.326e+05 / 23
p0 -601.4 ± 1.035
p1 0.3745 ± 0.0002198
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

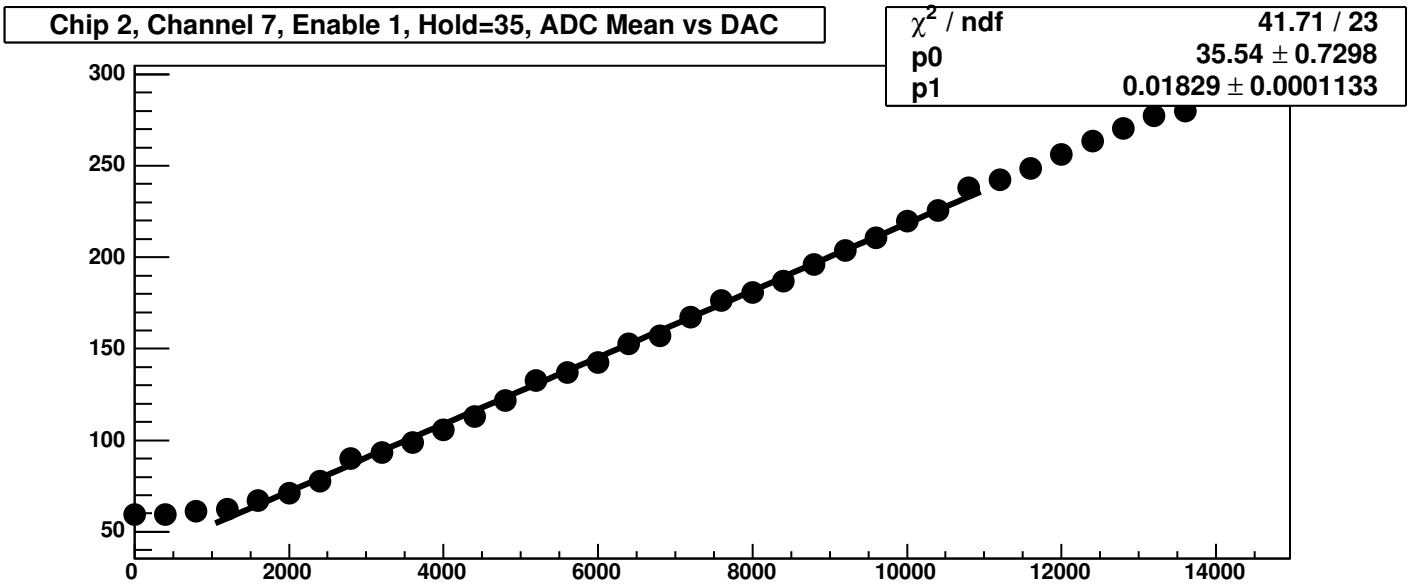

Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

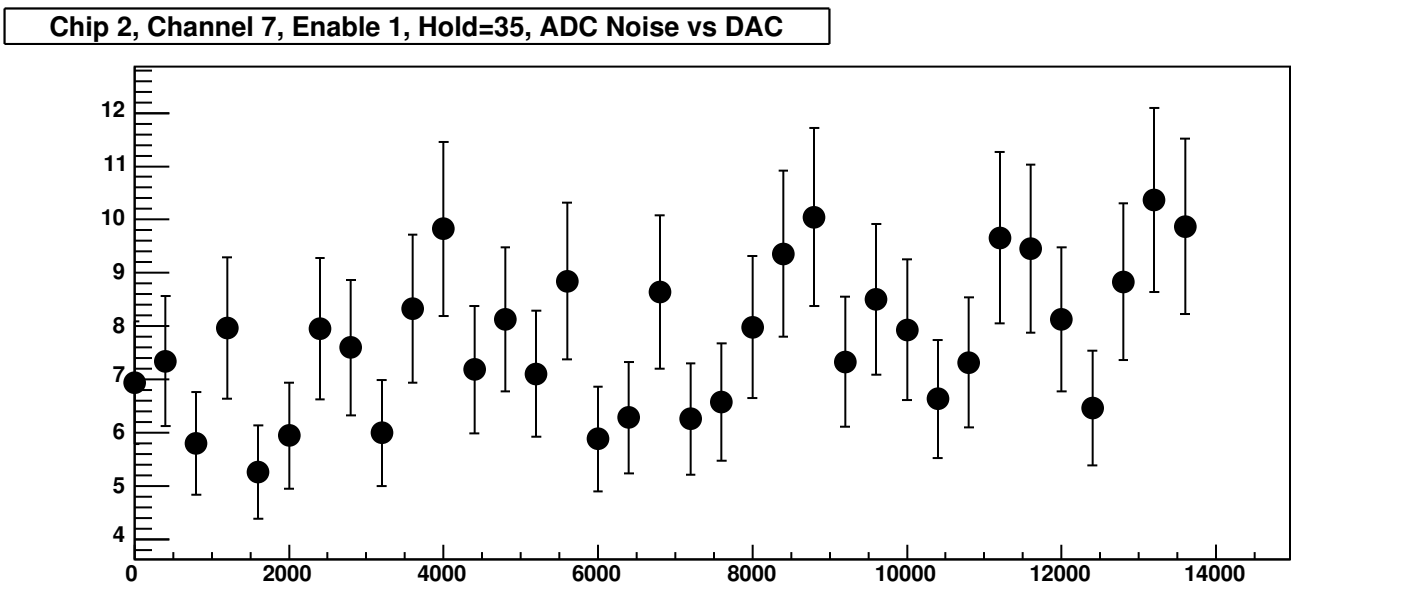

Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

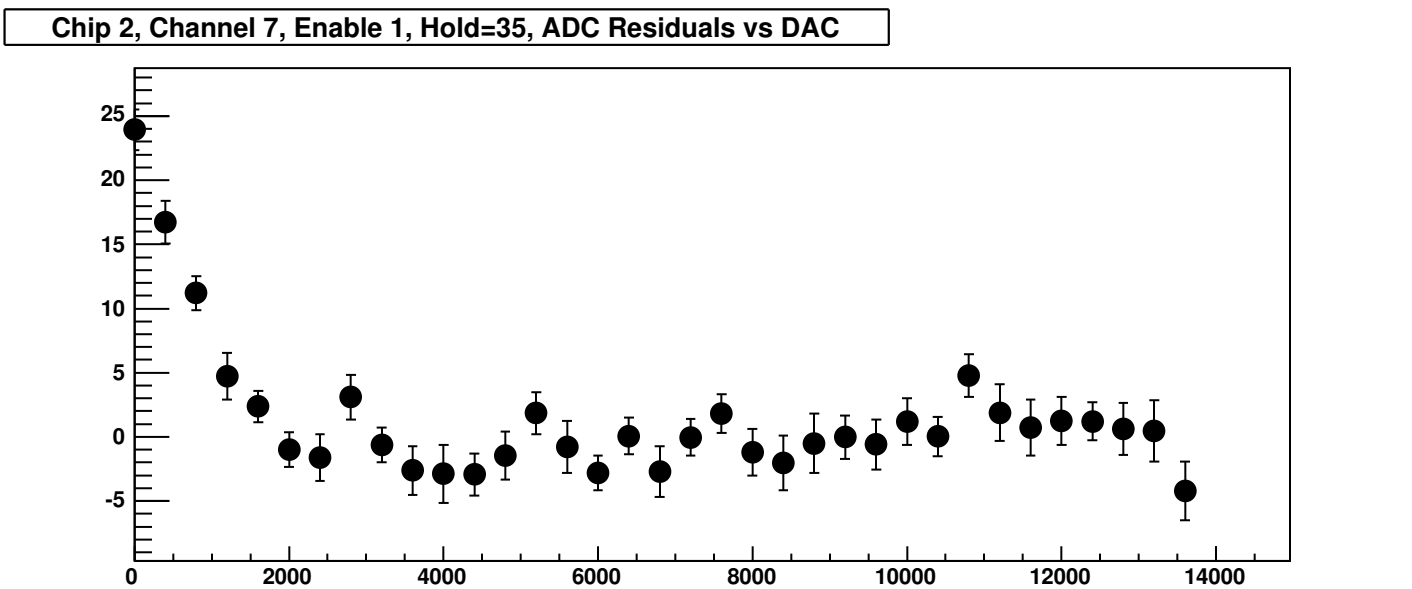

Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1253 / 23
p0 -165.5 ± 1.173
p1 0.1734 ± 0.0002032
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

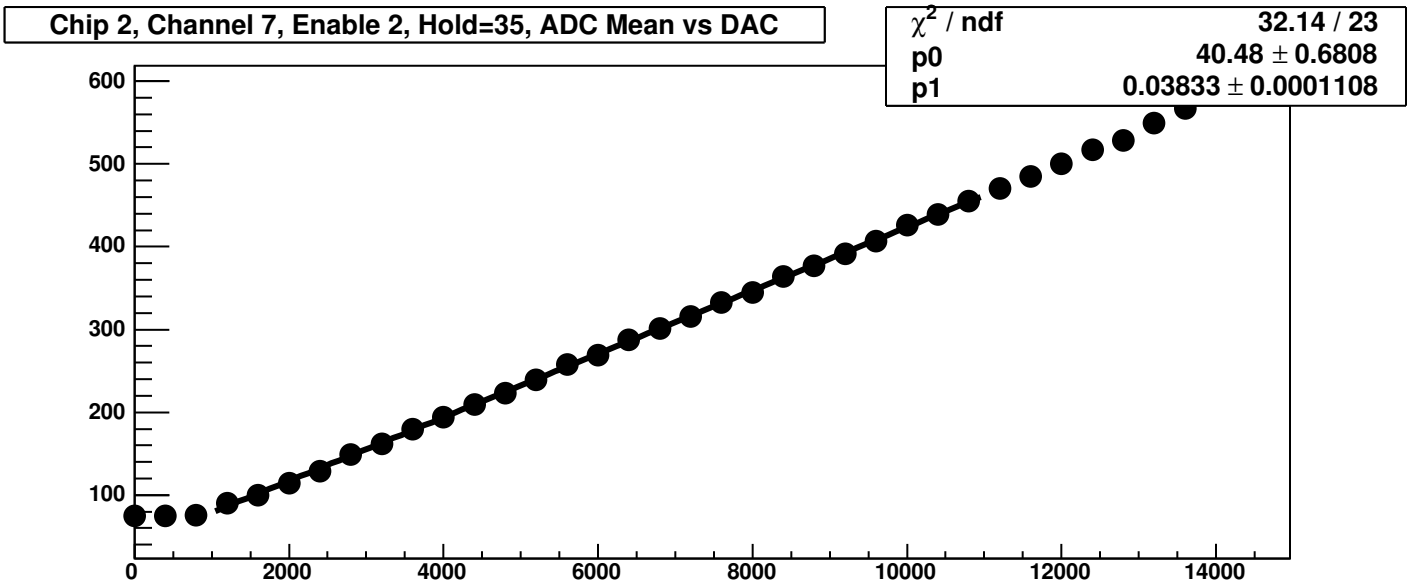

Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

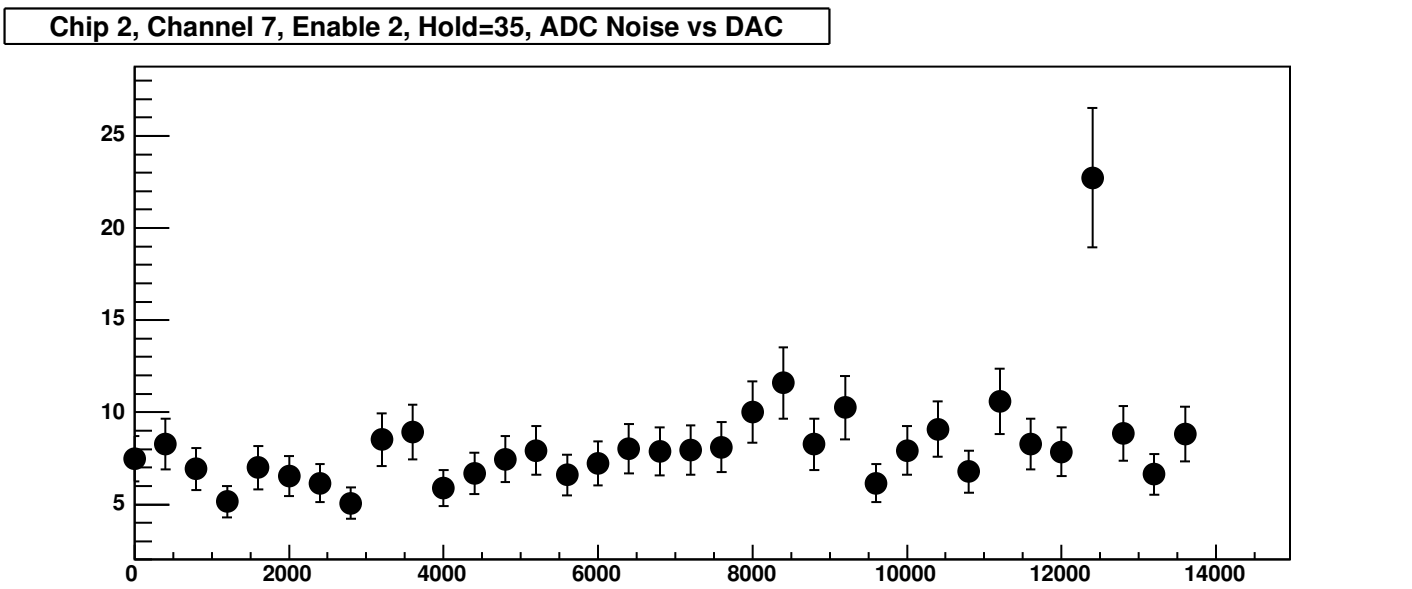

Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

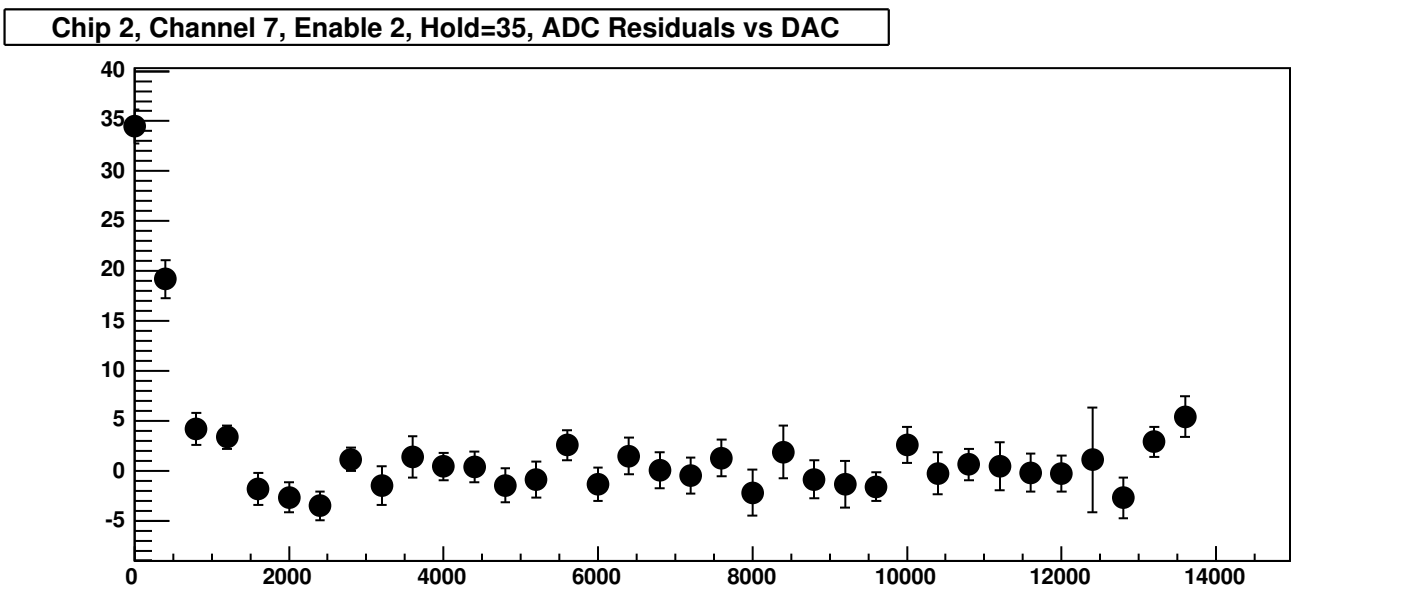

Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.71 / 23
p0 35.54 ± 0.7298
p1 0.01829 ± 0.0001133
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

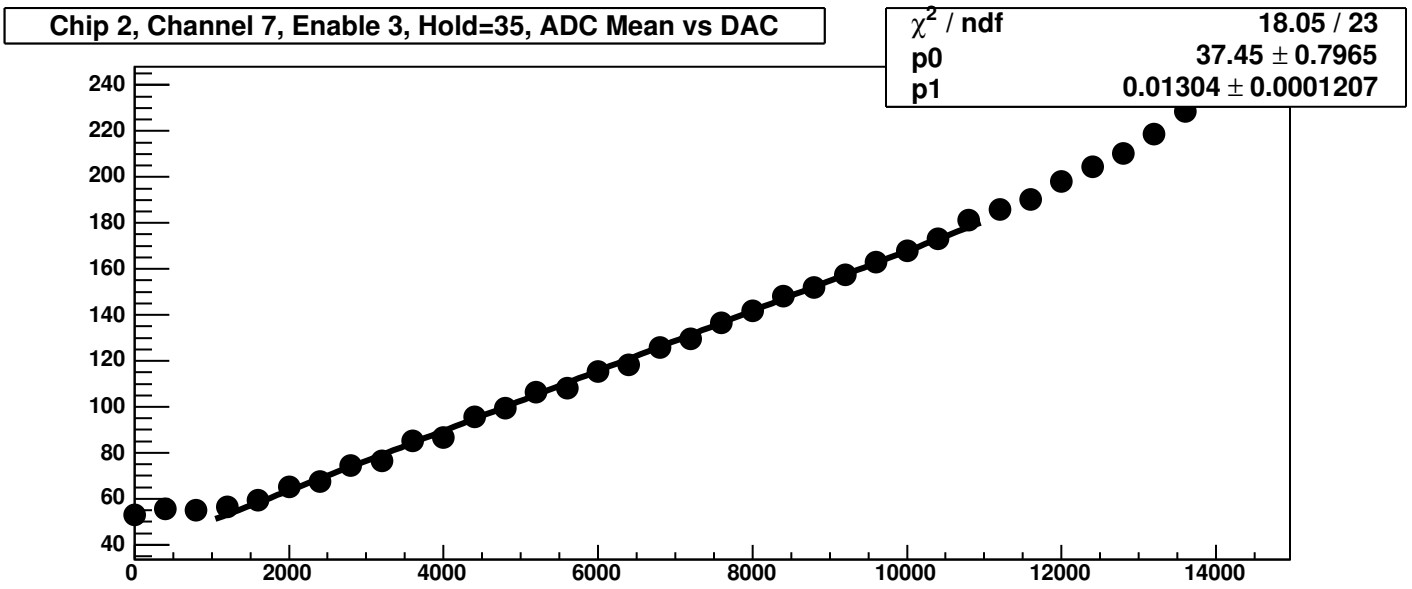

Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

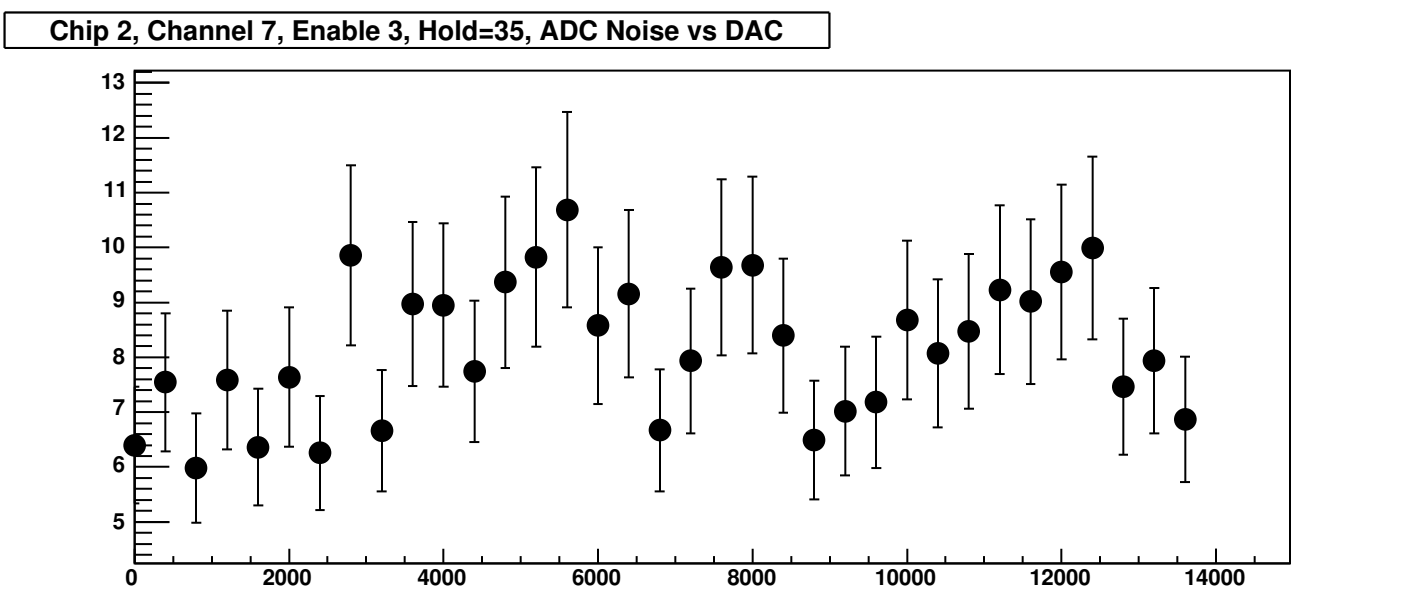

Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

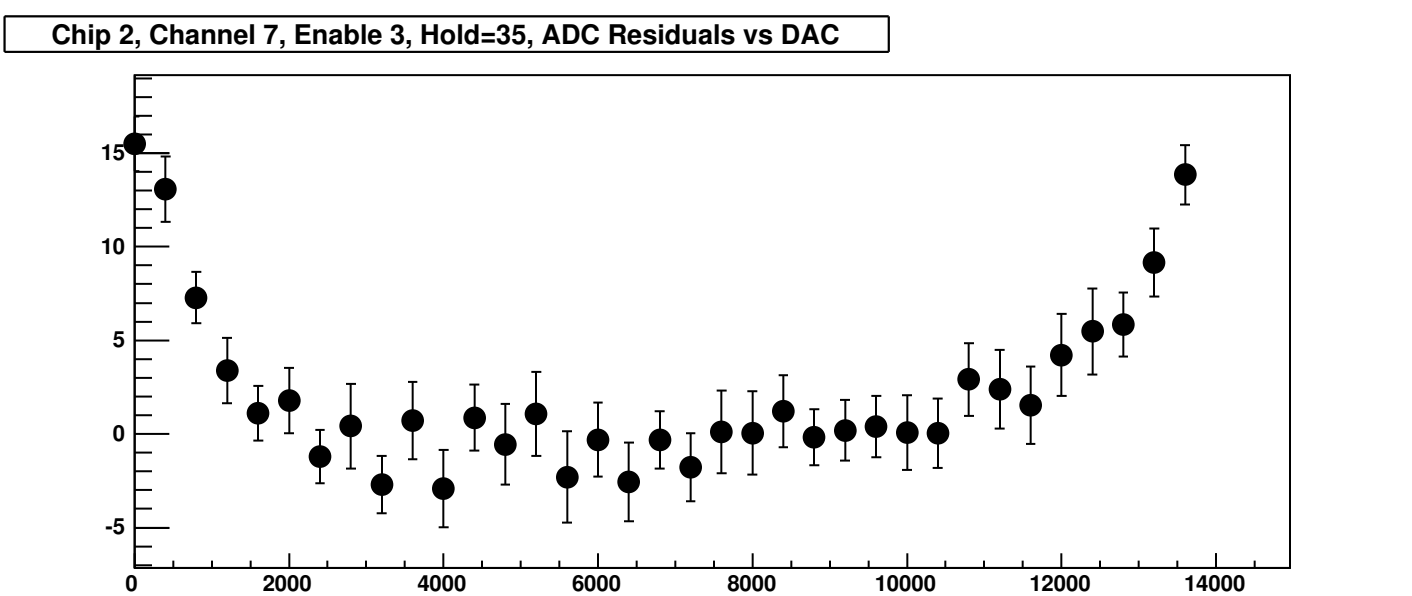

Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.14 / 23
p0 40.48 ± 0.6808
p1 0.03833 ± 0.0001108
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

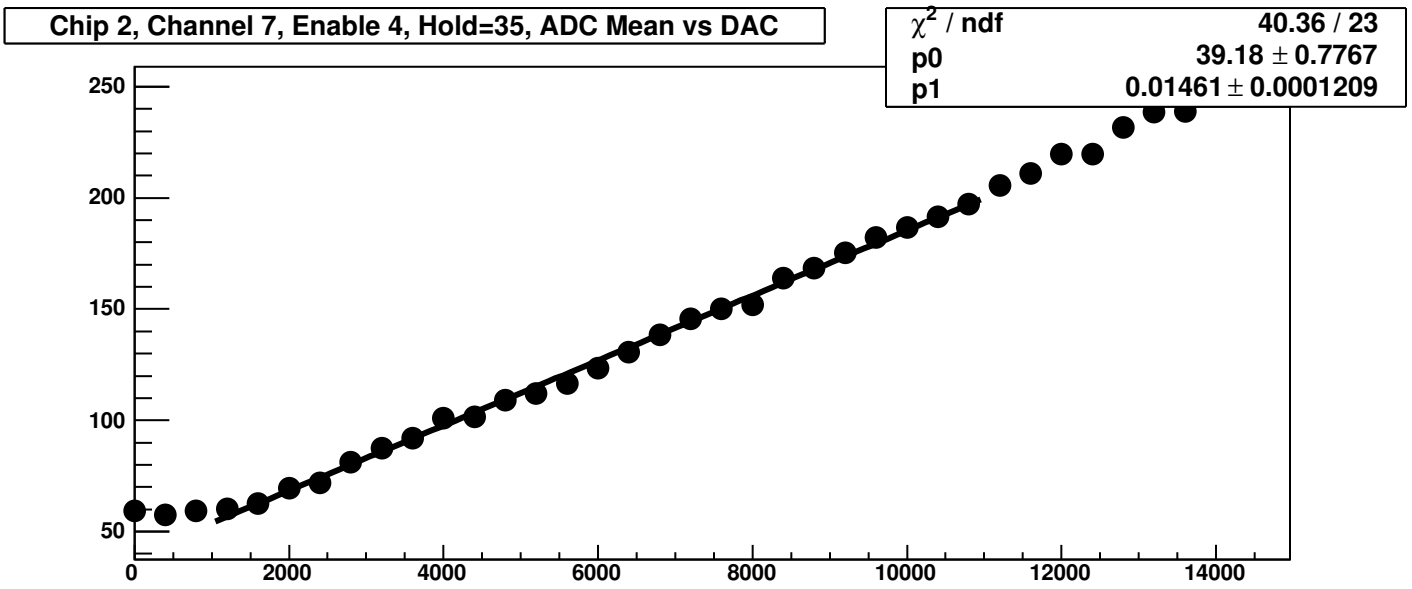

Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

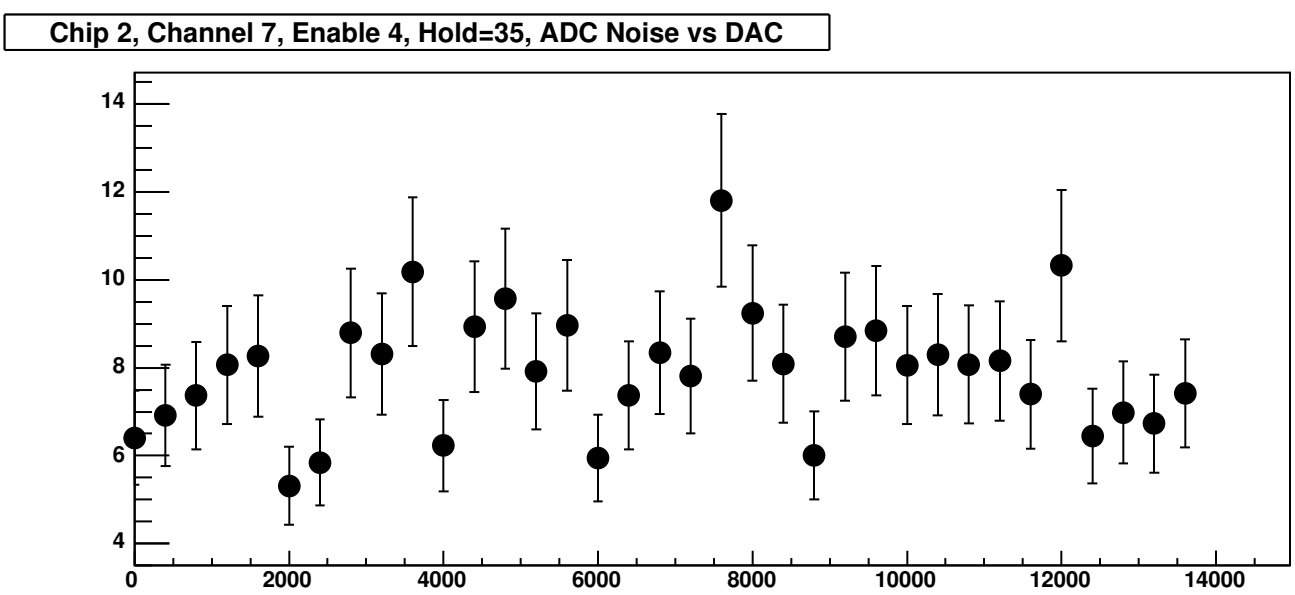

Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
40
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

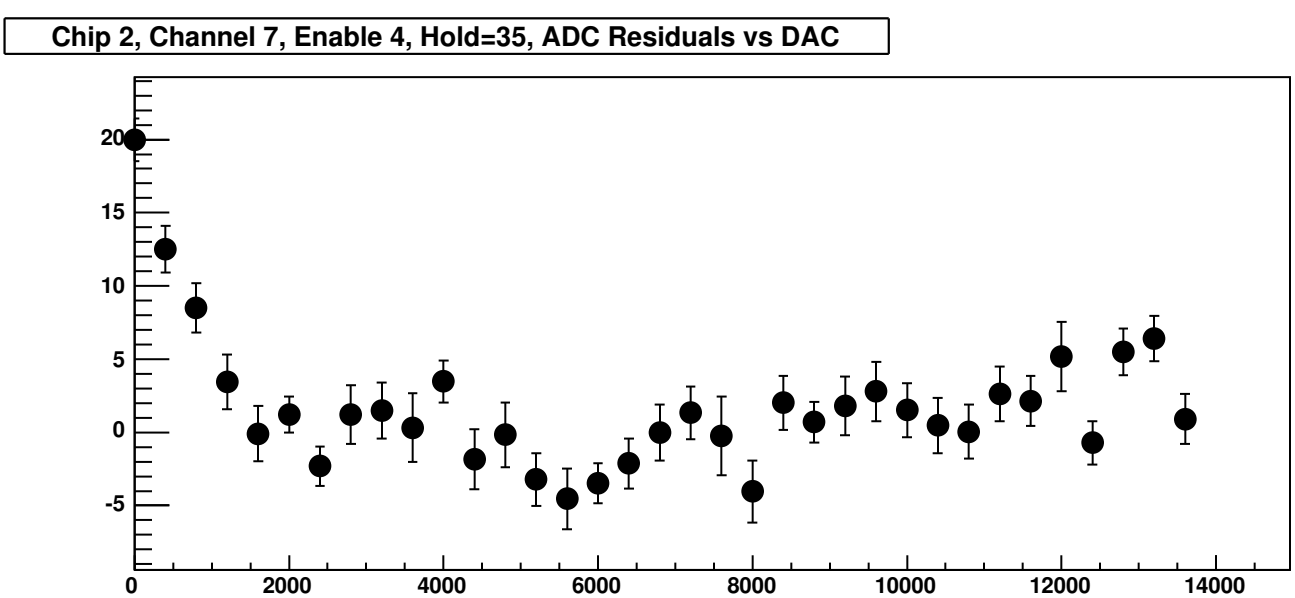

Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.05 / 23
p0 37.45 ± 0.7965
p1 0.01304 ± 0.0001207
240
220
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

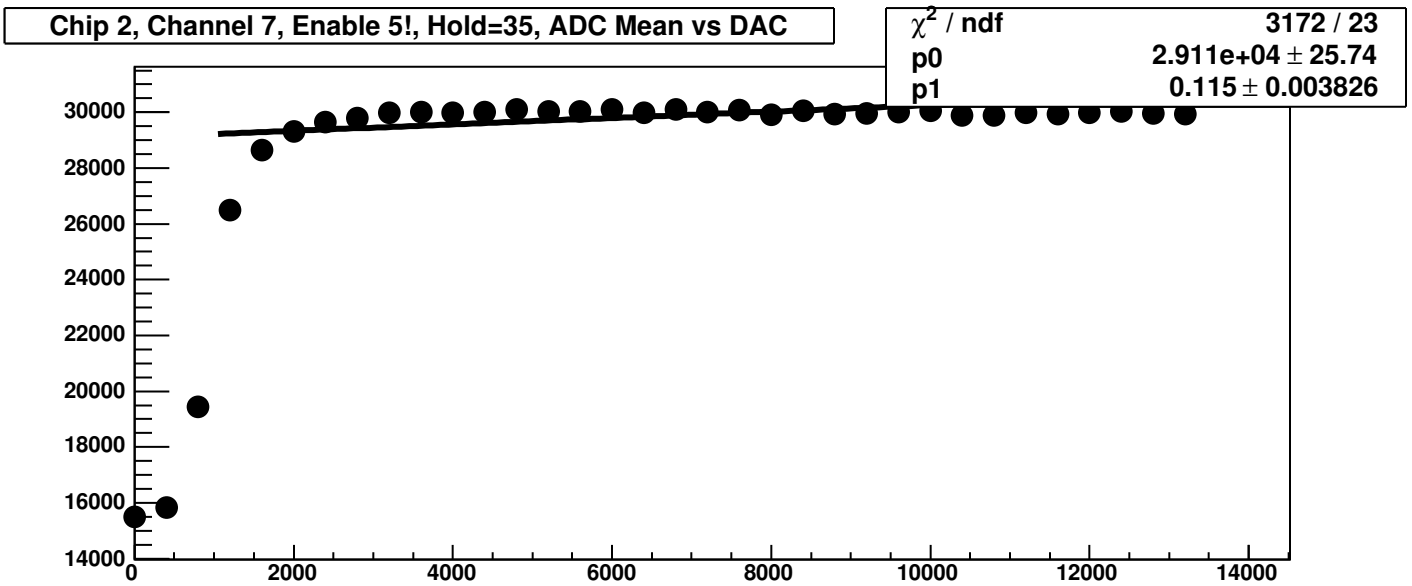

Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

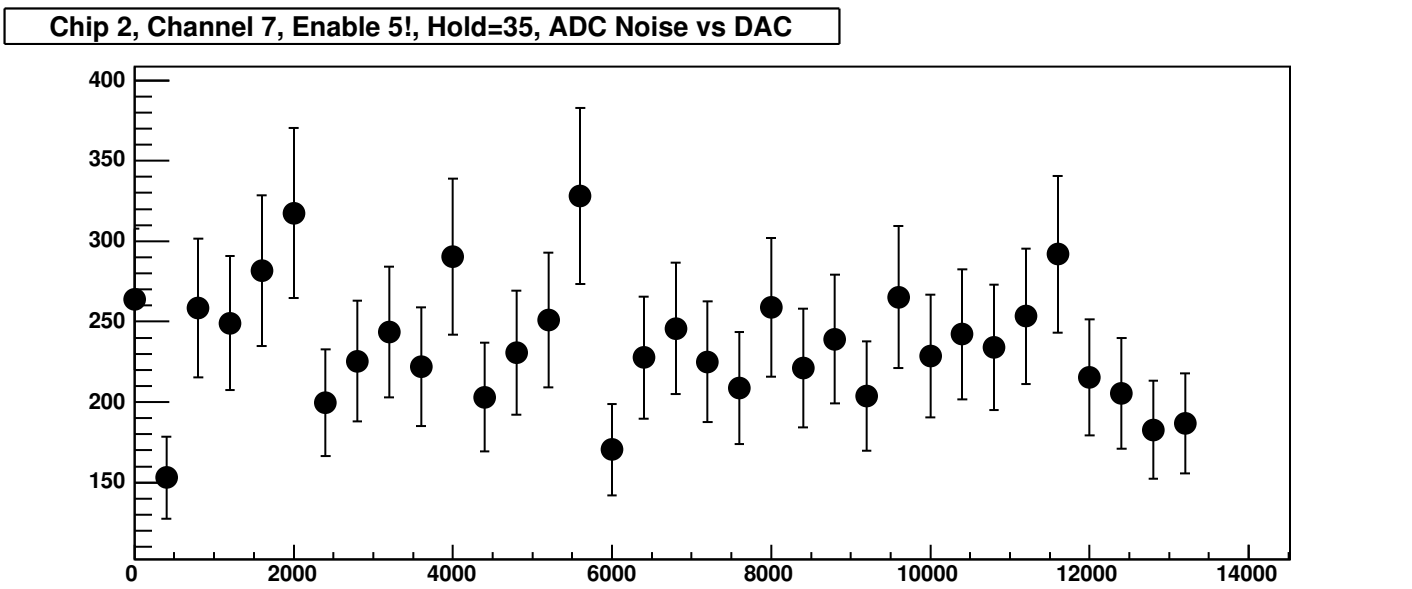

Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

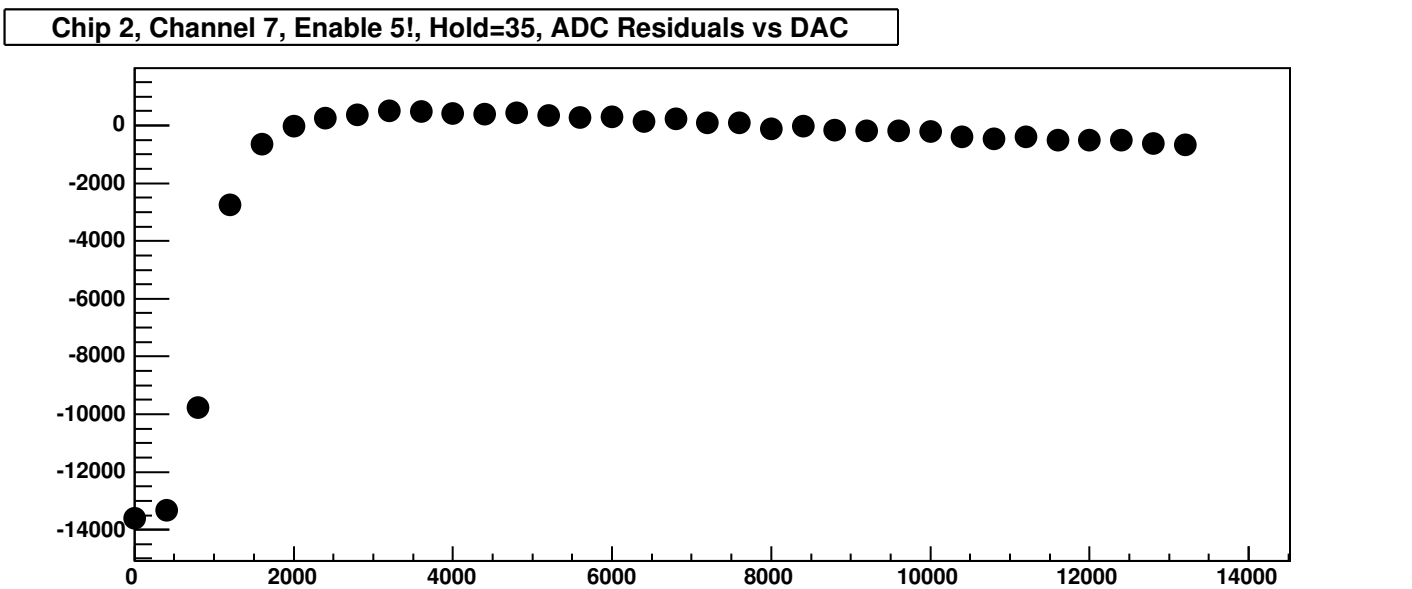

Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.36 / 23
p0 39.18 ± 0.7767
p1 0.01461 ± 0.0001209
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

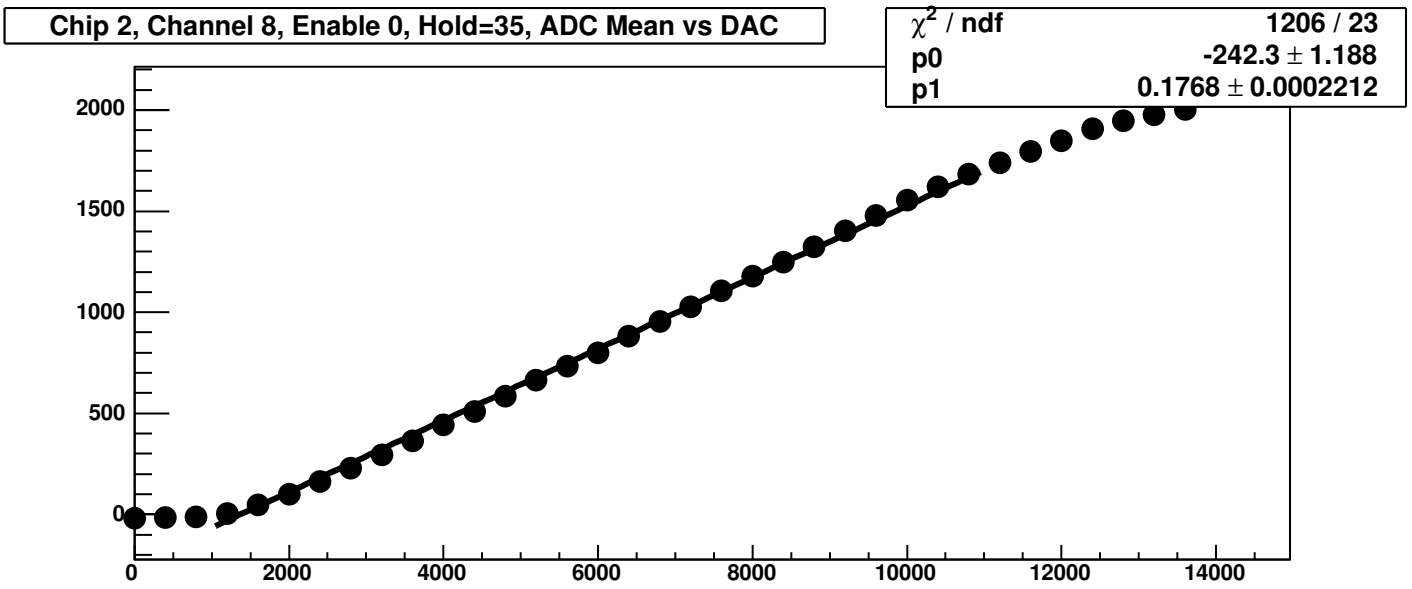

Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

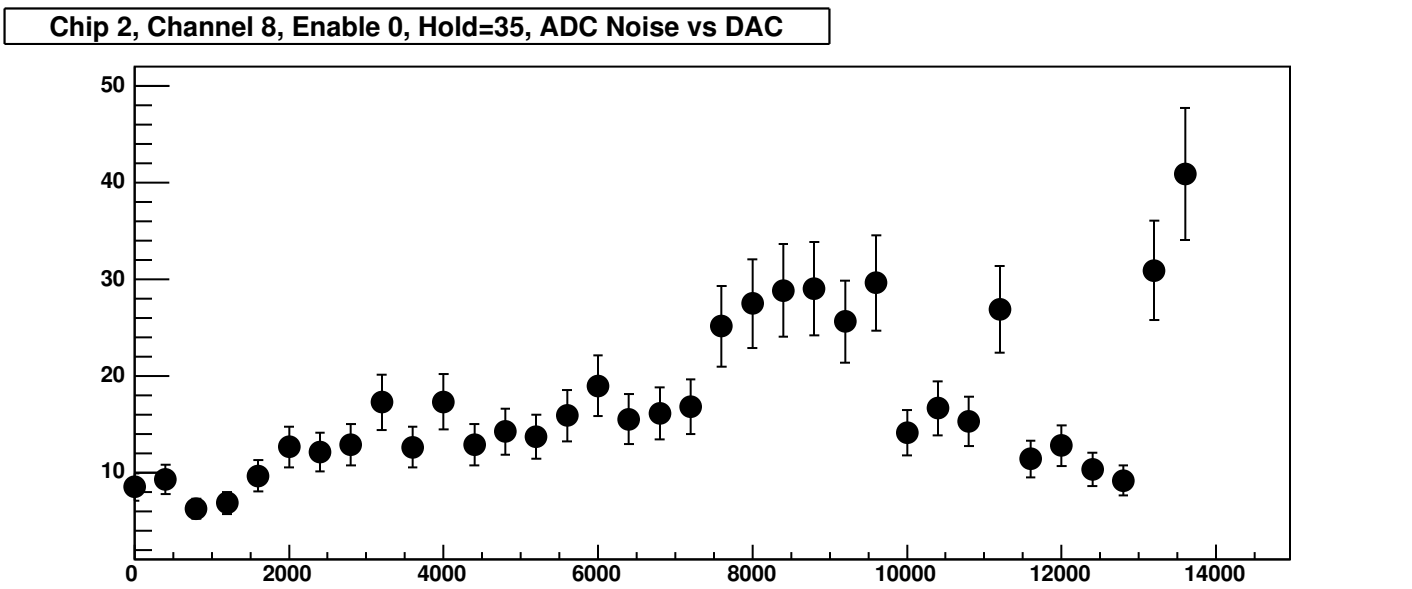

Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

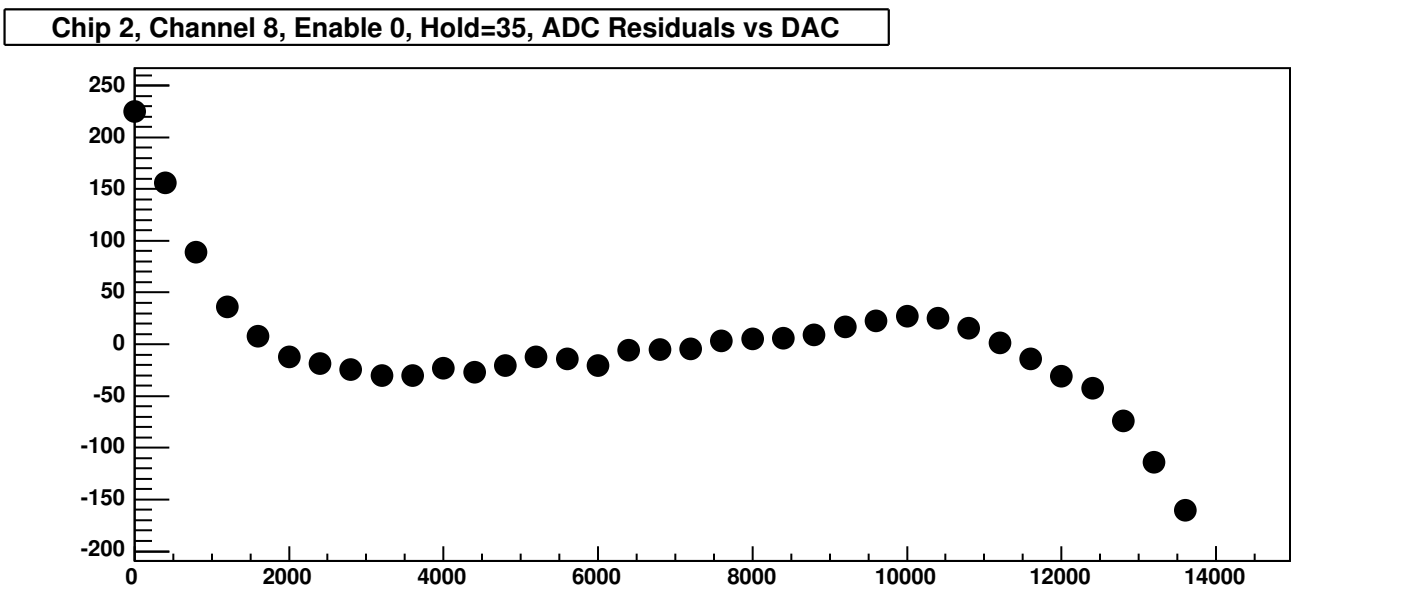

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3172 / 23
p0 2.911e+04 ± 25.74
p1 0.115 ± 0.003826
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

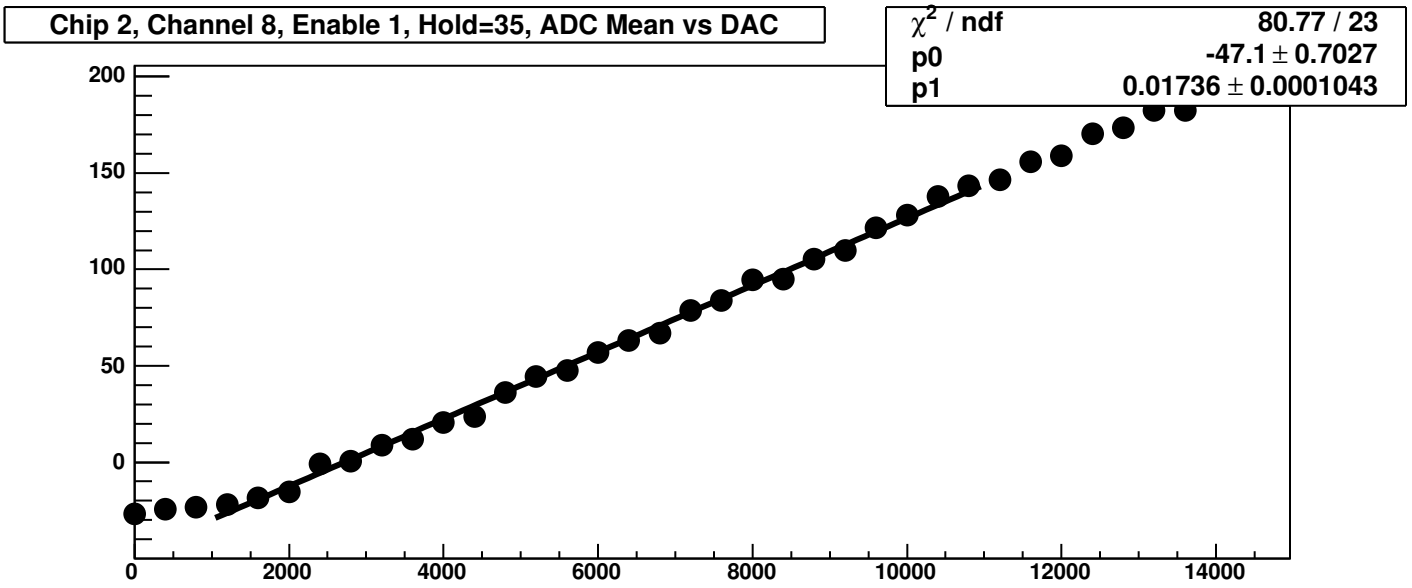

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

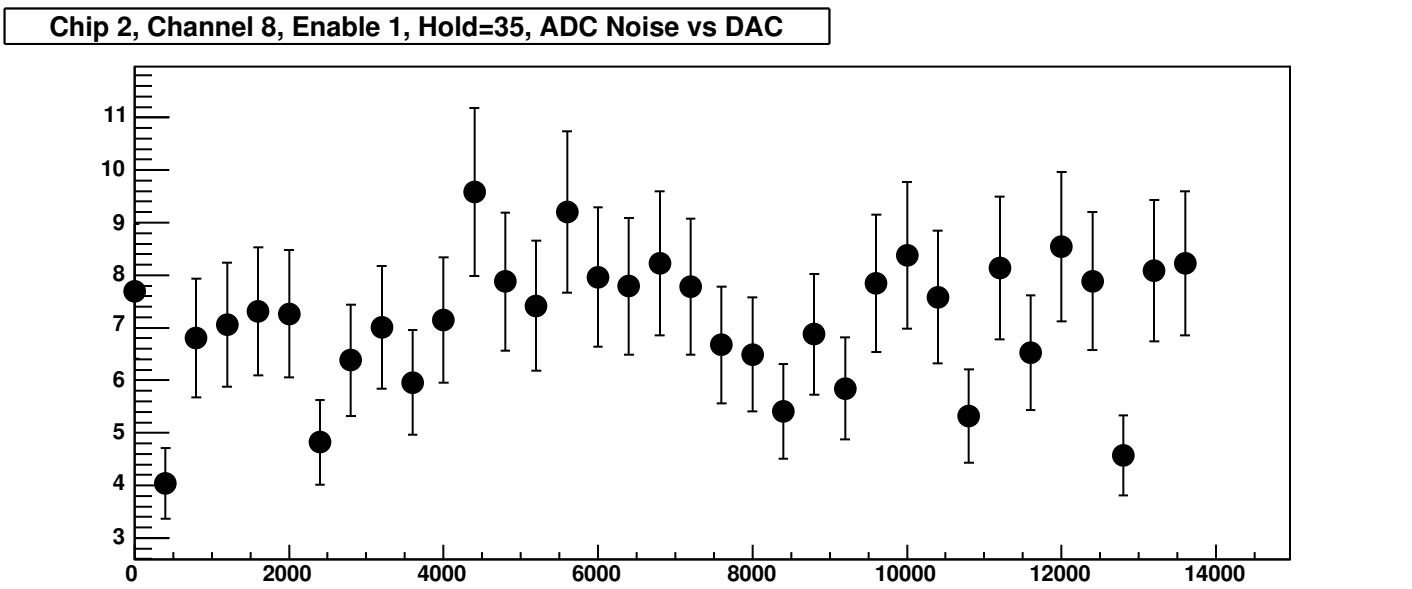

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

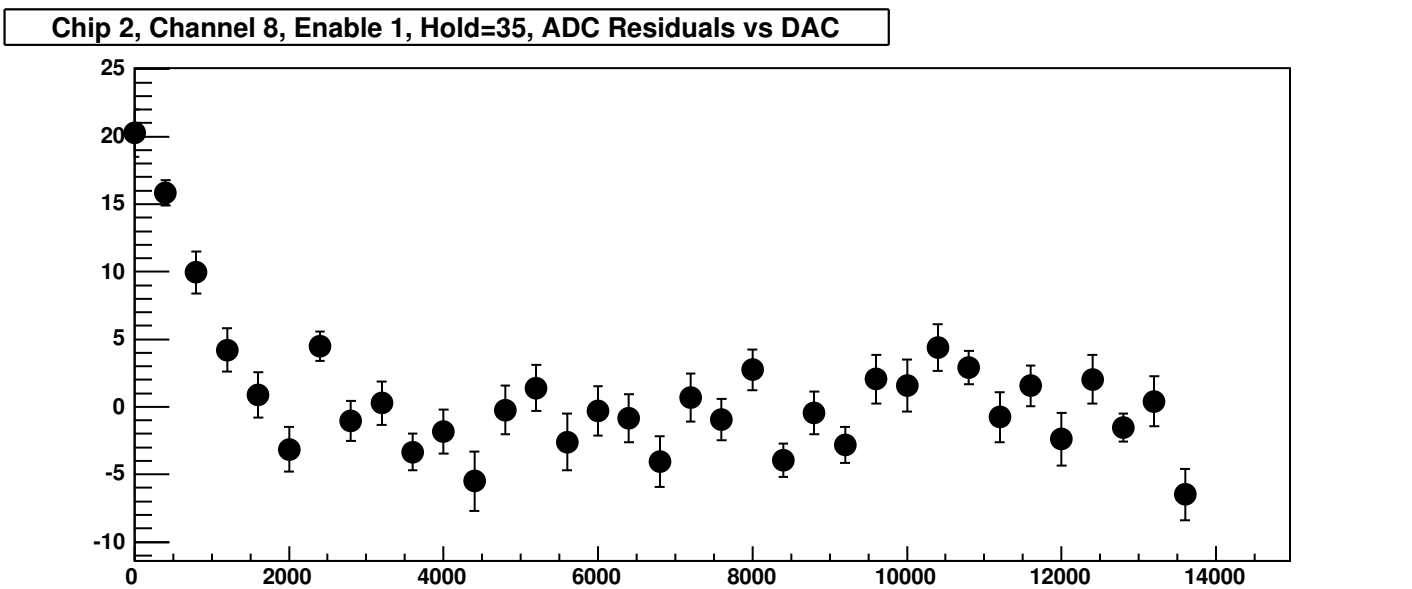

Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1206 / 23
p0 -242.3 ± 1.188
p1 0.1768 ± 0.0002212
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

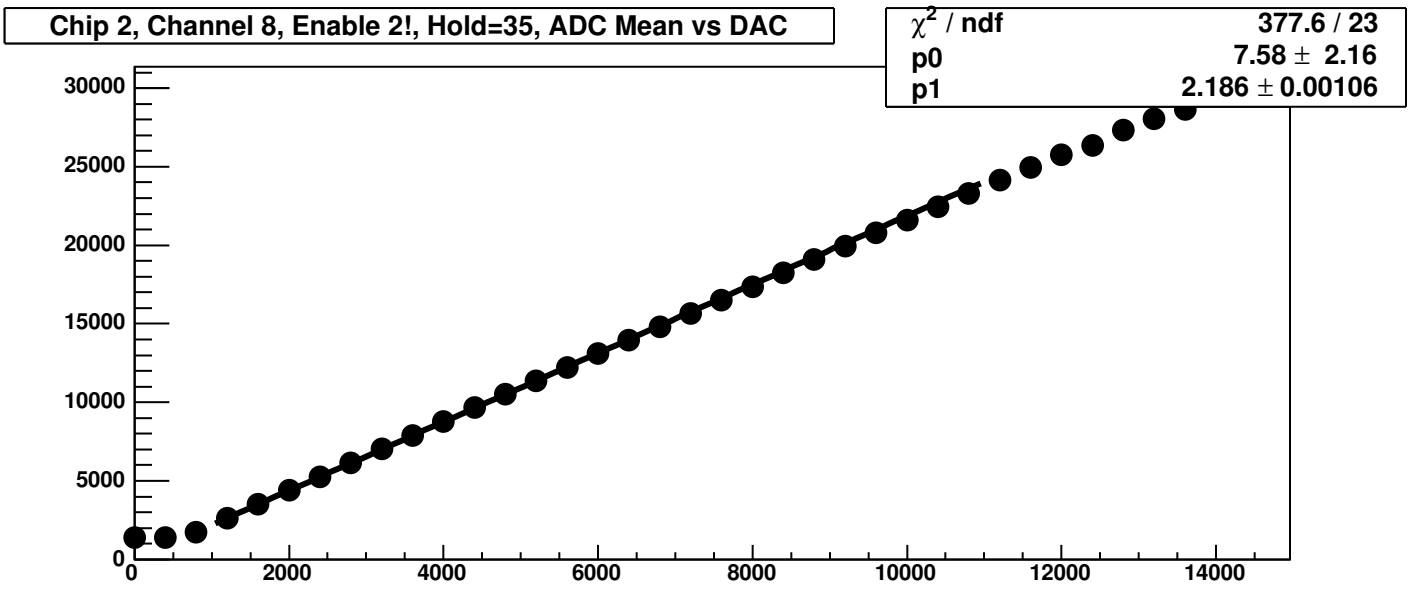

Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

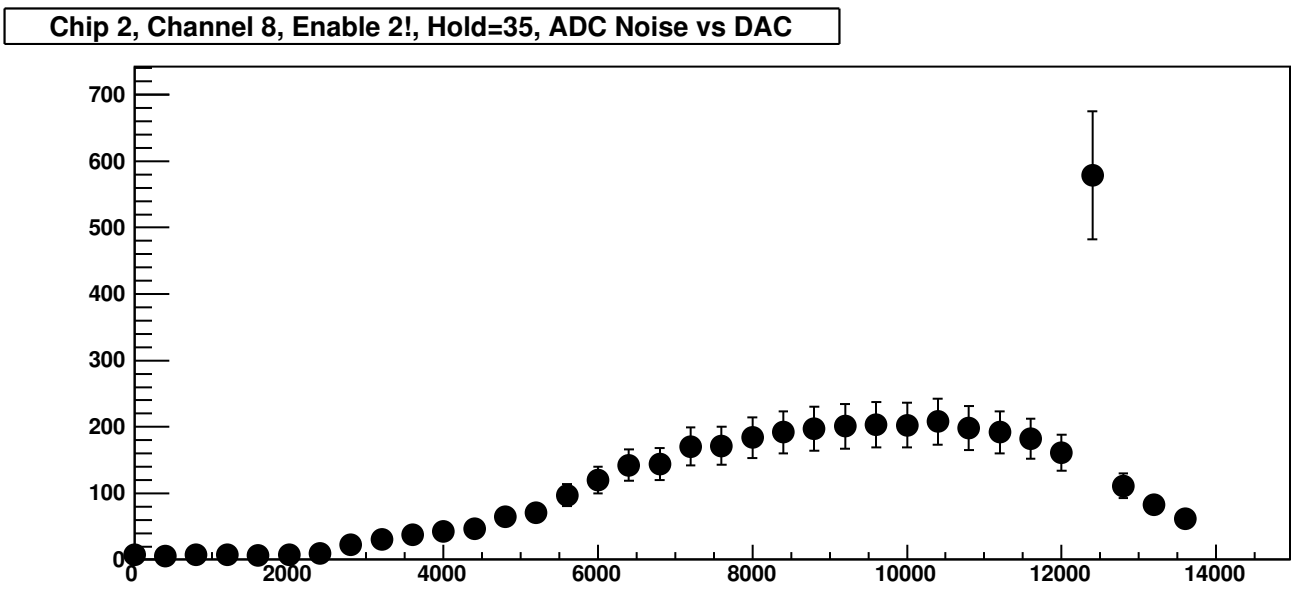

Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

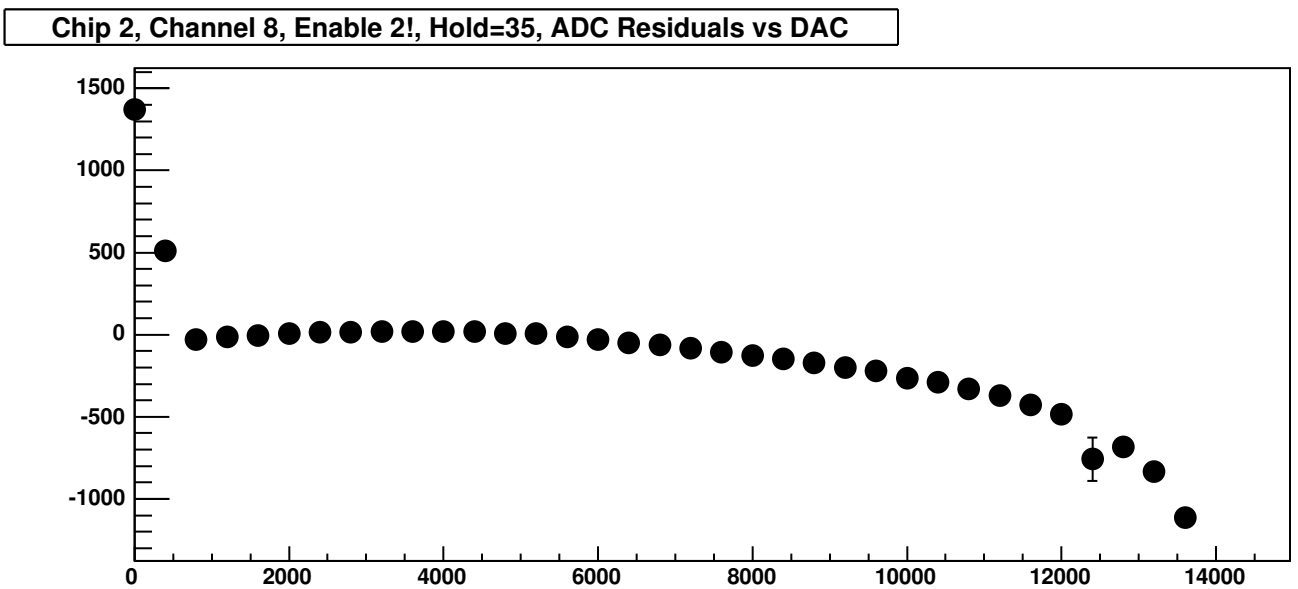

Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 80.77 / 23
p0 -47.1 ± 0.7027
p1 0.01736 ± 0.0001043
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

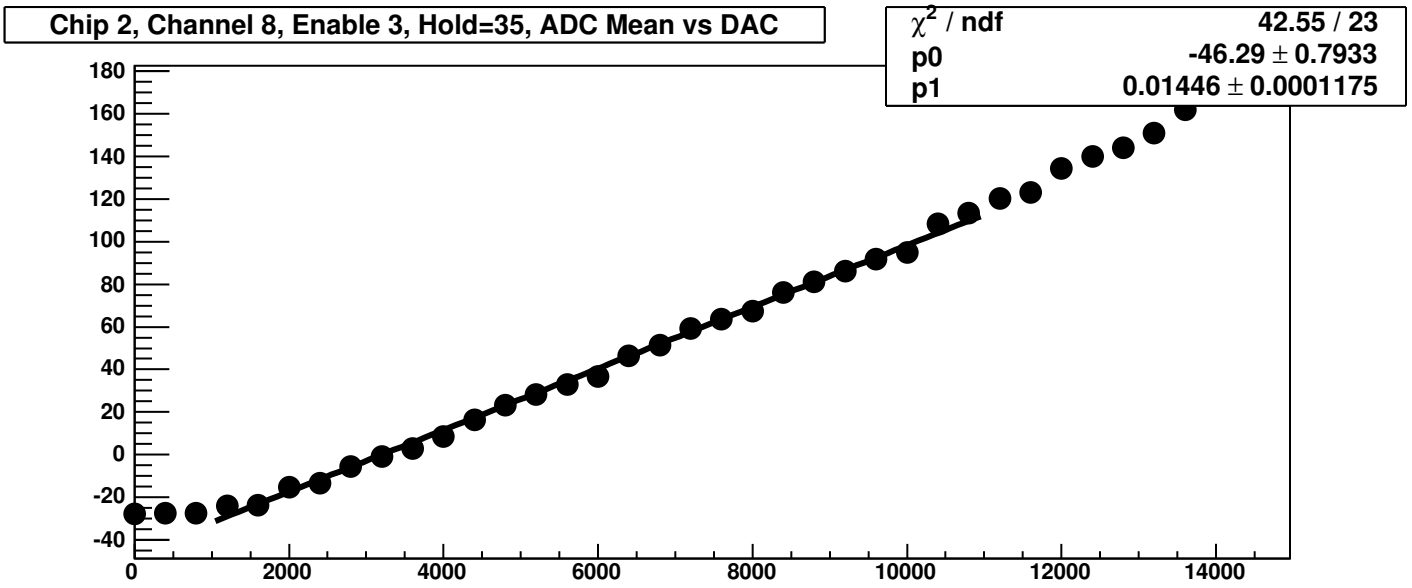

Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

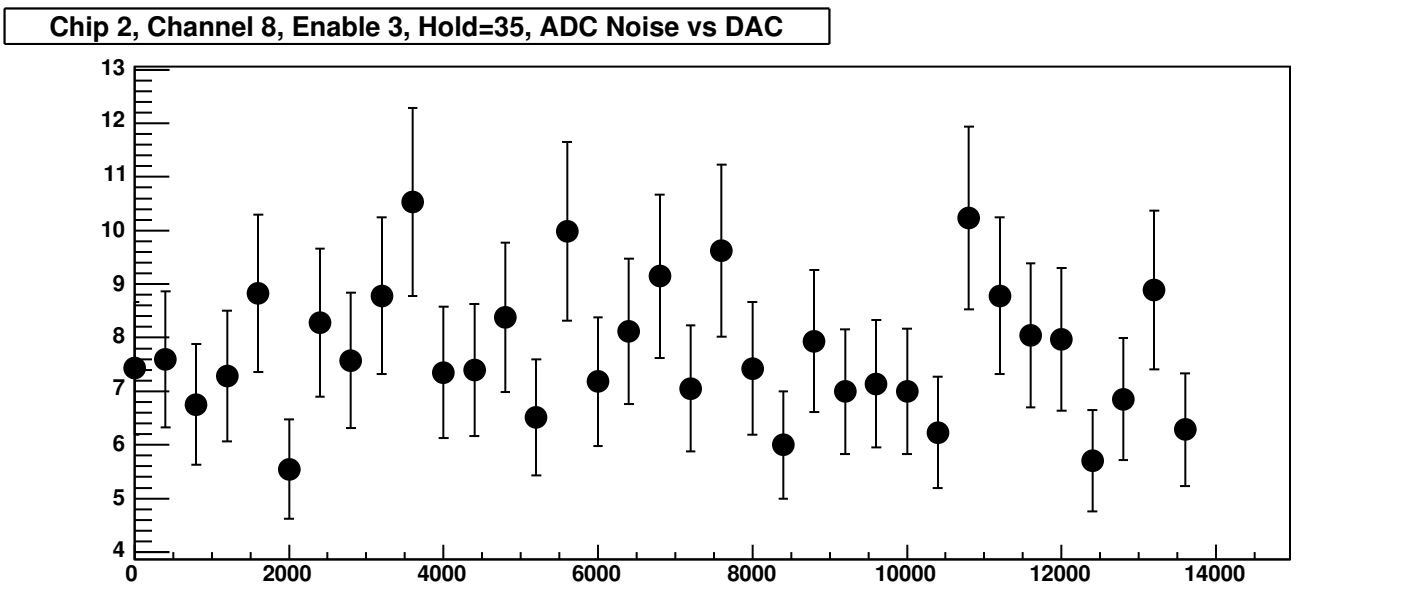

Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

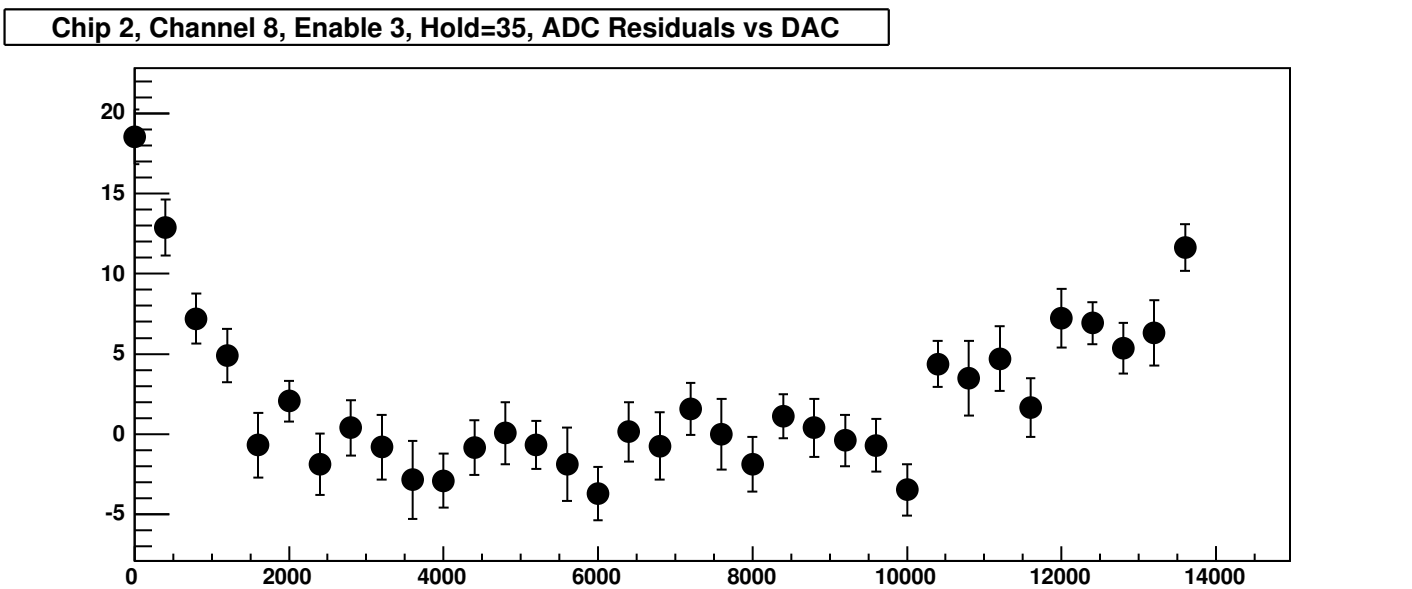

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 377.6 / 23
p0 7.58 ± 2.16
p1 2.186 ± 0.00106
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

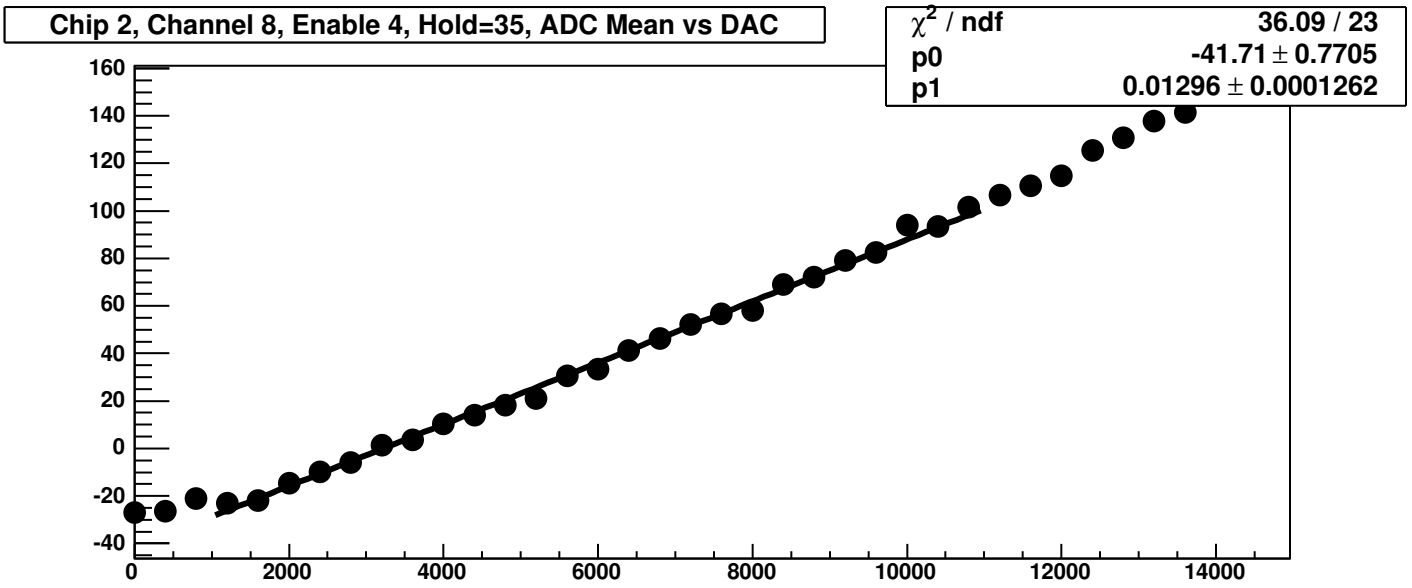

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

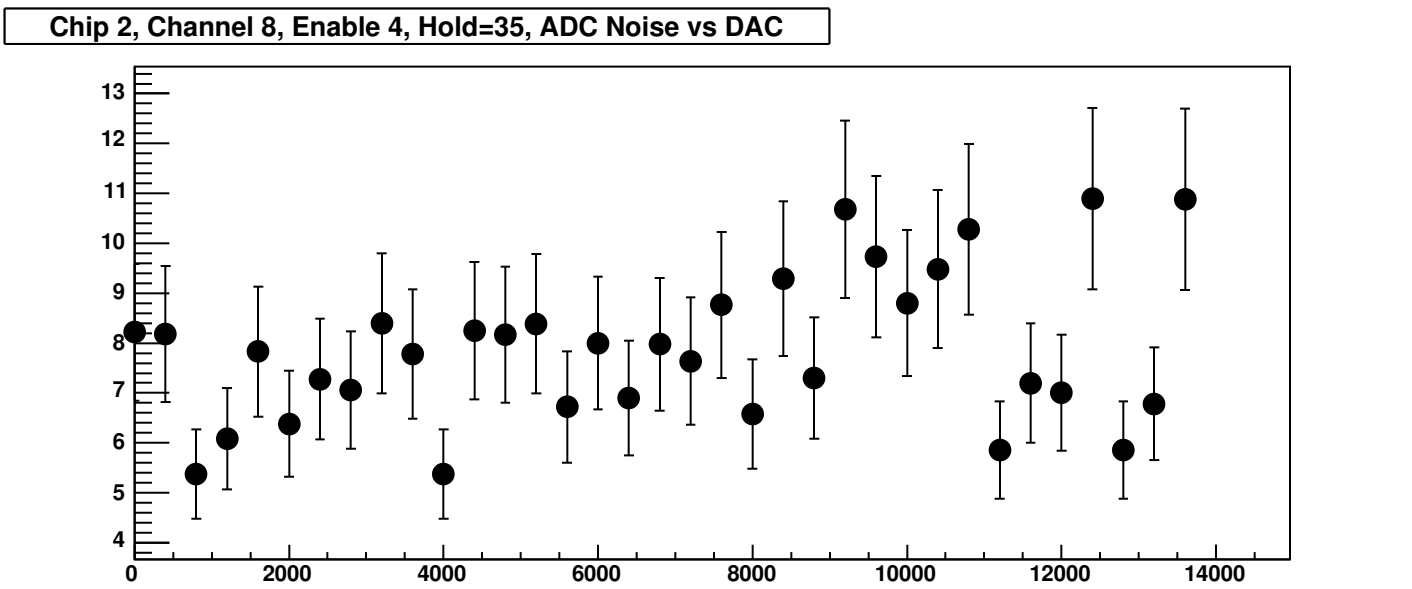

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

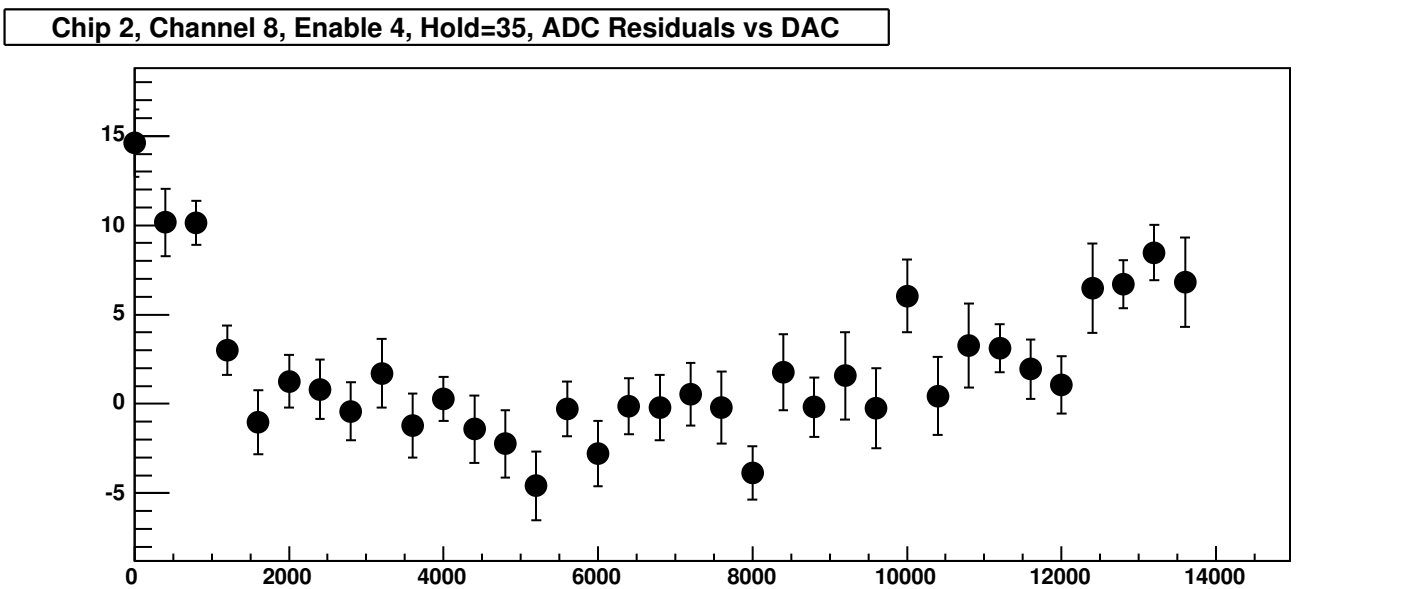

Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.55 / 23
p0 -46.29 ± 0.7933
p1 0.01446 ± 0.0001175
180
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

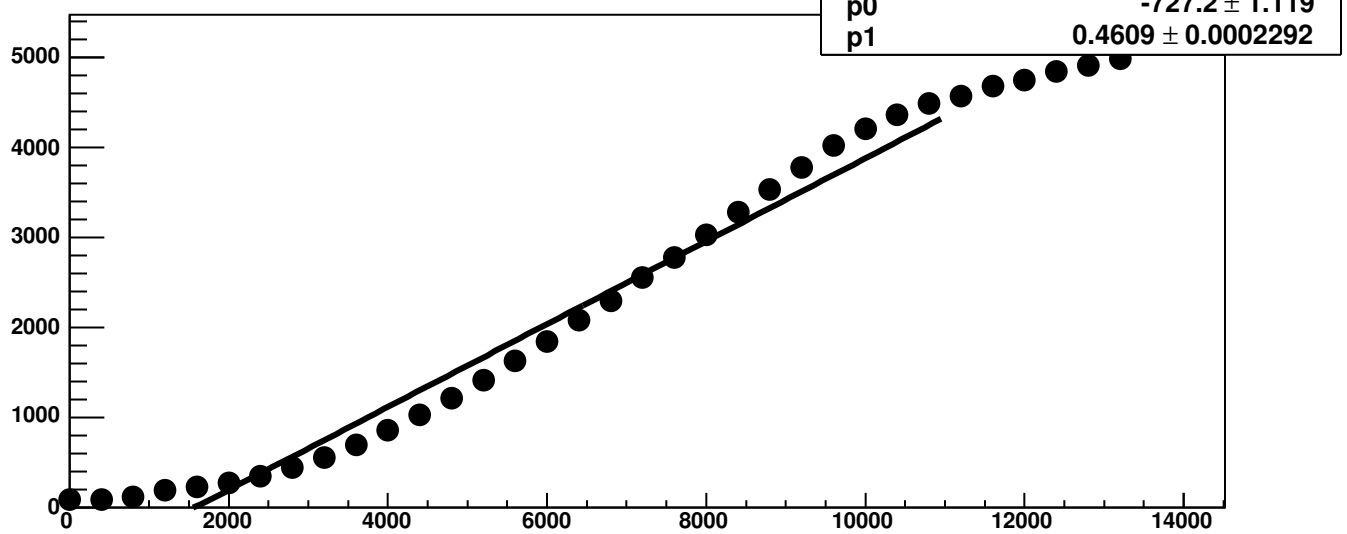

Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.09 / 23
p0 -41.71 ± 0.7705
p1 0.01296 ± 0.0001262
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

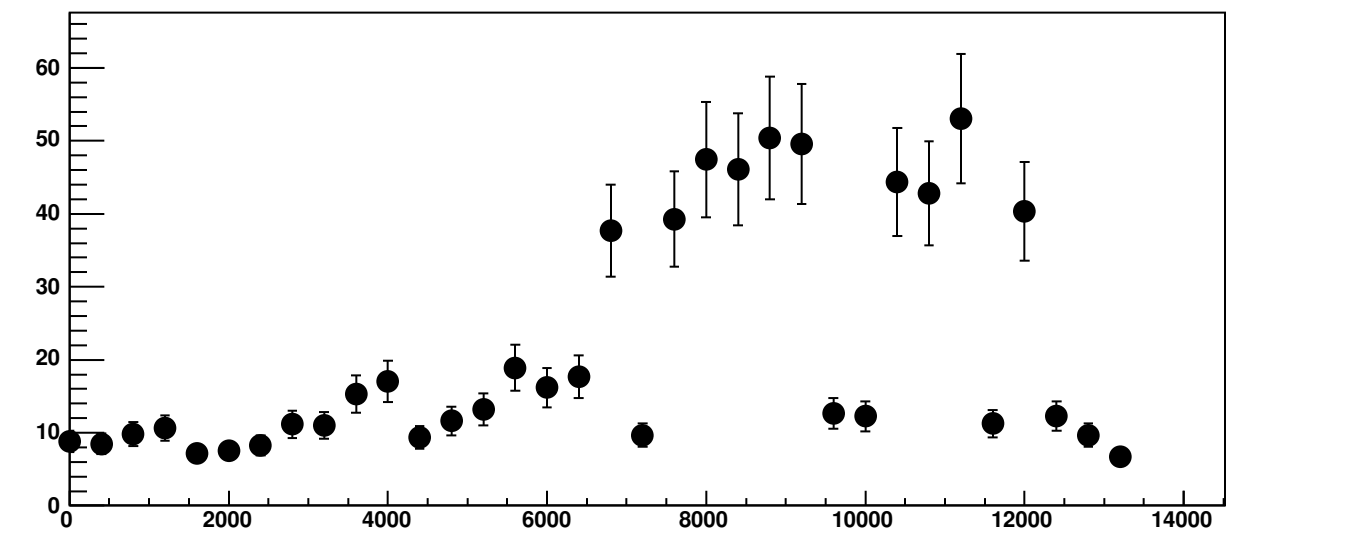

Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

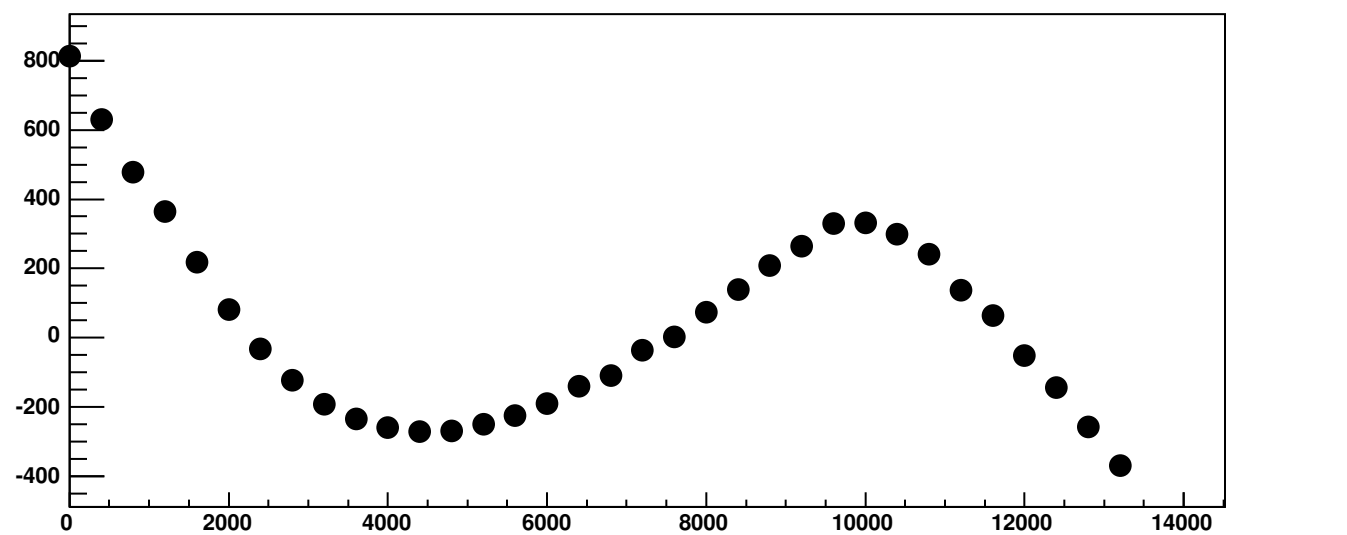
Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

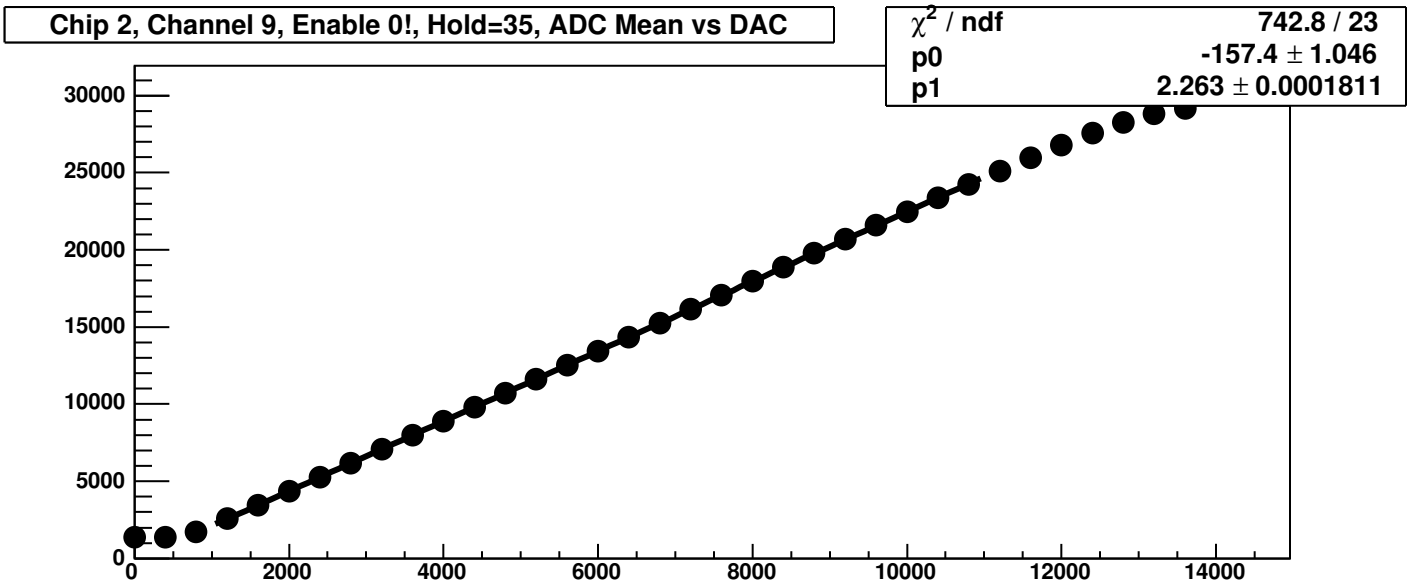


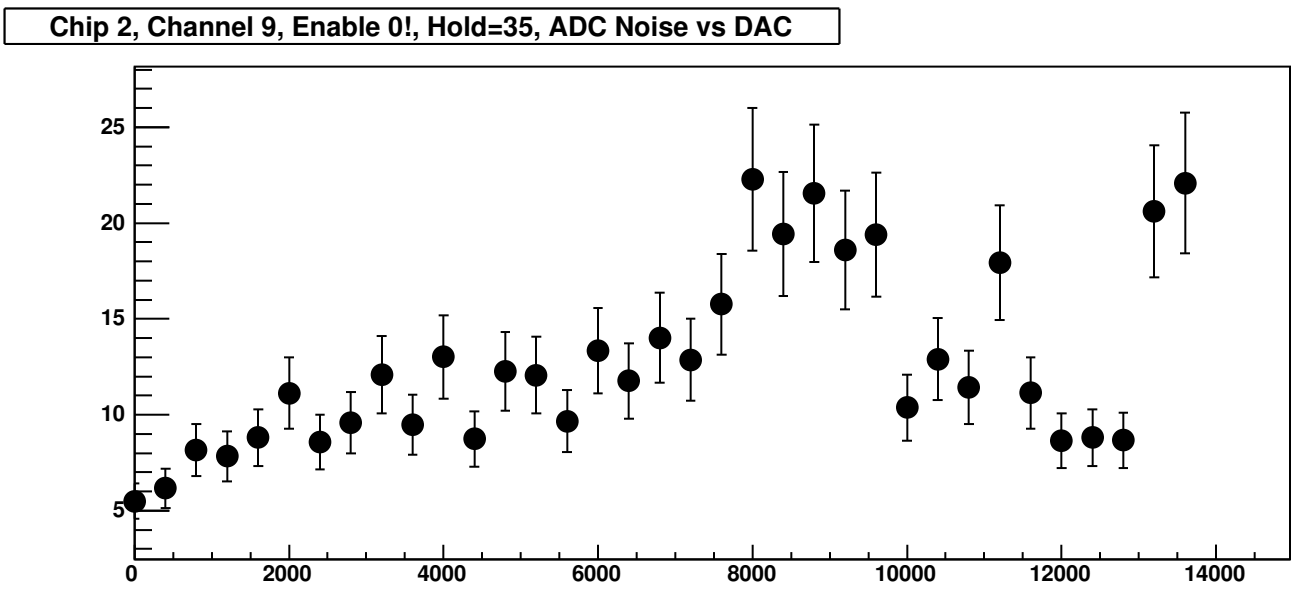
Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

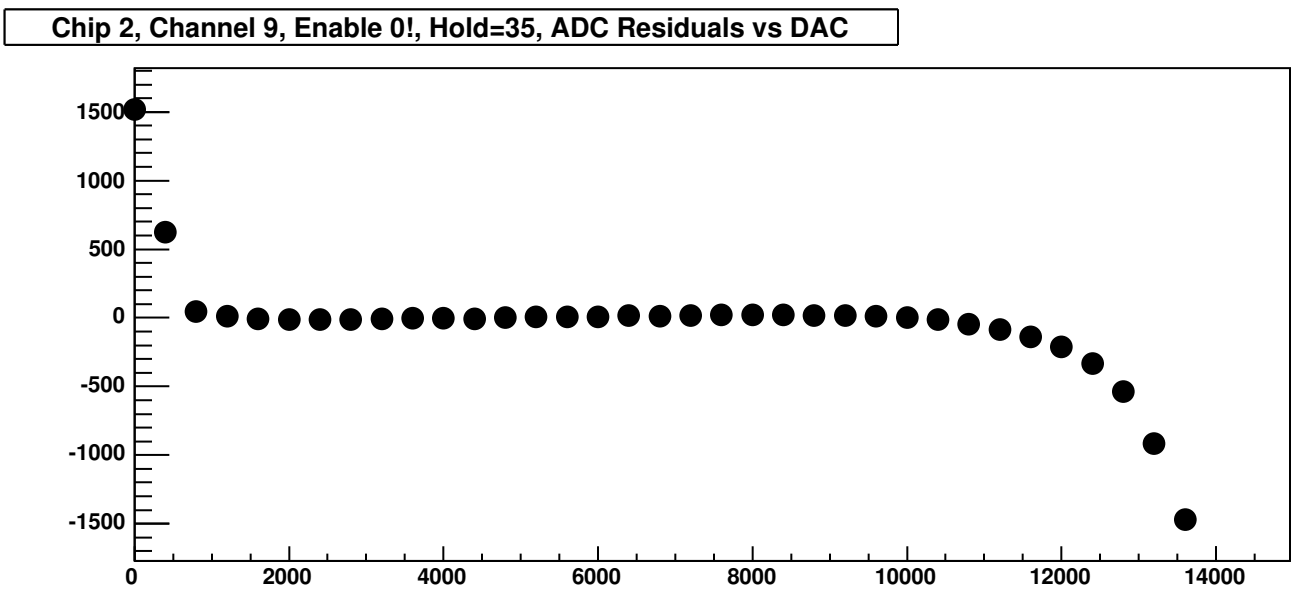


Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

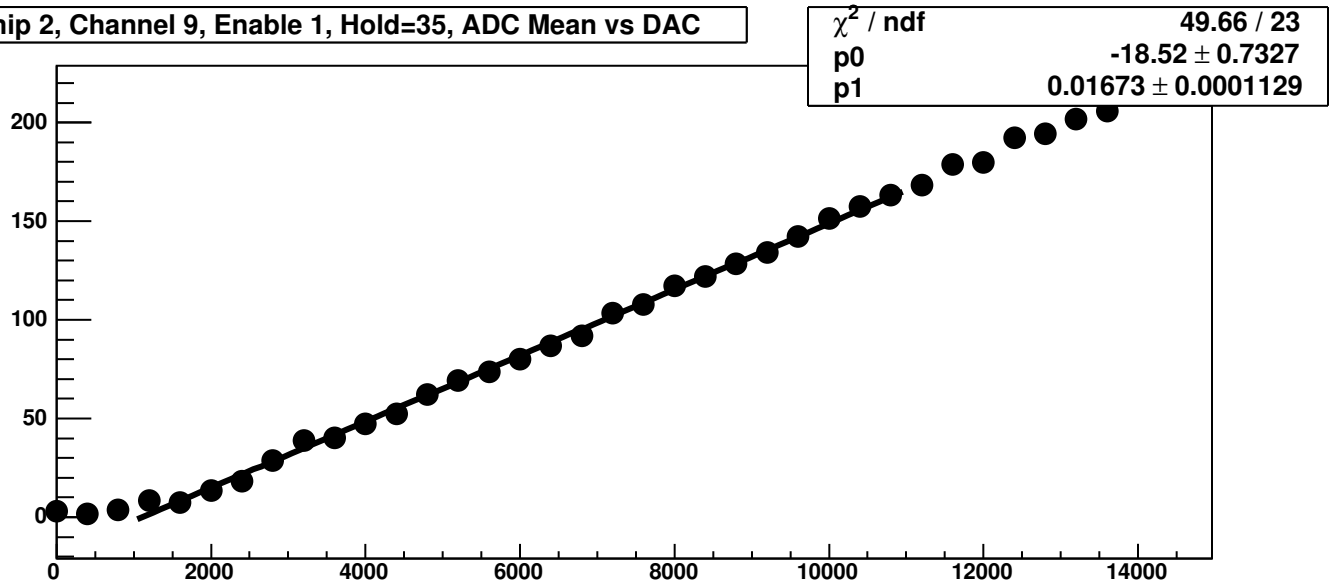



Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 742.8 / 23
p0 -157.4 ± 1.046
p1 2.263 ± 0.0001811
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

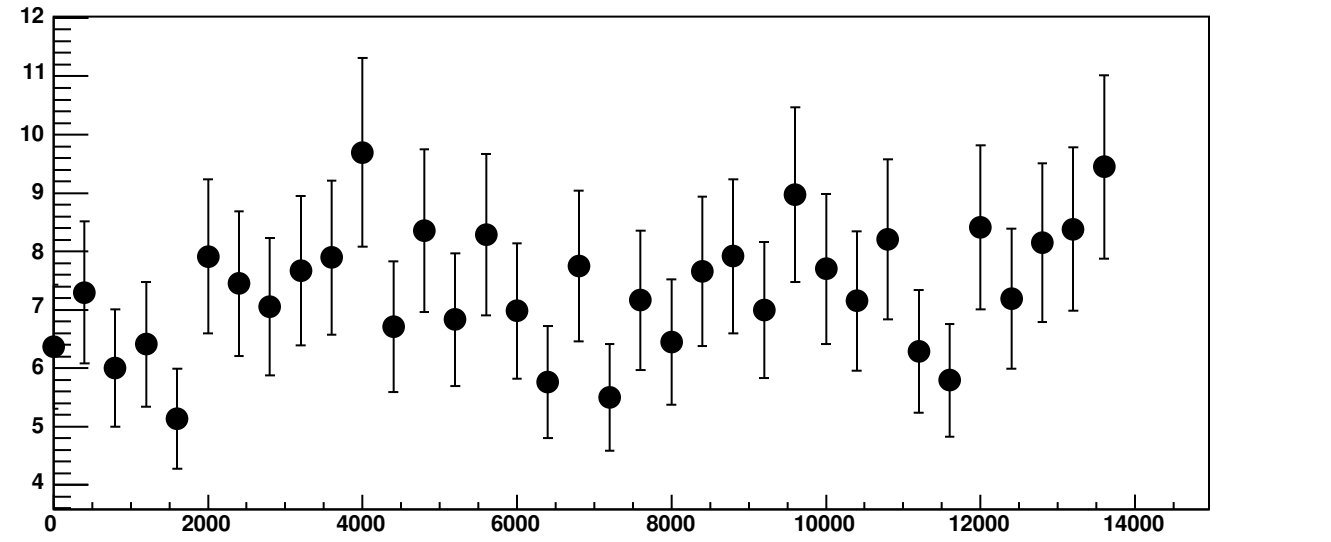

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

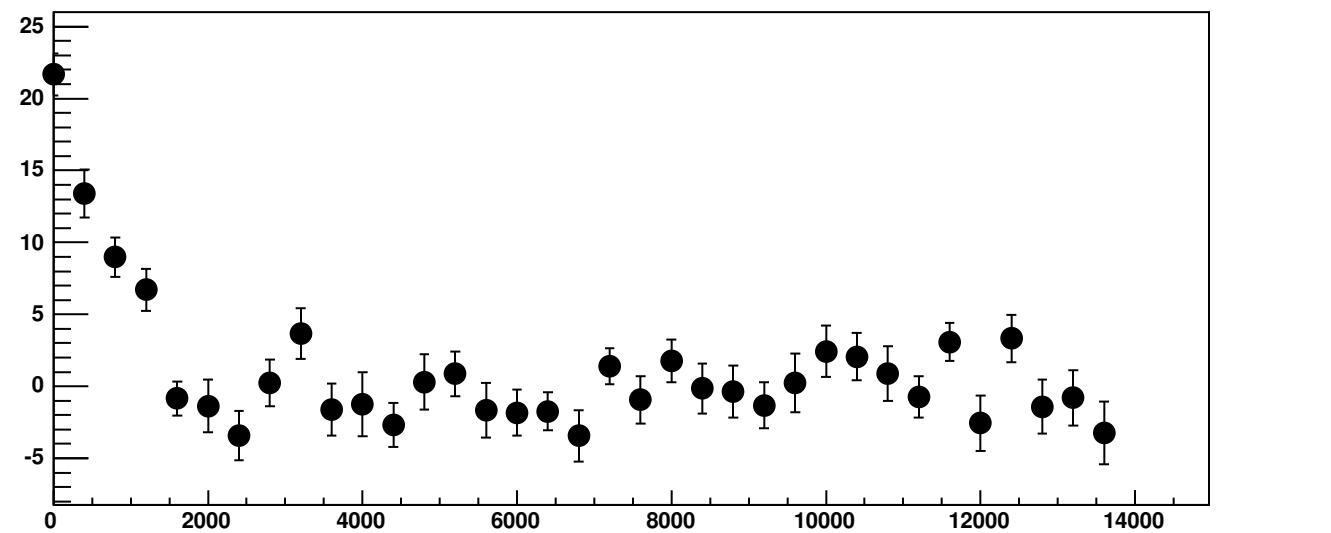
Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

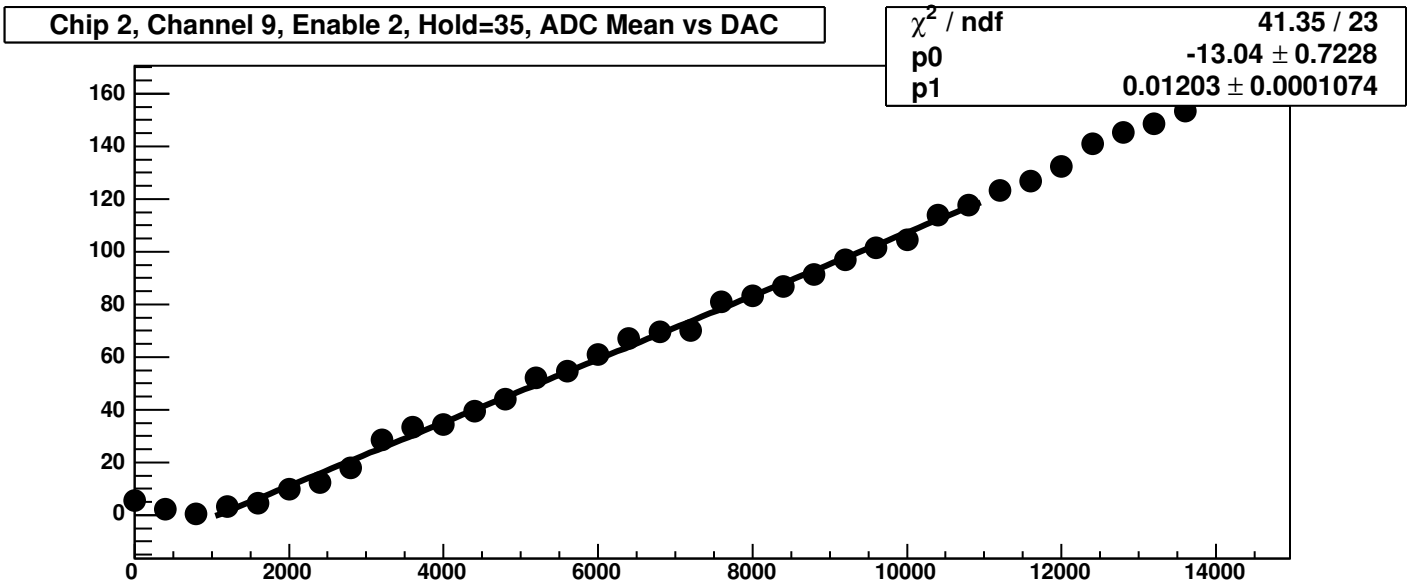


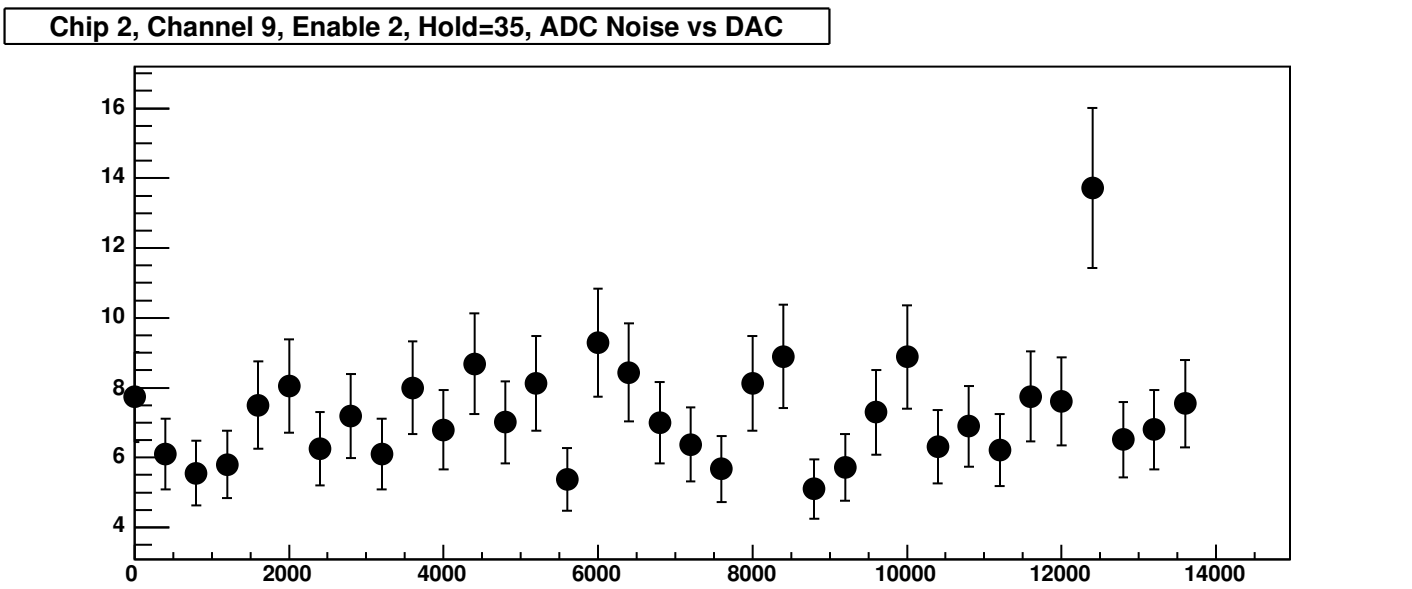
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

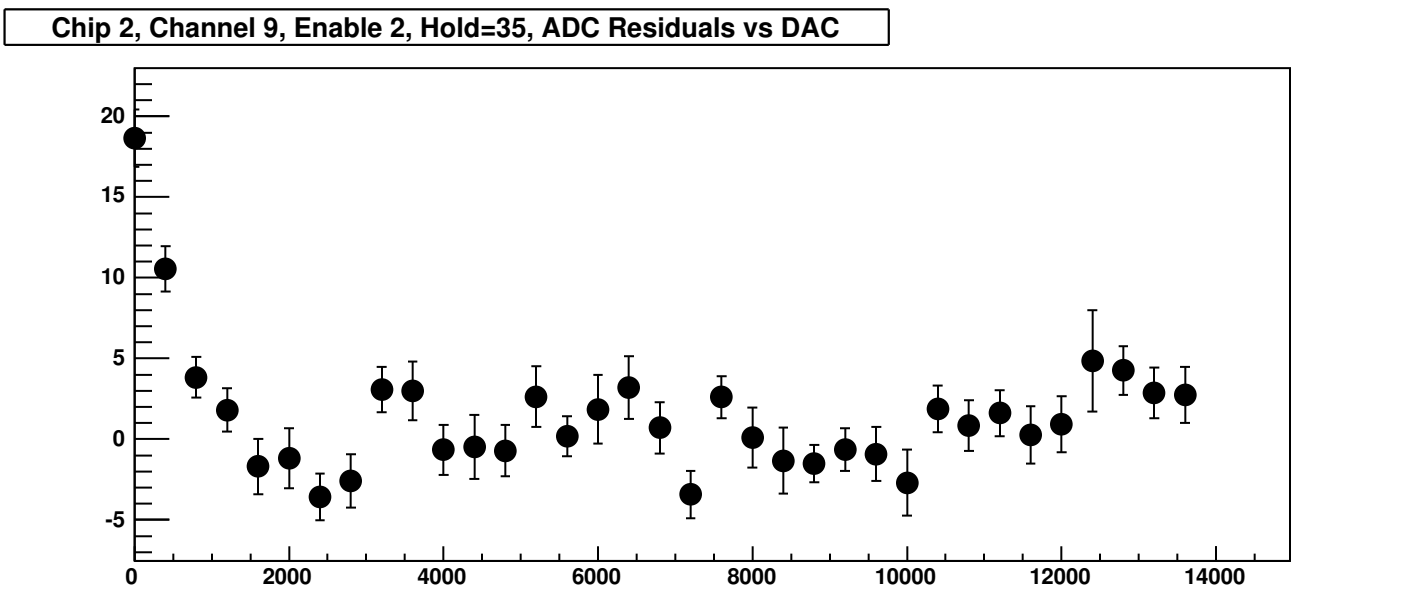


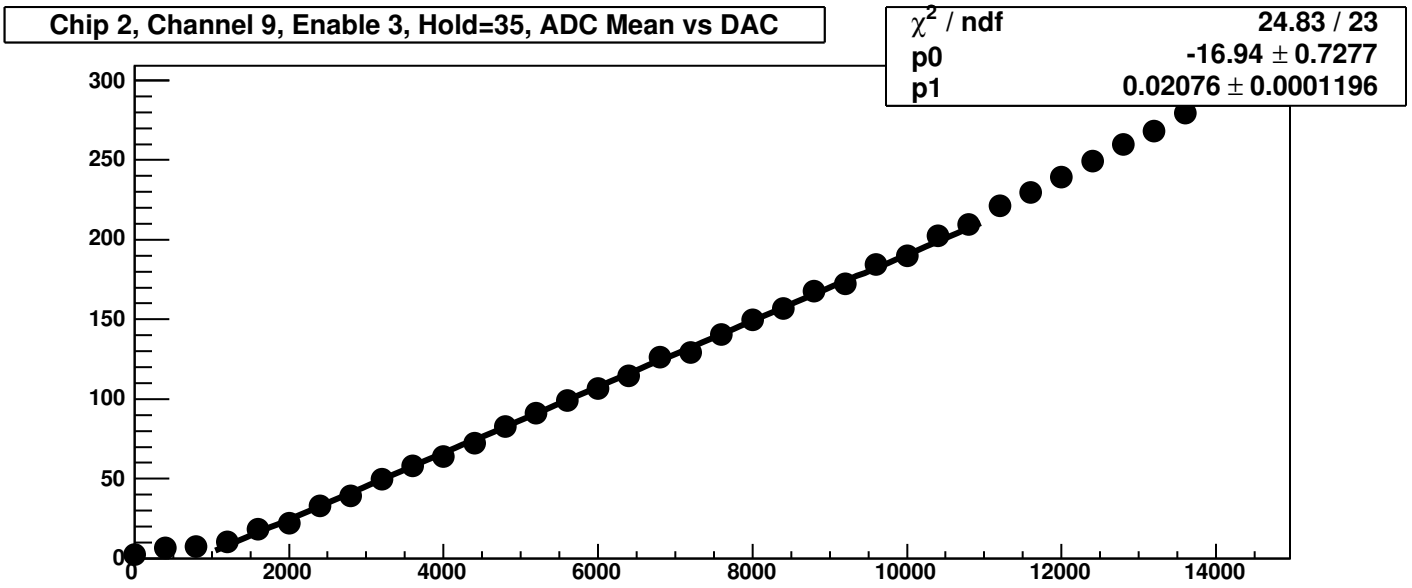
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

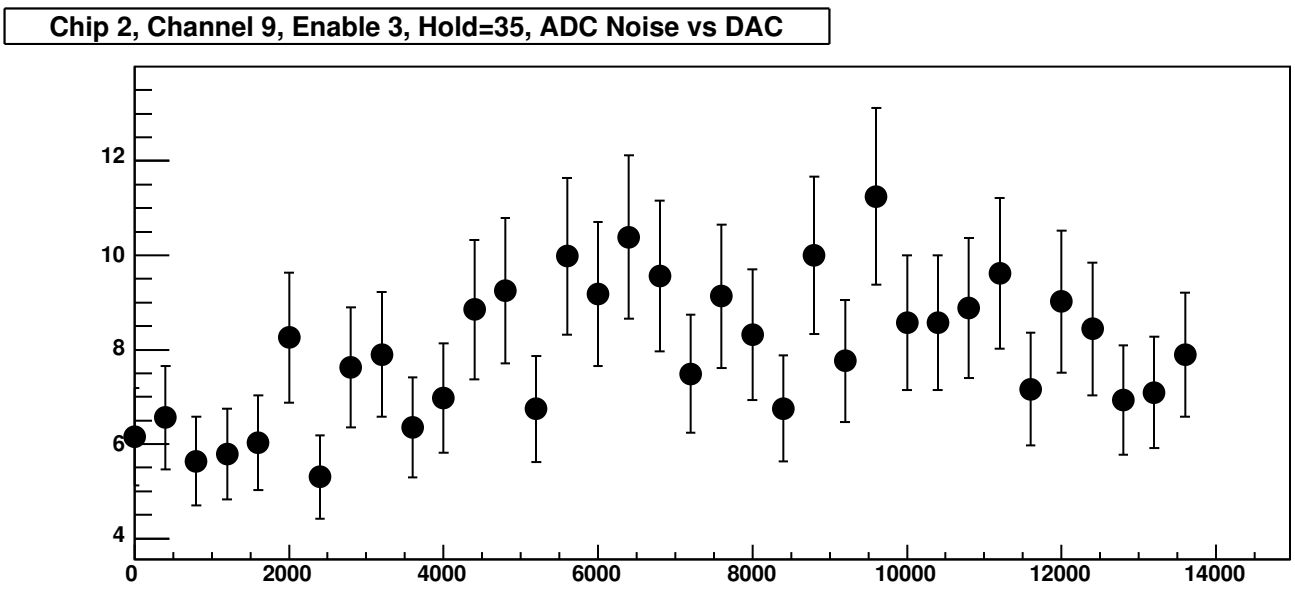


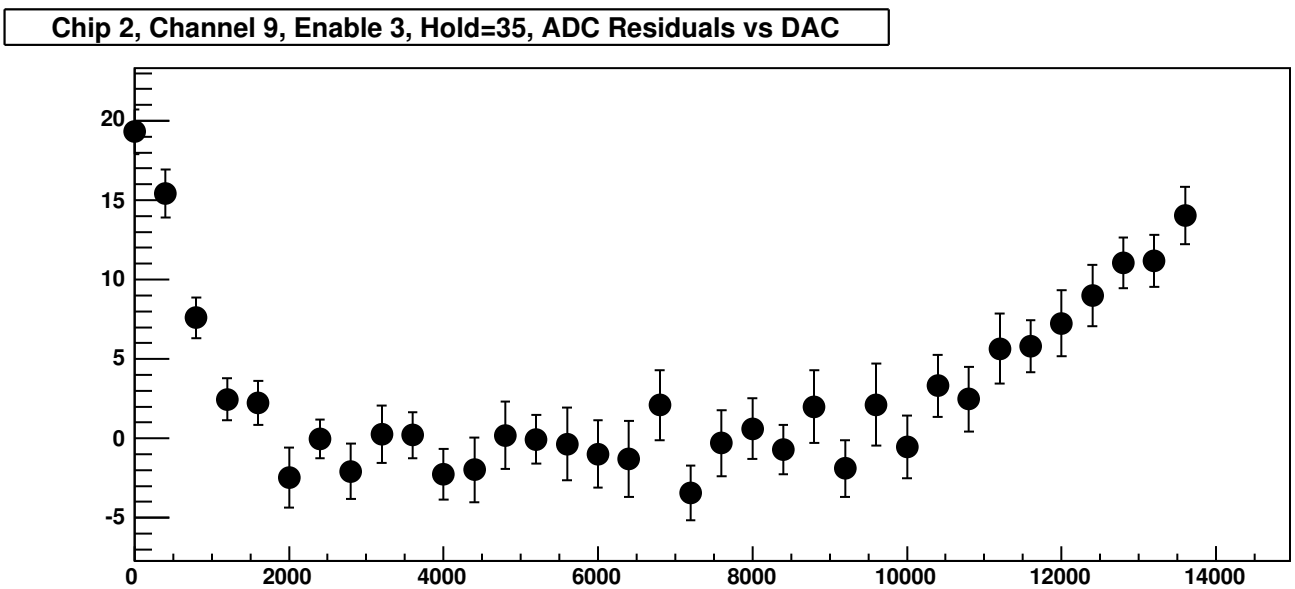

Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.35 / 23
p0 -13.04 ± 0.7228
p1 0.01203 ± 0.0001074
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

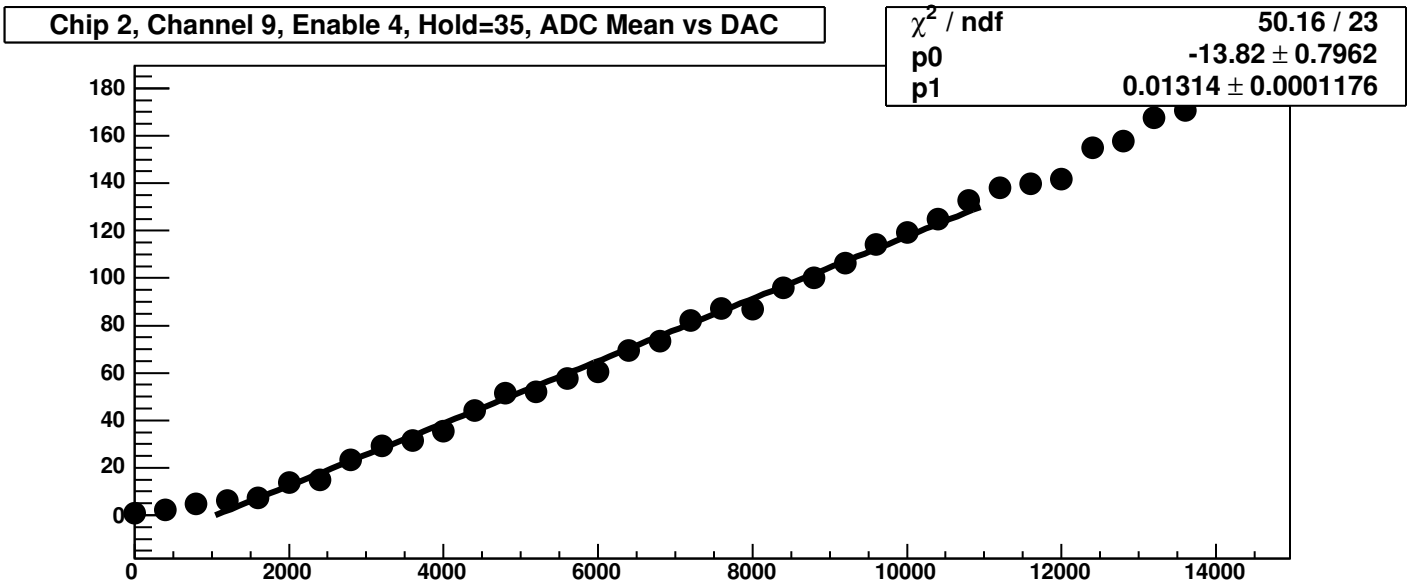

Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

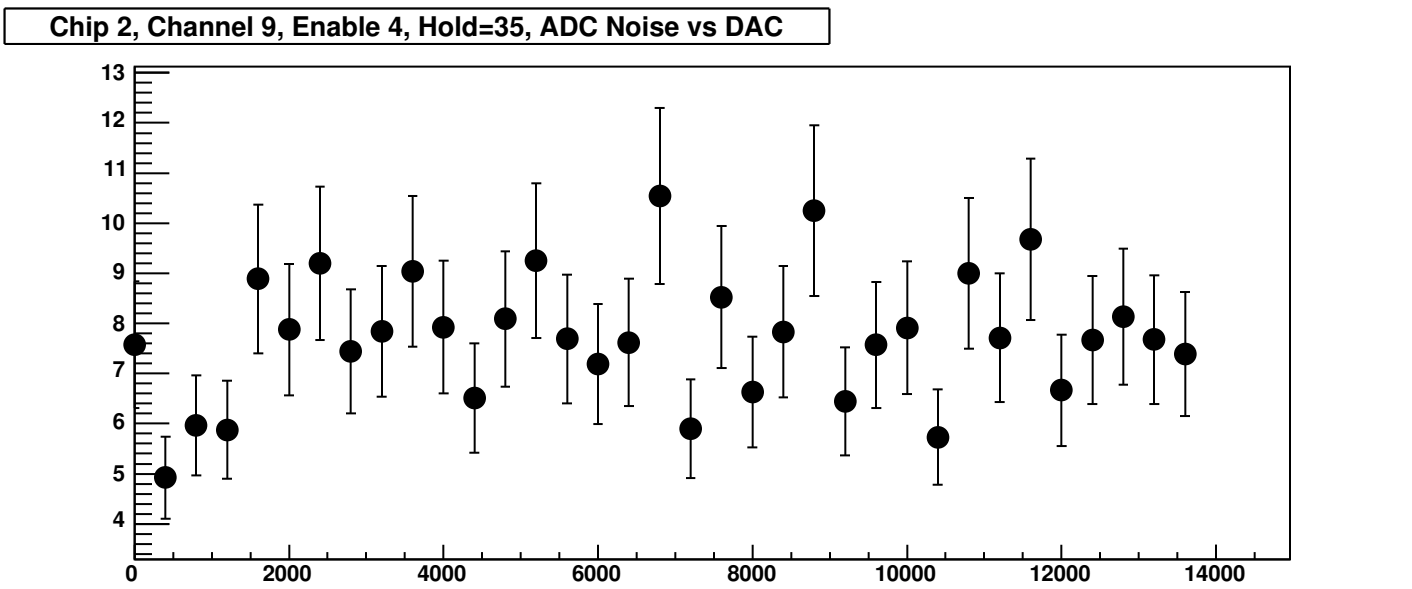

Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

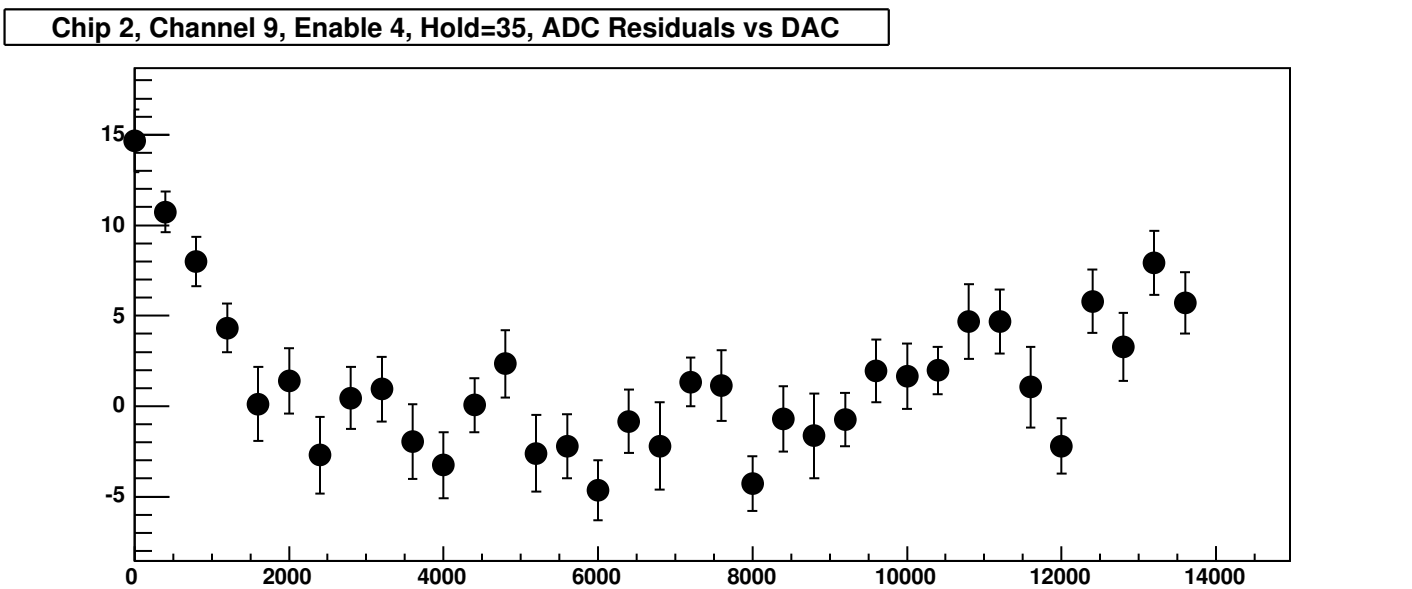

Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.83 / 23
p0 -16.94 ± 0.7277
p1 0.02076 ± 0.0001196
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

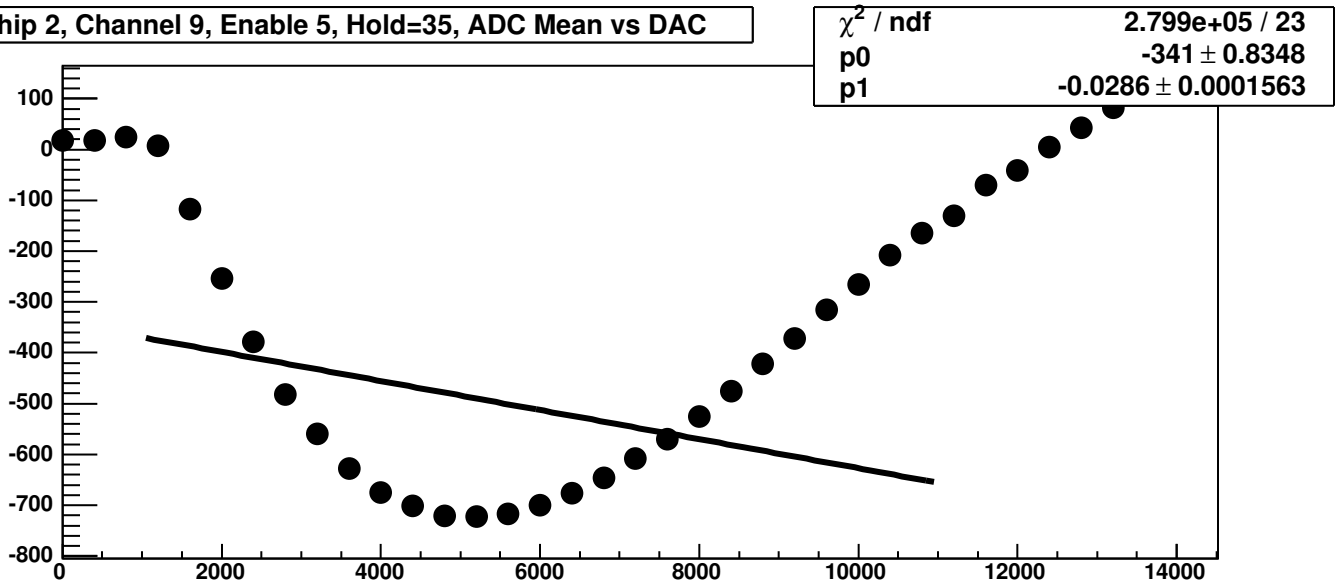

Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.16 / 23
p0 -13.82 ± 0.7962
p1 0.01314 ± 0.0001176
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

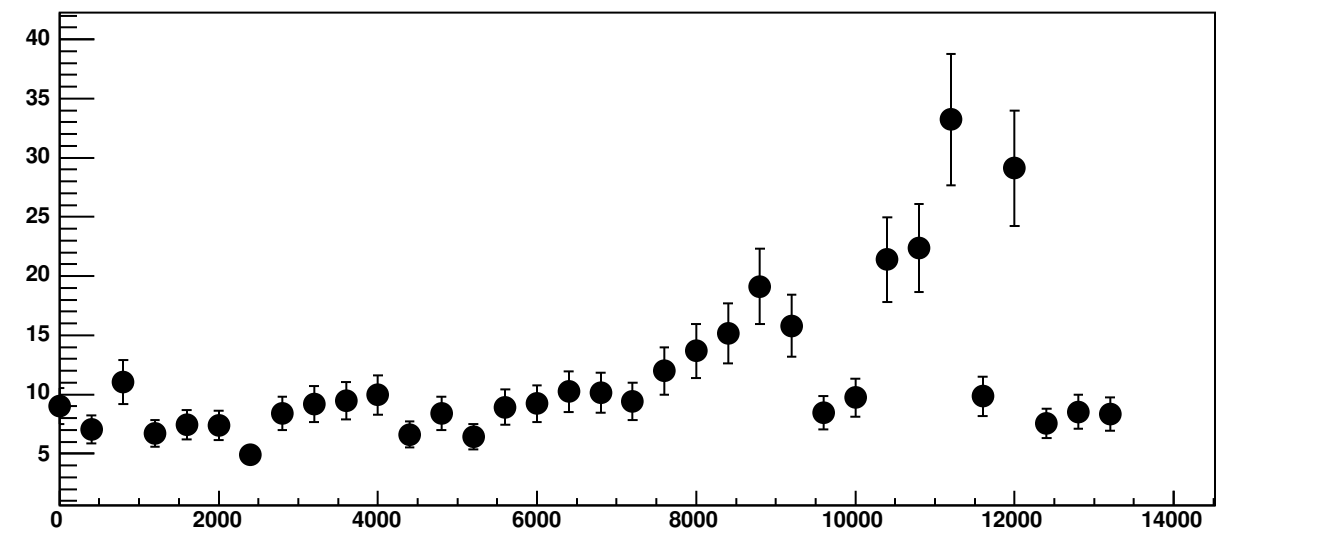

Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

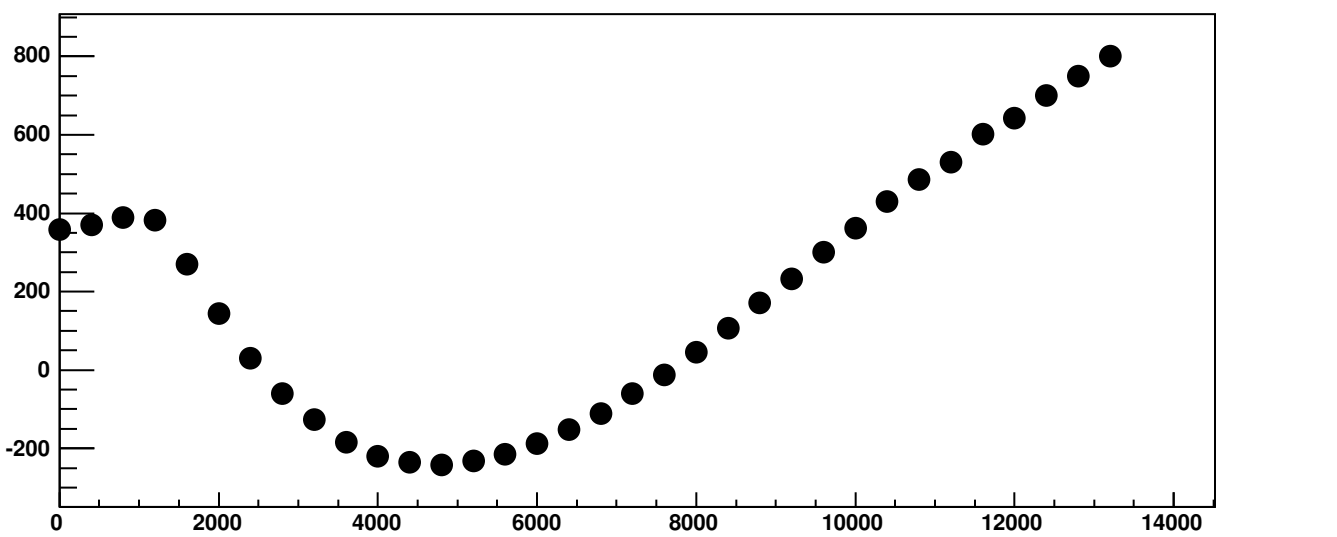
Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

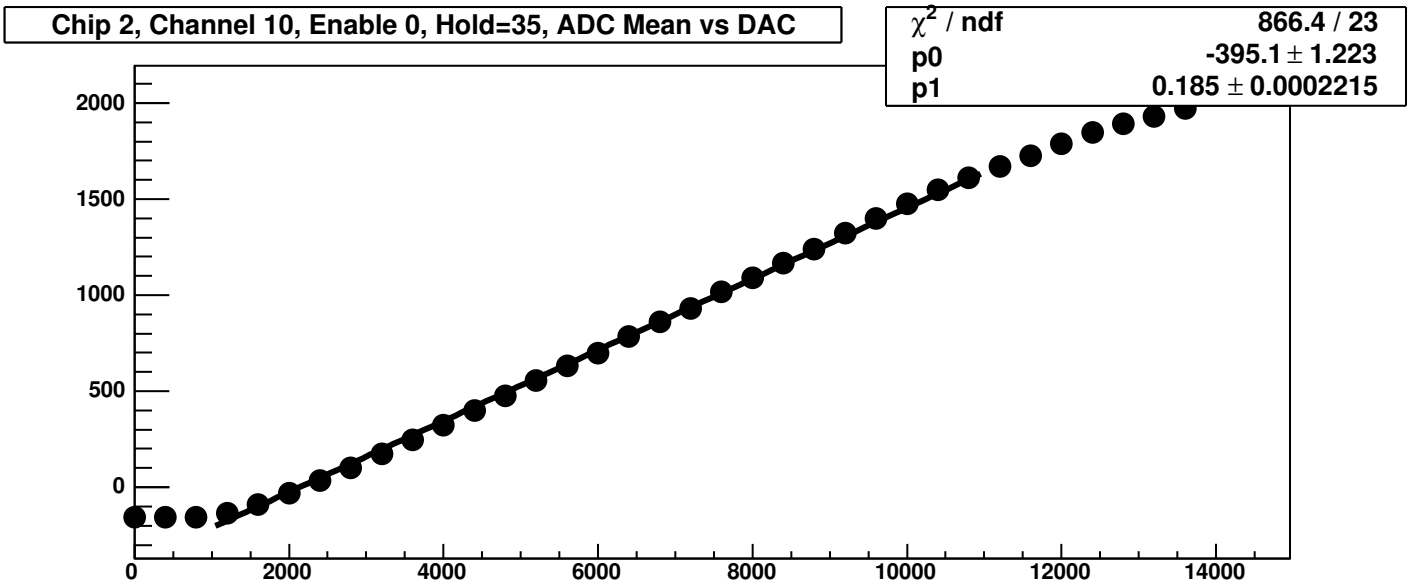


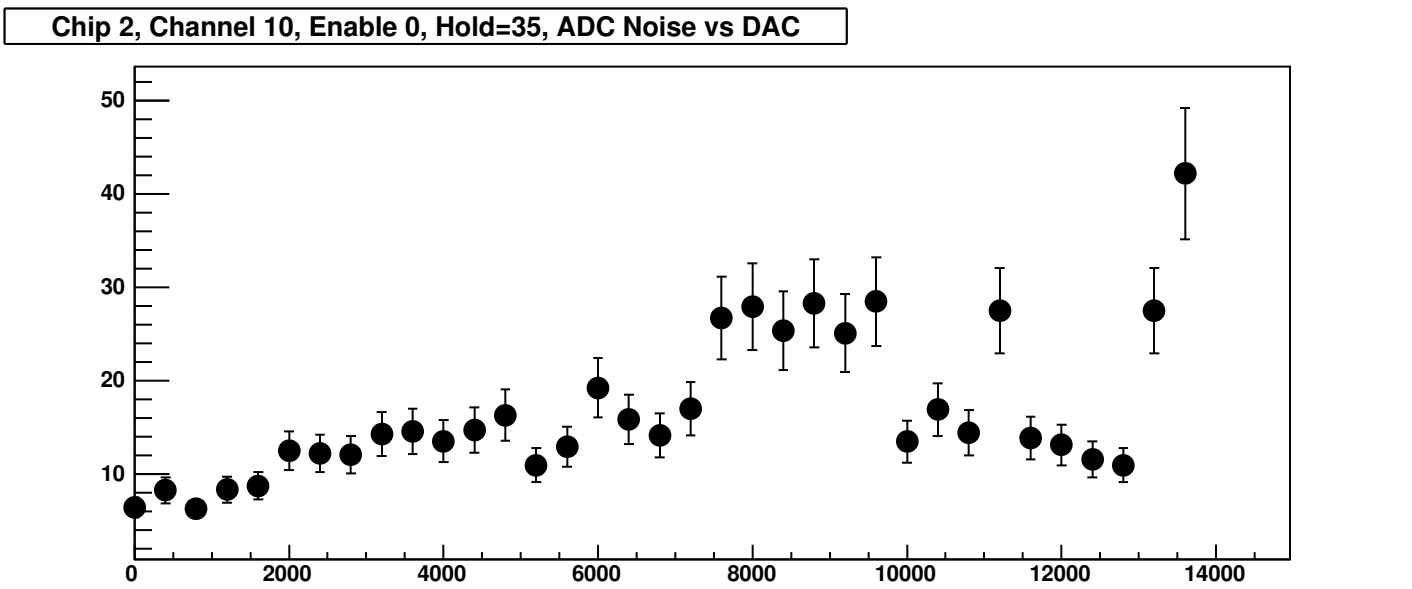
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

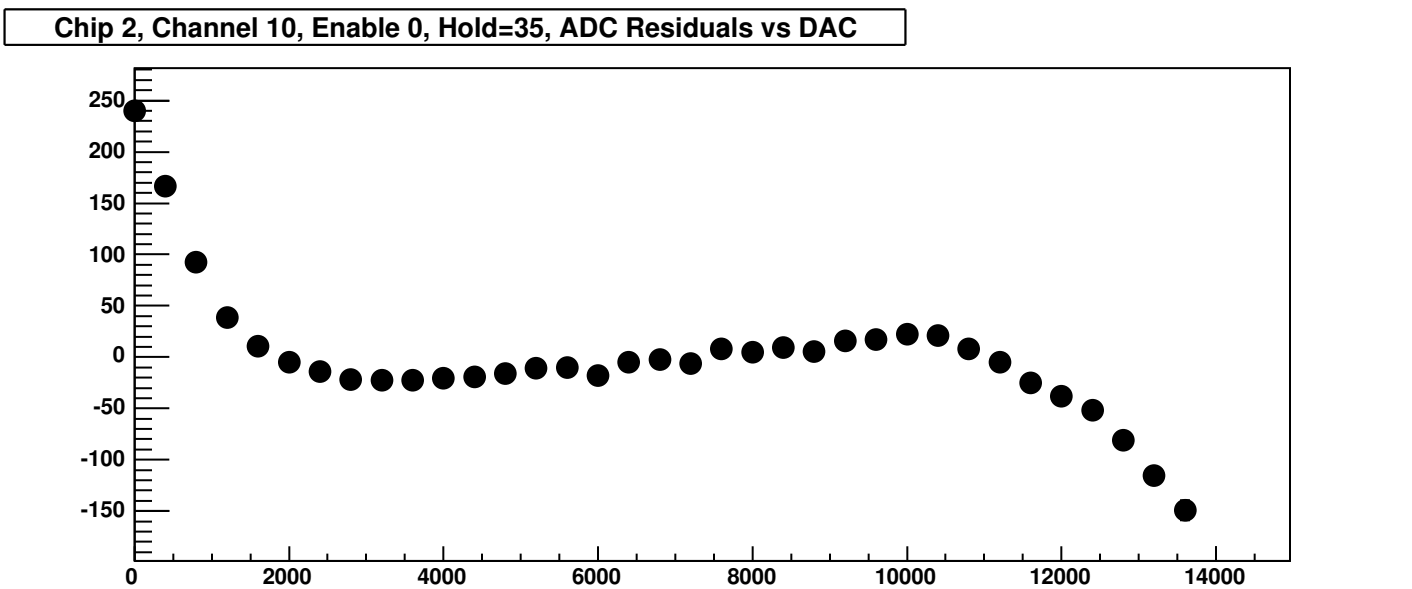


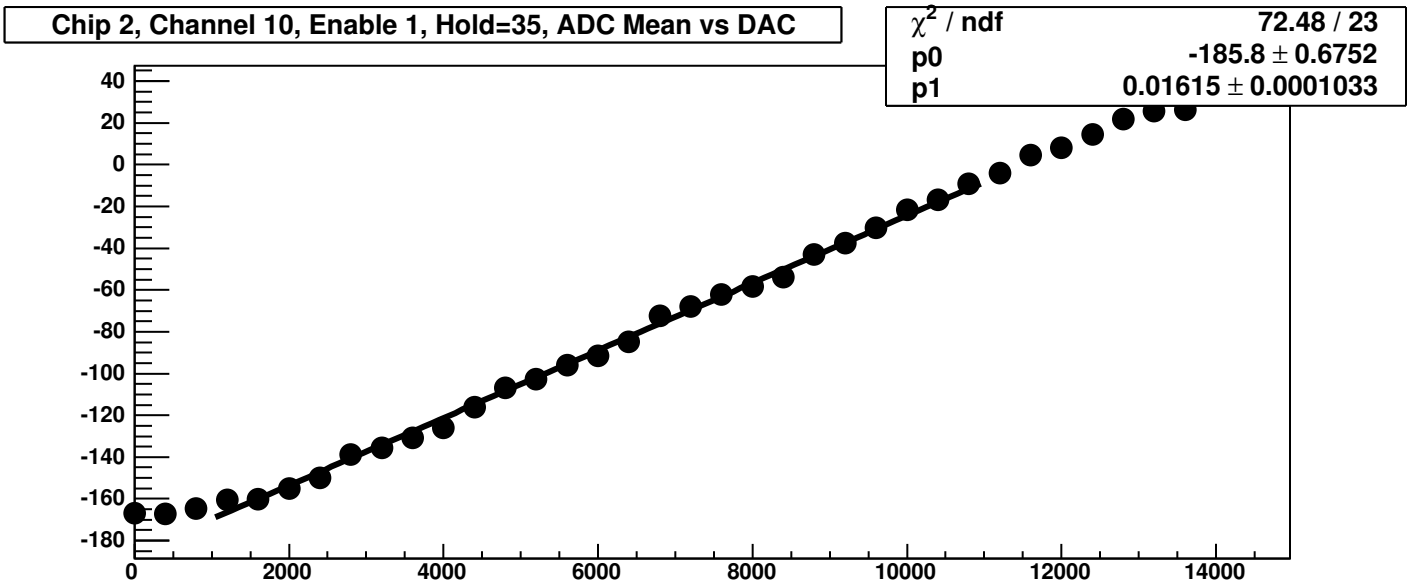
Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

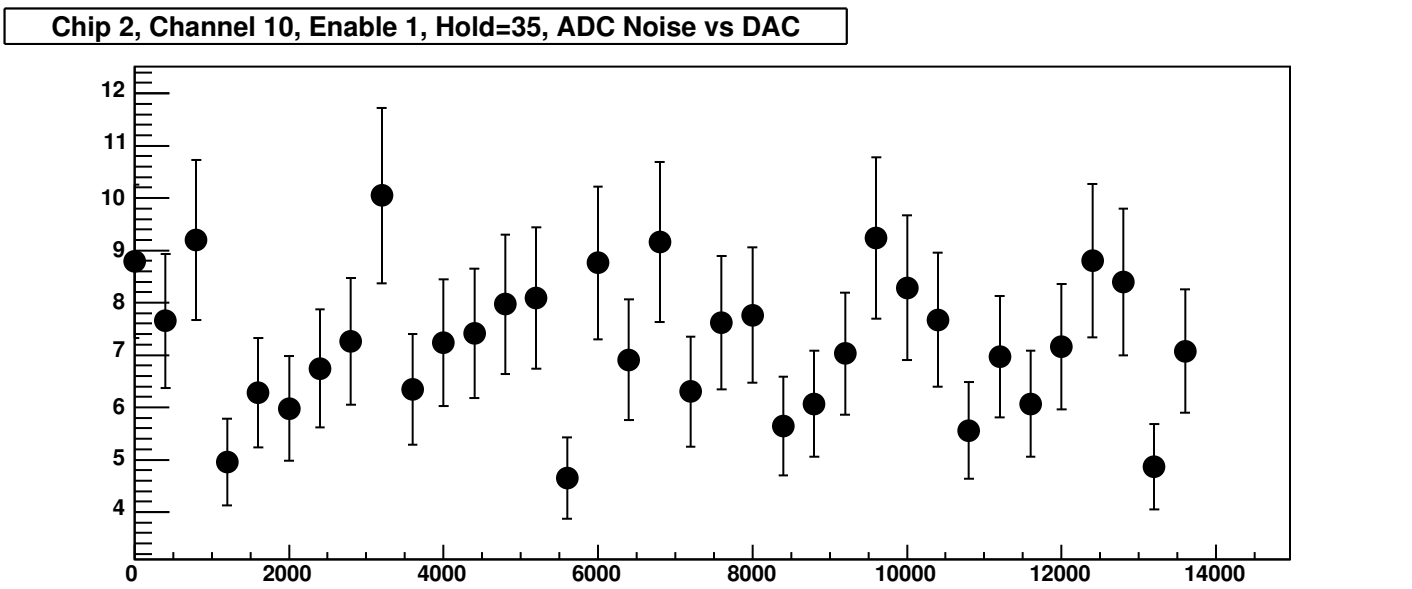


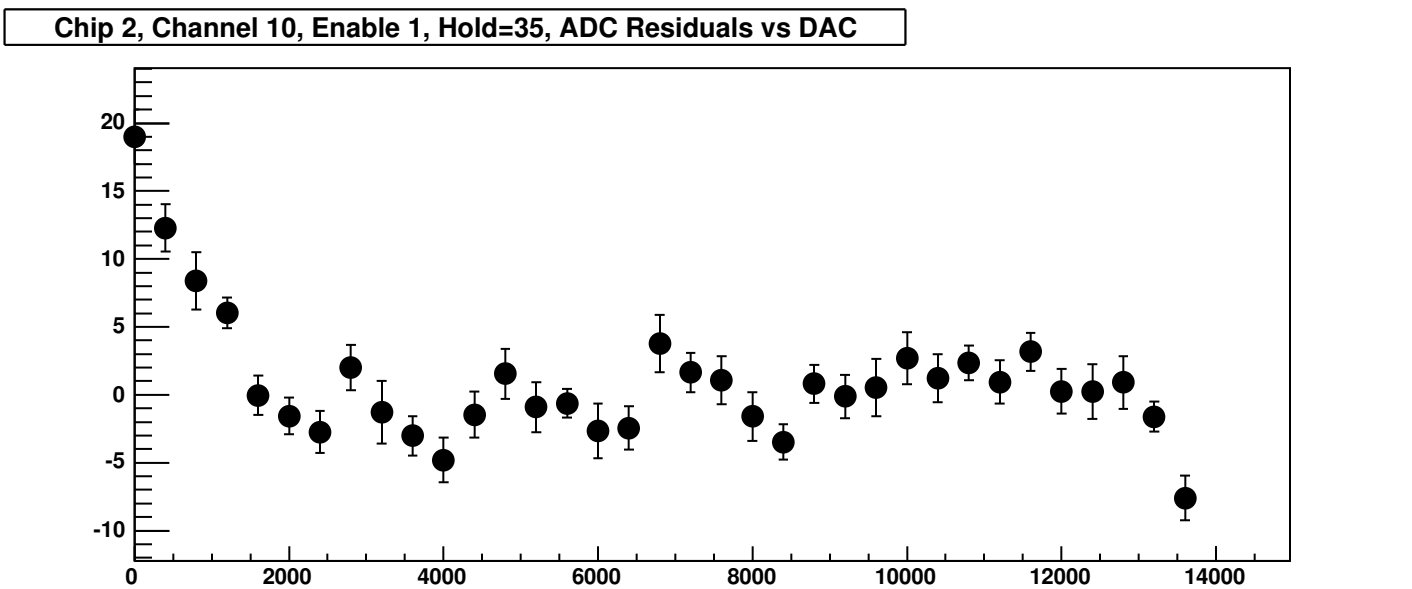

Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 866.4 / 23
p0 -395.1 ± 1.223
p1 0.185 ± 0.0002215
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

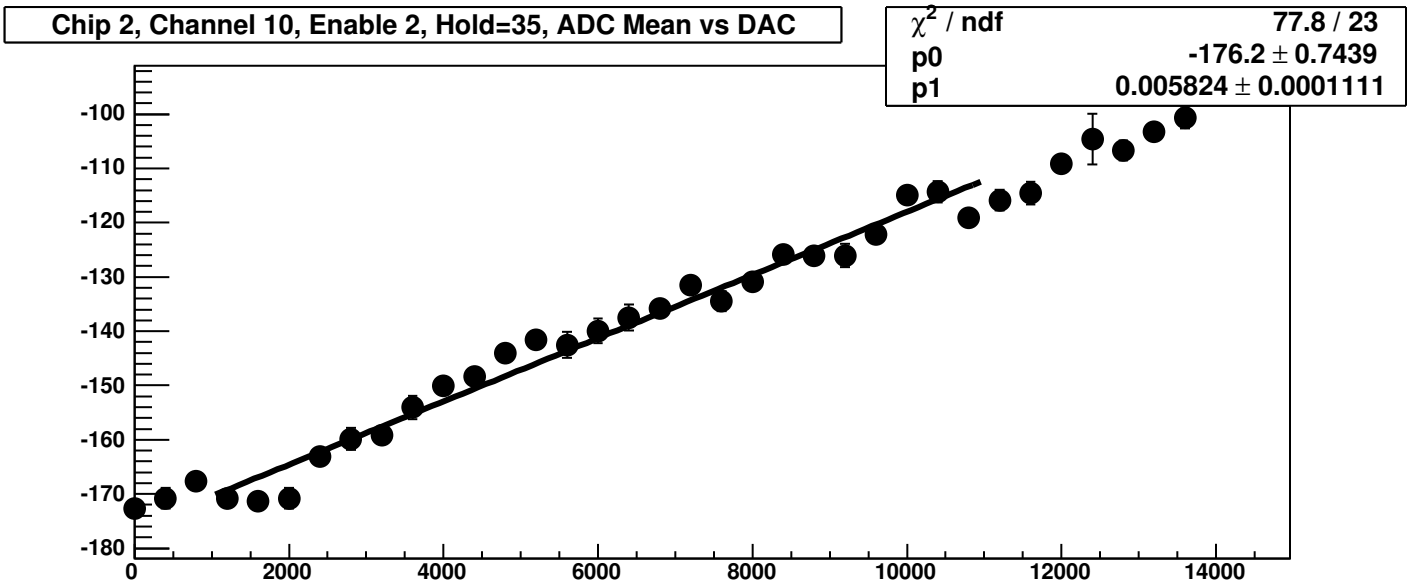

Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

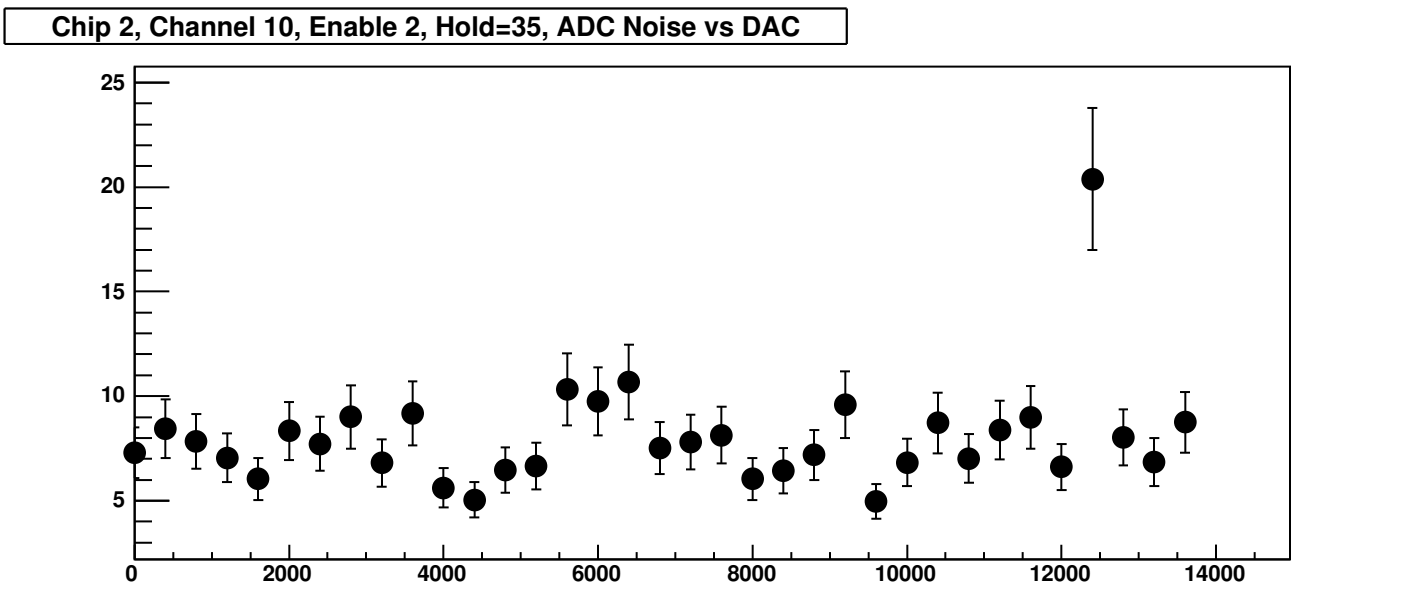

Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

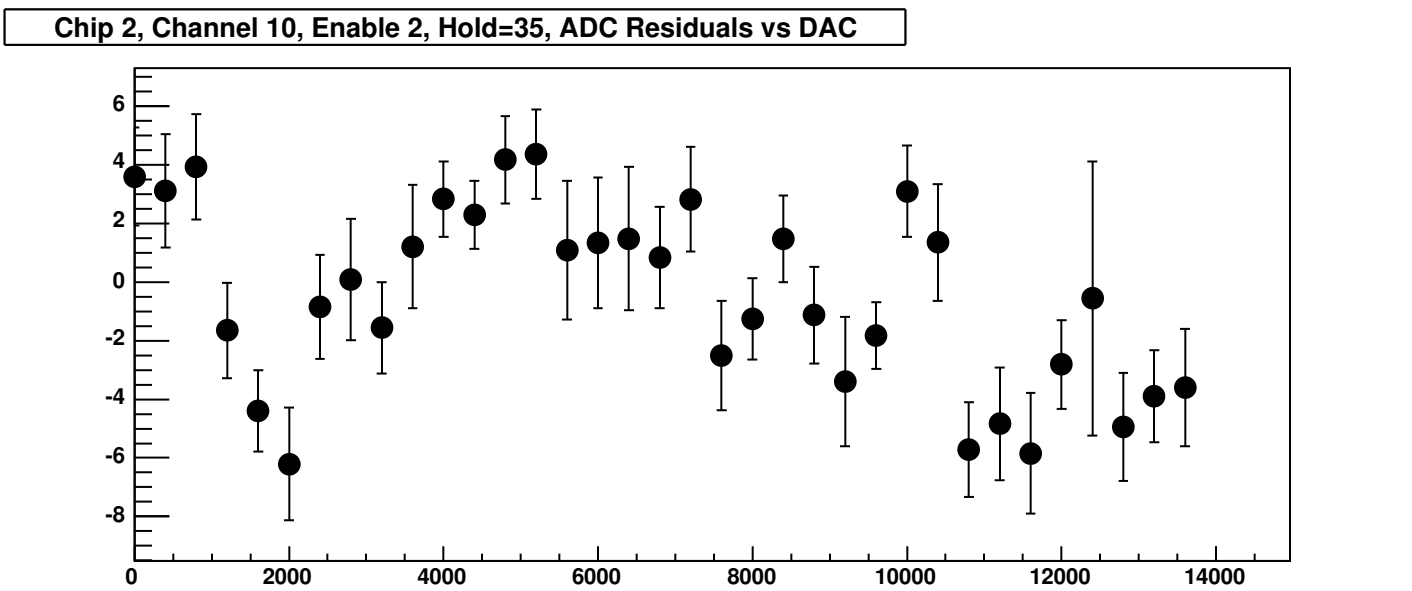

Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 72.48 / 23
p0 -185.8 ± 0.6752
p1 0.01615 ± 0.0001033
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

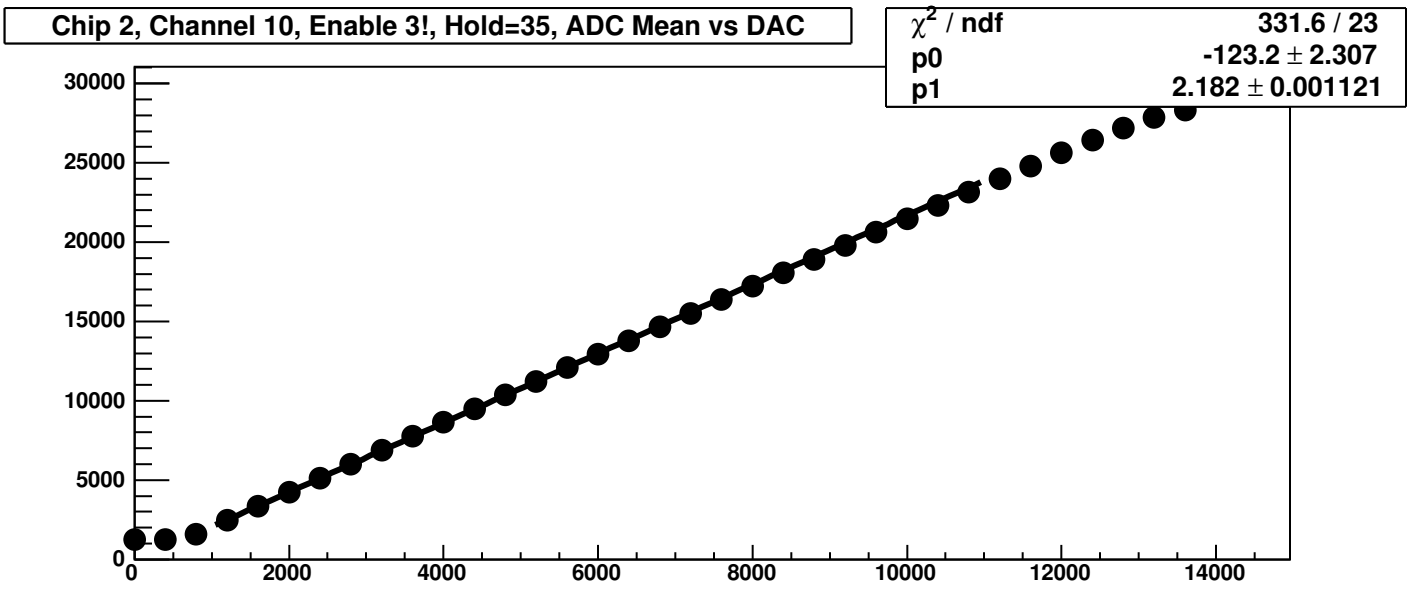

Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

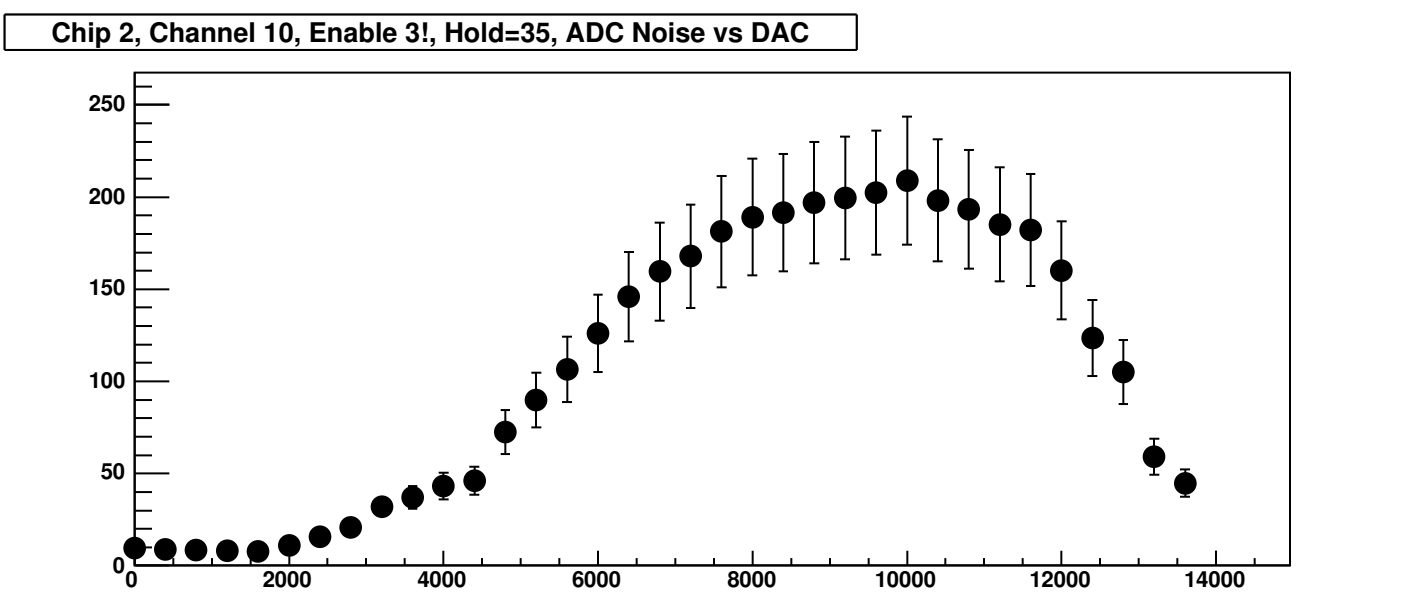

Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

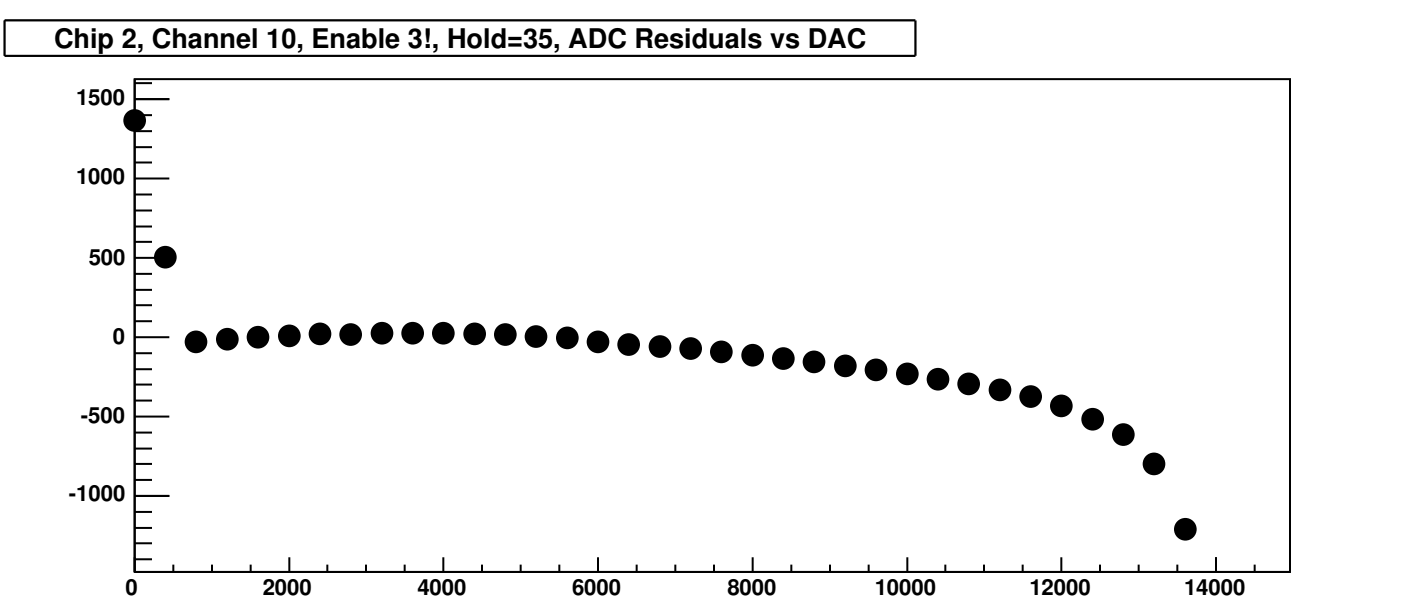

Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 77.8 / 23
p0 -176.2 ± 0.7439
p1 0.005824 ± 0.0001111
-100
-110
-120
-130
-140
-150
-160
-170
-180
0
2000
4000
6000
8000
10000
12000
14000

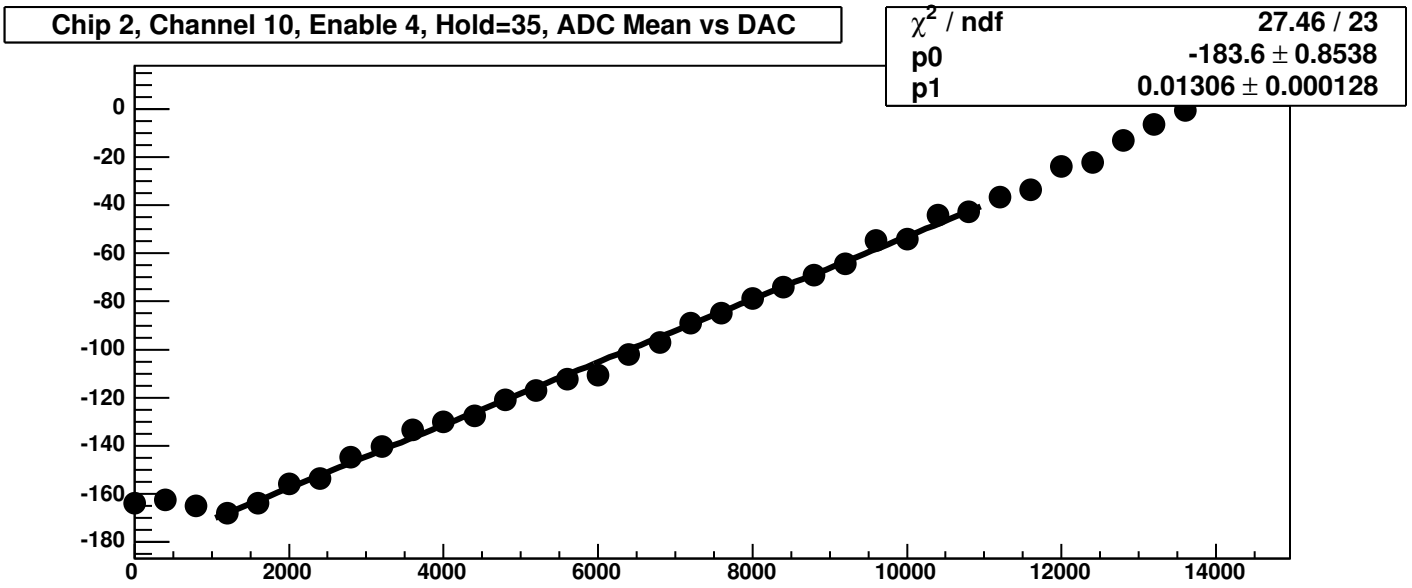

Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

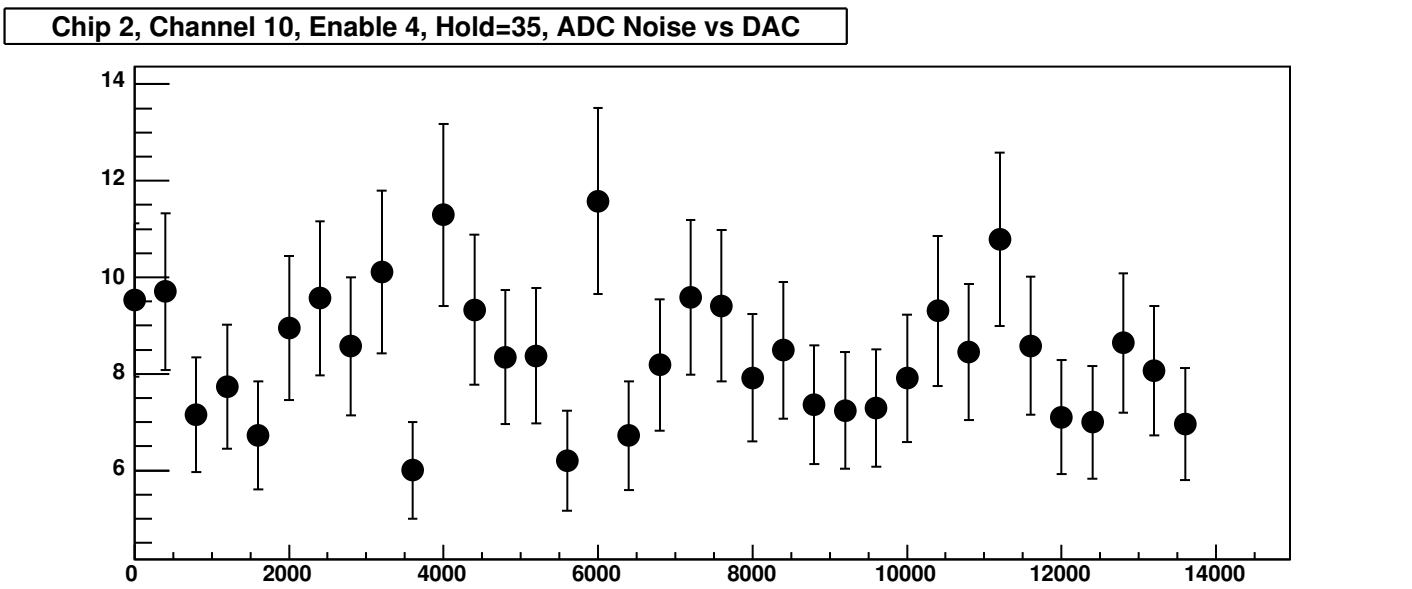

Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

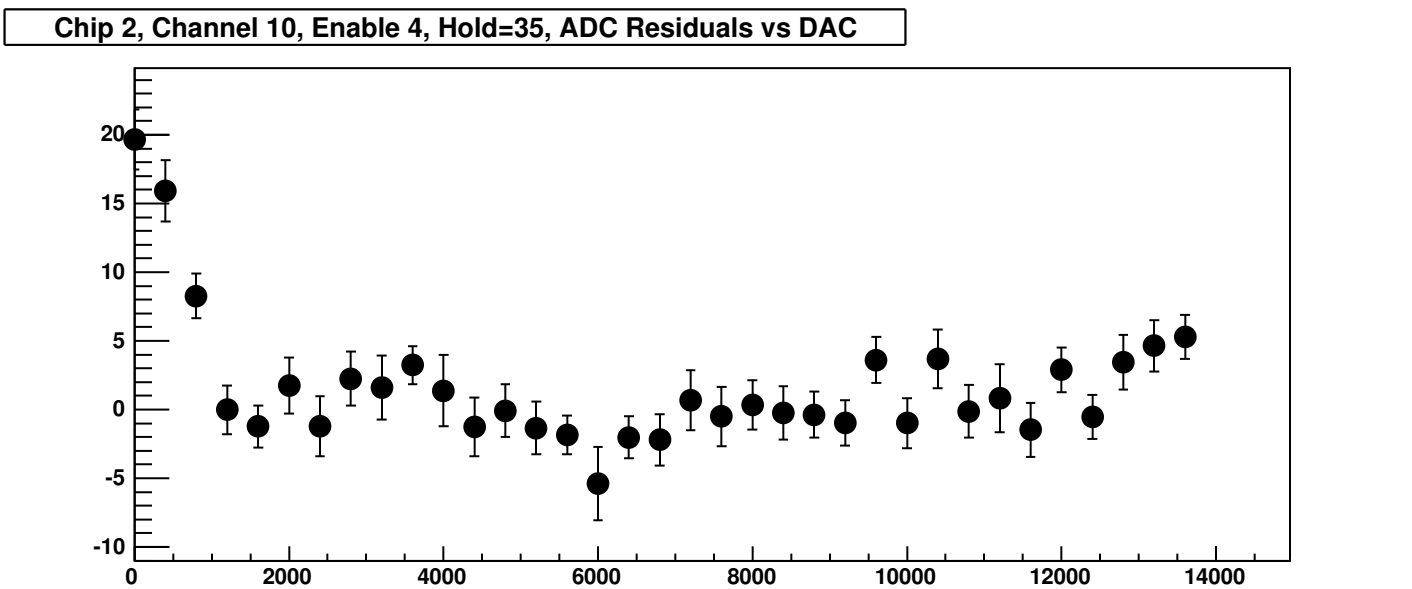

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 331.6 / 23
p0 -123.2 ± 2.307
p1 2.182 ± 0.001121
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

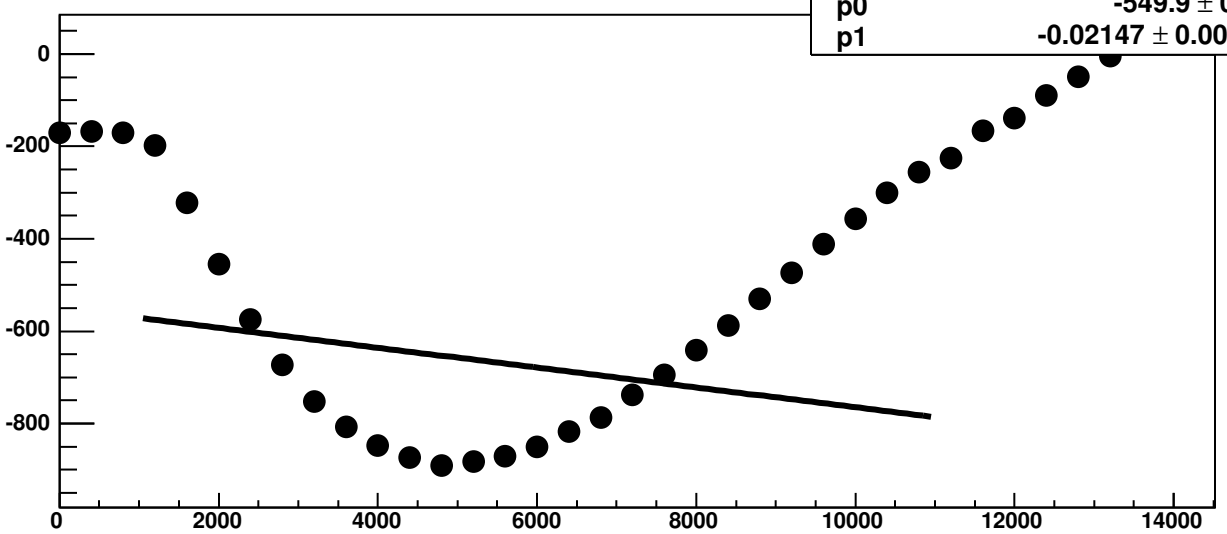

Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.46 / 23
p0 -183.6 ± 0.8538
p1 0.01306 ± 0.000128
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

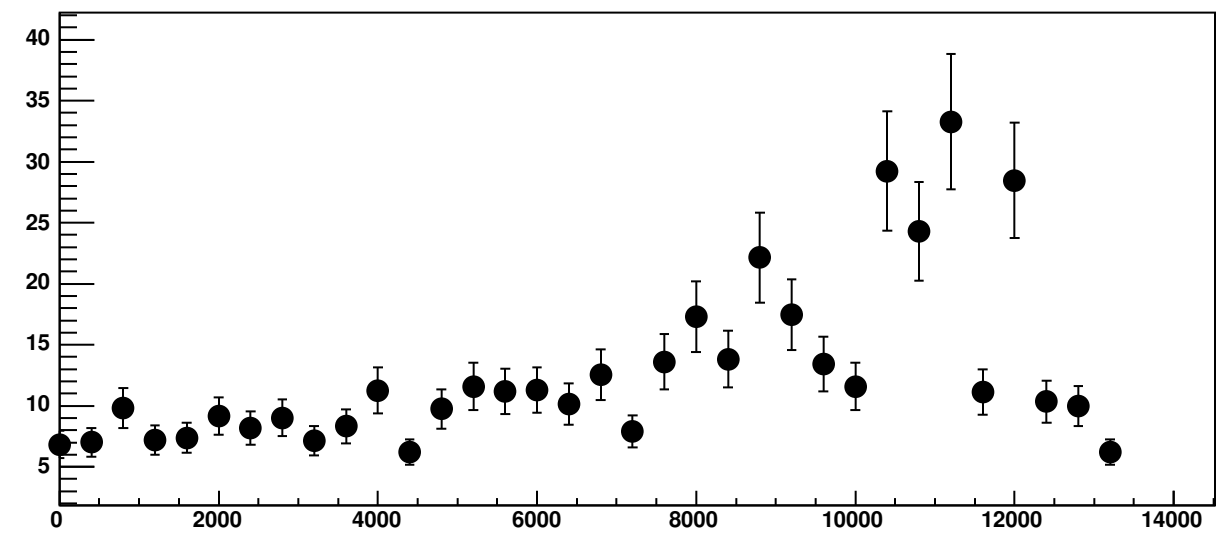

Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

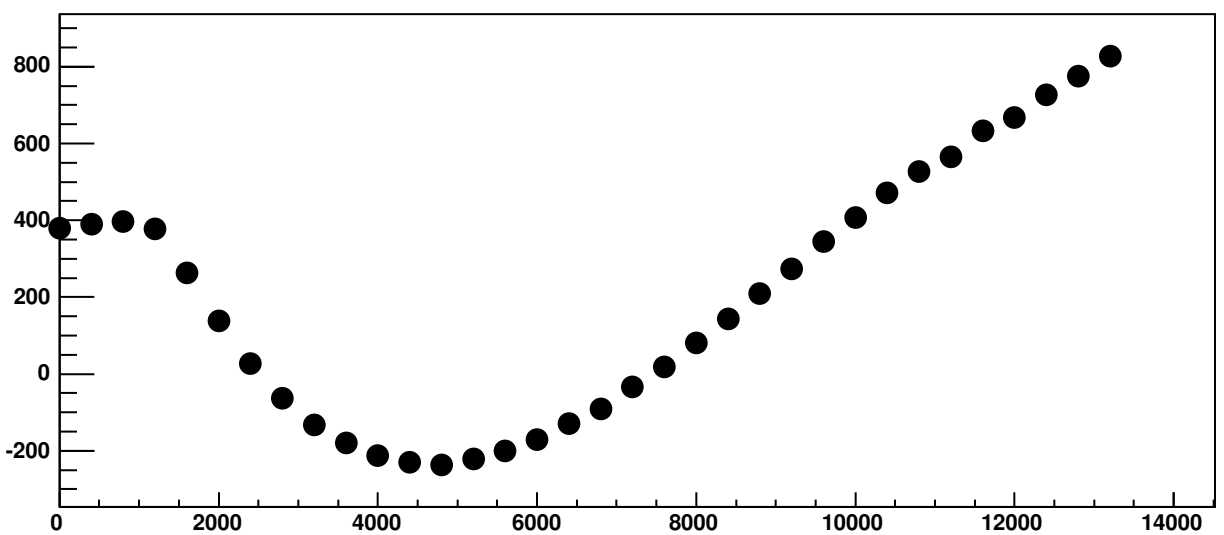
χ^2 / ndf	2.224e+05 / 23
p0	-549.9 ± 0.9595
p1	-0.02147 ± 0.0001834

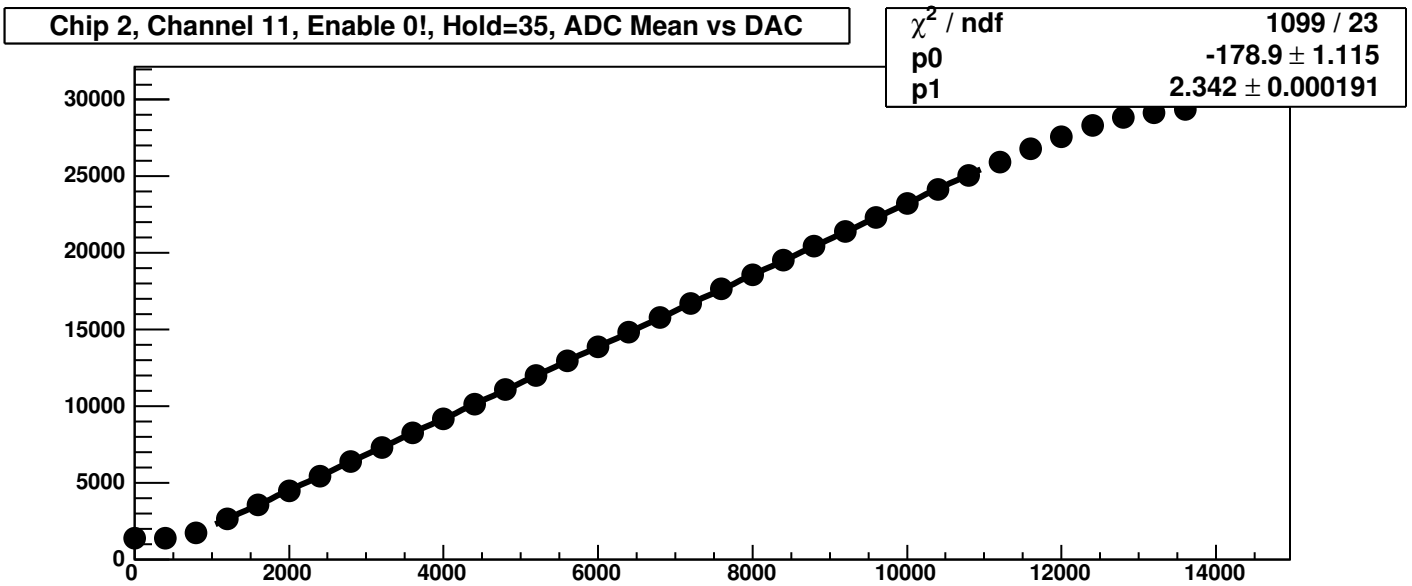


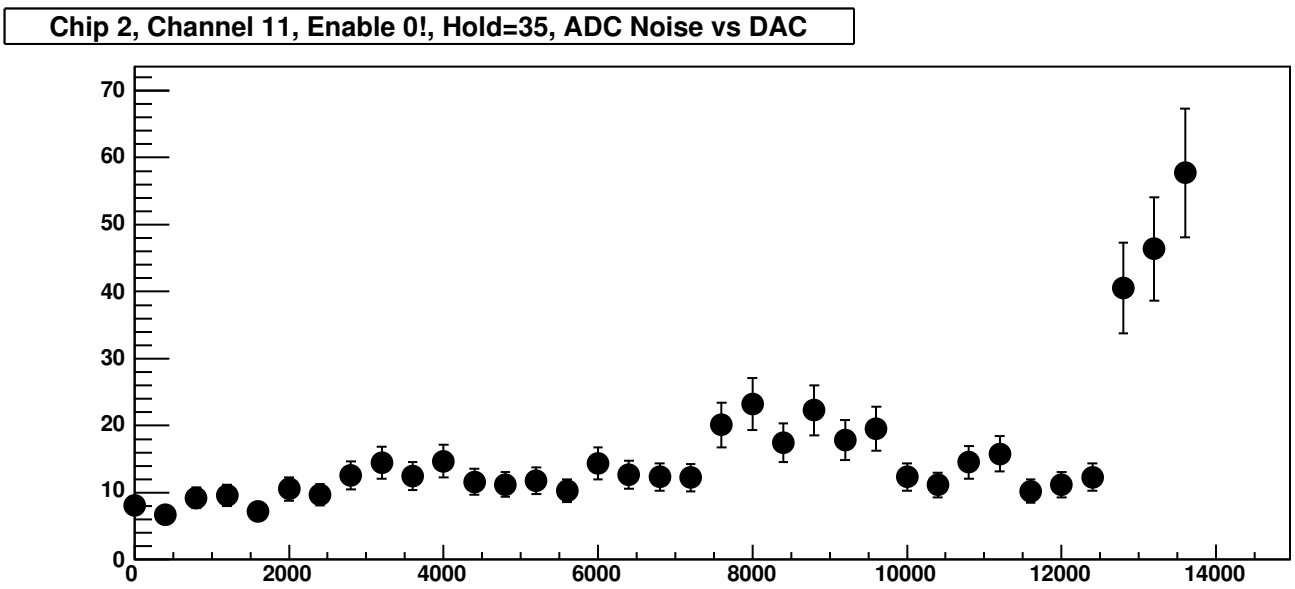
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

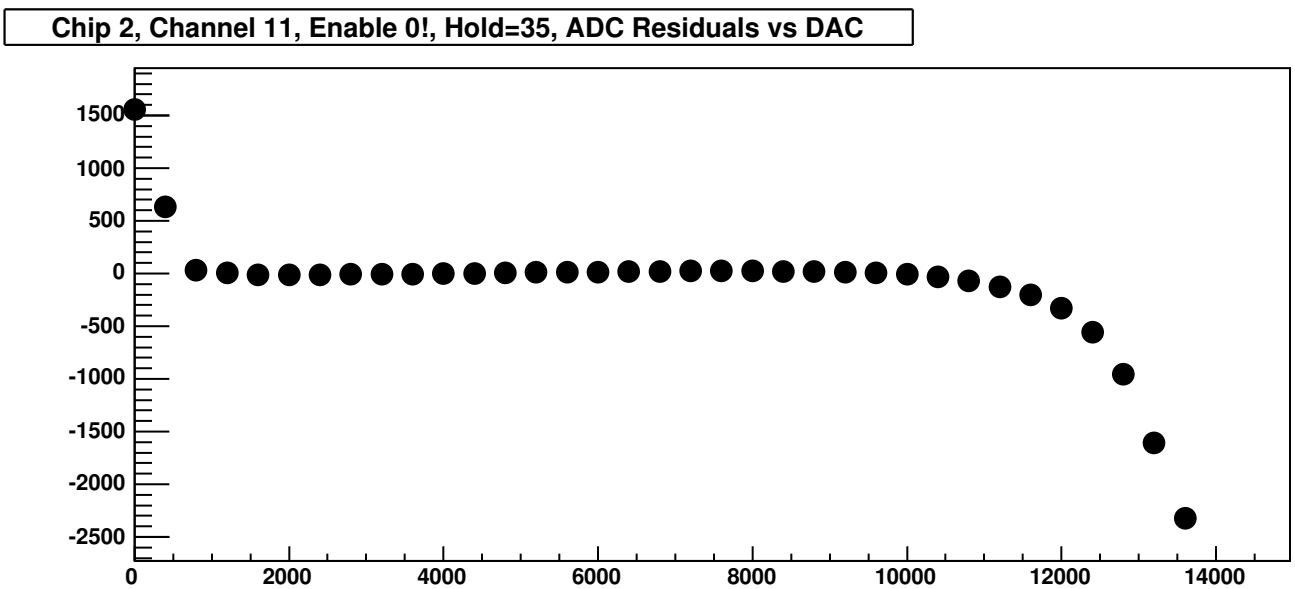


Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

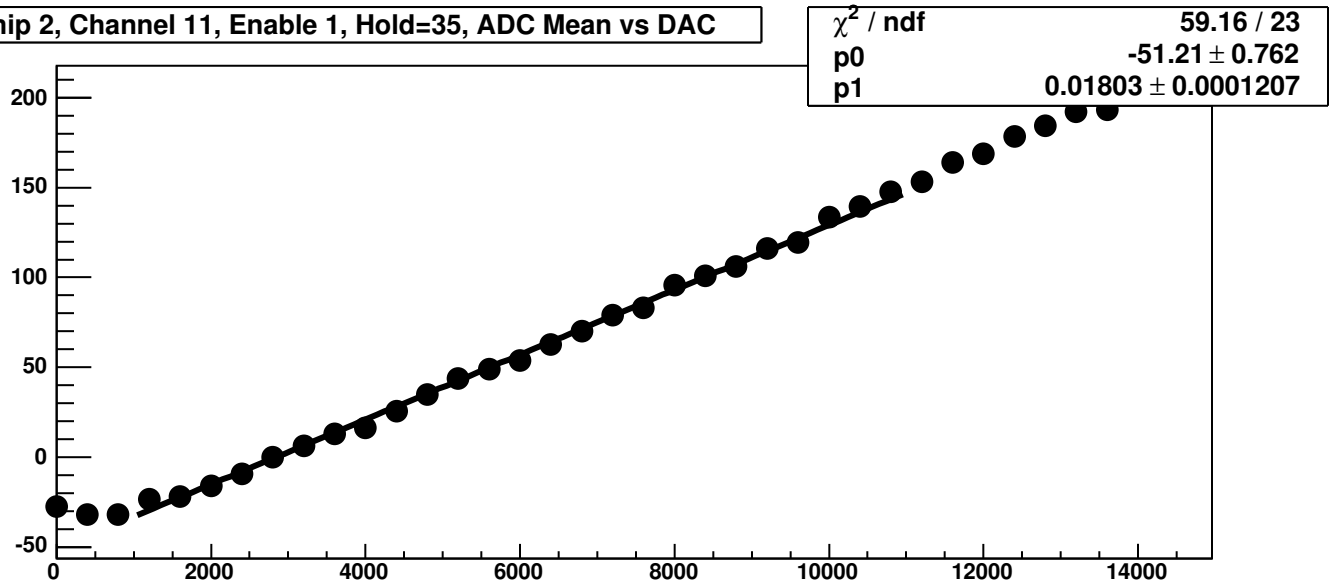



Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1099 / 23
p0 -178.9 ± 1.115
p1 2.342 ± 0.000191
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

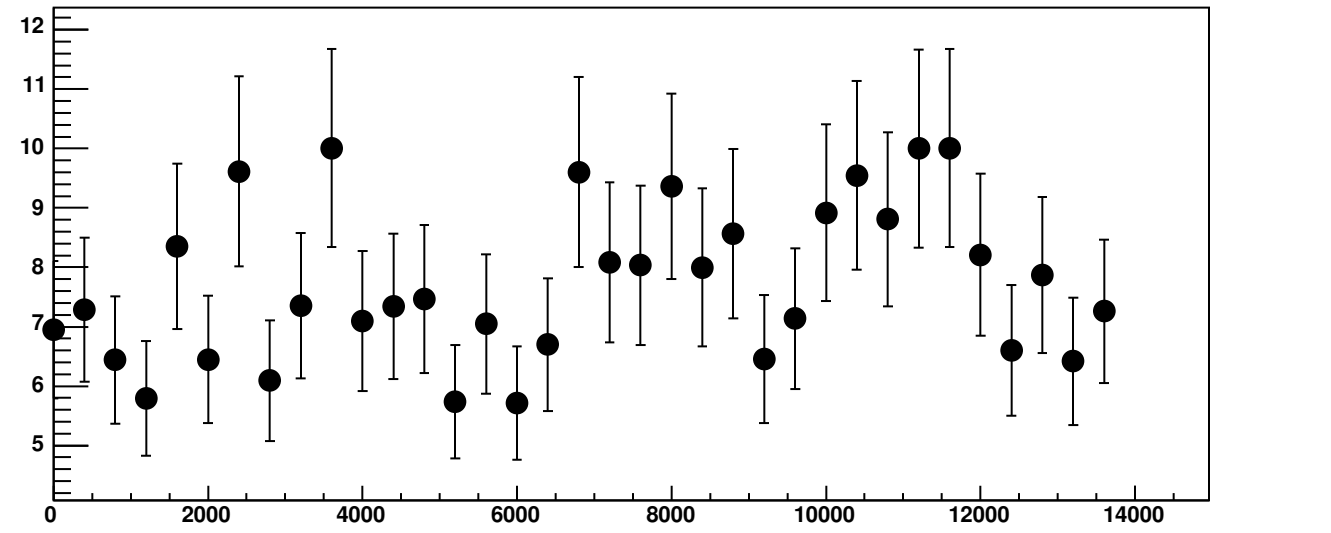

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

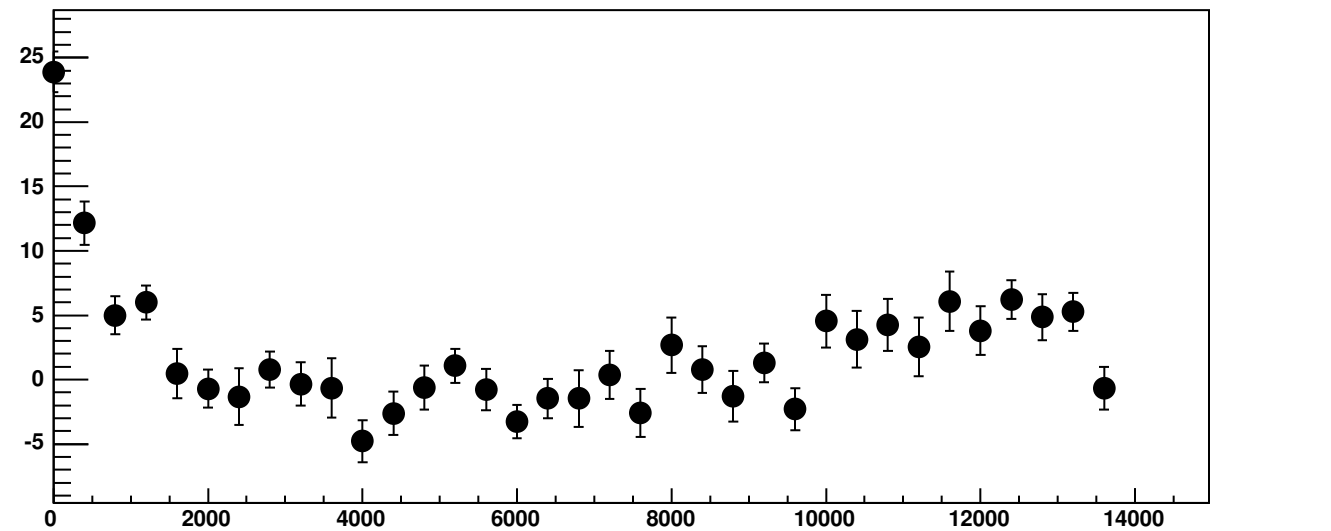
Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

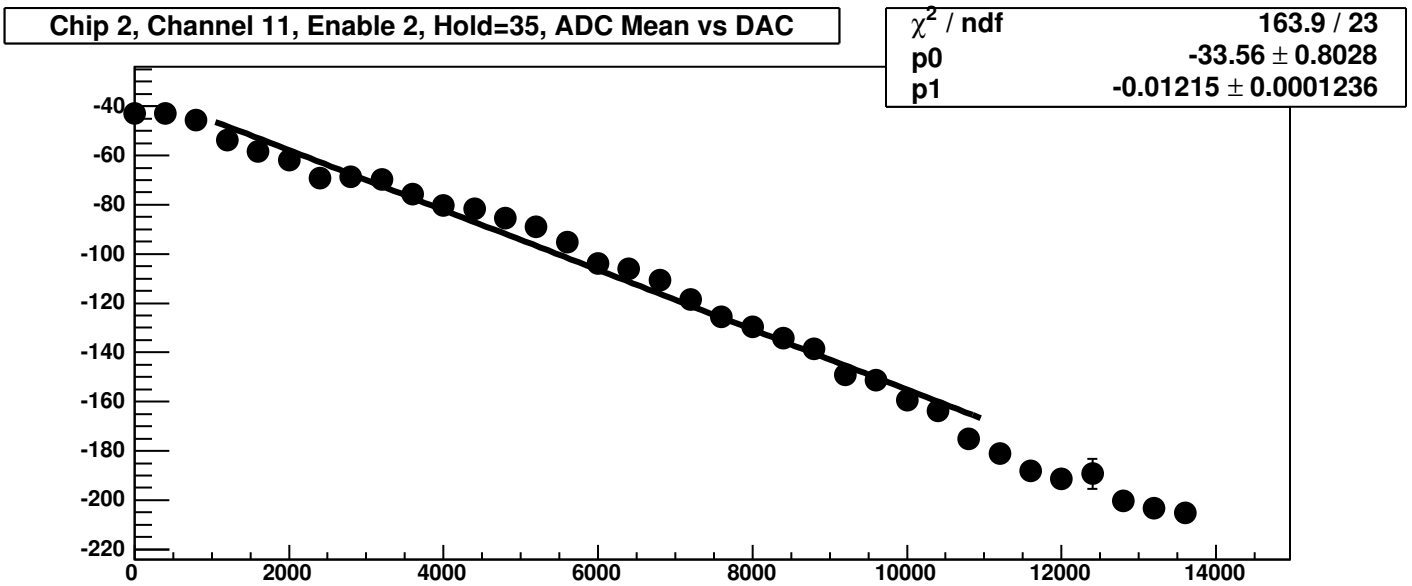


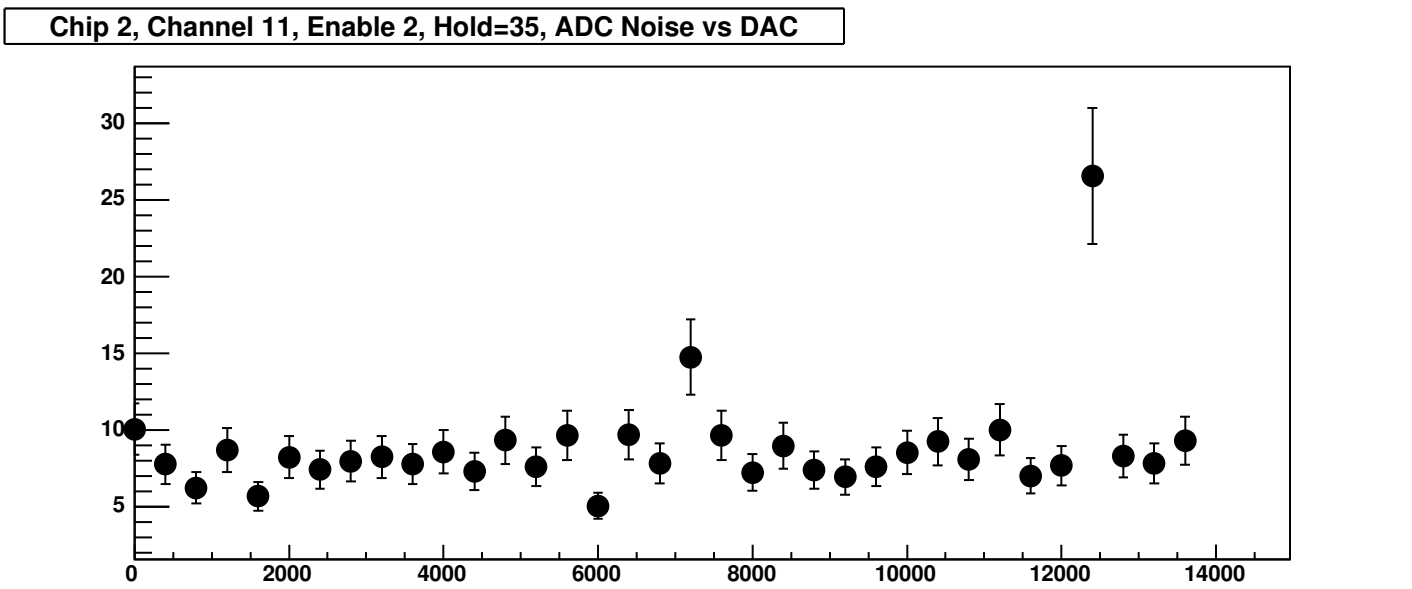
Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

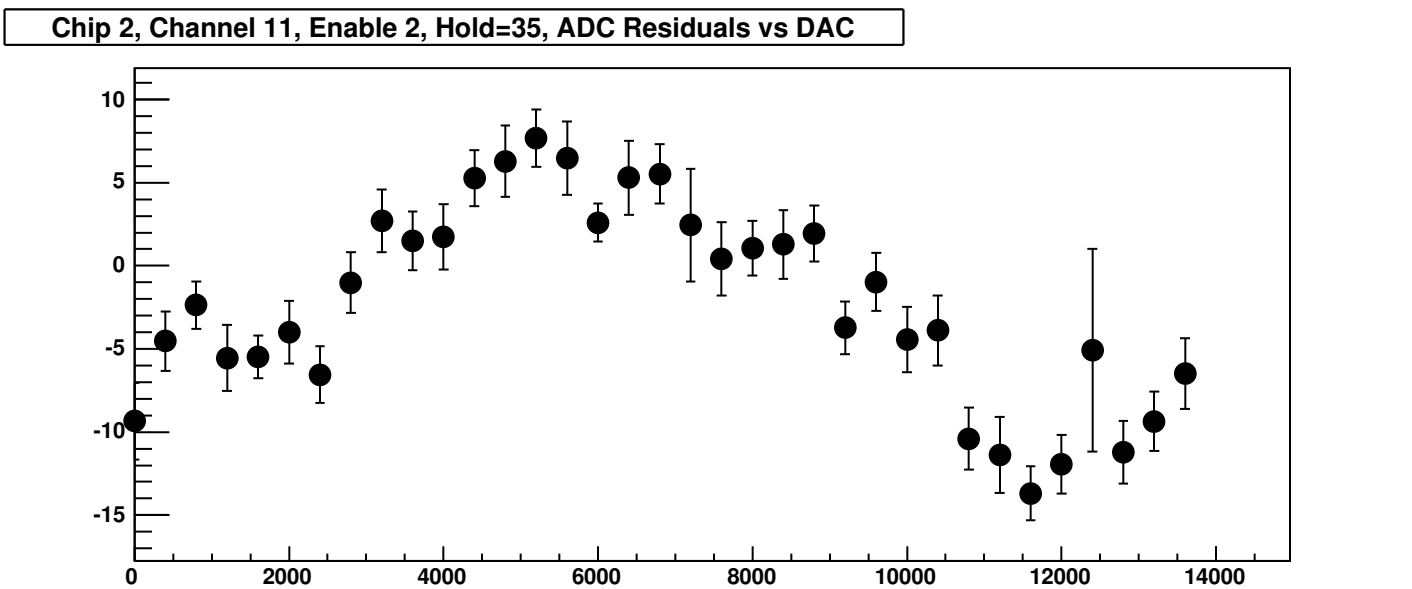


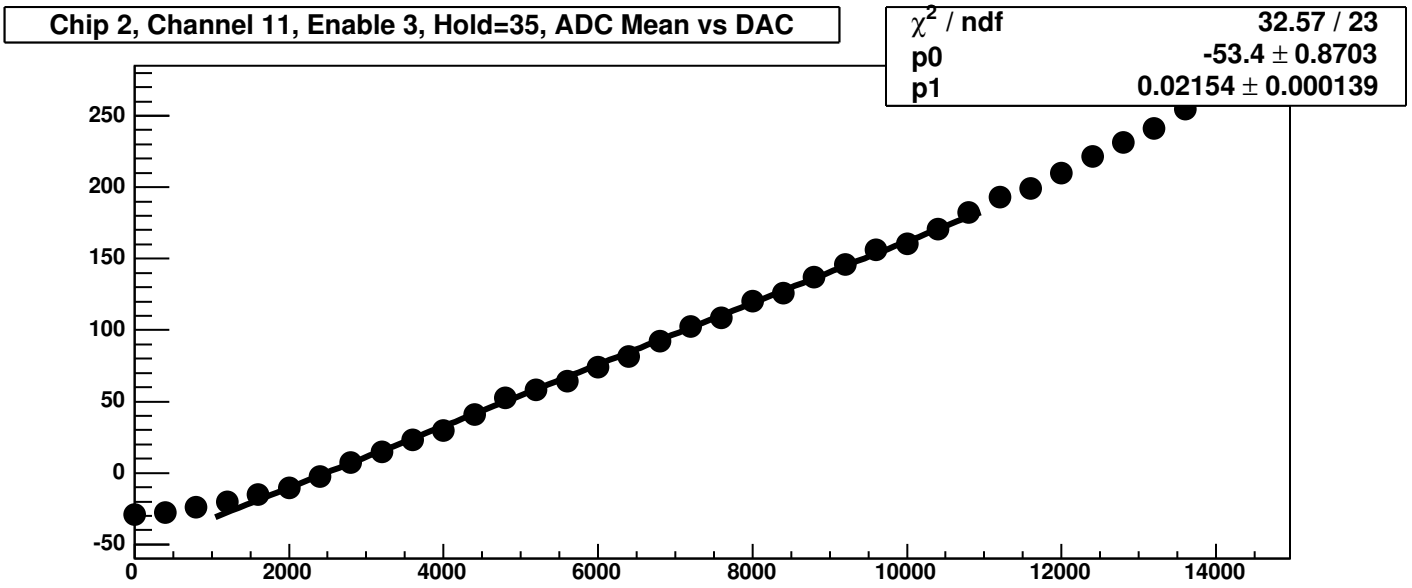
Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

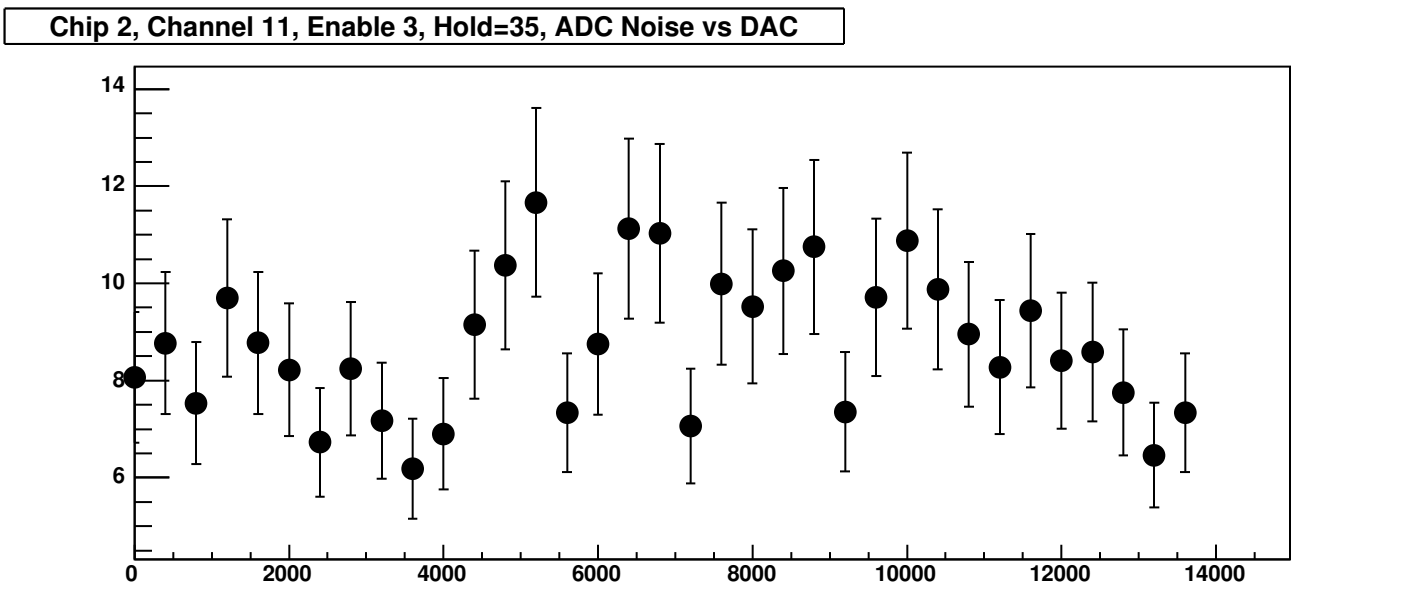


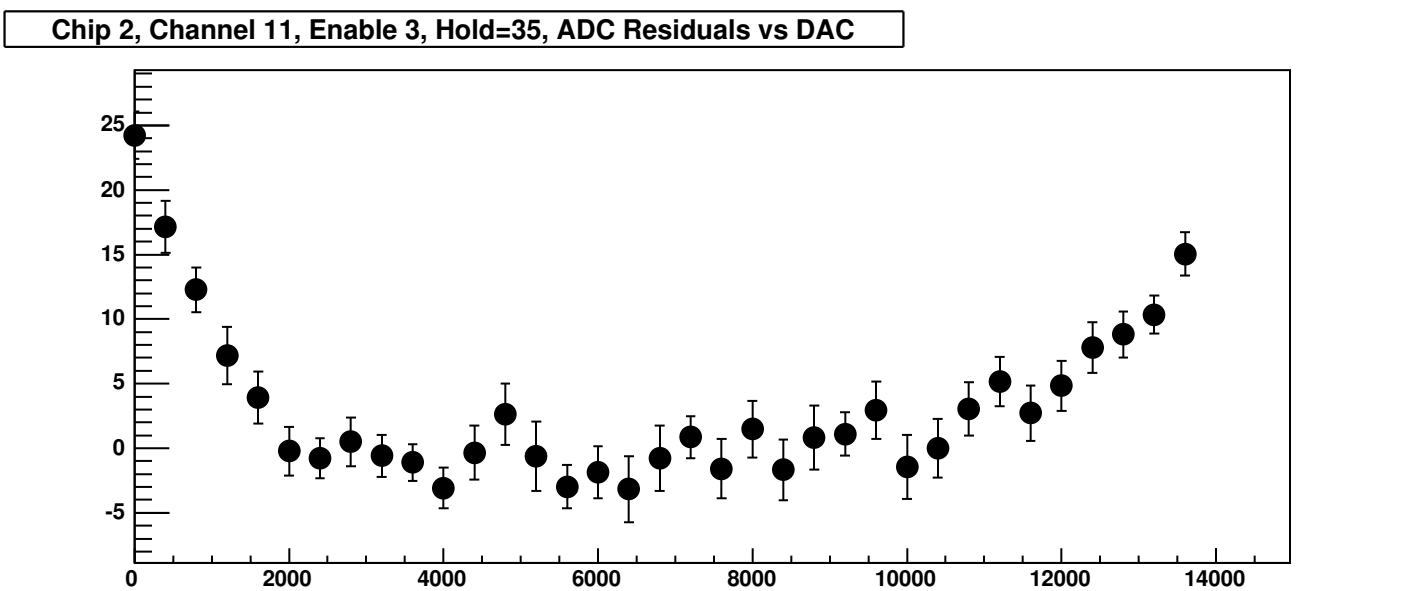

Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 163.9 / 23
p0 -33.56 ± 0.8028
p1 -0.01215 ± 0.0001236
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

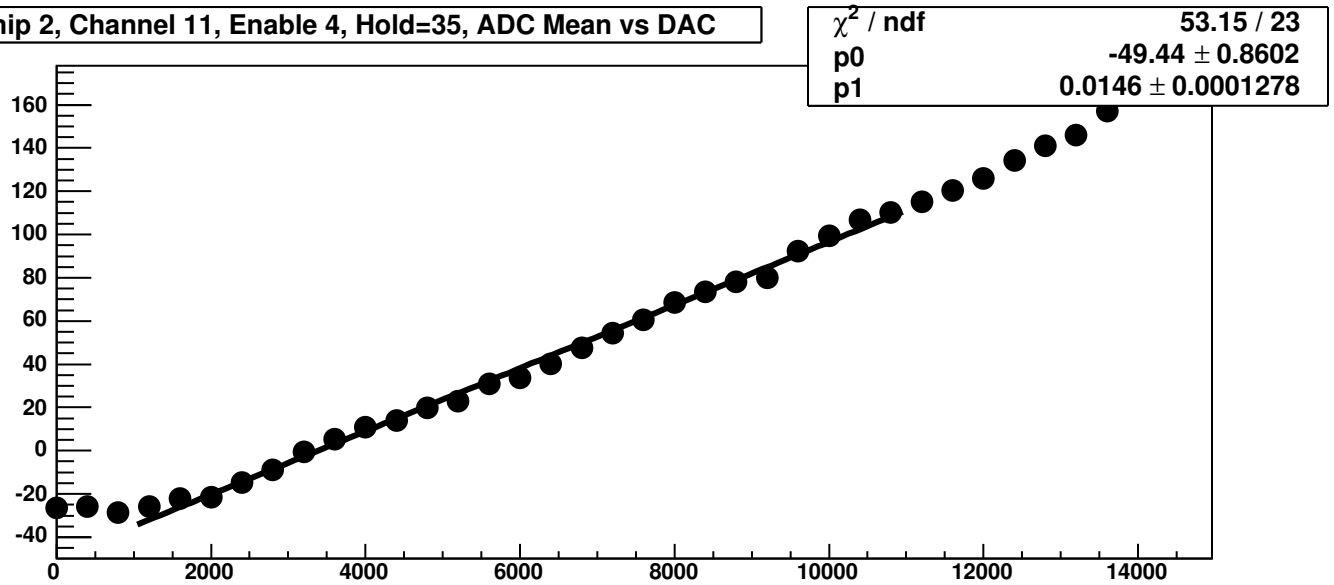

Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.57 / 23
p0 -53.4 ± 0.8703
p1 0.02154 ± 0.000139
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

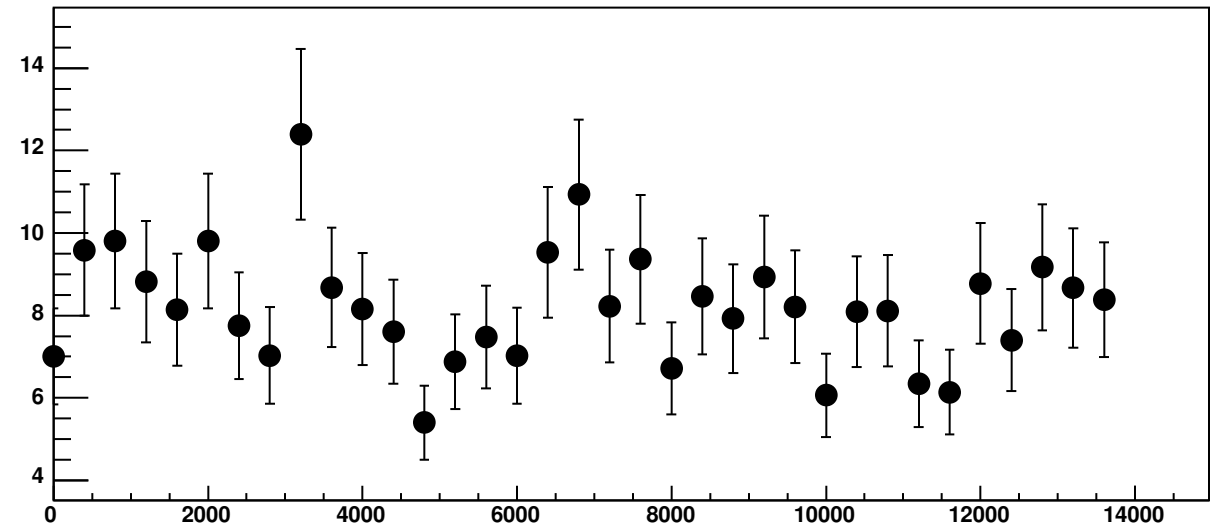

Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

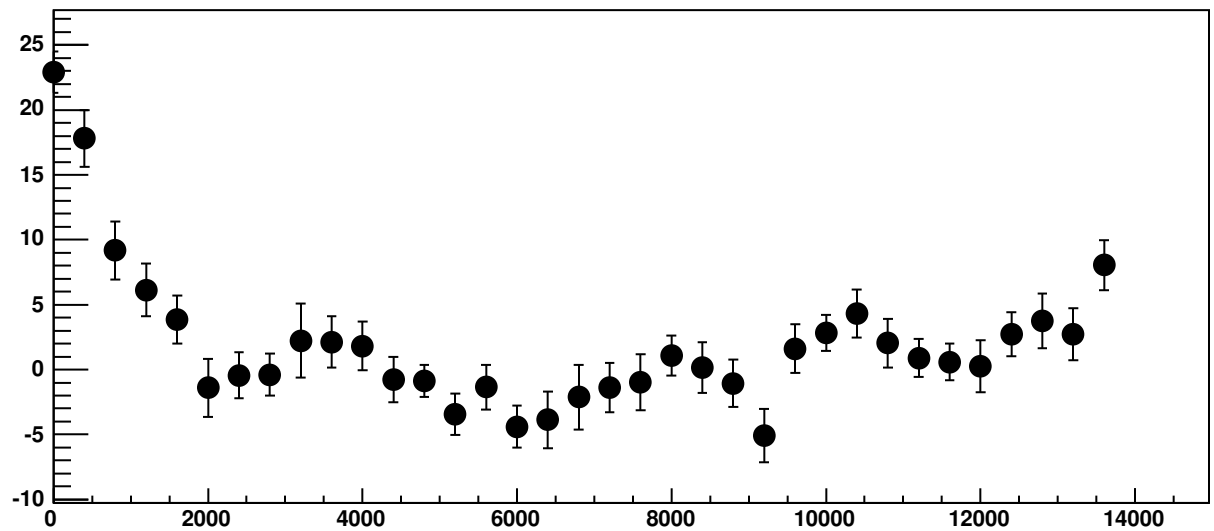
Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



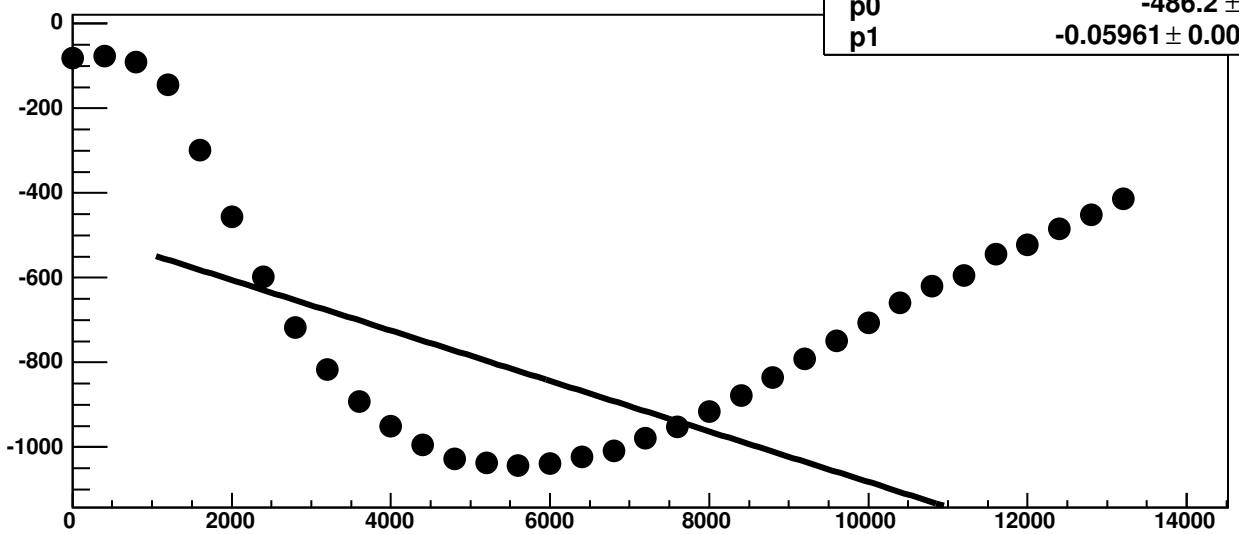
Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



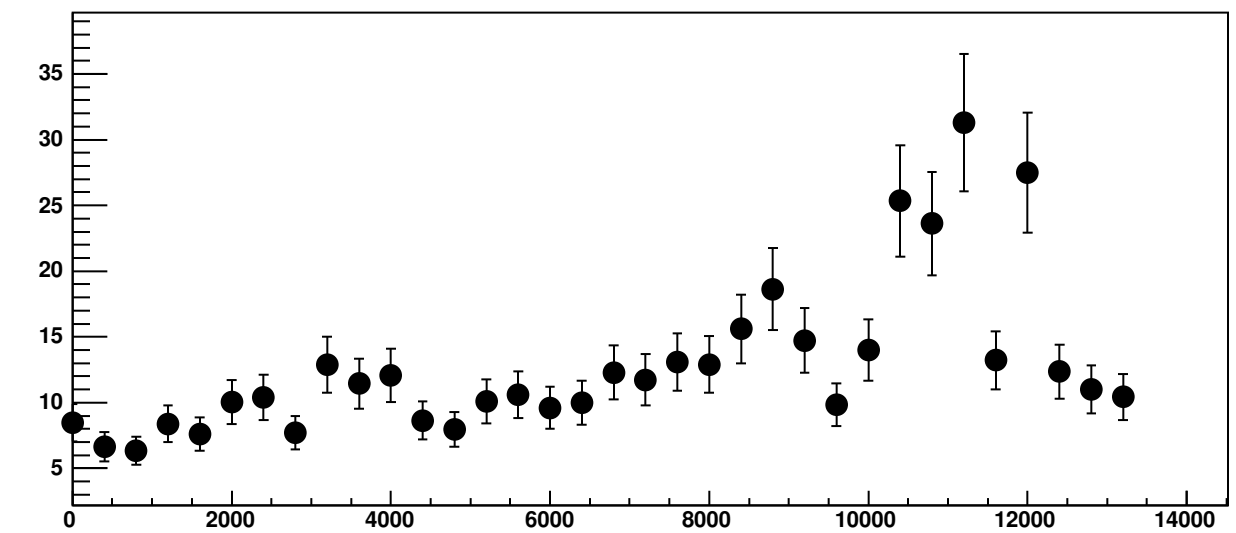
Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



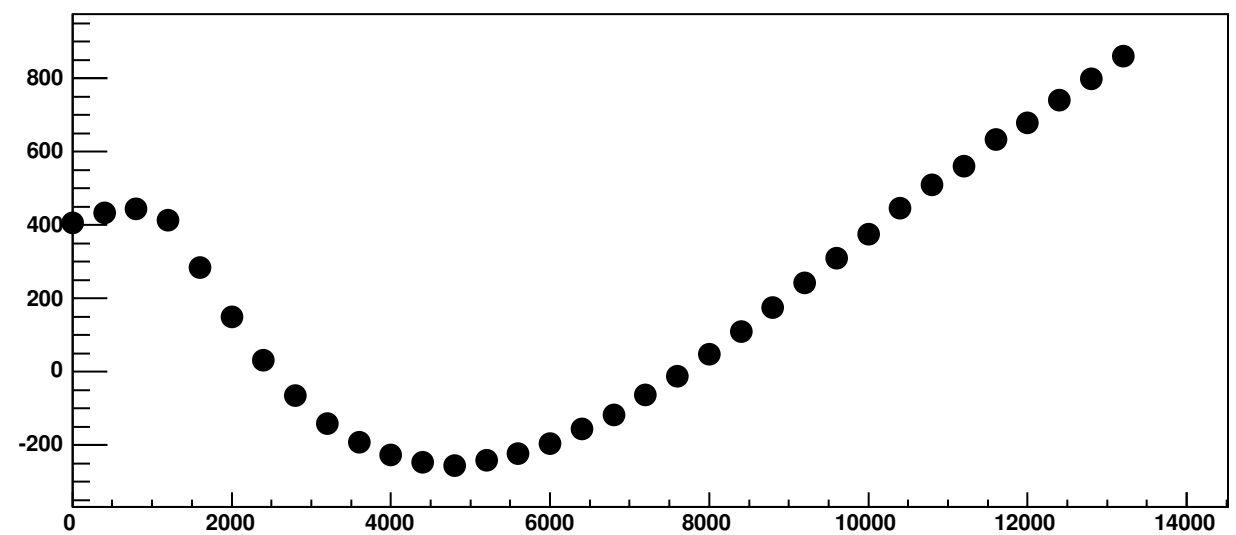
Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC



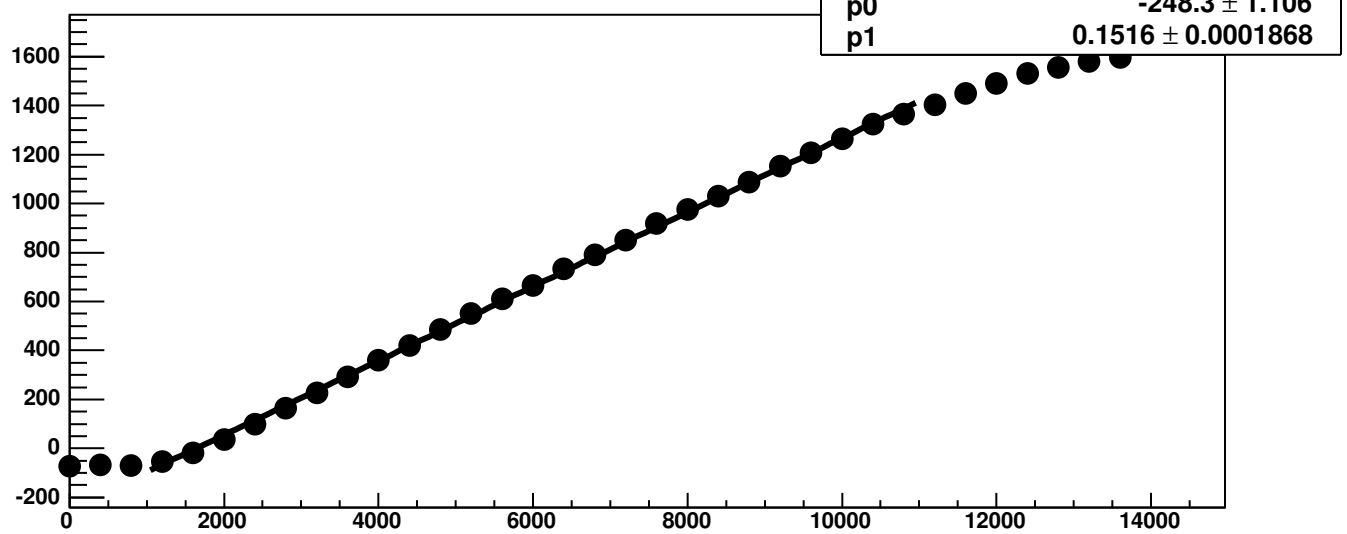
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC



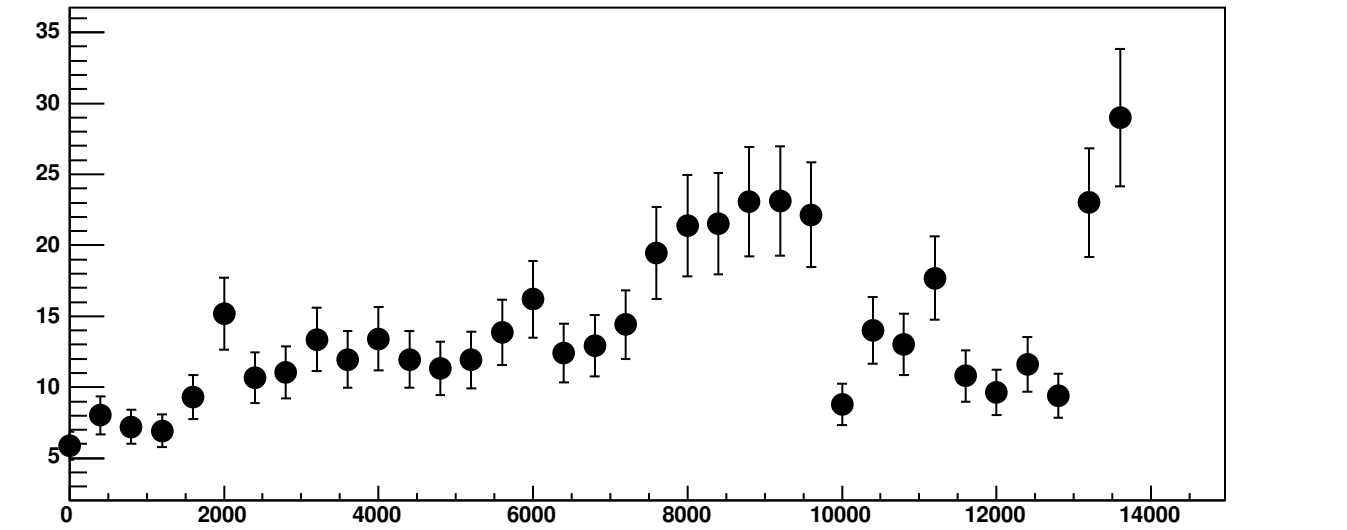
Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC



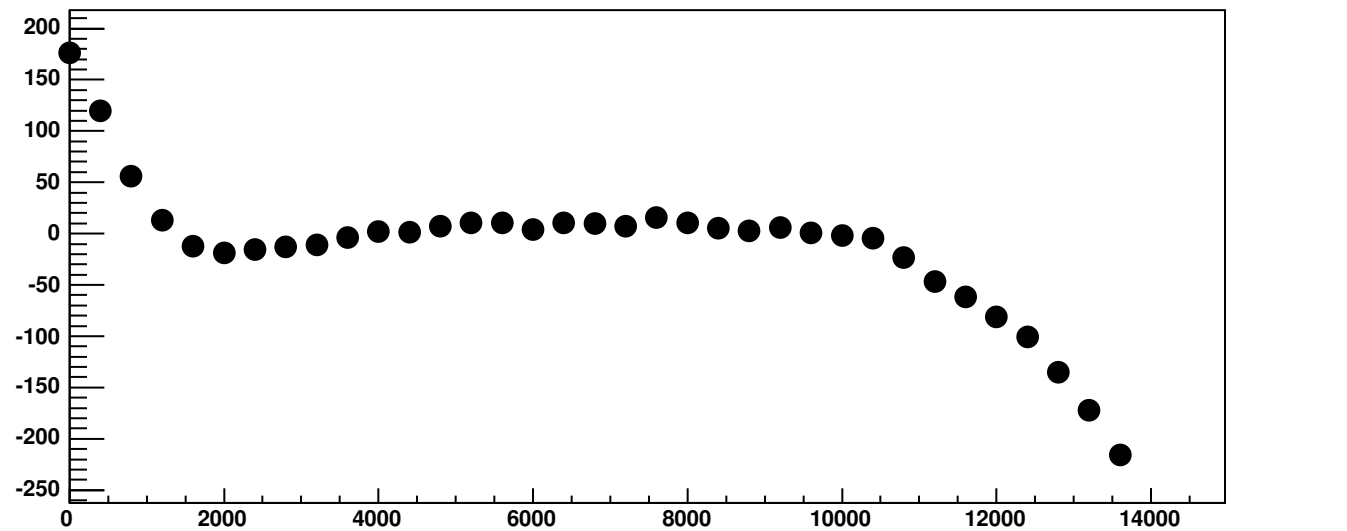
Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

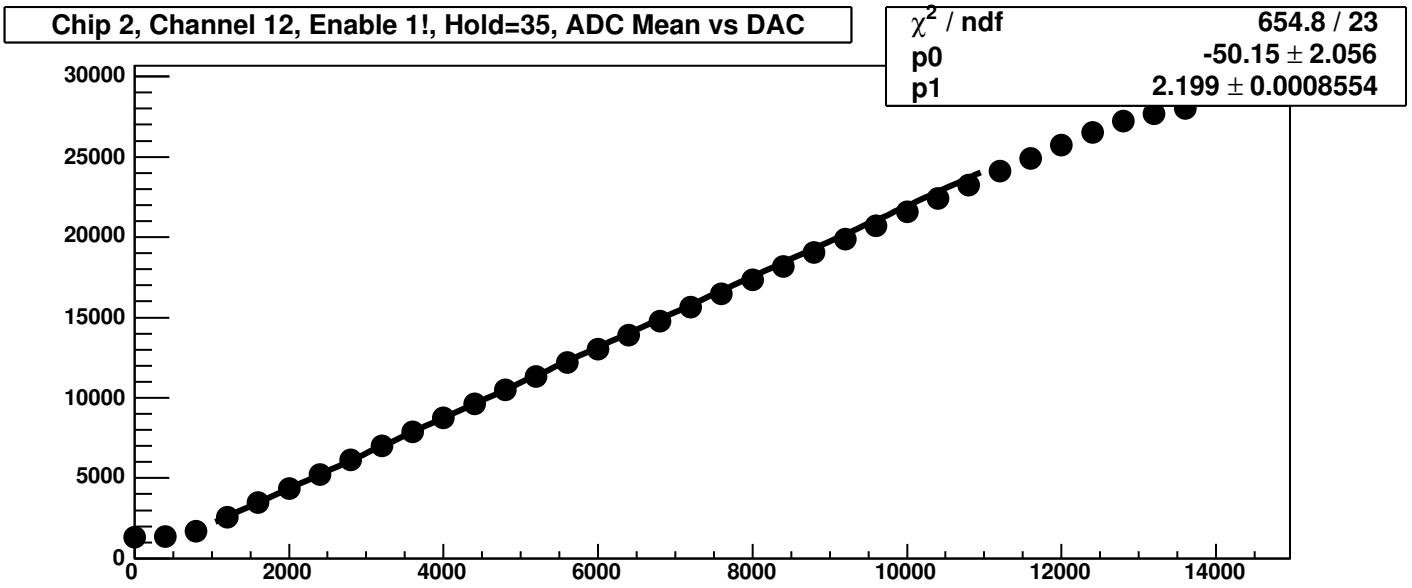


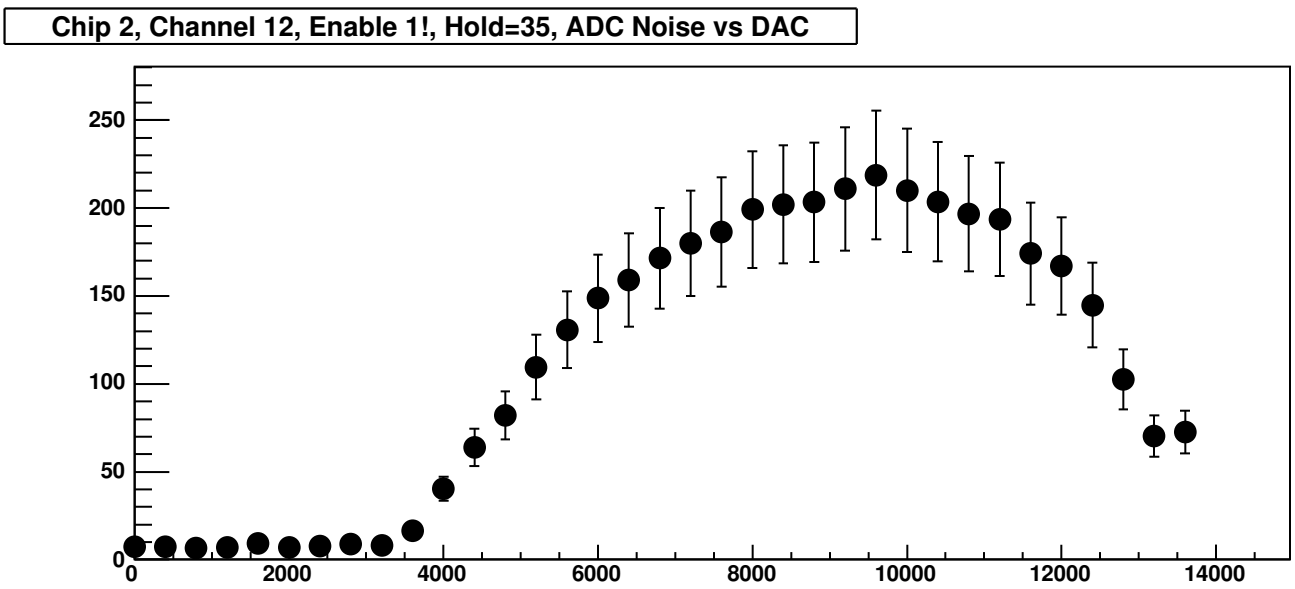
Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

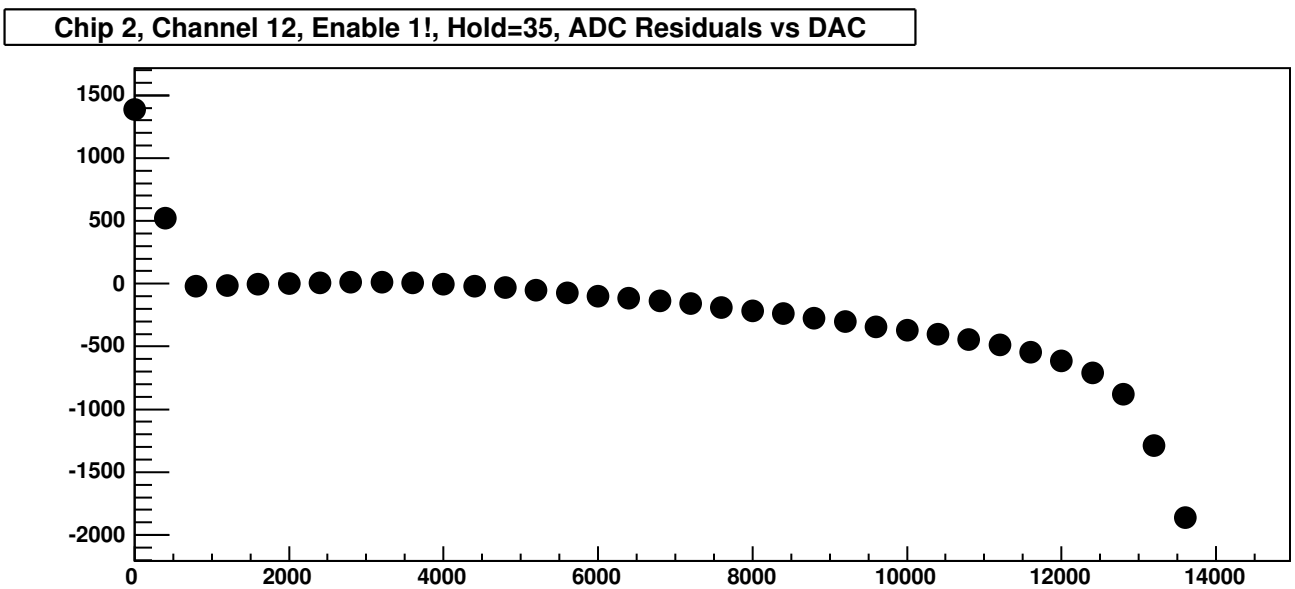


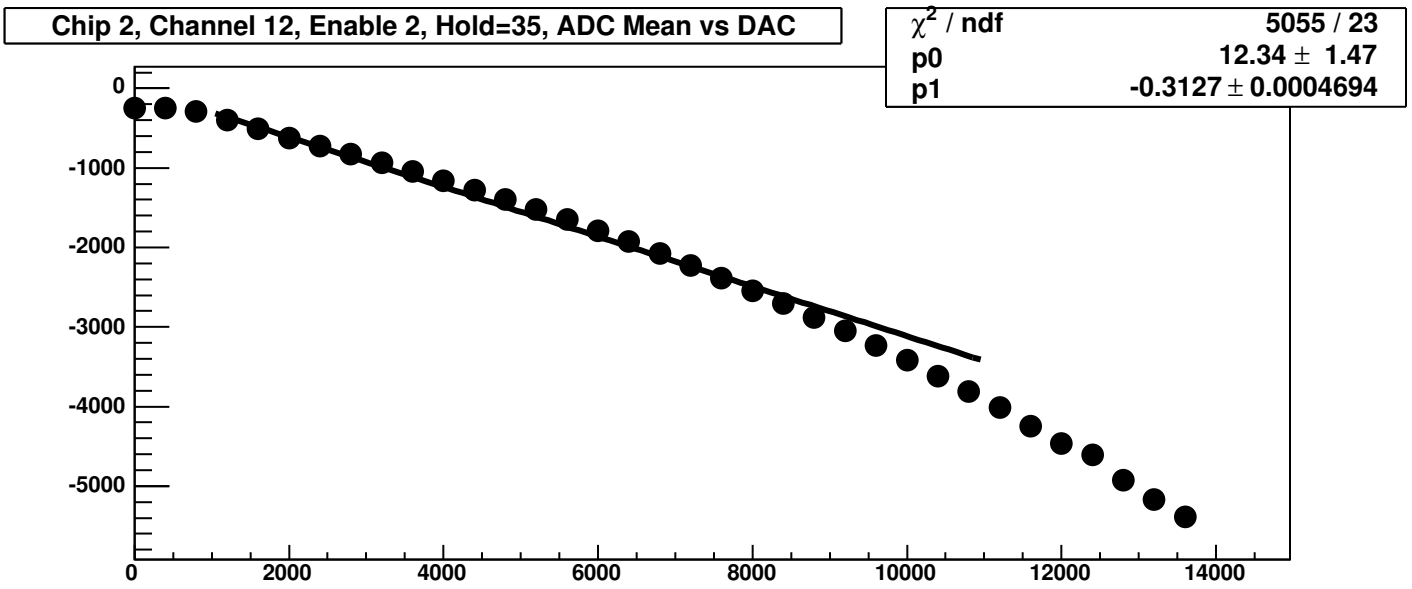
Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

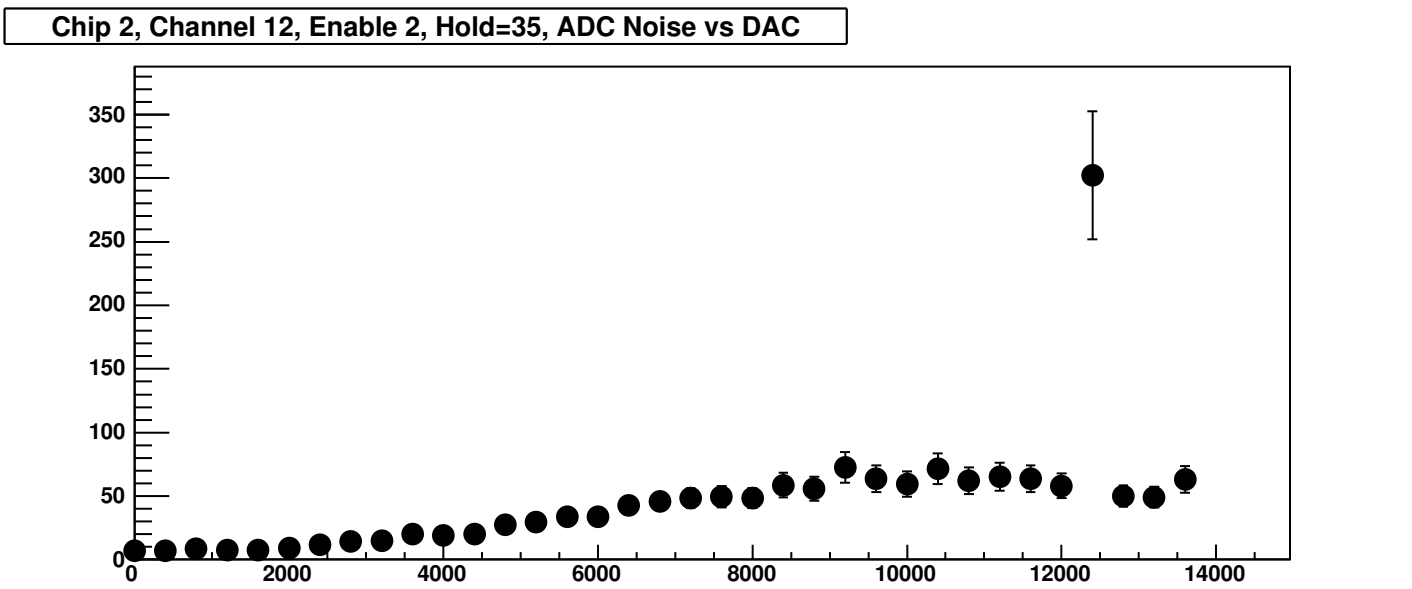


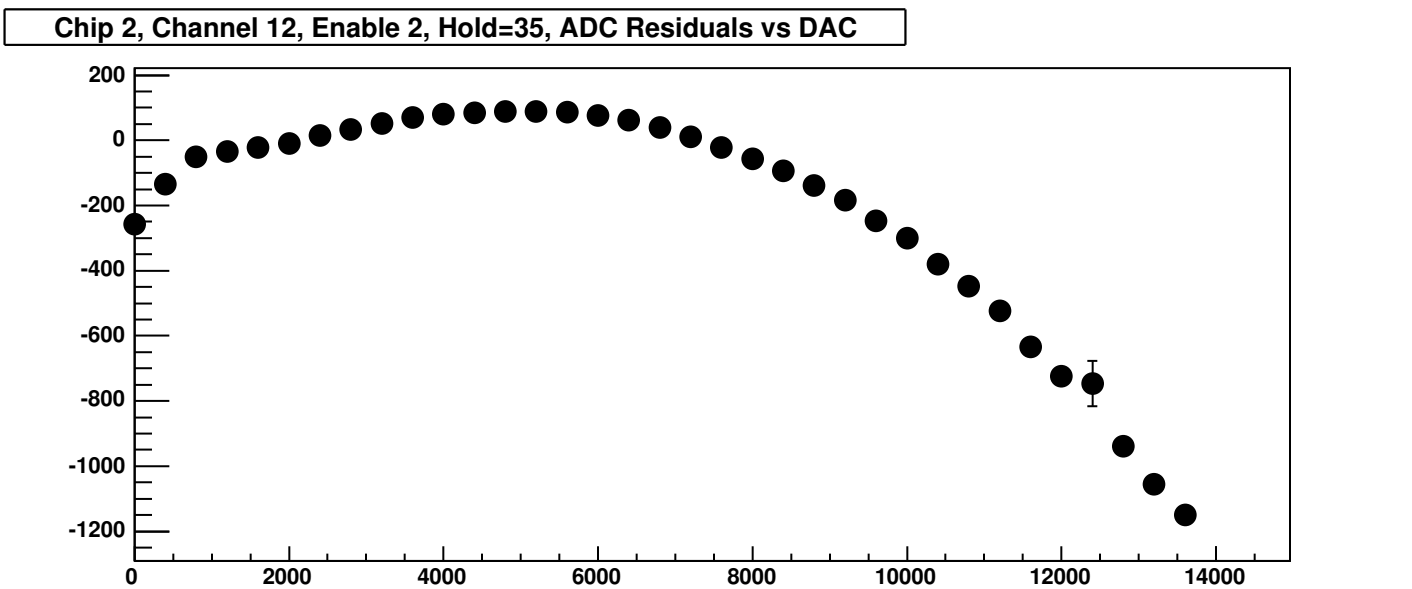

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 654.8 / 23
p0 -50.15 ± 2.056
p1 2.199 ± 0.0008554
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

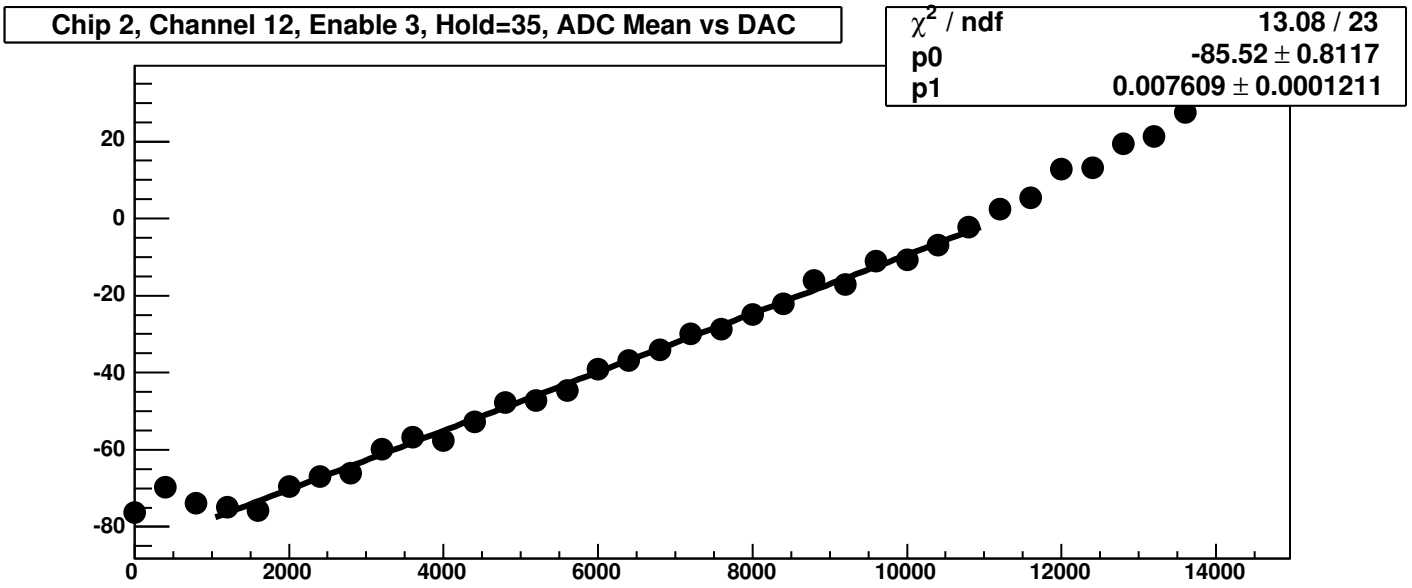

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

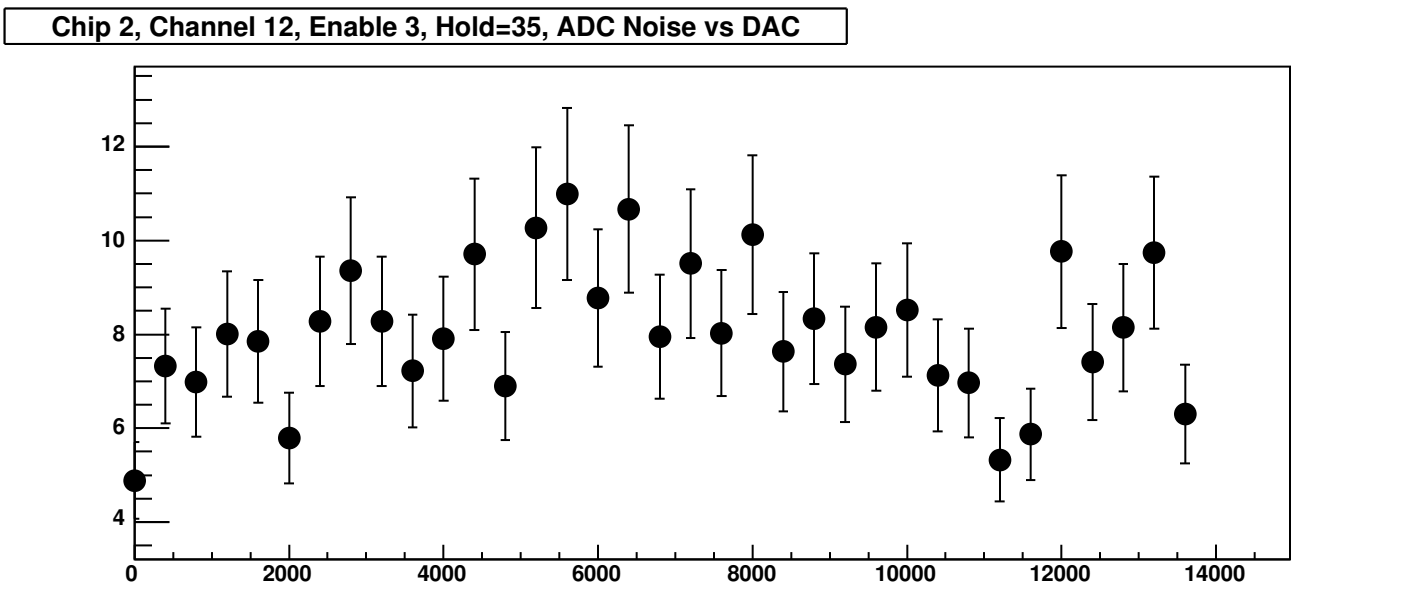

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

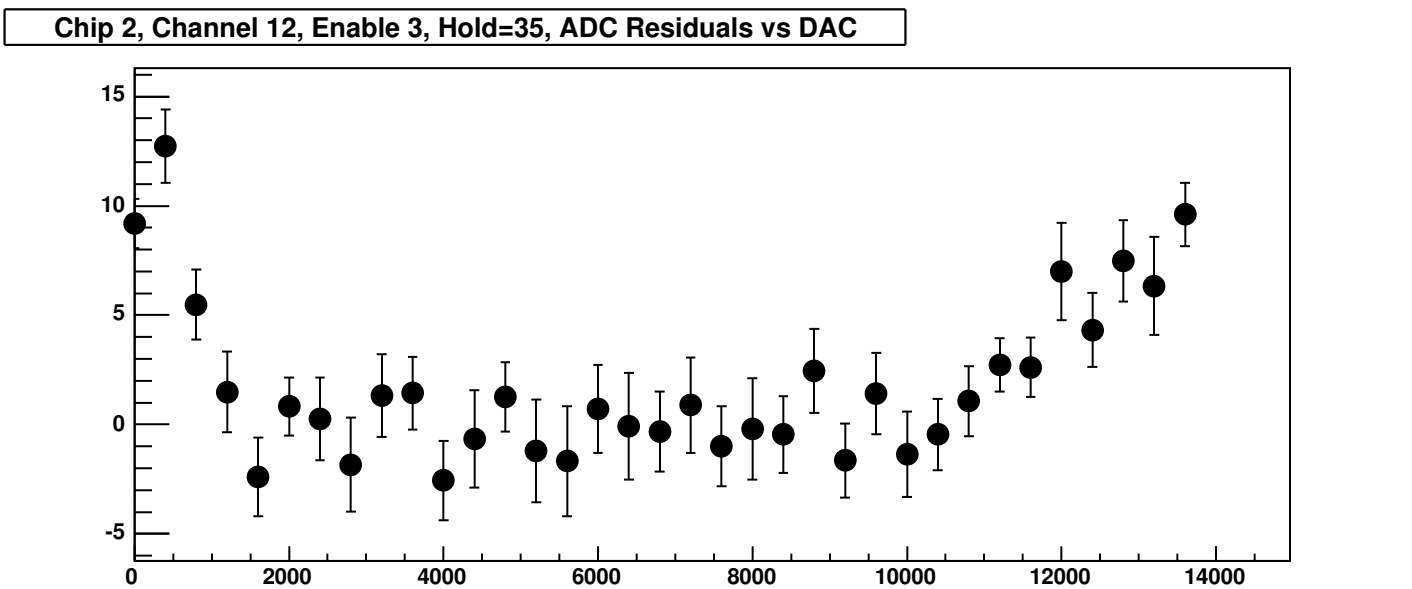

Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 5055 / 23
p0 12.34 ± 1.47
p1 -0.3127 ± 0.0004694
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

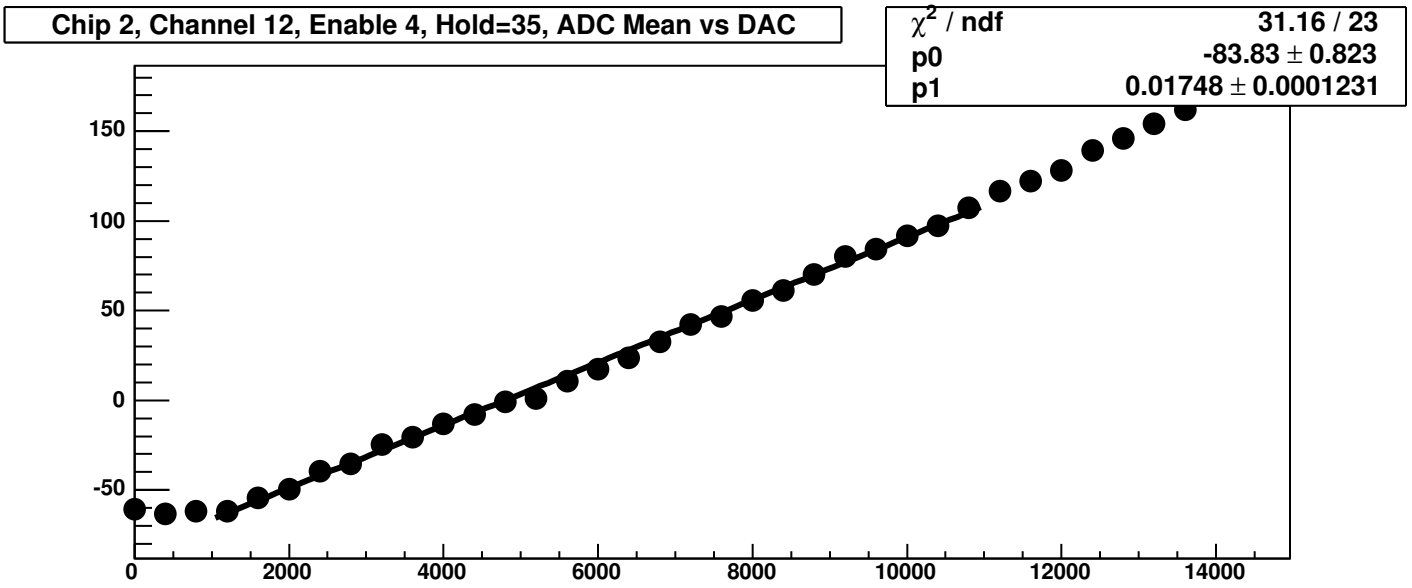

Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

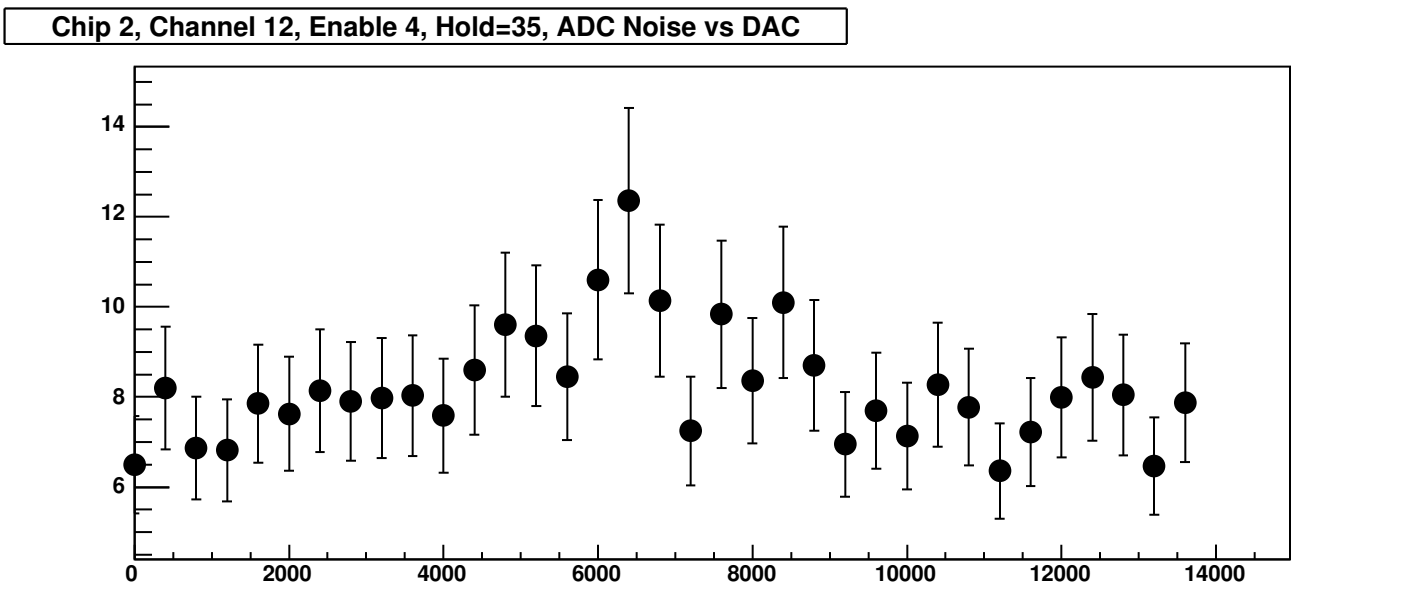

Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
2000
4000
6000
8000
10000
12000
14000

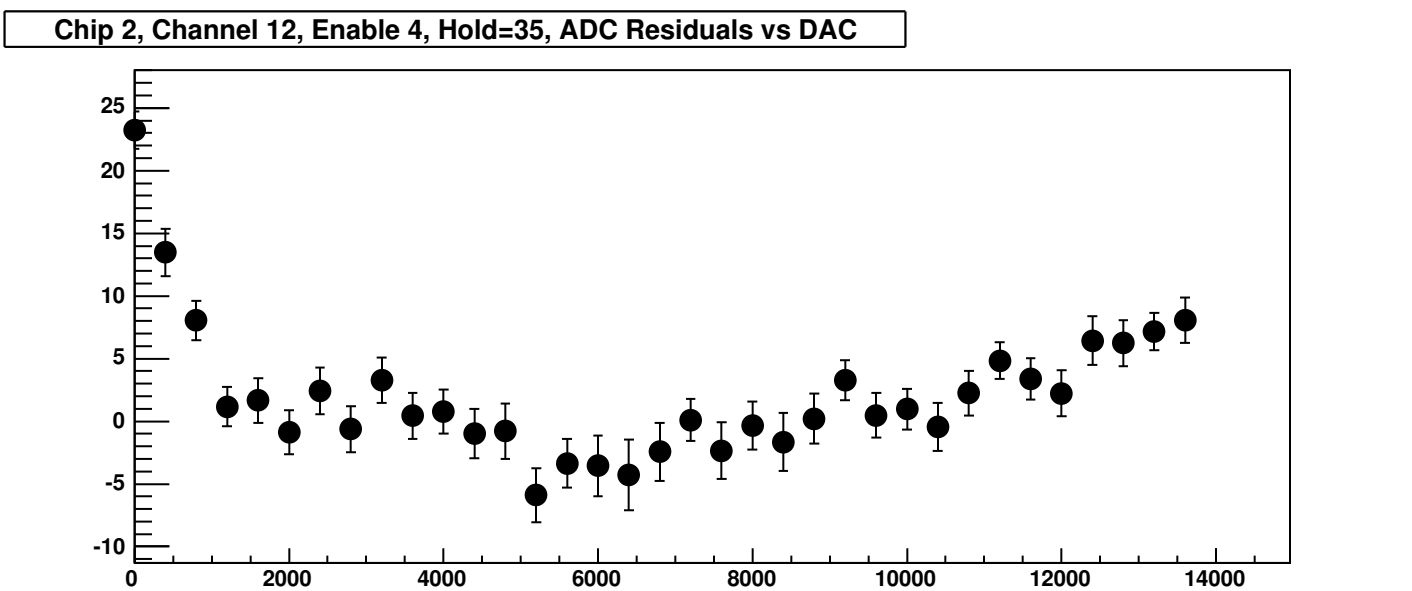

Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 13.08 / 23
p0 -85.52 ± 0.8117
p1 0.007609 ± 0.0001211
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

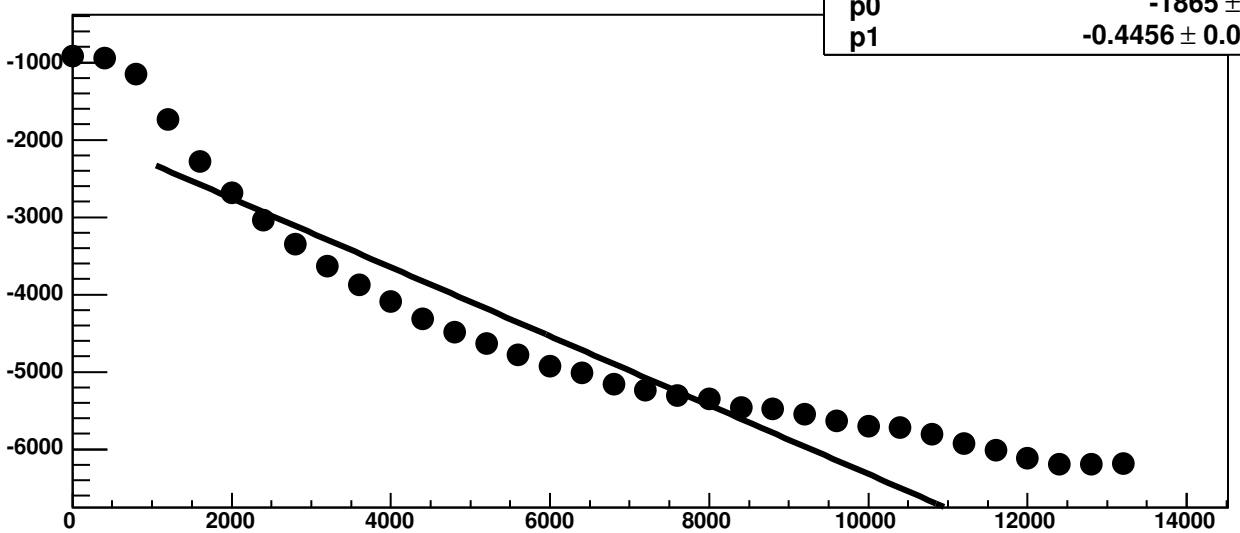

Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.16 / 23
p0 -83.83 ± 0.823
p1 0.01748 ± 0.0001231
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

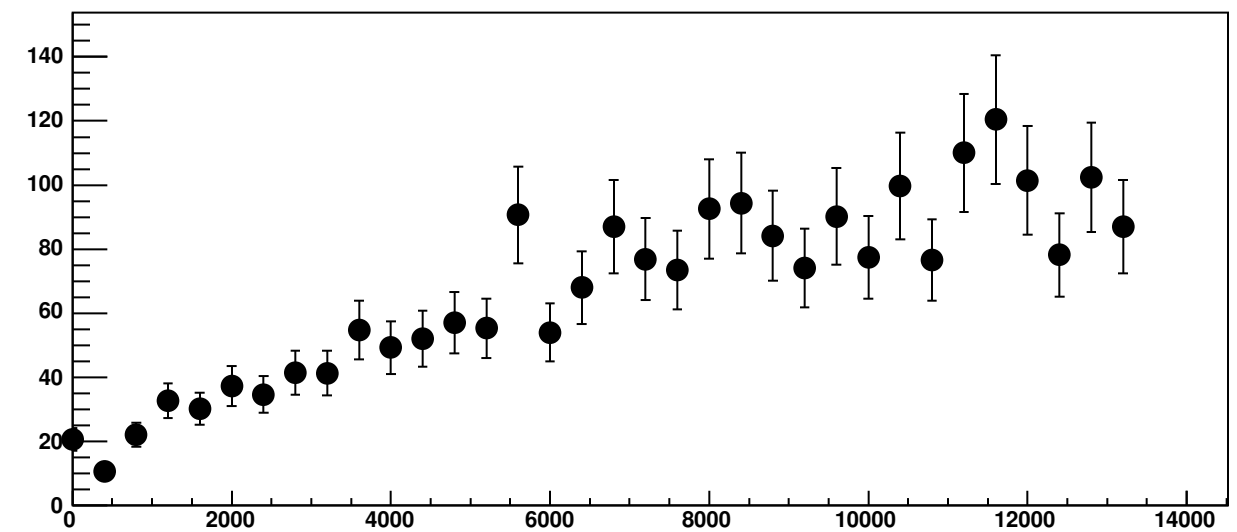

Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

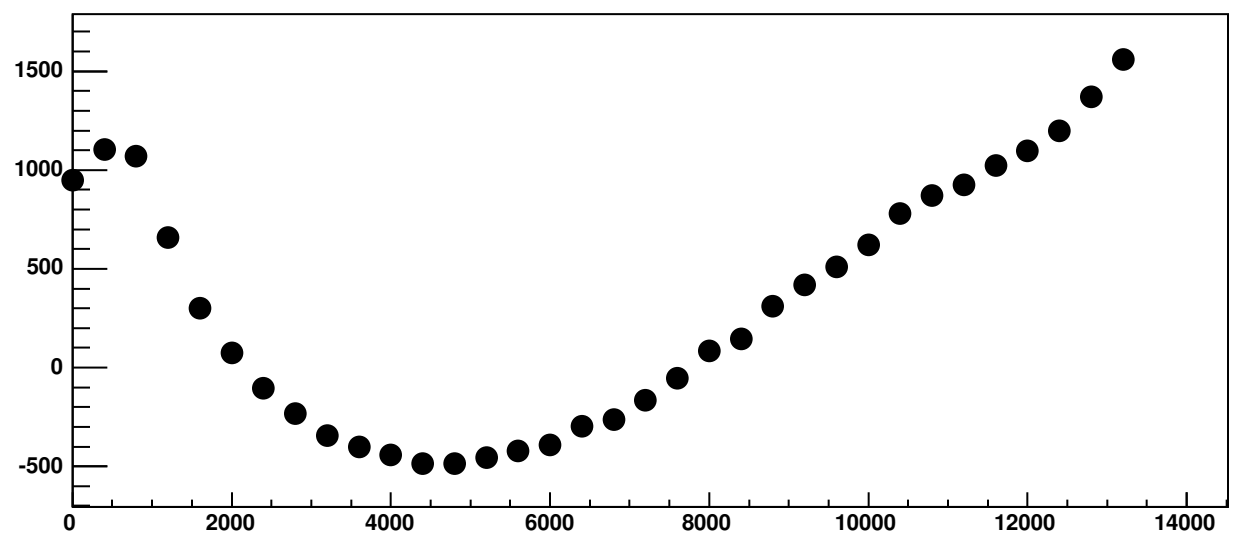
χ^2 / ndf	2.701e+04 / 23
p0	-1865 ± 4.454
p1	-0.4456 ± 0.000916



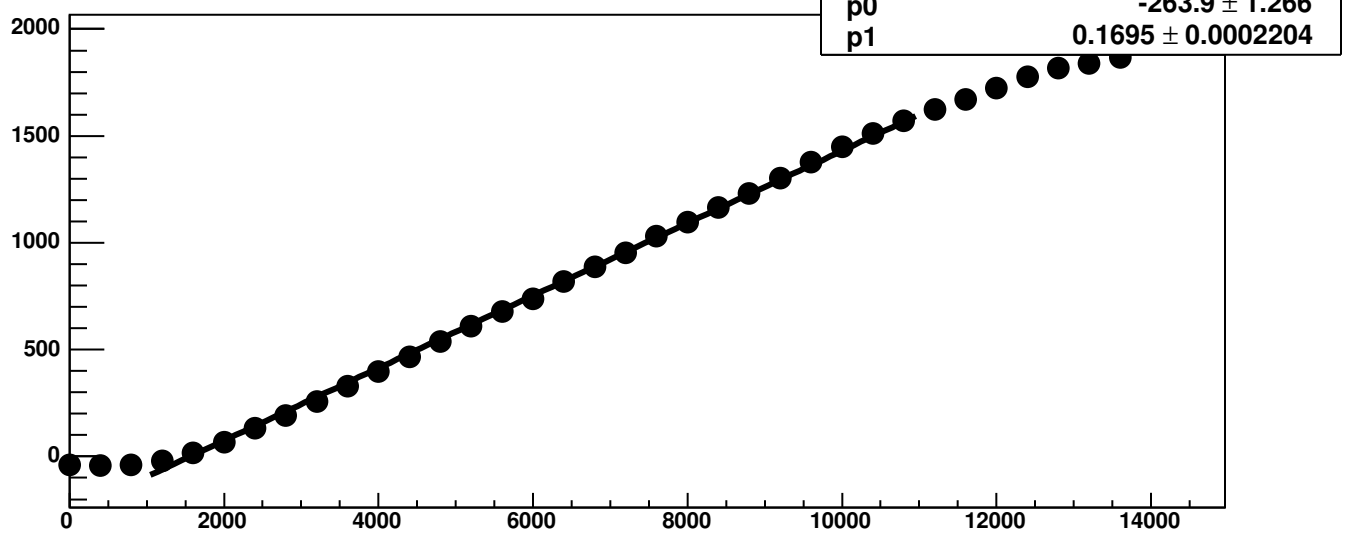
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



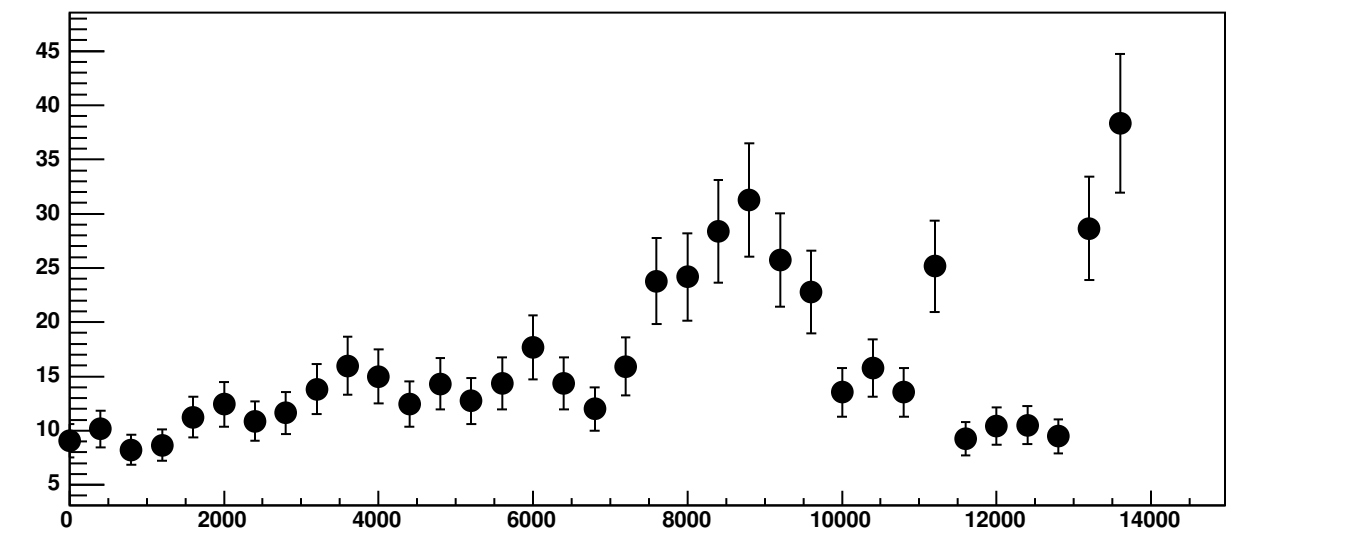
Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



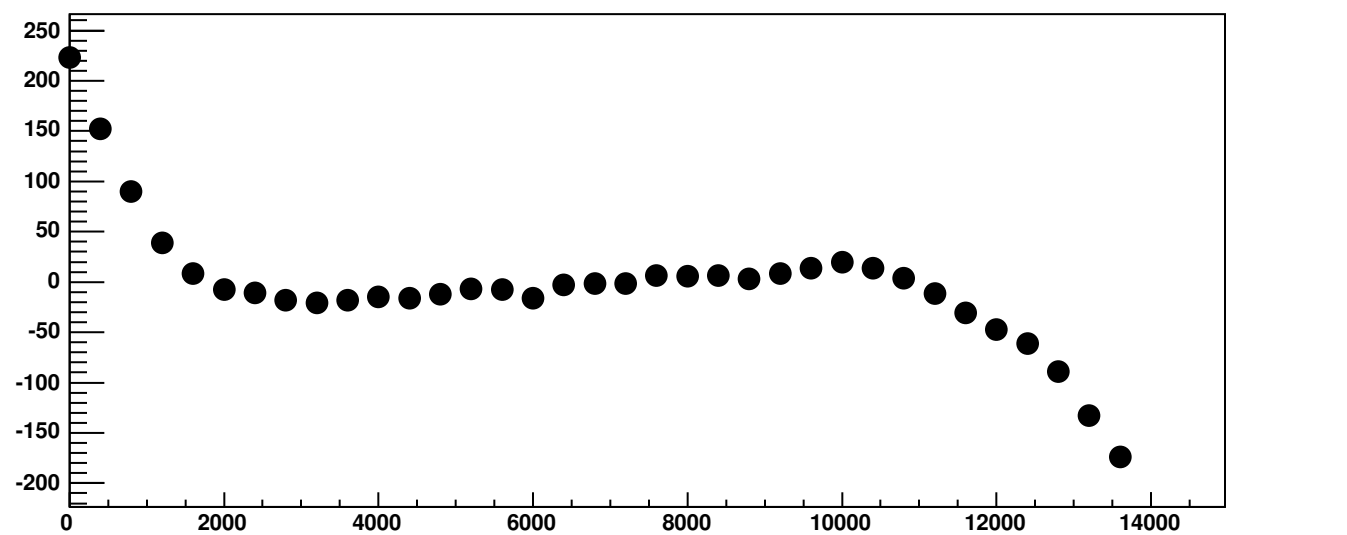
Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



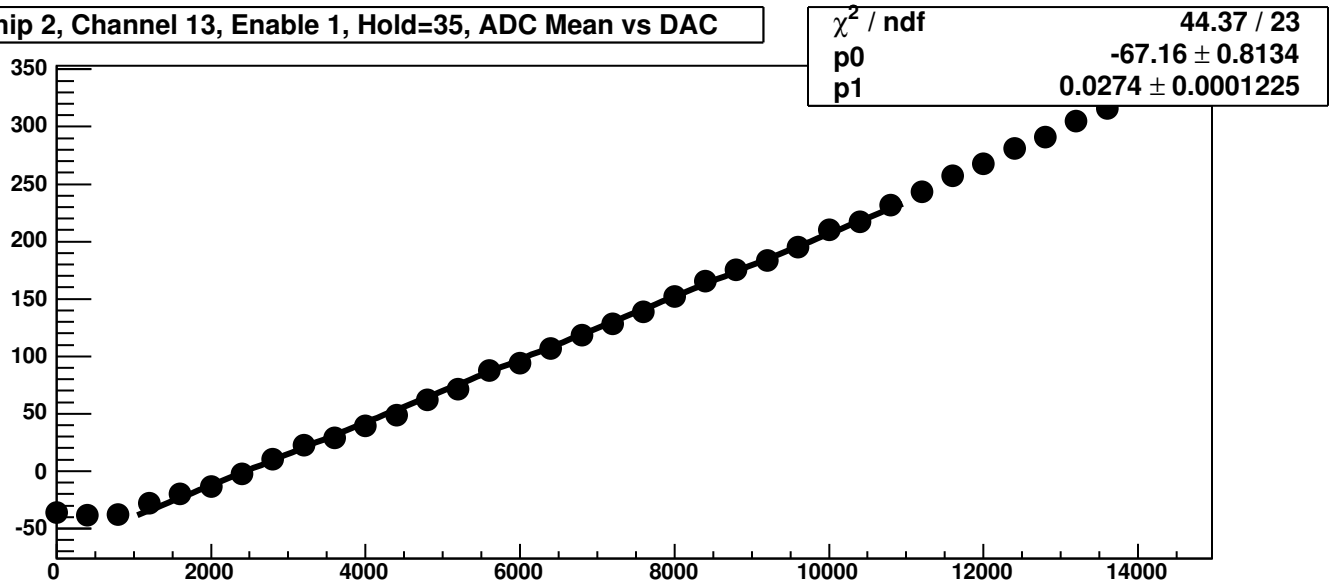
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



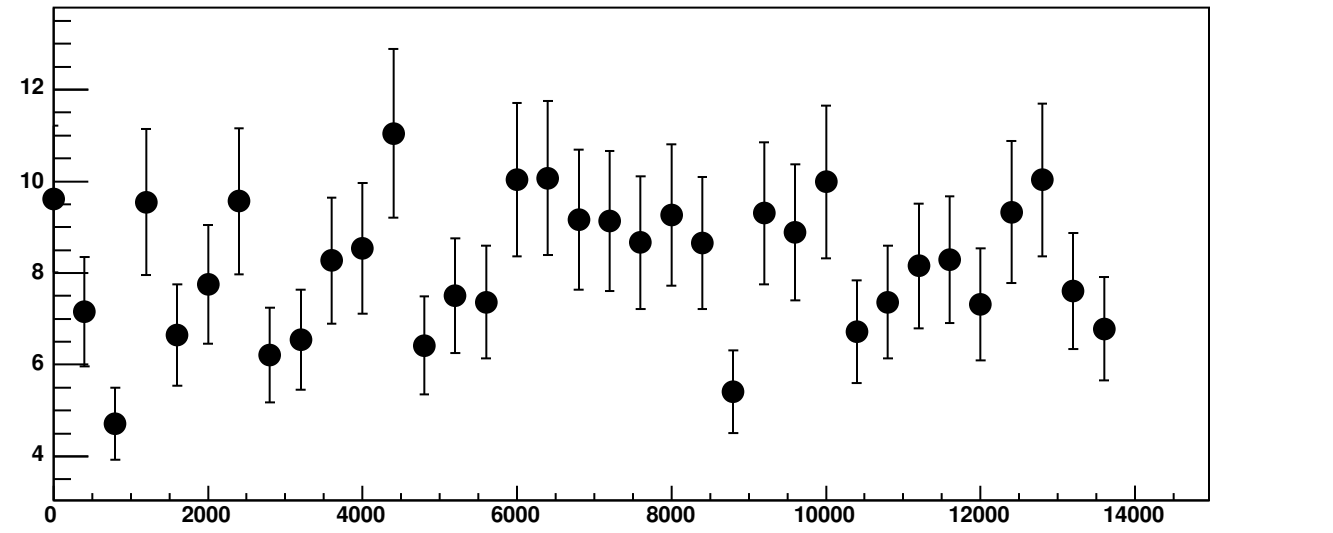
Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



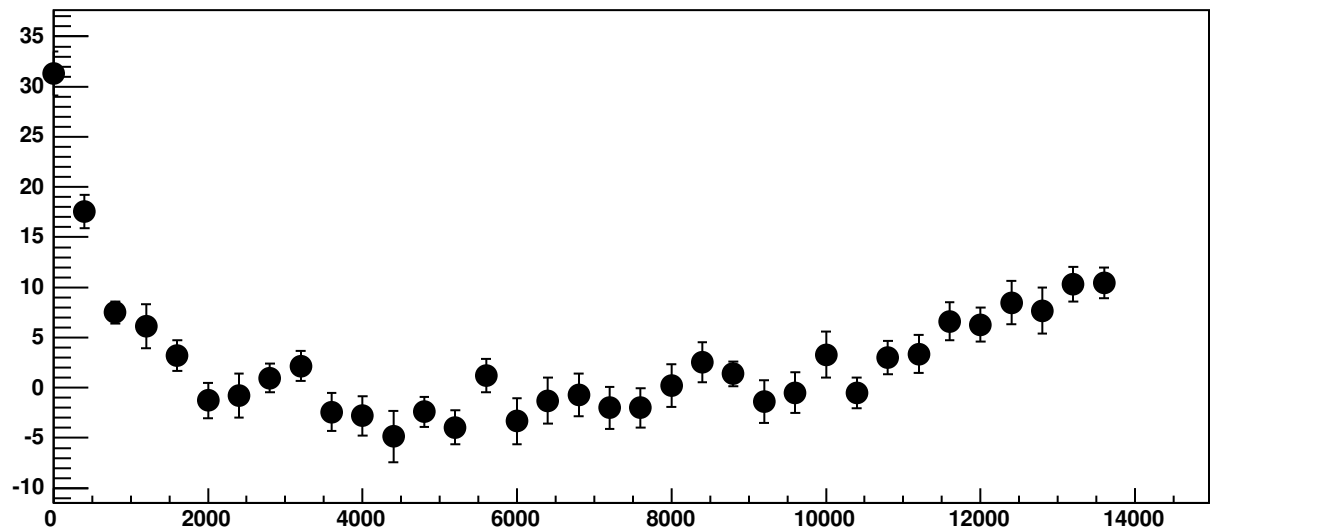
Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

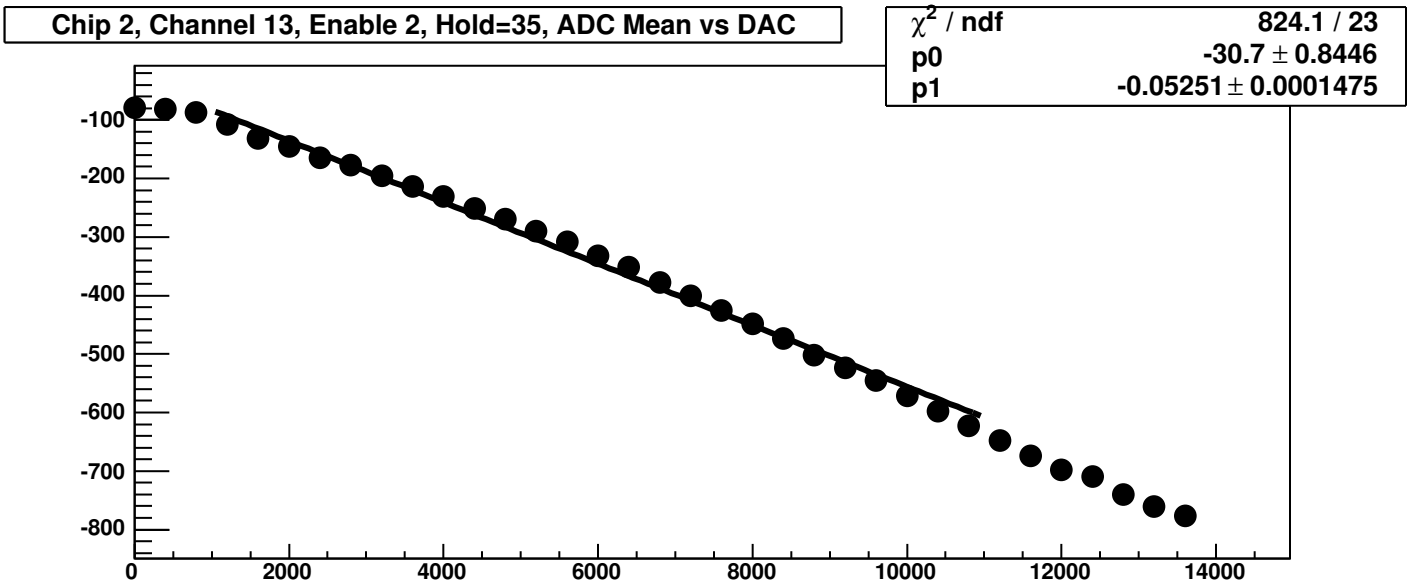


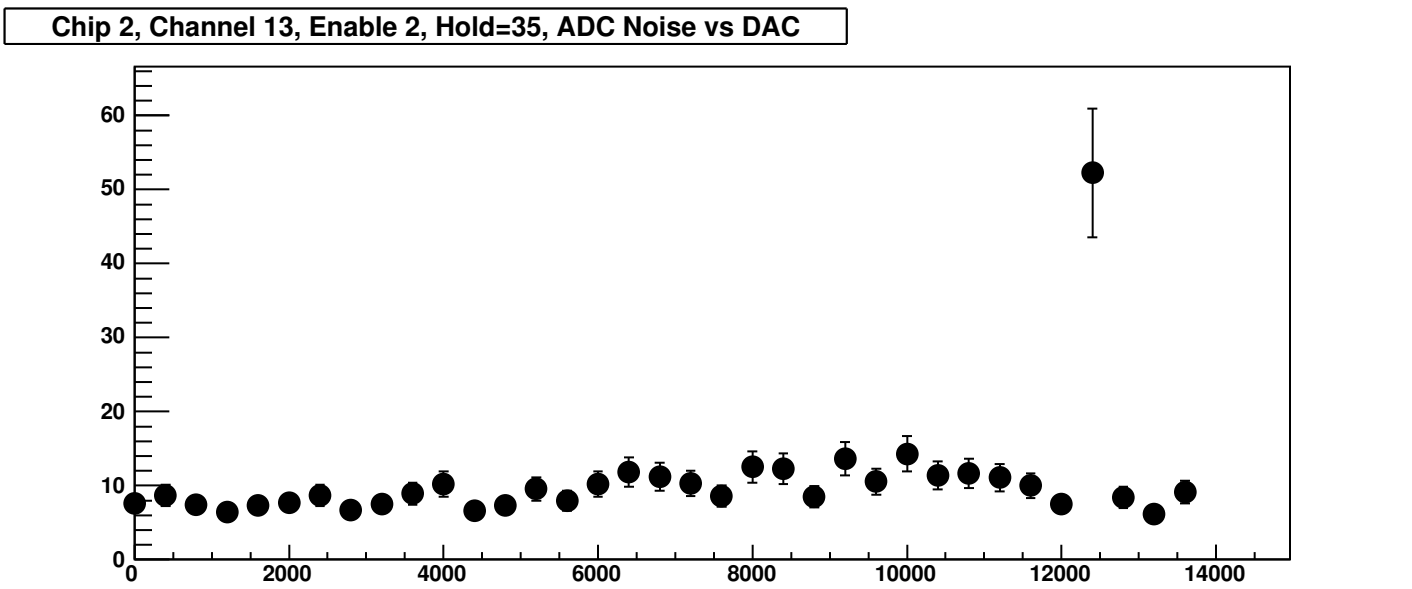
Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

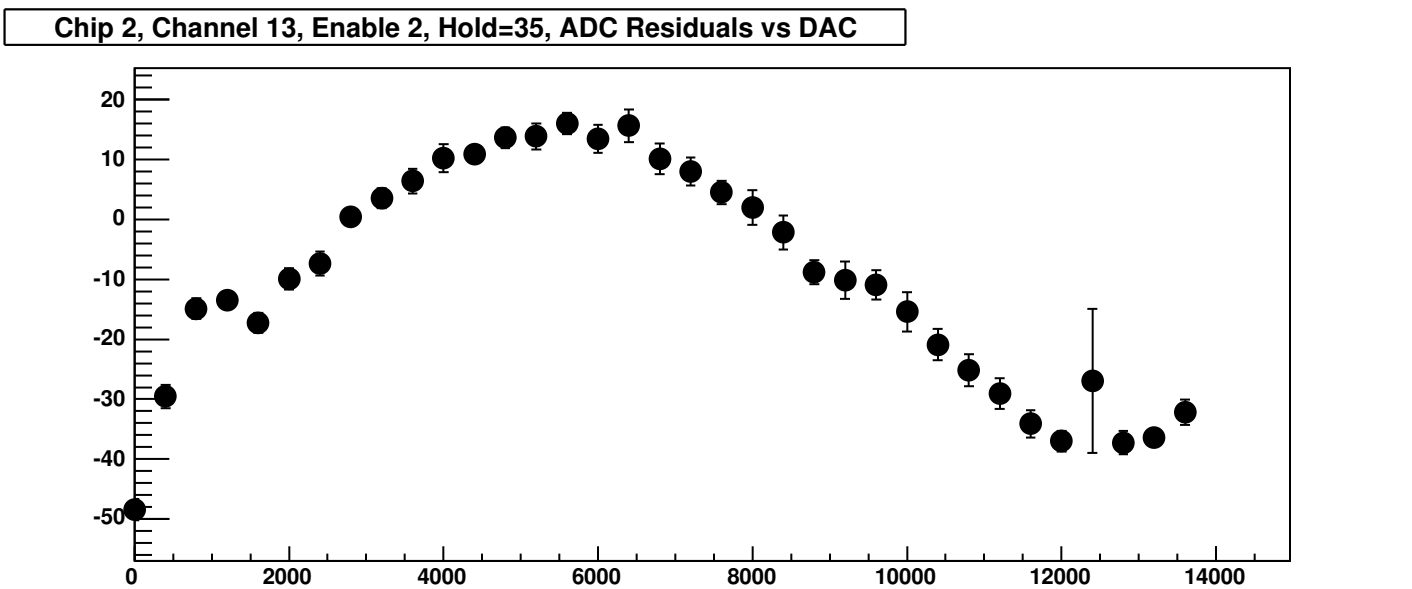


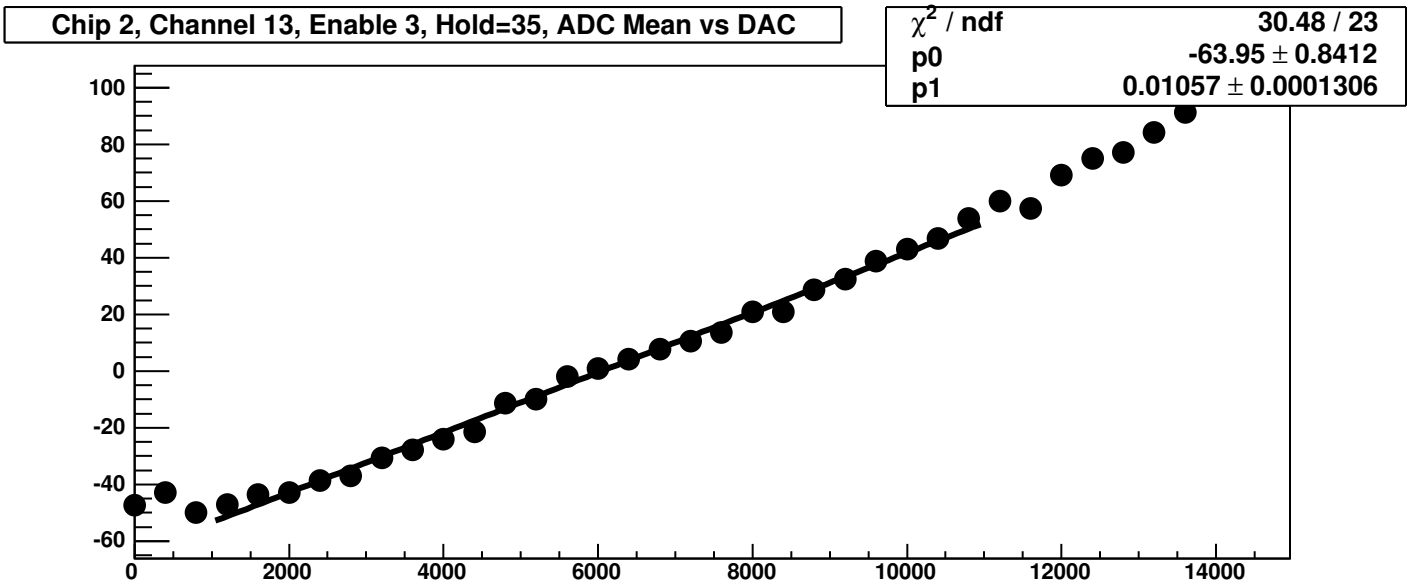
Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

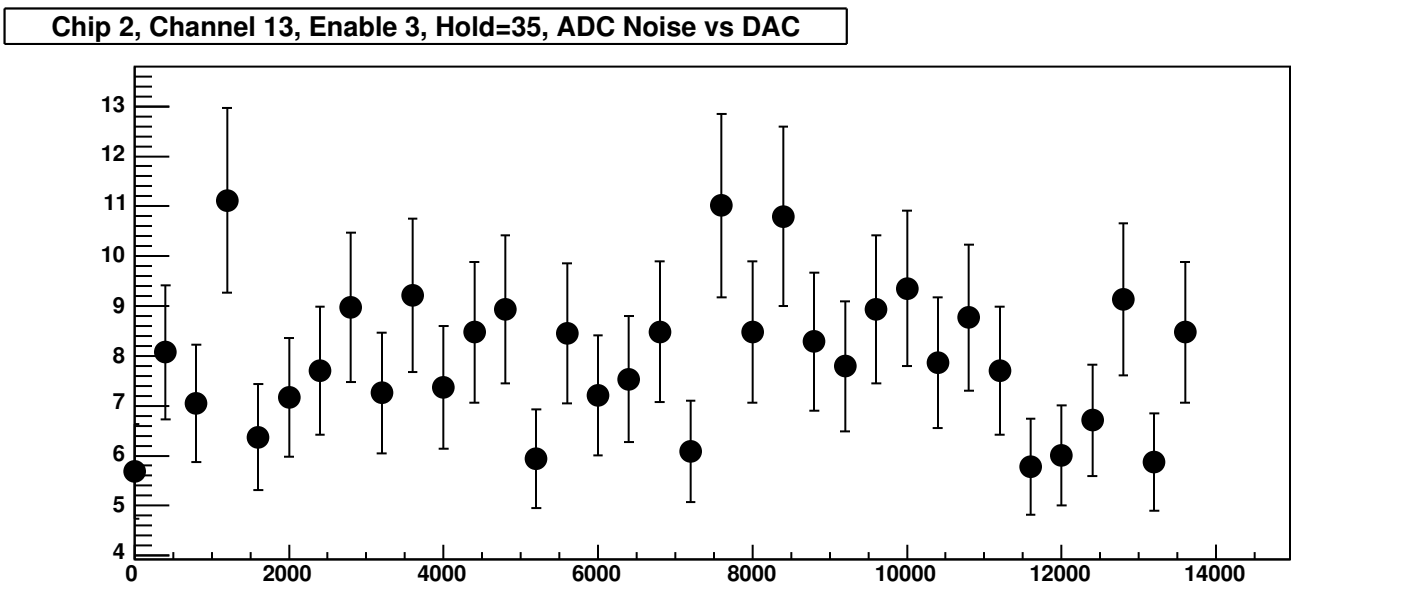


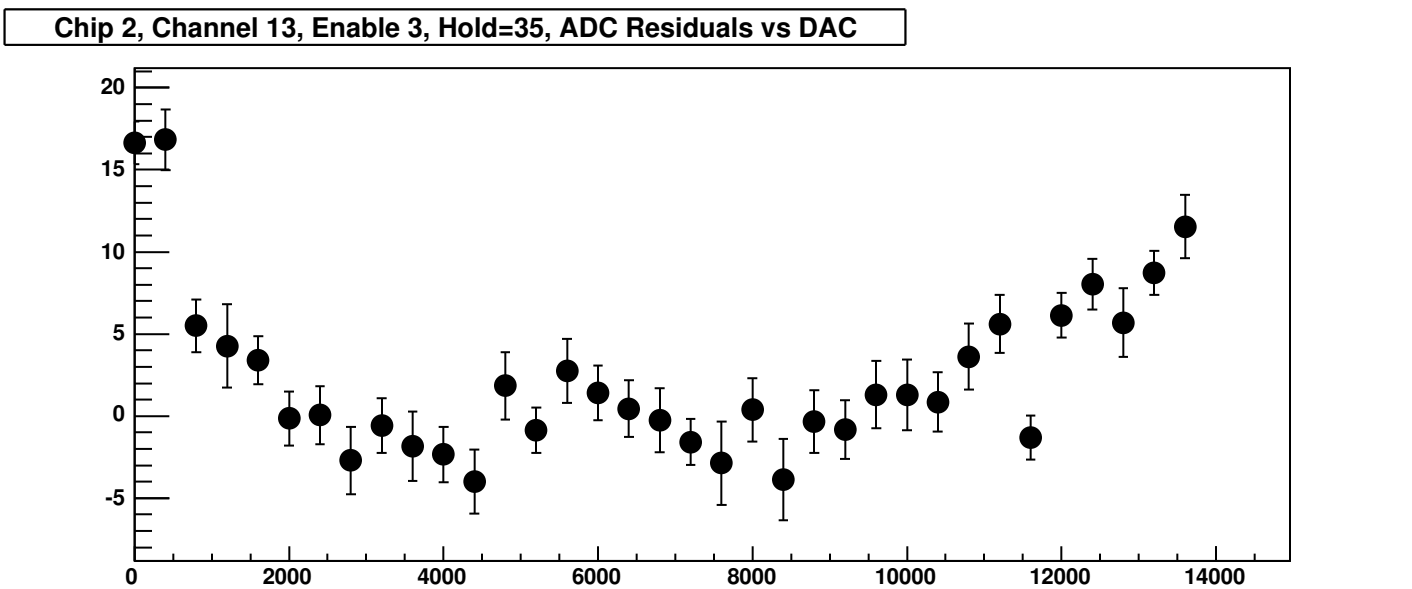

Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 824.1 / 23
p0 -30.7 ± 0.8446
p1 -0.05251 ± 0.0001475
-100
-200
-300
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000

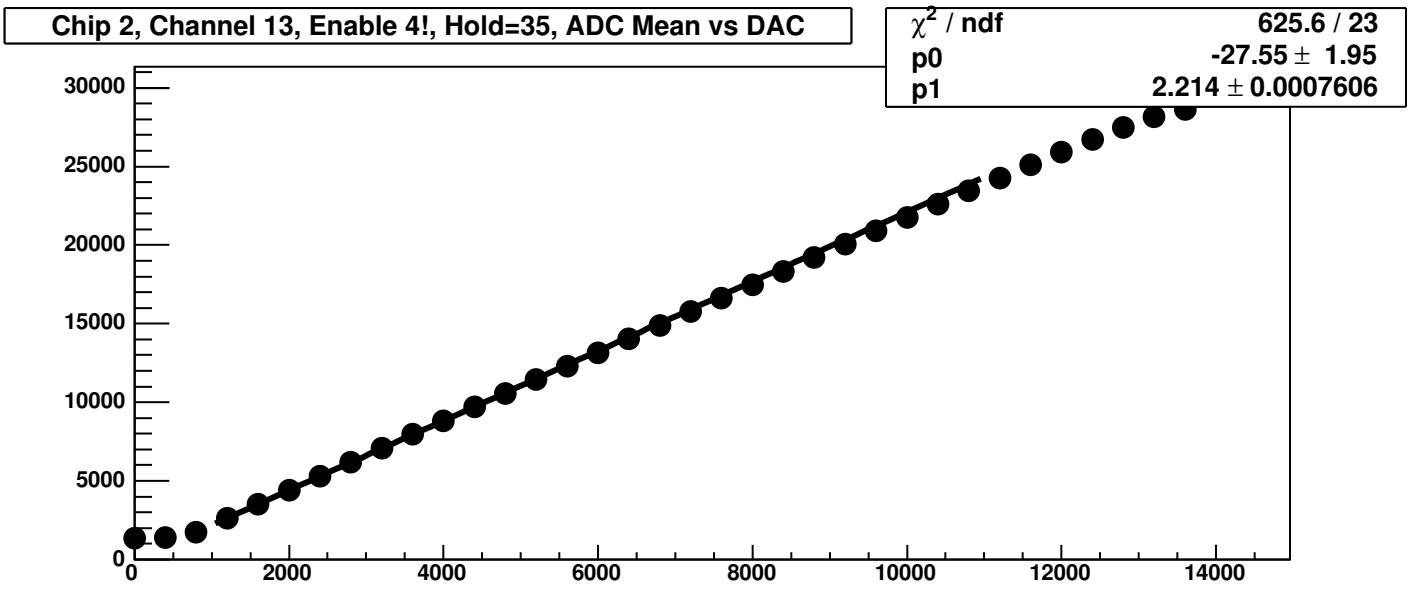

Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

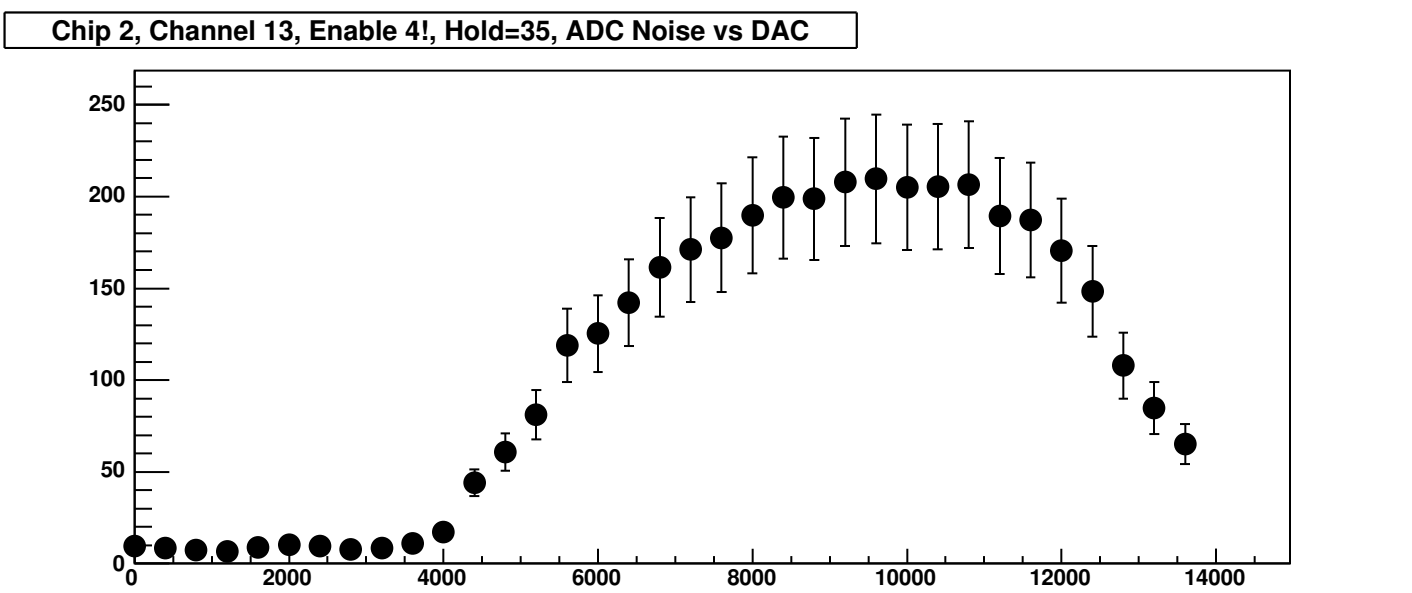

Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

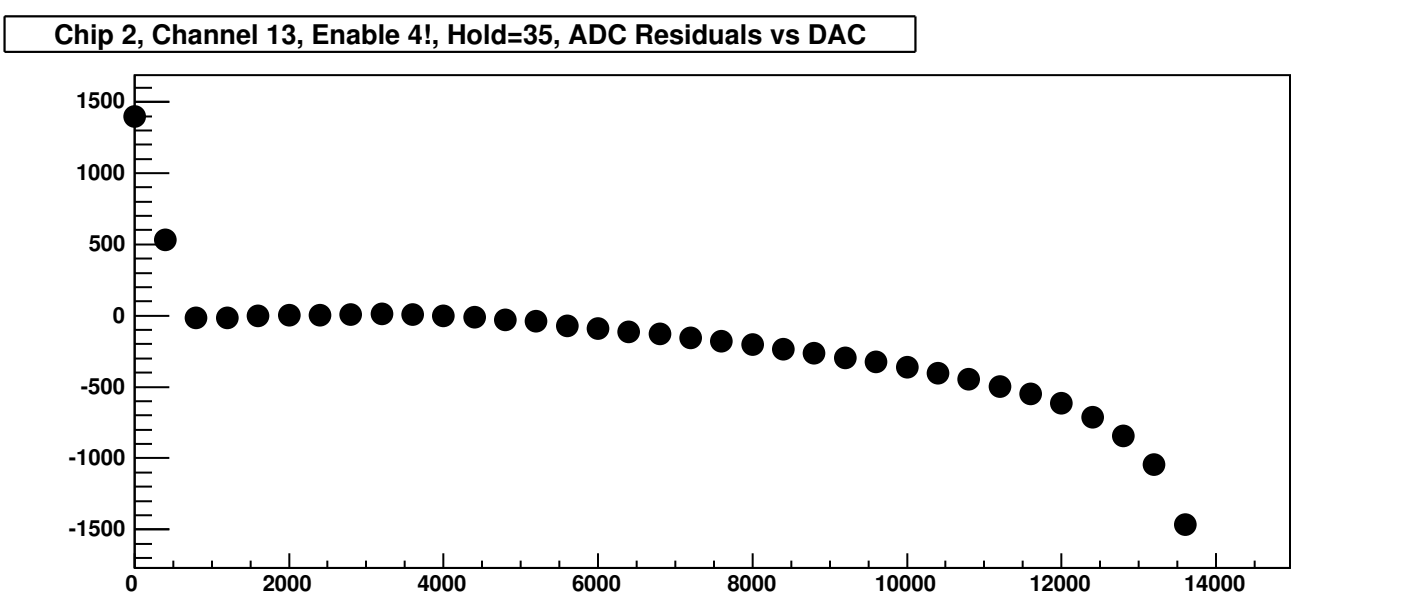

Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.48 / 23
p0 -63.95 ± 0.8412
p1 0.01057 ± 0.0001306
100
80
60
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

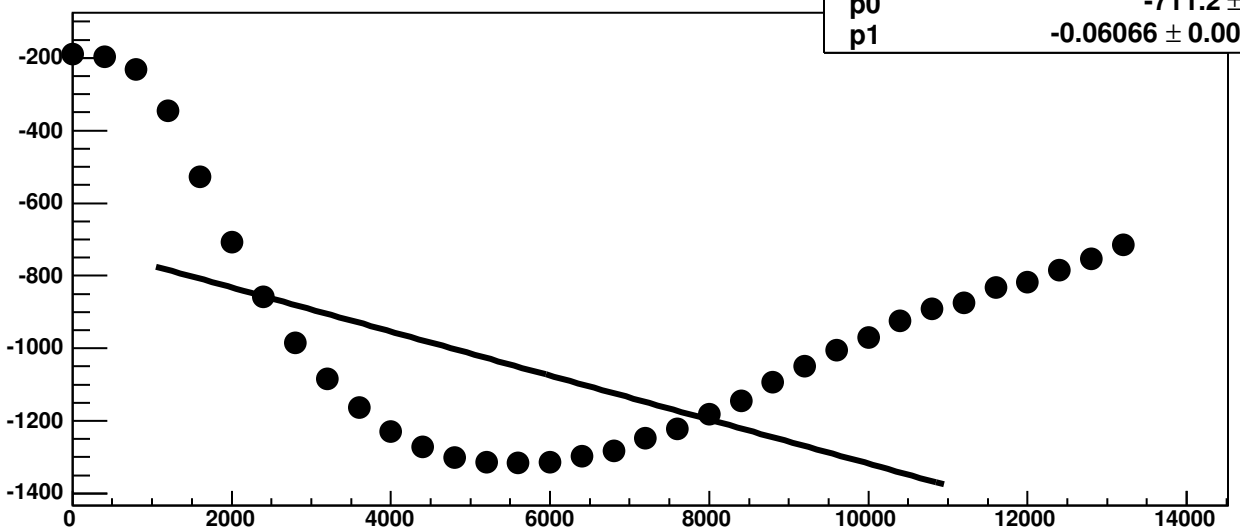

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 625.6 / 23
p0 -27.55 ± 1.95
p1 2.214 ± 0.0007606
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

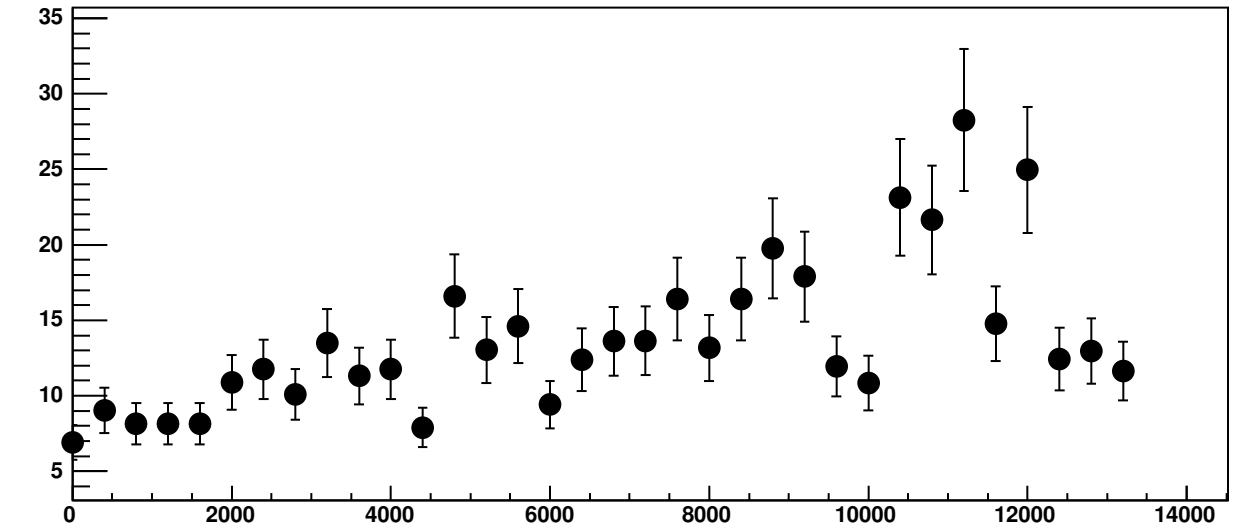

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

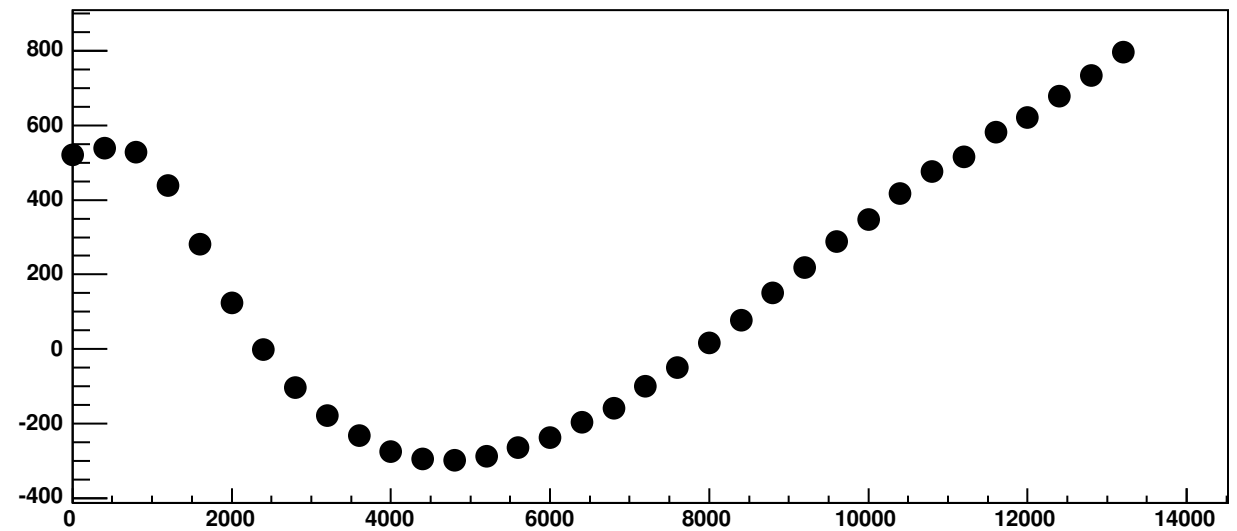
χ^2 / ndf	2.234e+05 / 23
p0	-711.2 ± 1.115
p1	-0.06066 ± 0.0001954



Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

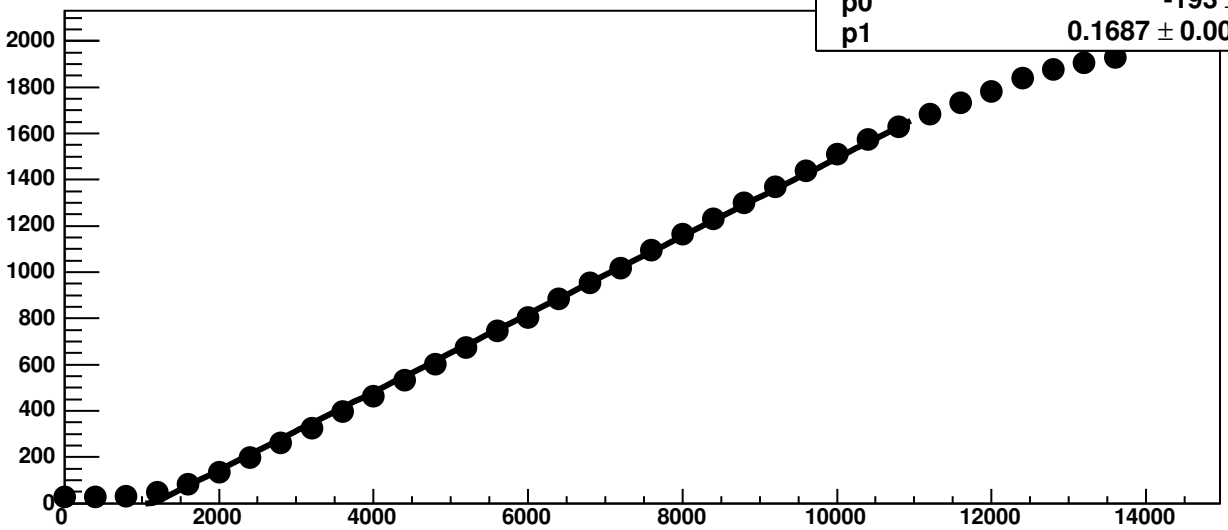


Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

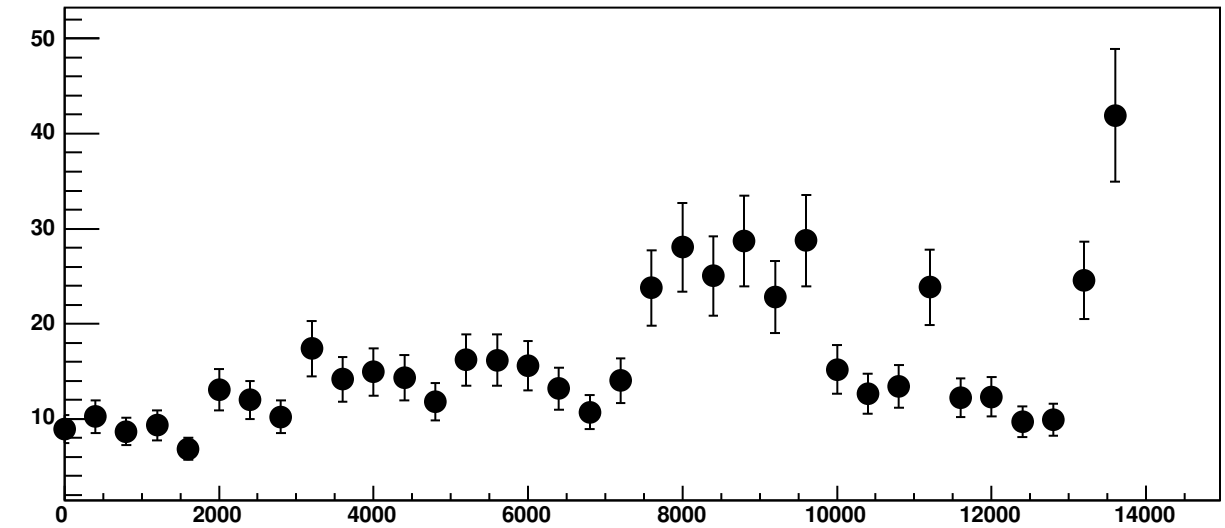


Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

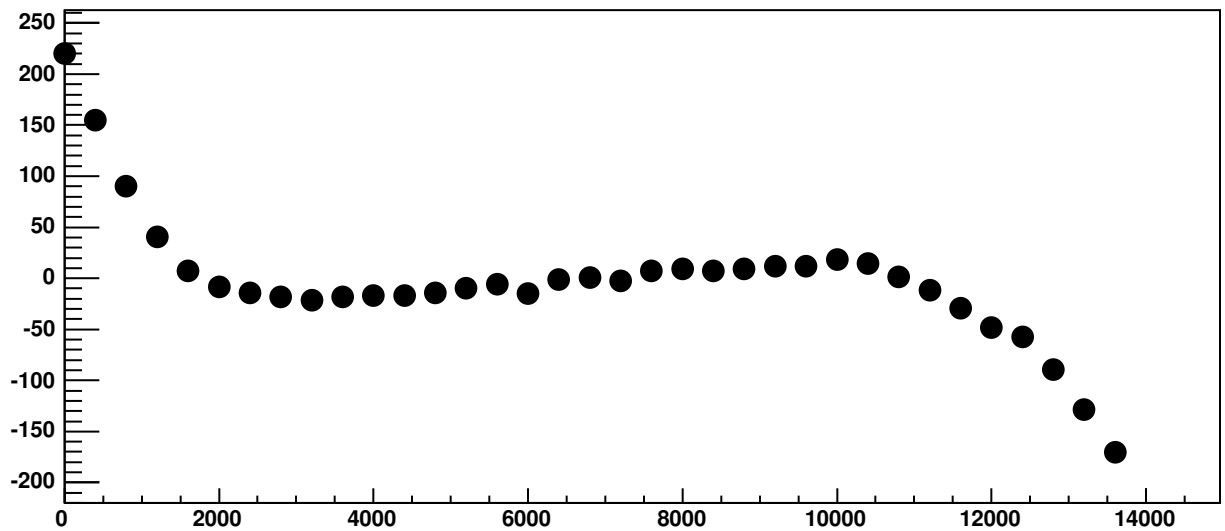
χ^2 / ndf	706.9 / 23
p0	-193 ± 1.16
p1	0.1687 ± 0.0002058

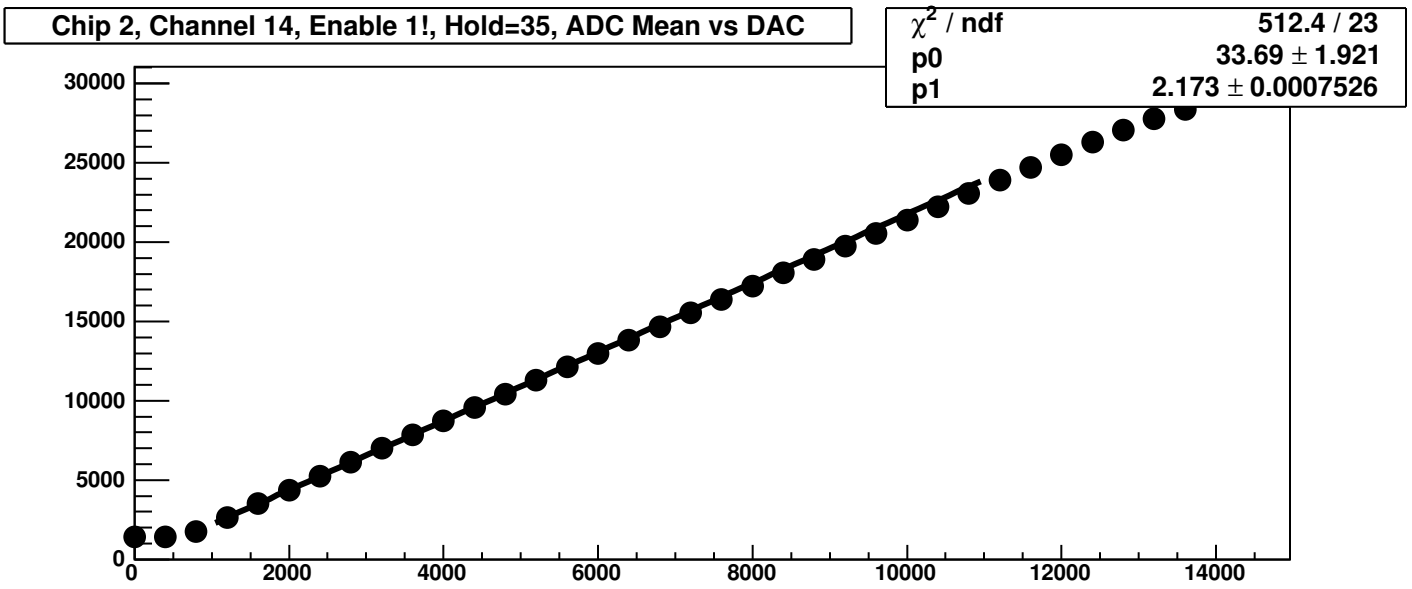


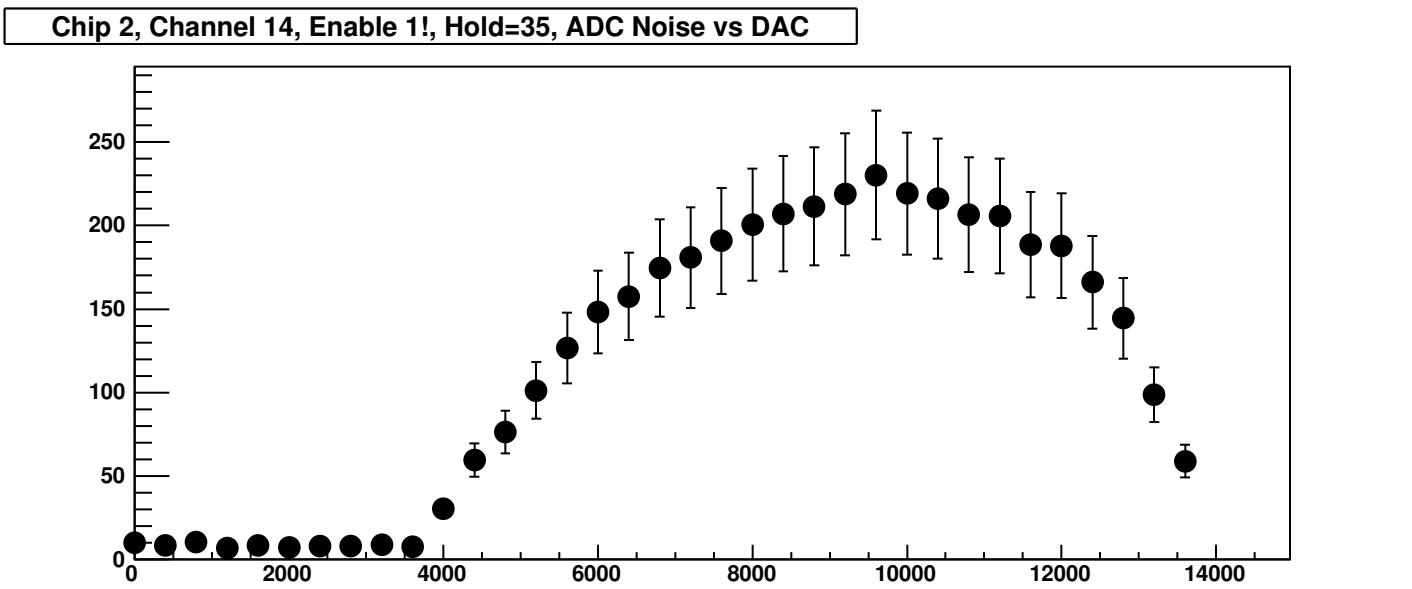
Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

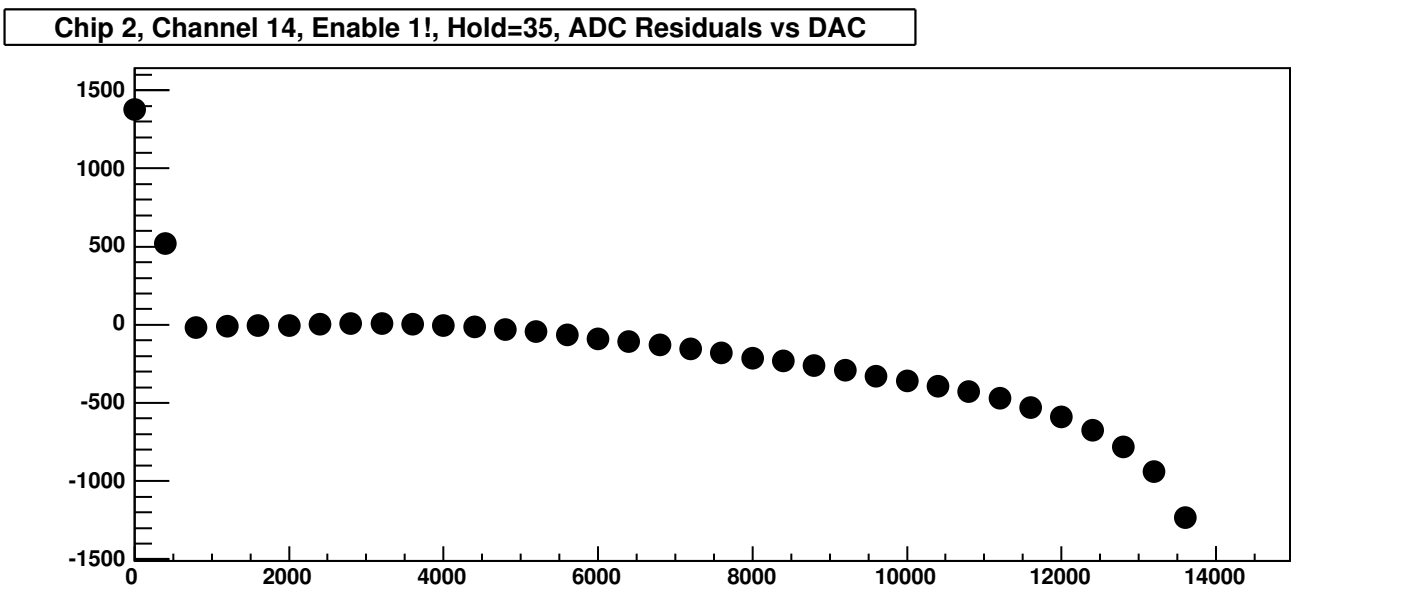


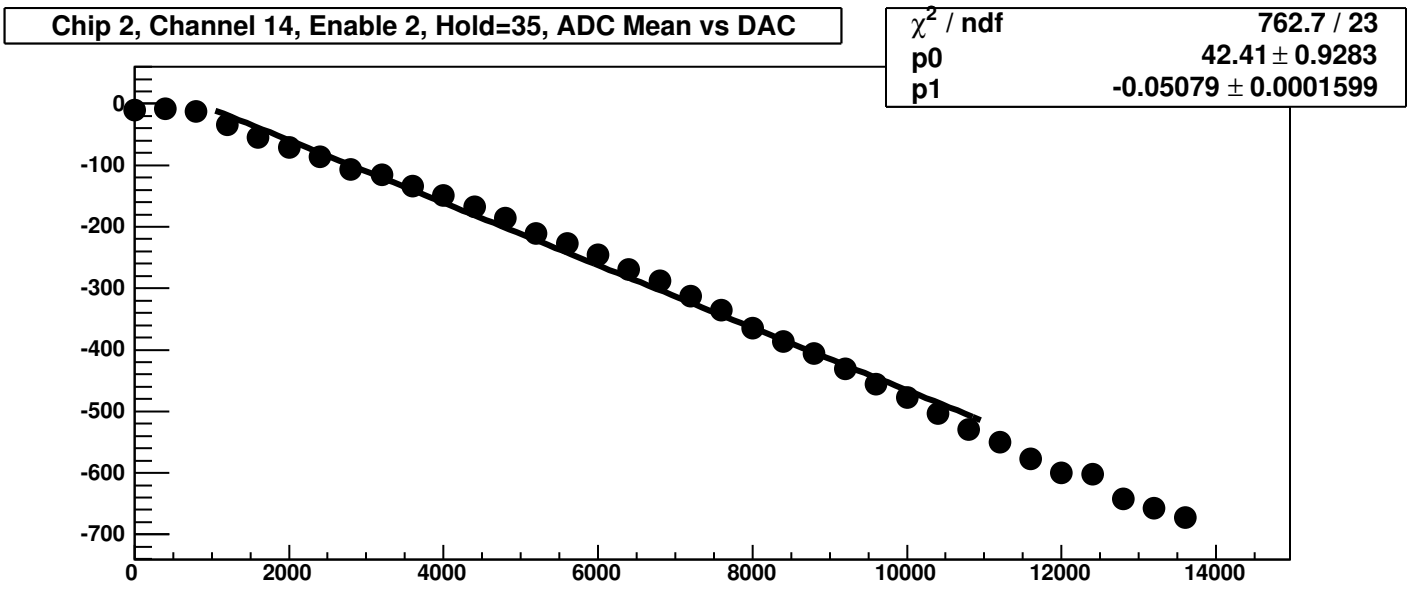
Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

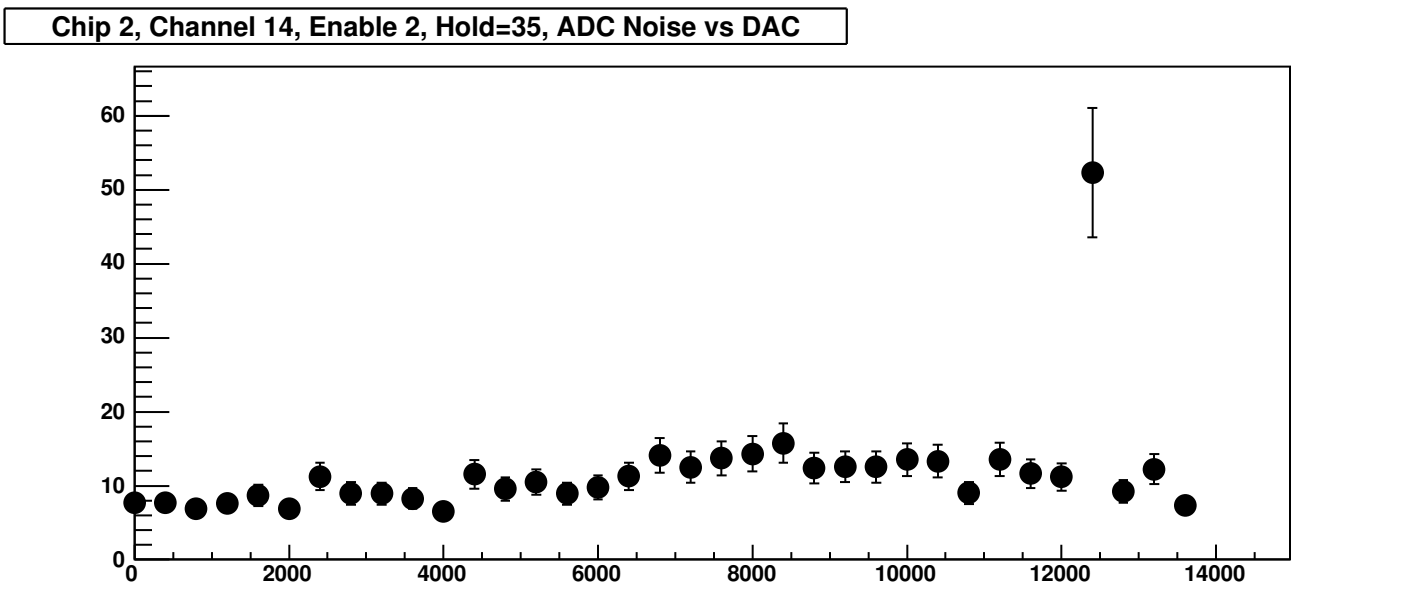


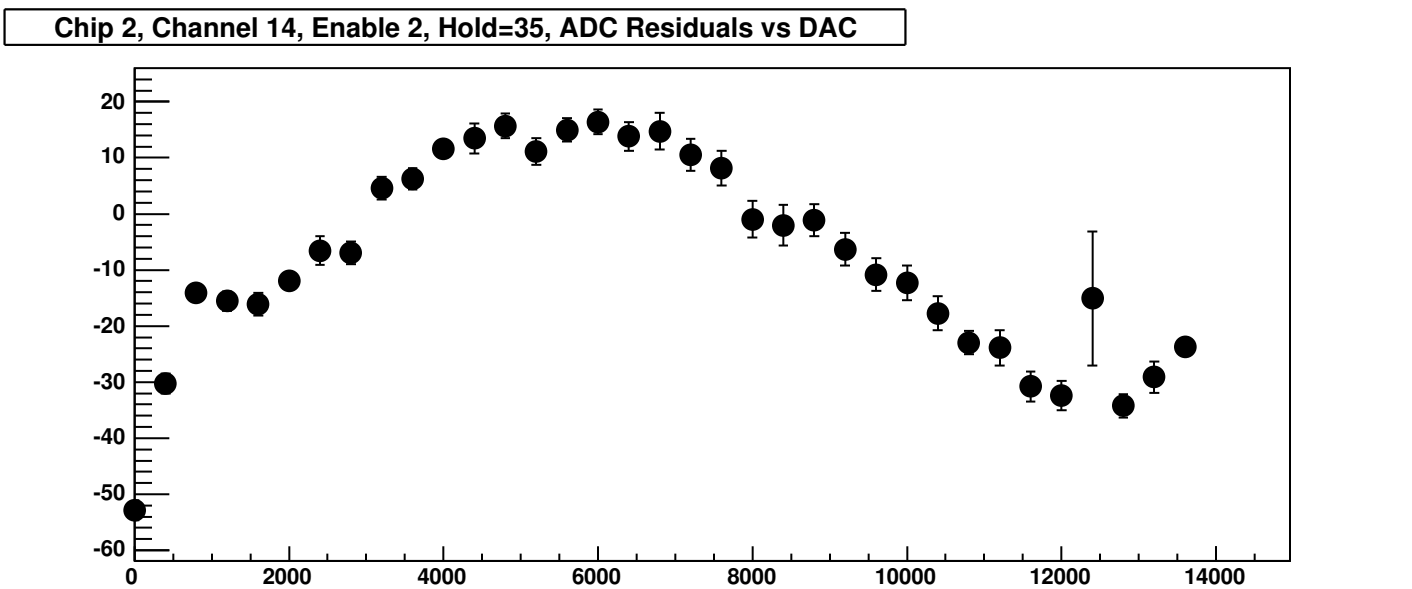

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 512.4 / 23
p0 33.69 ± 1.921
p1 2.173 ± 0.0007526
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

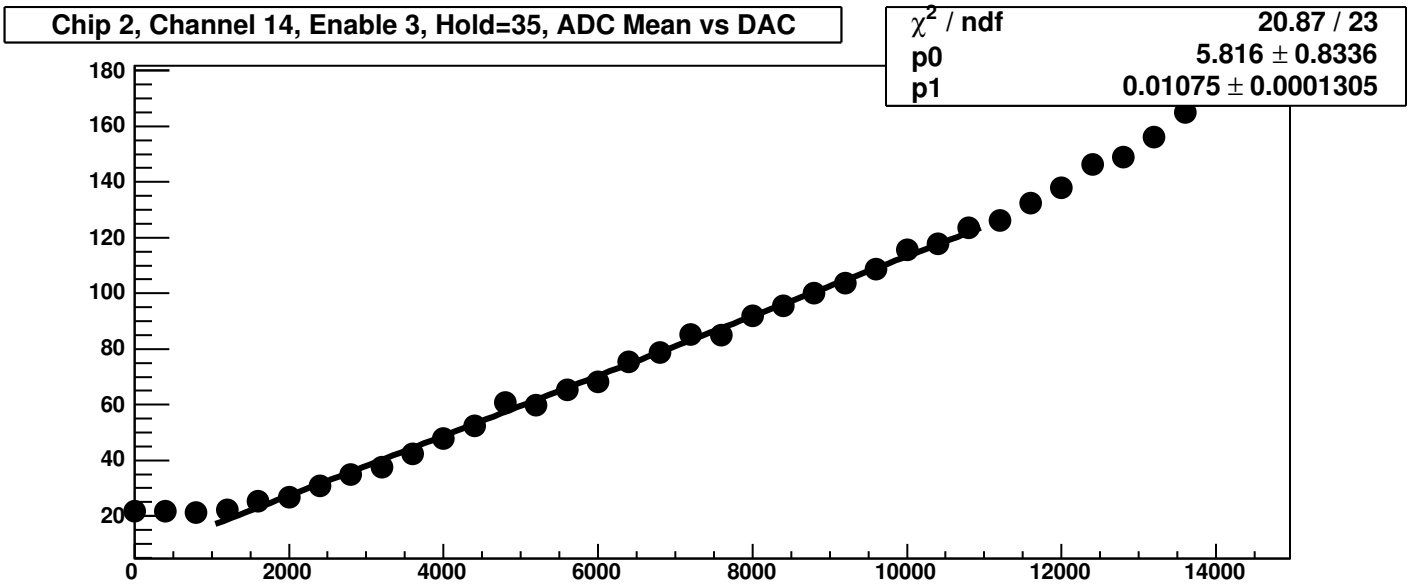

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

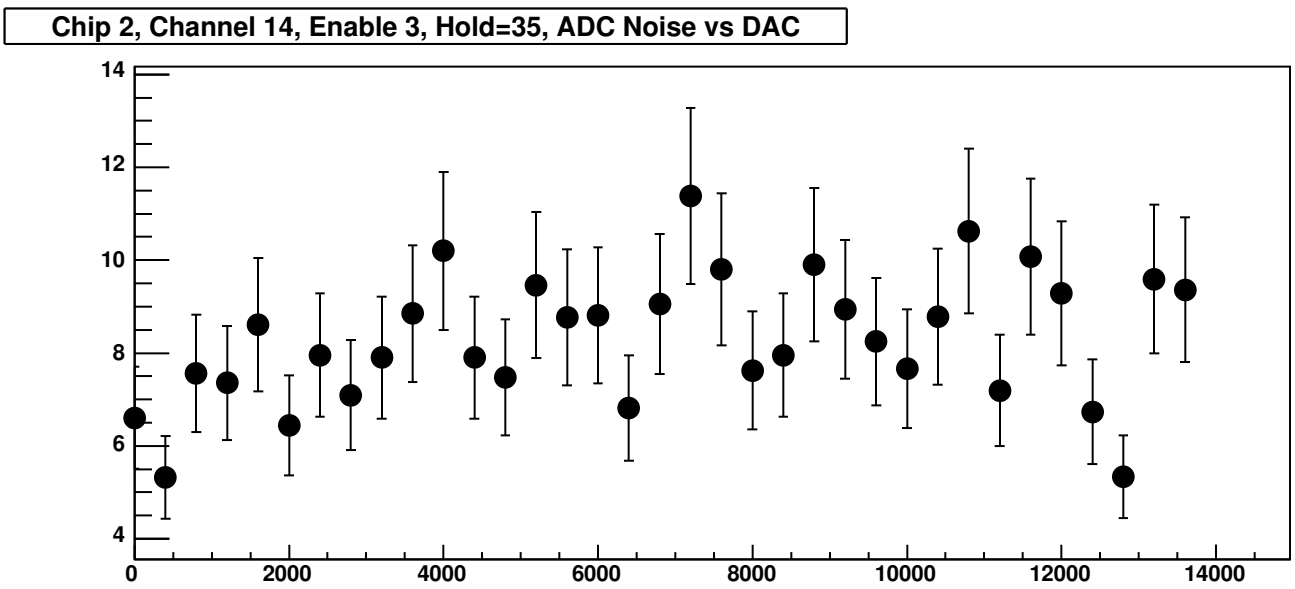

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

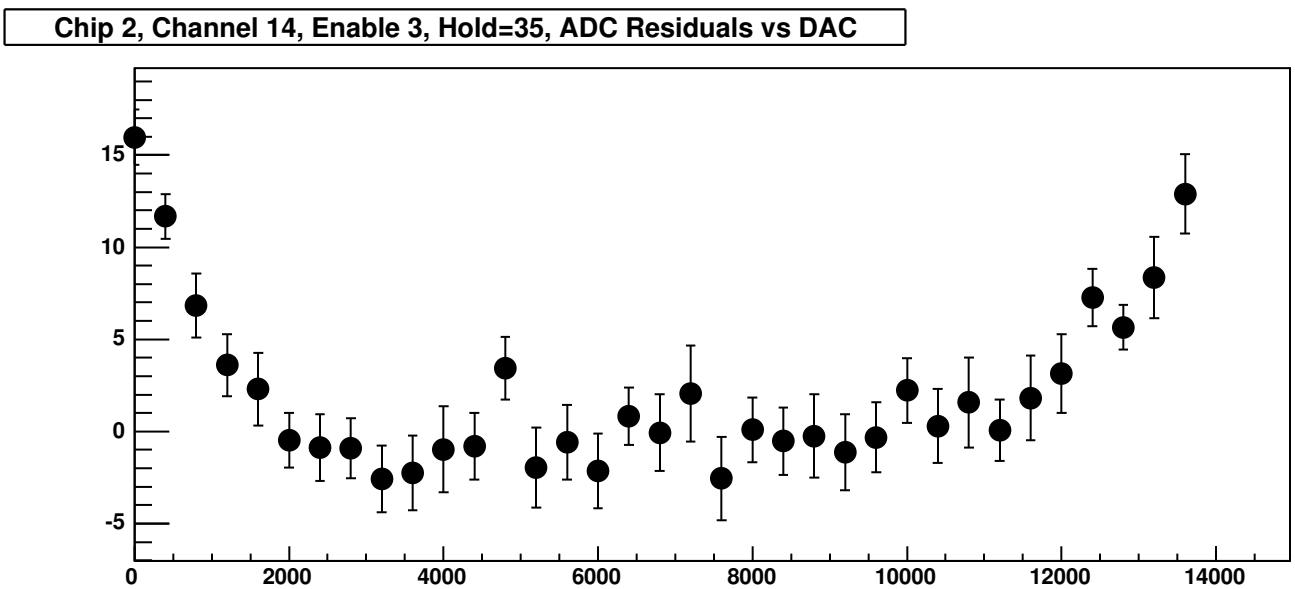

Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 762.7 / 23
p0 42.41 ± 0.9283
p1 -0.05079 ± 0.0001599
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

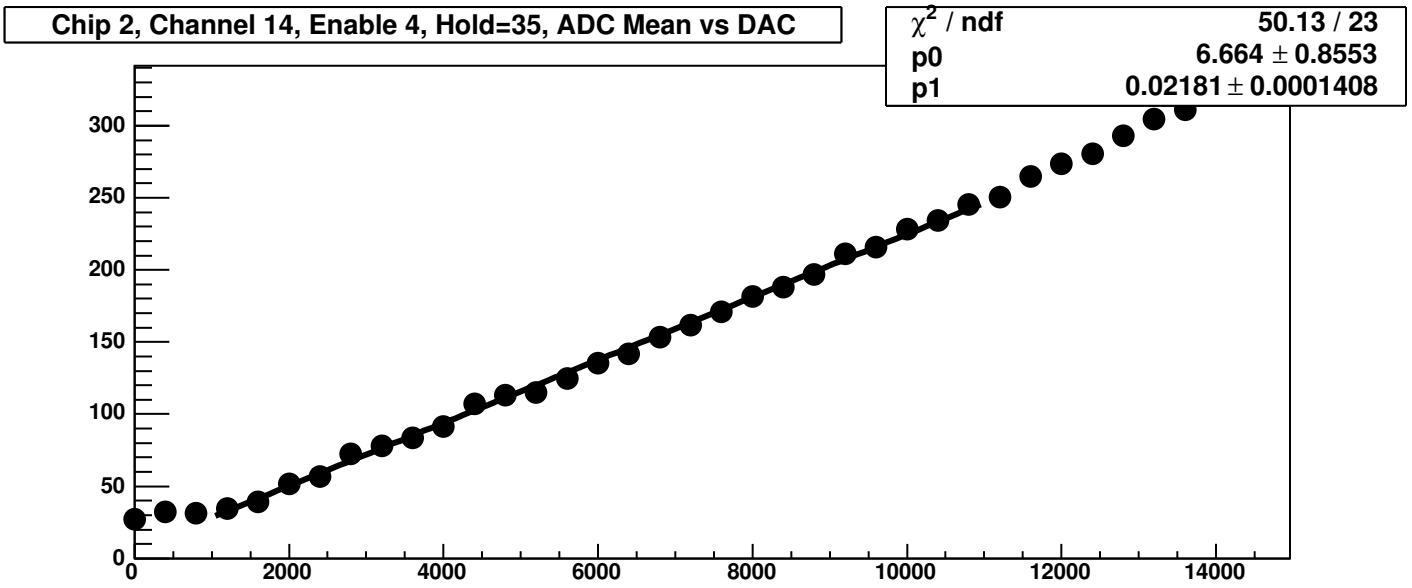

Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

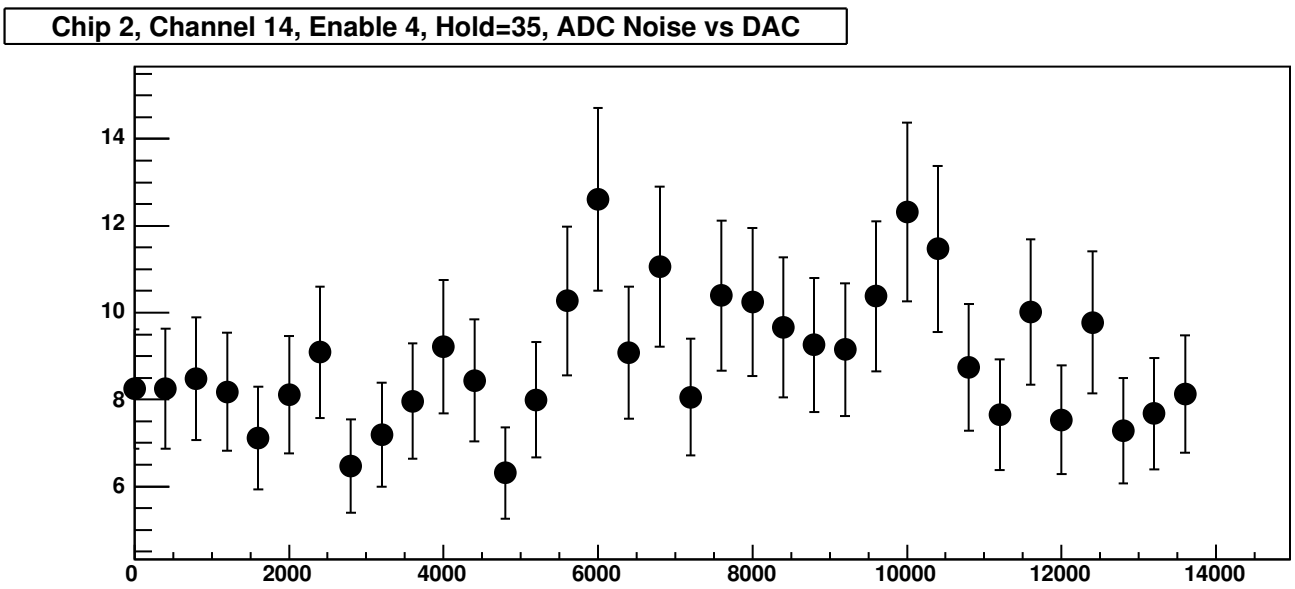

Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000

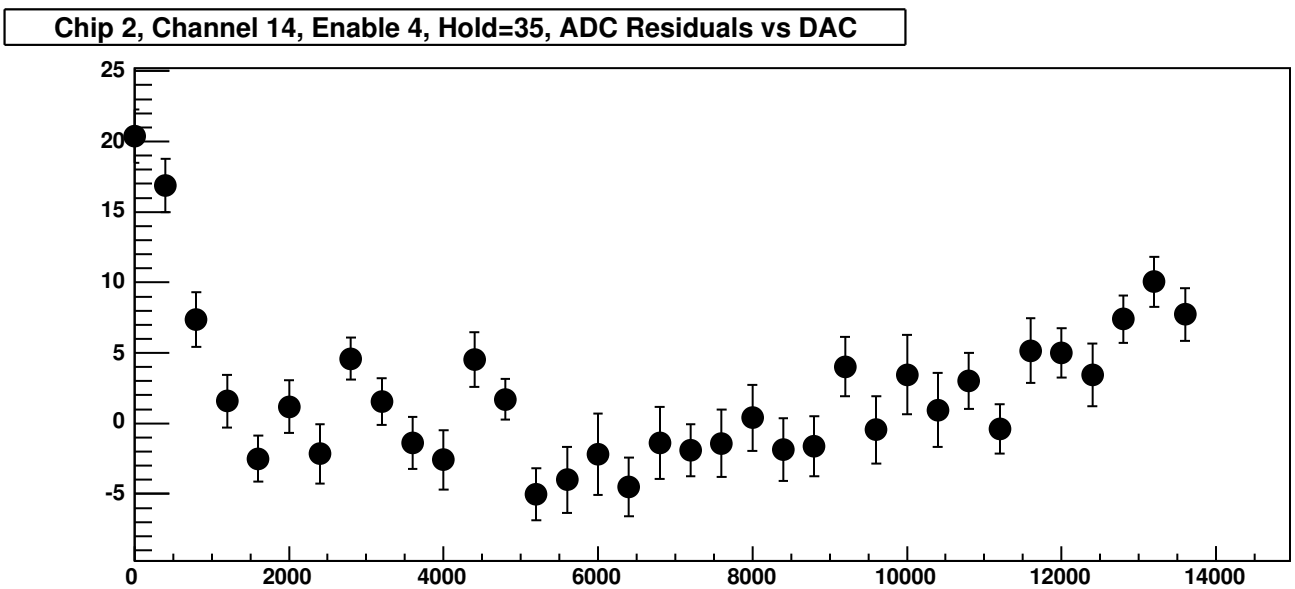

Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.87 / 23
p0 5.816 ± 0.8336
p1 0.01075 ± 0.0001305
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

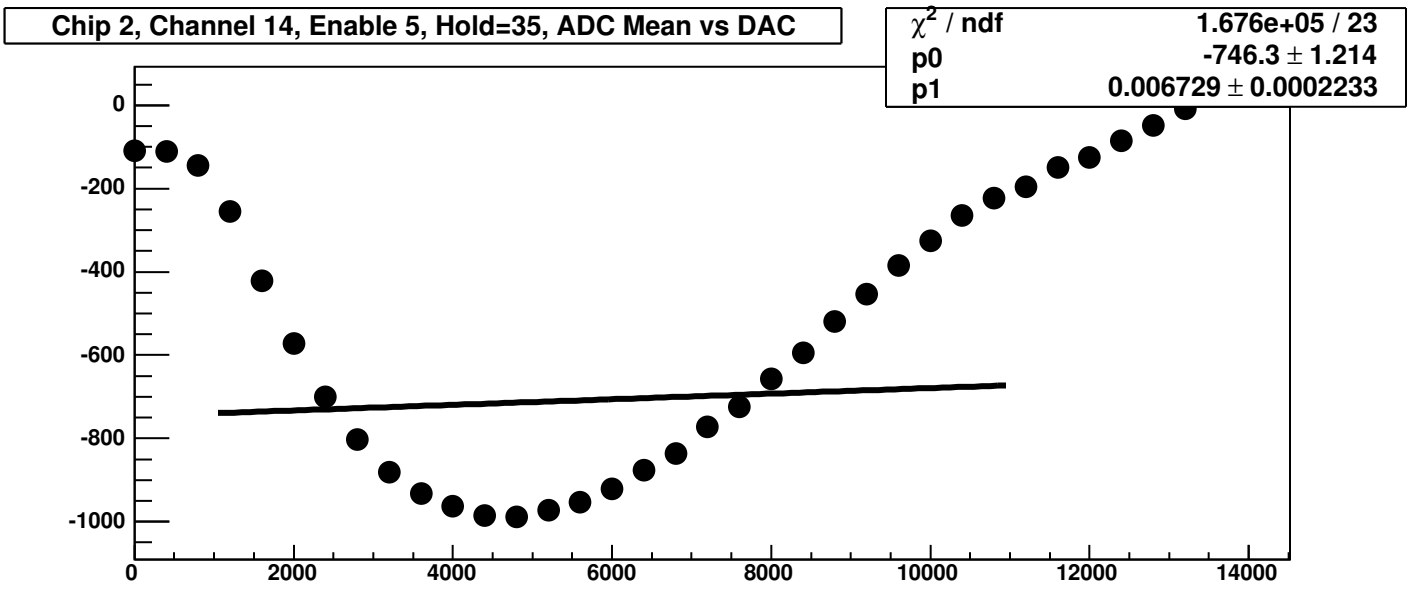

Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

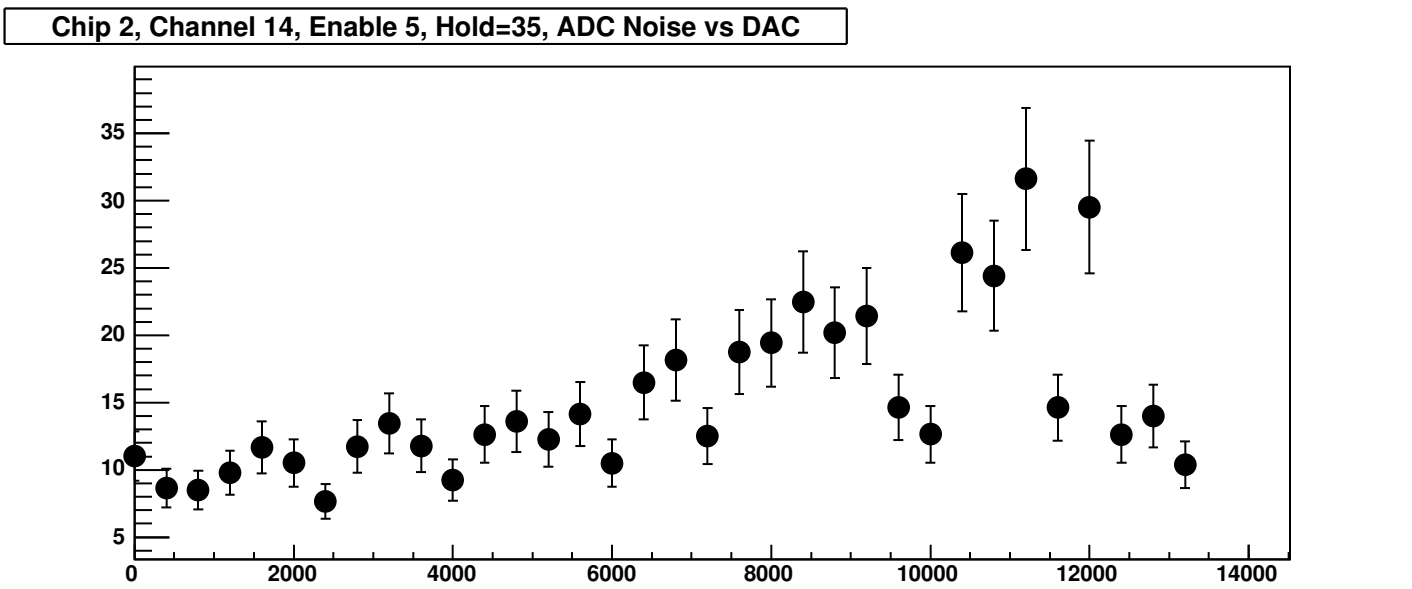

Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

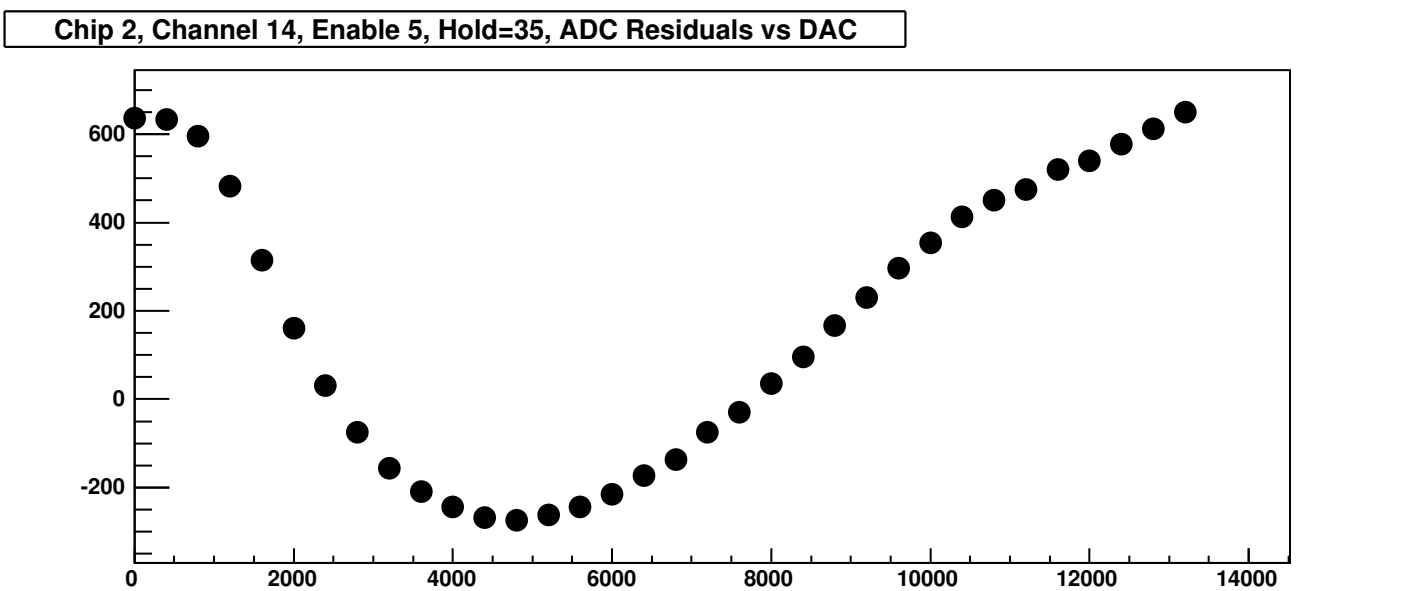

Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.13 / 23
p0 6.664 ± 0.8553
p1 0.02181 ± 0.0001408
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

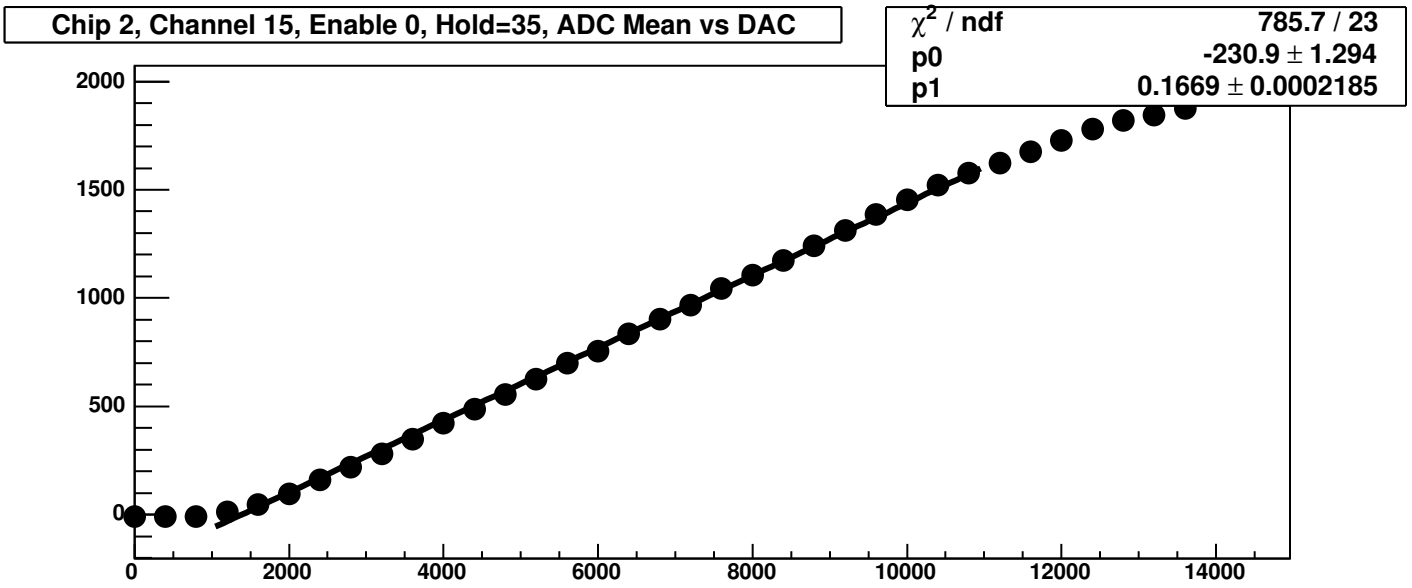

Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

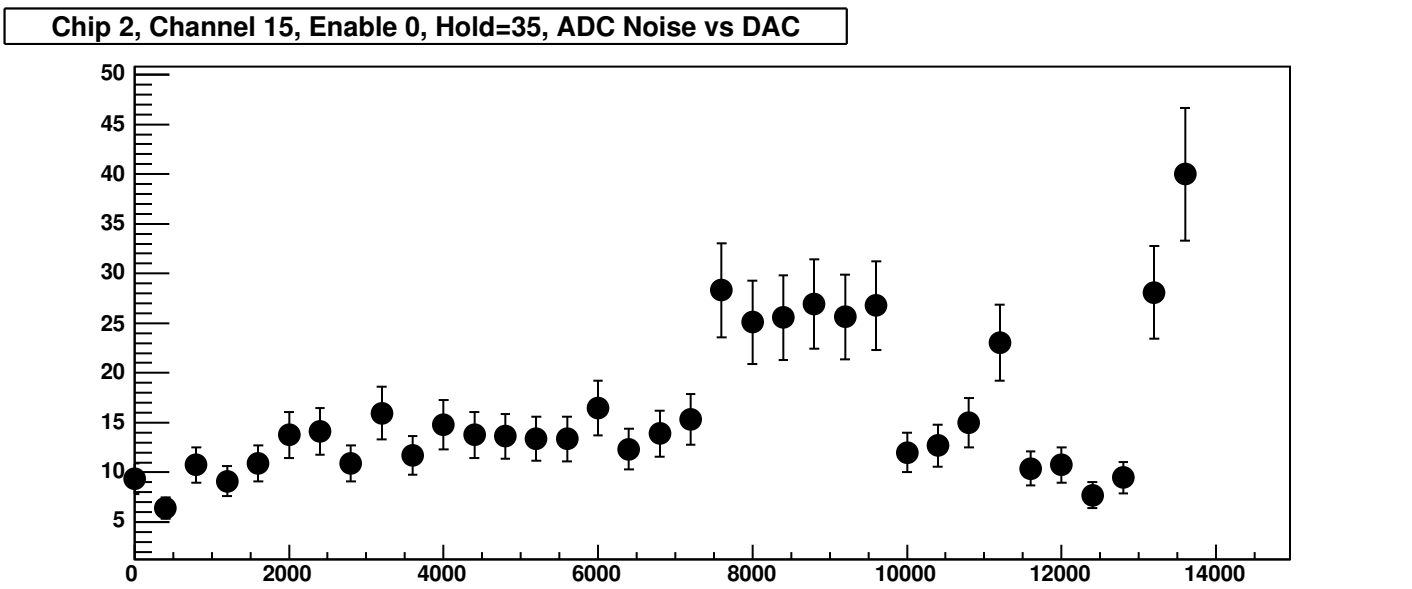

Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

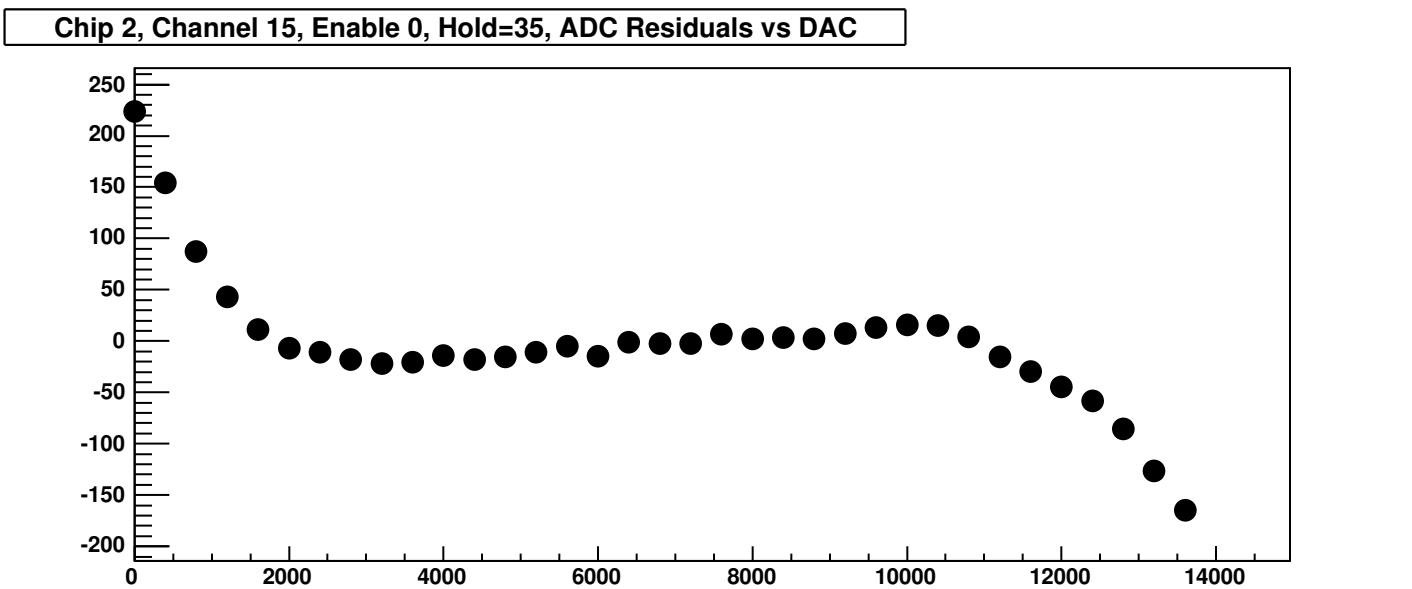

Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.676e+05 / 23
p0 -746.3 ± 1.214
p1 0.006729 ± 0.0002233
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

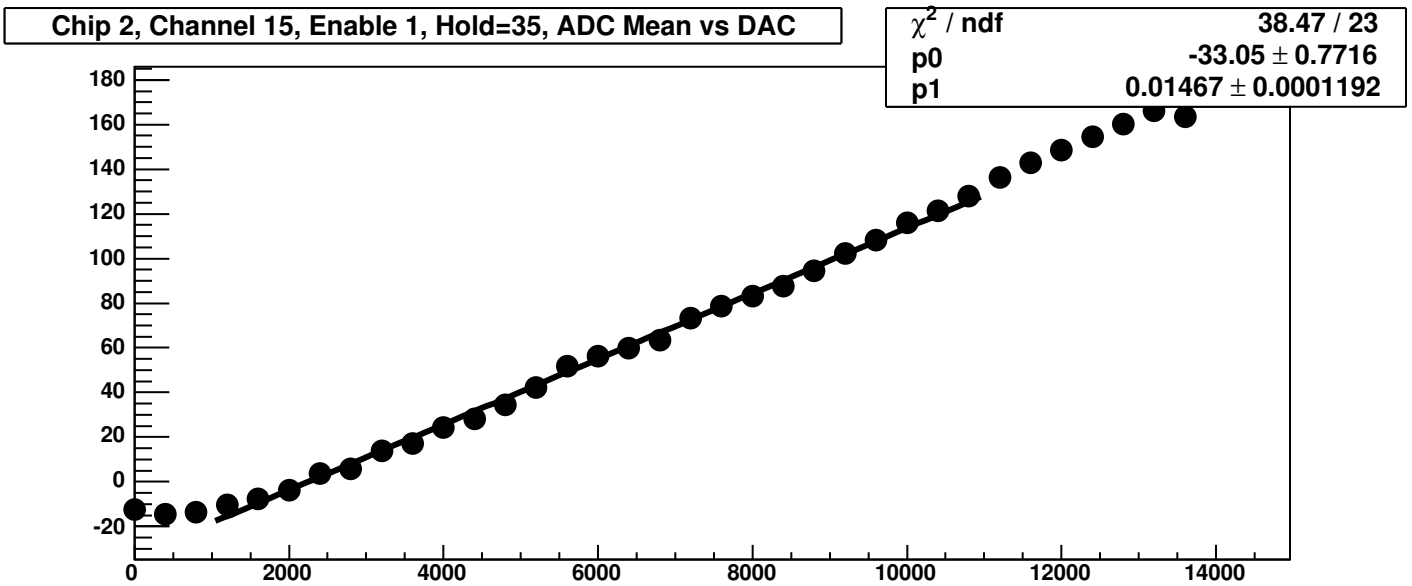

Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

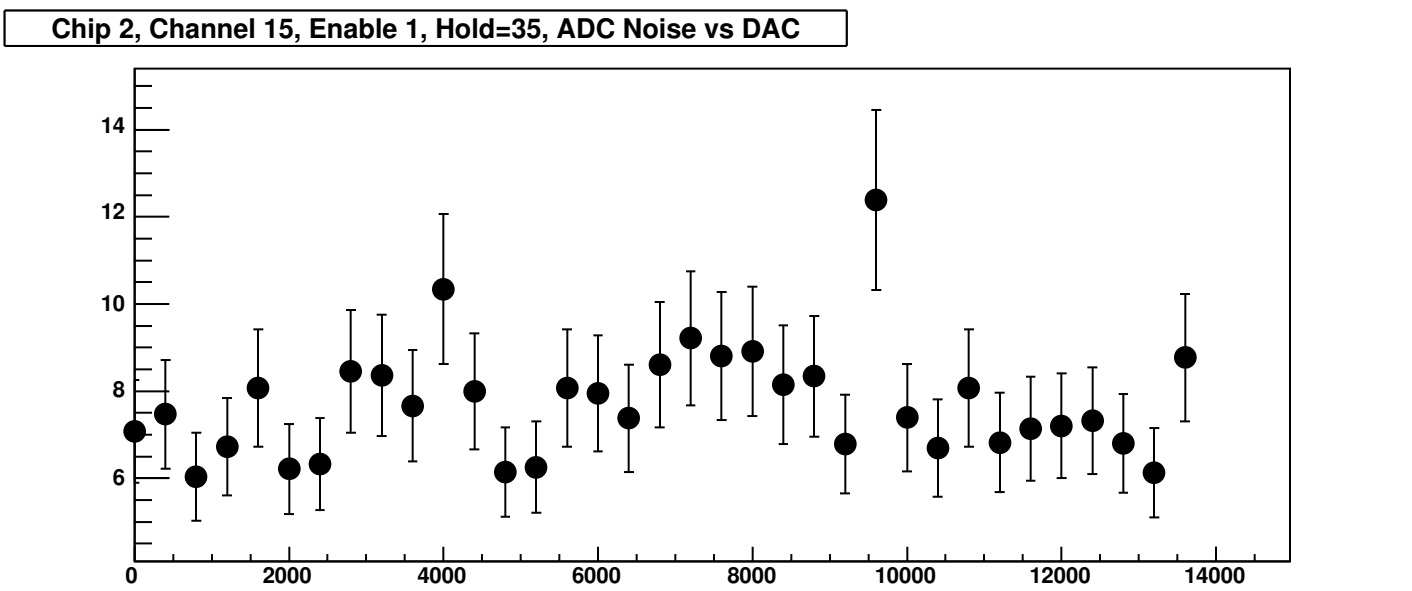

Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

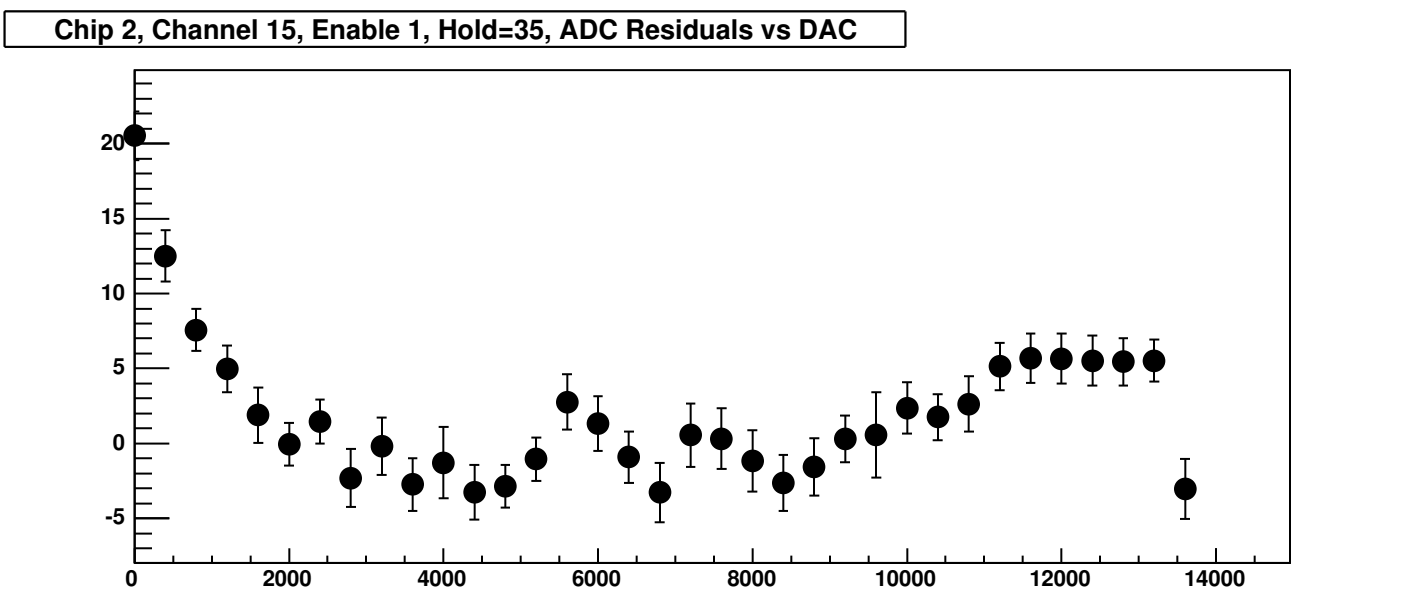

Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 785.7 / 23
p0 -230.9 ± 1.294
p1 0.1669 ± 0.0002185
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

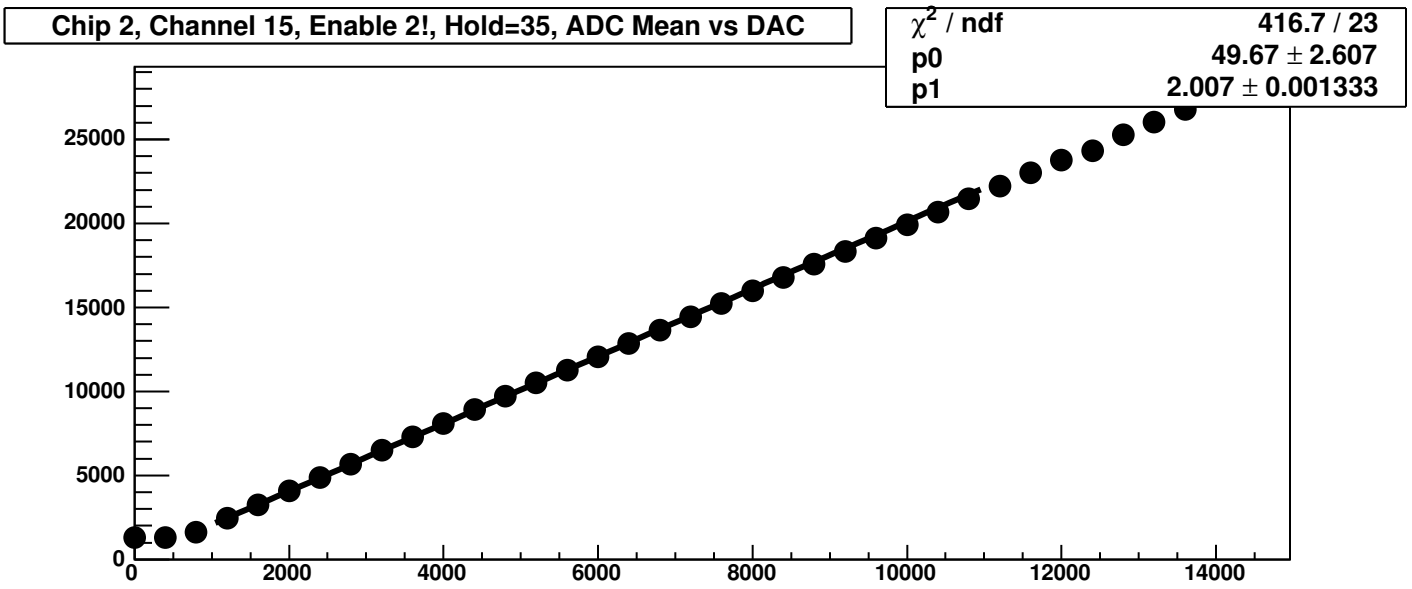

Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

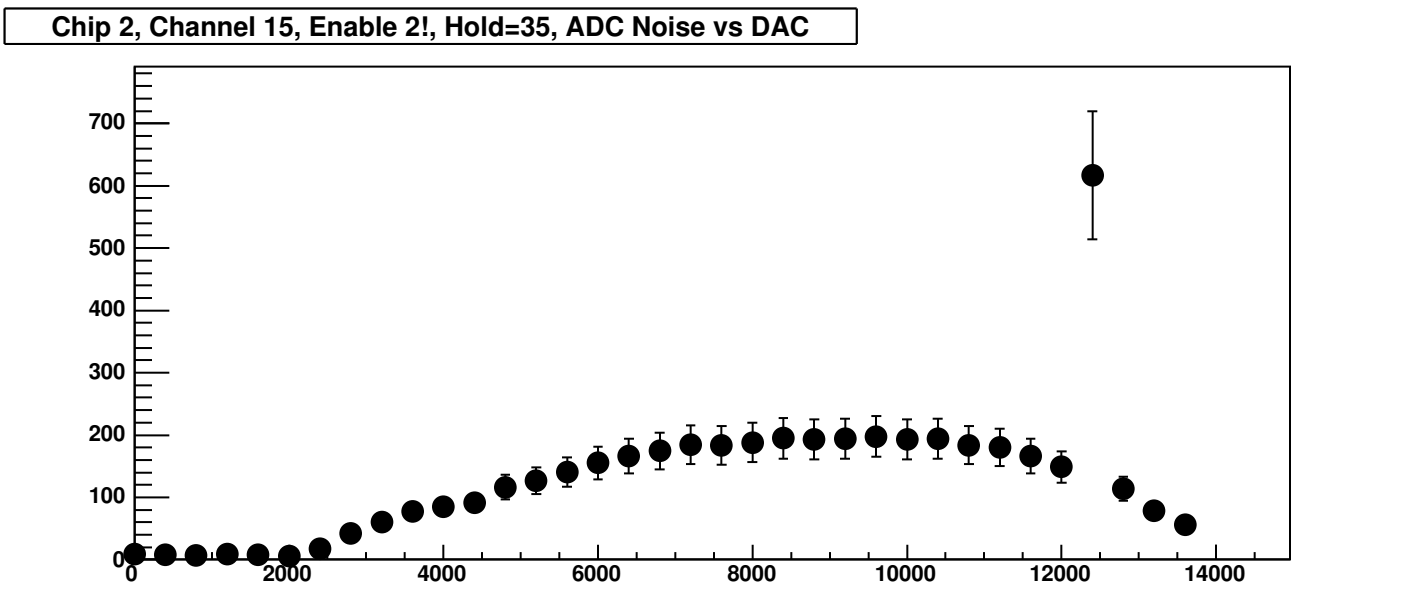

Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

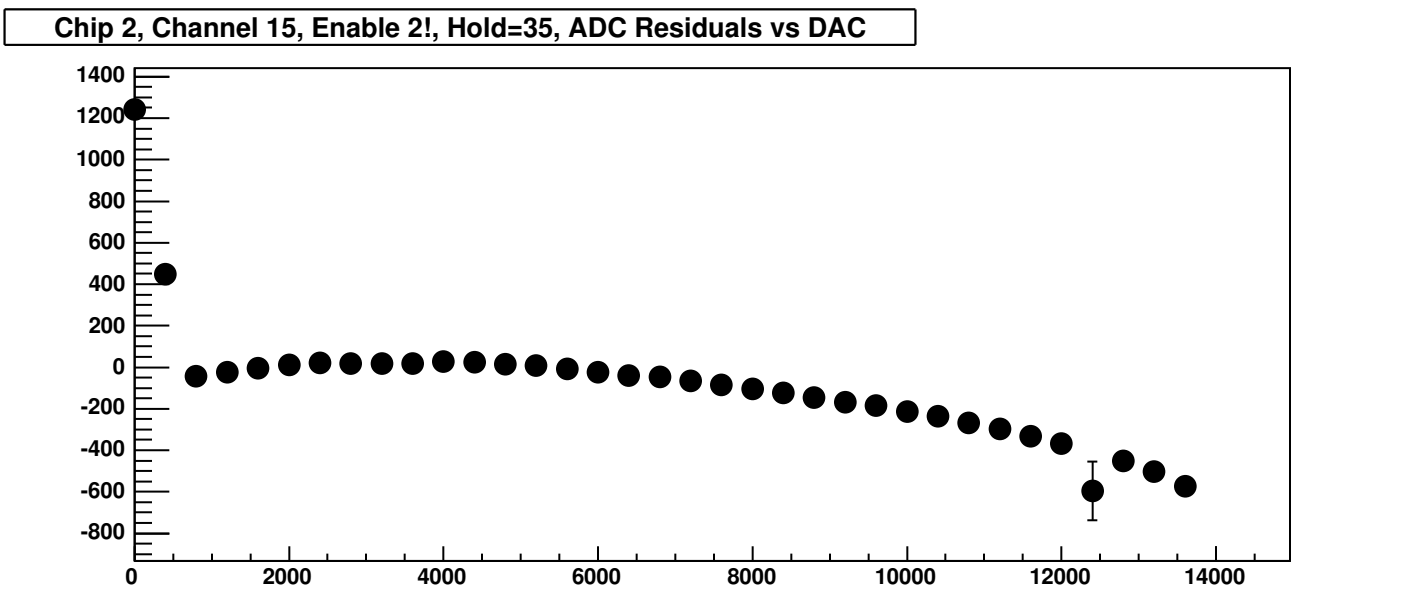

Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.47 / 23
p0 -33.05 ± 0.7716
p1 0.01467 ± 0.0001192
180
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

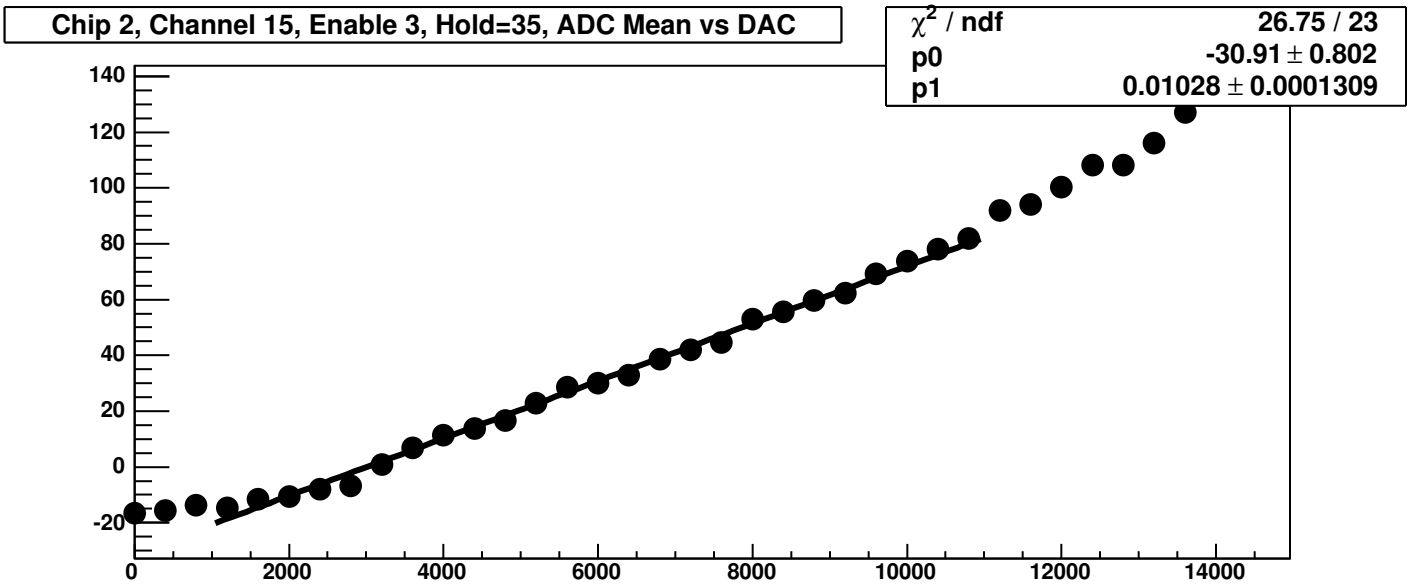

Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

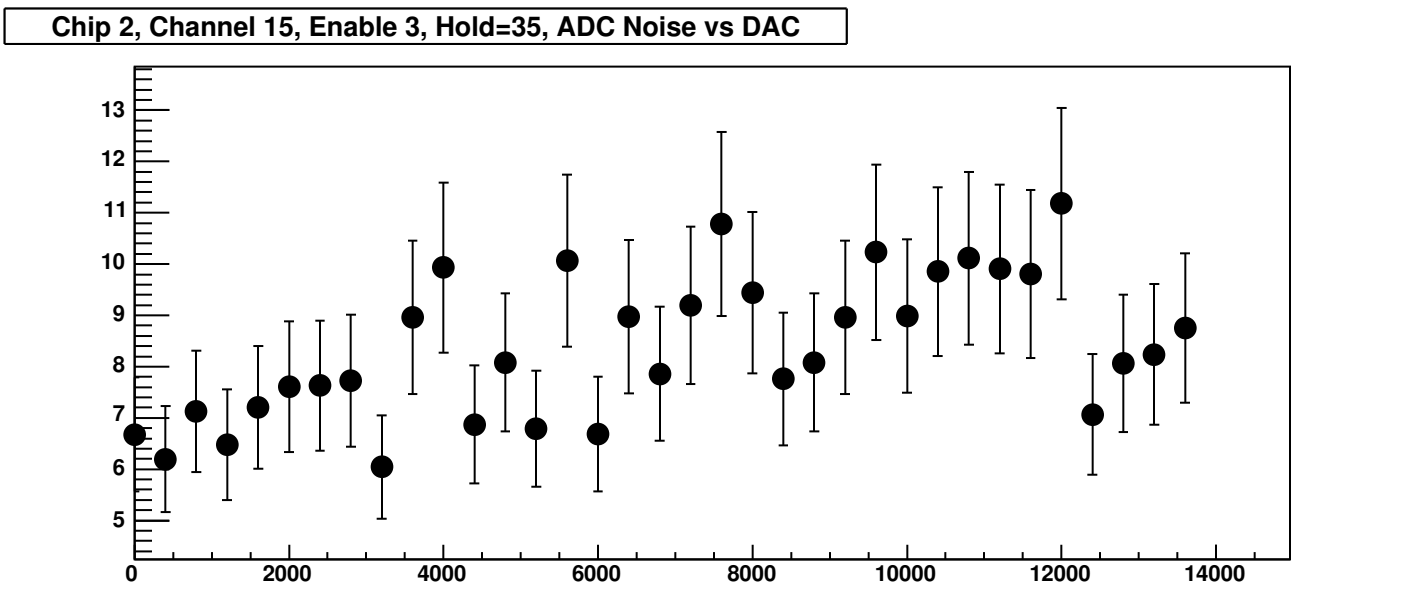

Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

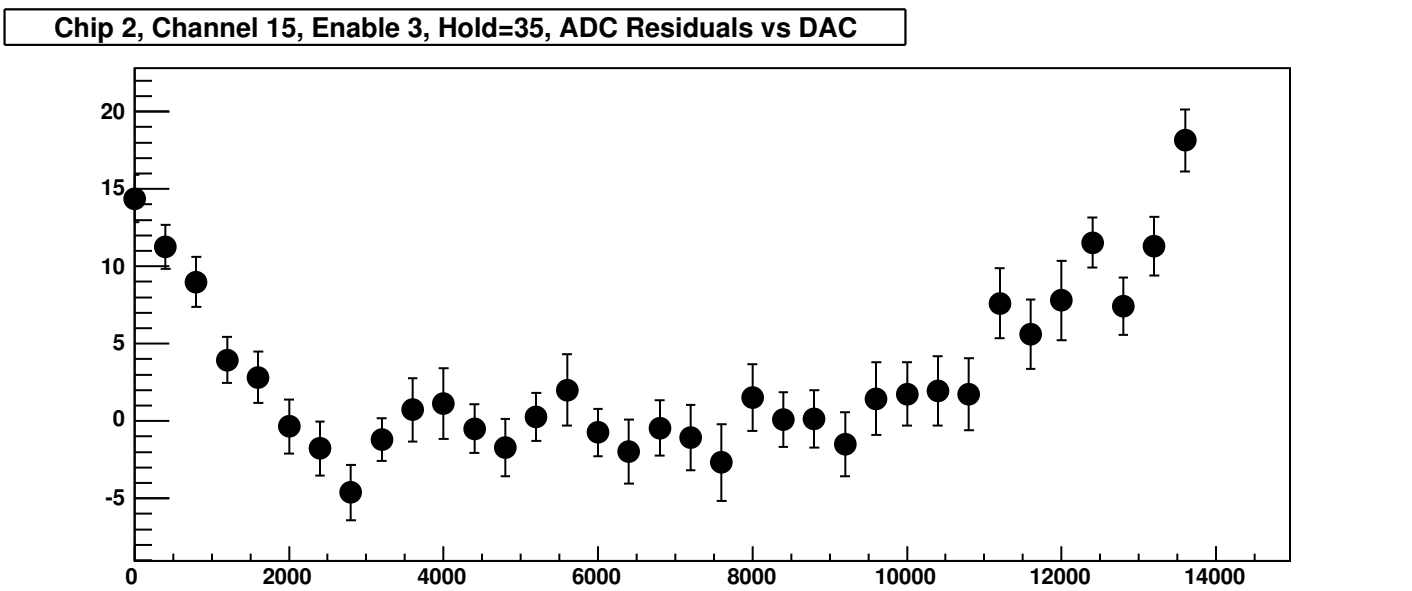

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 416.7 / 23
p0 49.67 ± 2.607
p1 2.007 ± 0.001333
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

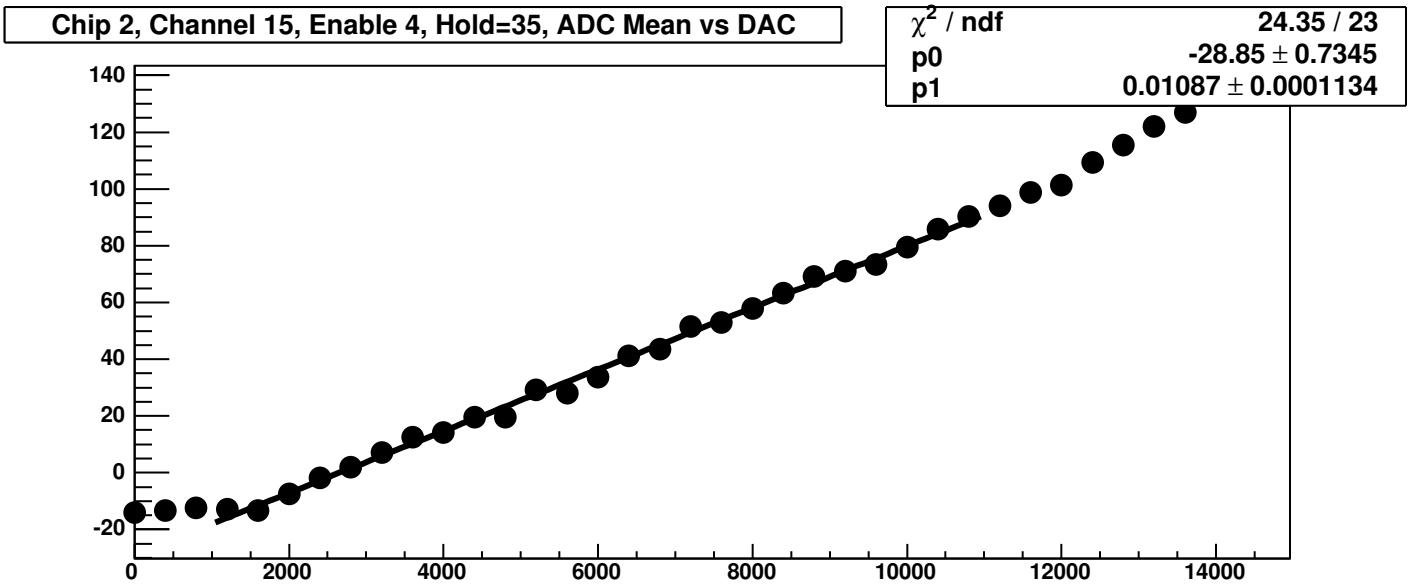

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

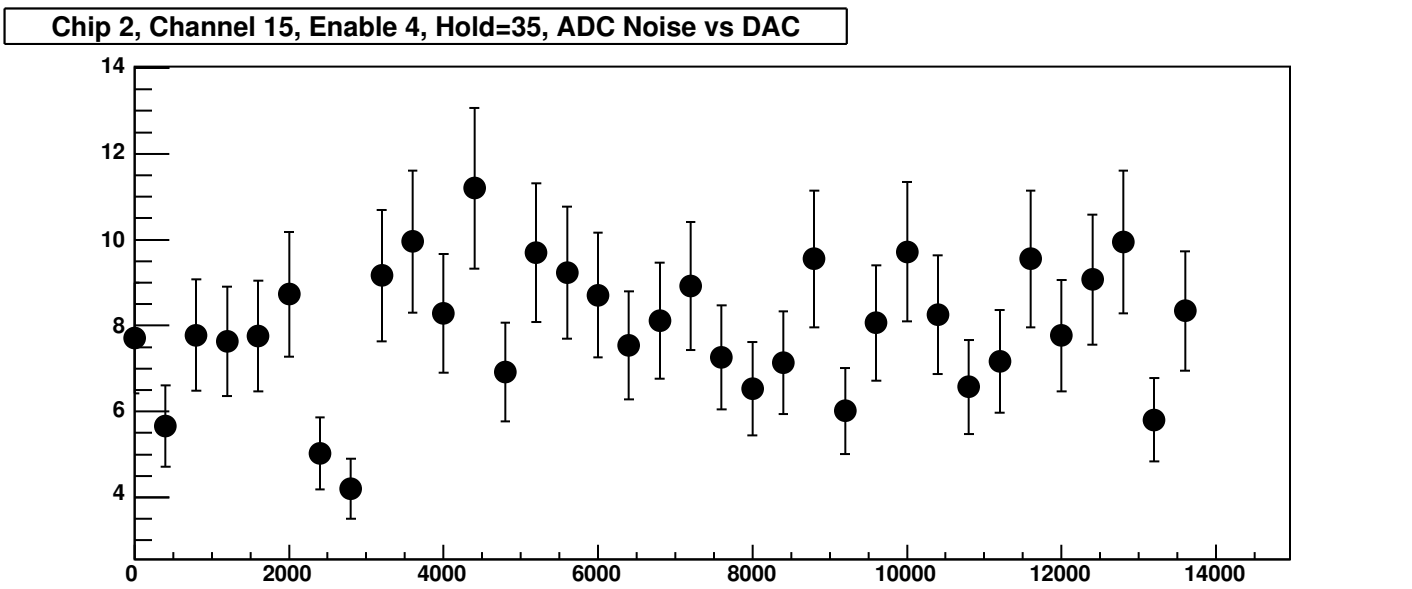

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

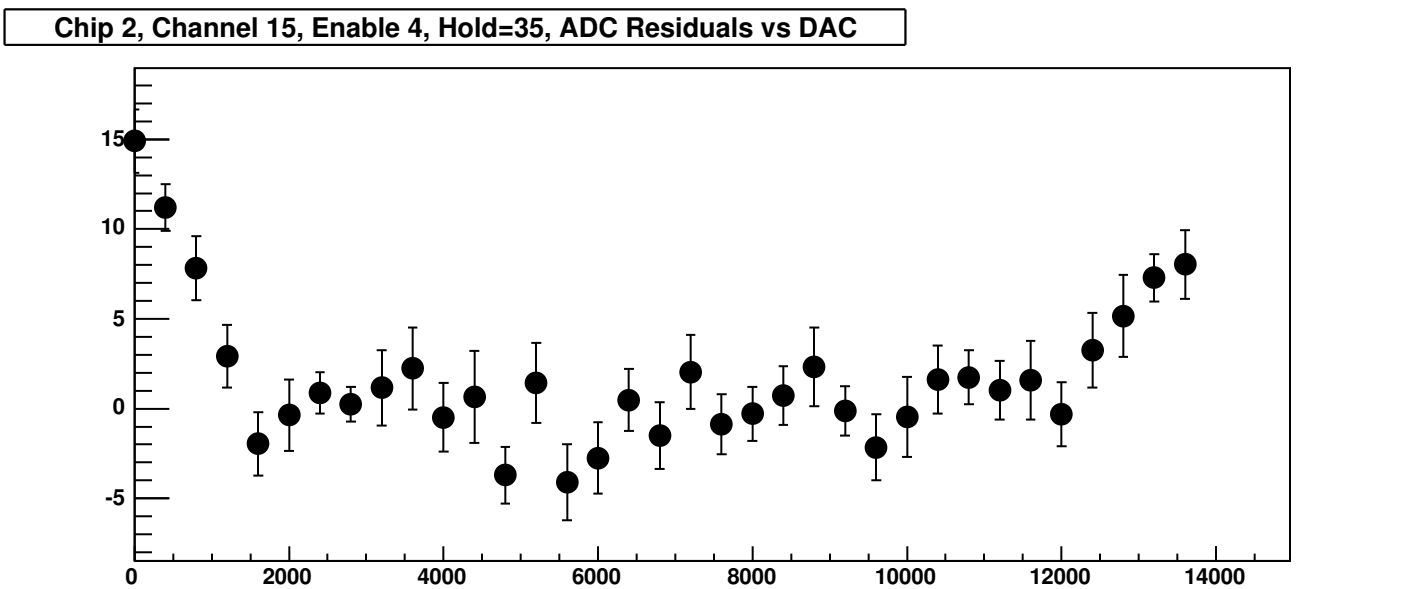

Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.75 / 23
p0 -30.91 ± 0.802
p1 0.01028 ± 0.0001309
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

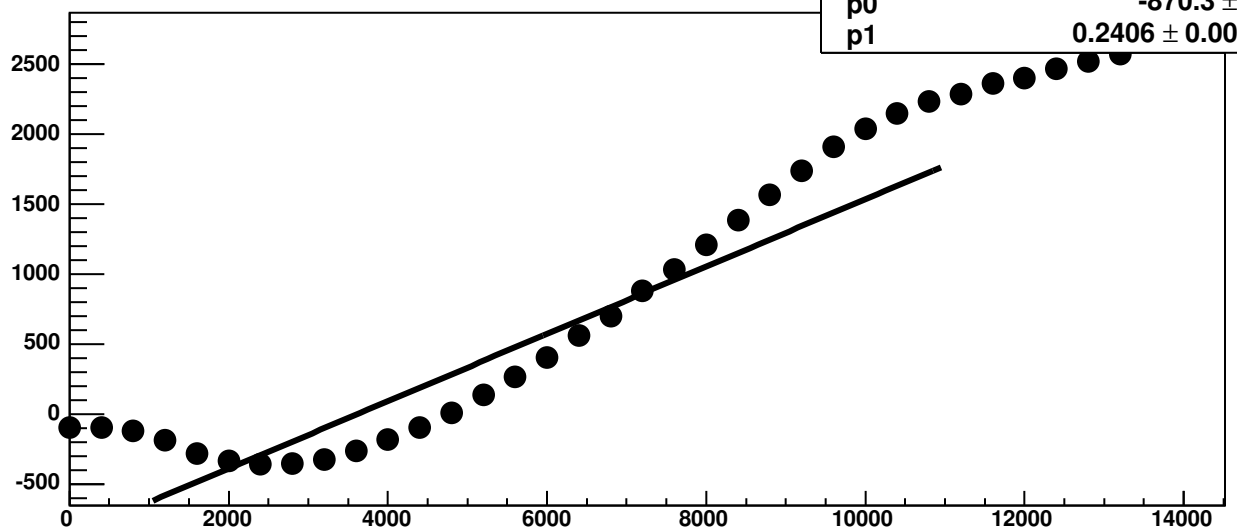

Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.35 / 23
p0 -28.85 ± 0.7345
p1 0.01087 ± 0.0001134
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

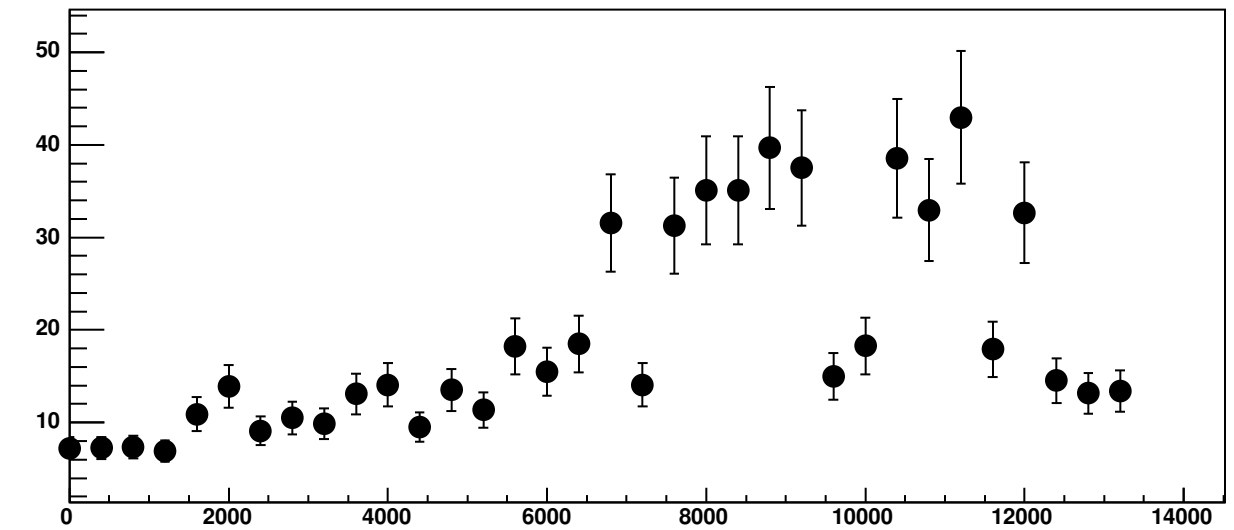

Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

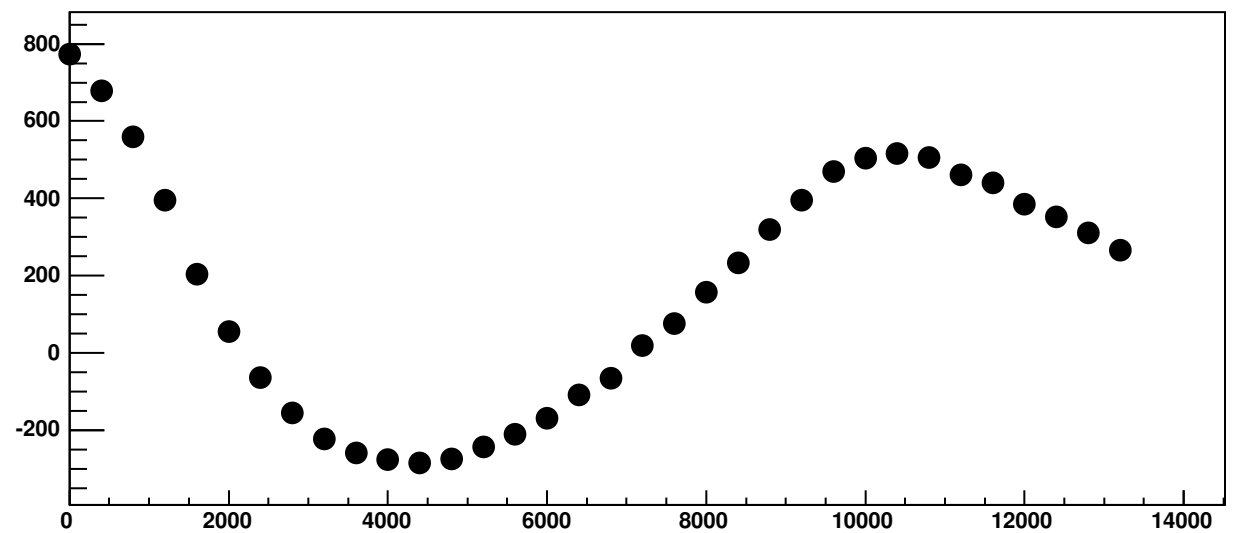
χ^2 / ndf	1.835e+05 / 23
p0	-870.3 ± 1.208
p1	0.2406 ± 0.0002575



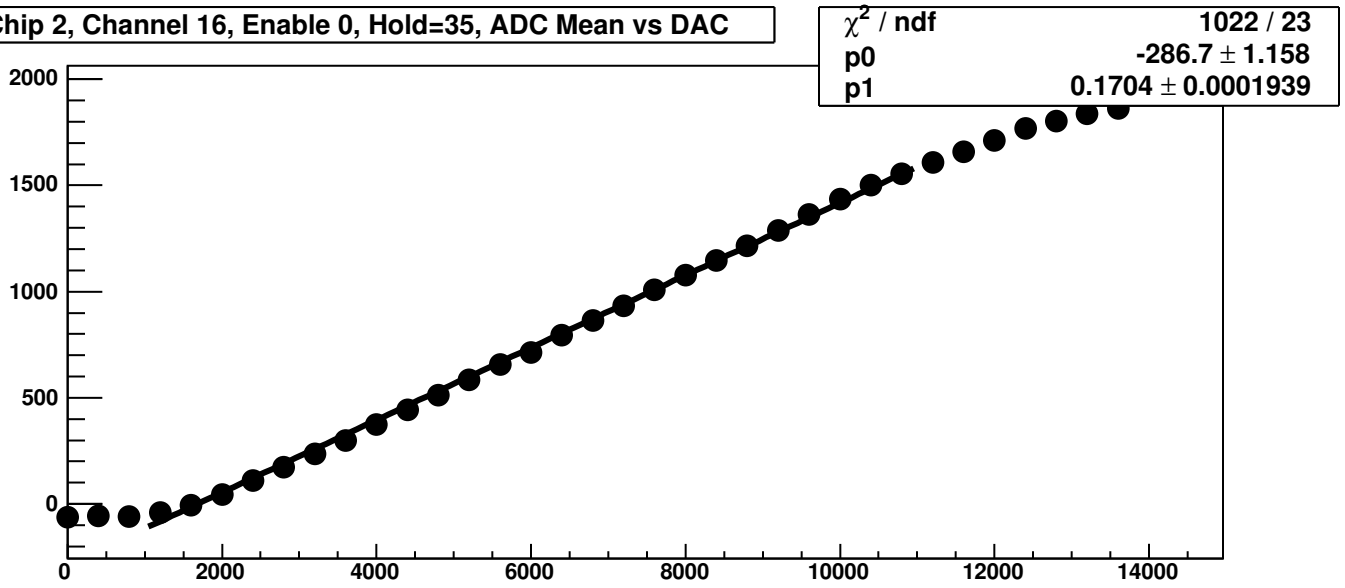
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC



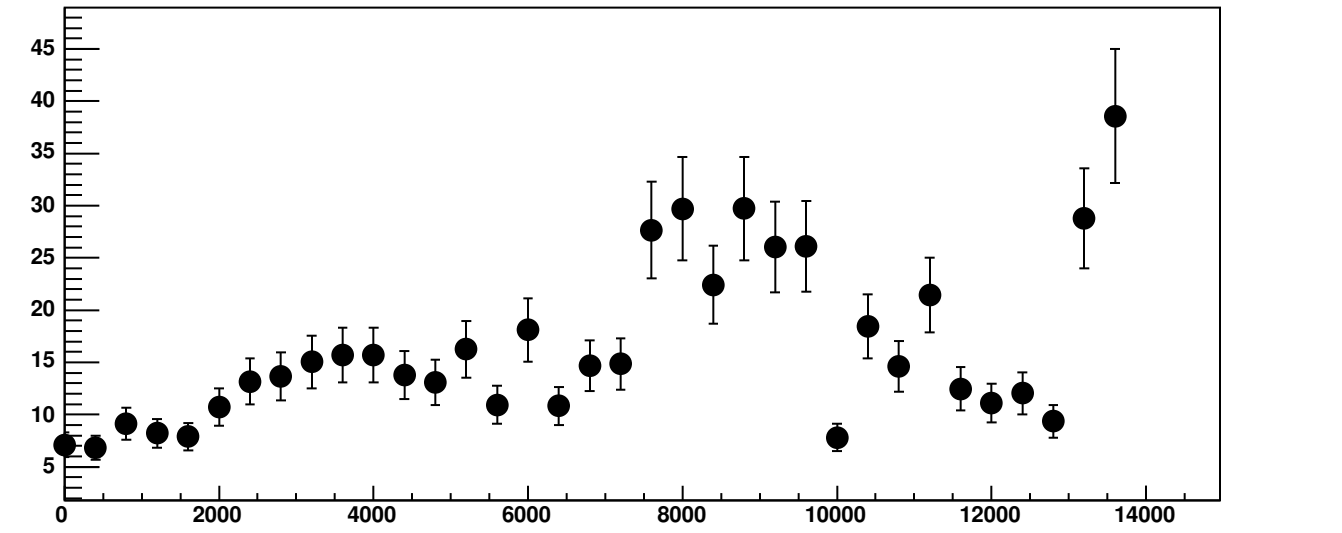
Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC



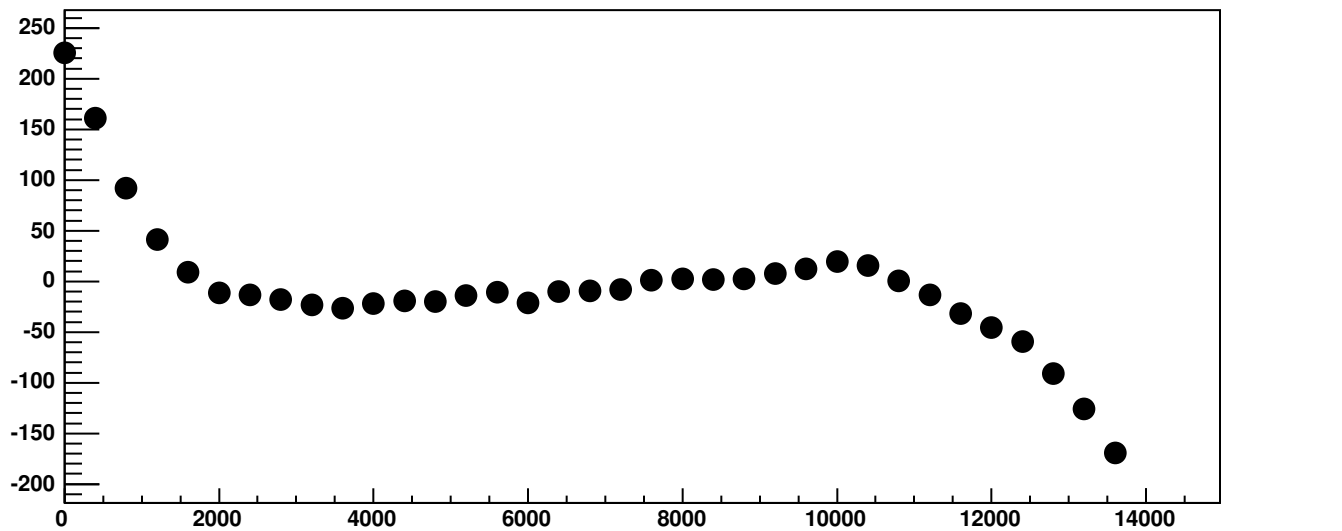
Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

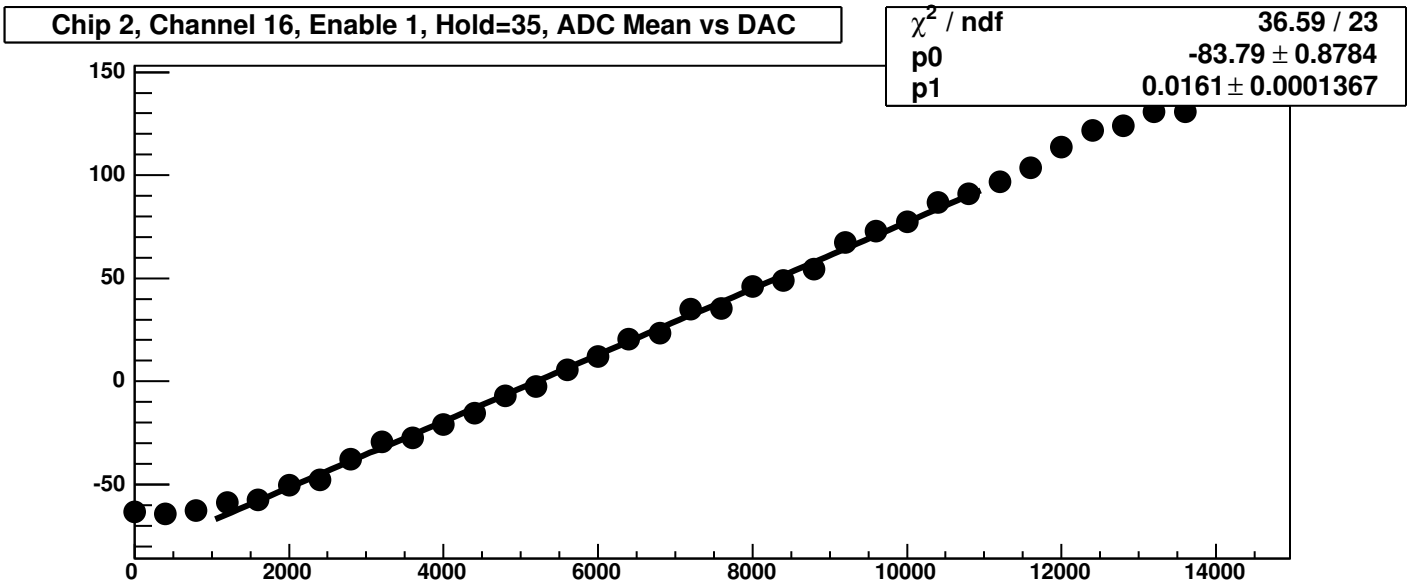


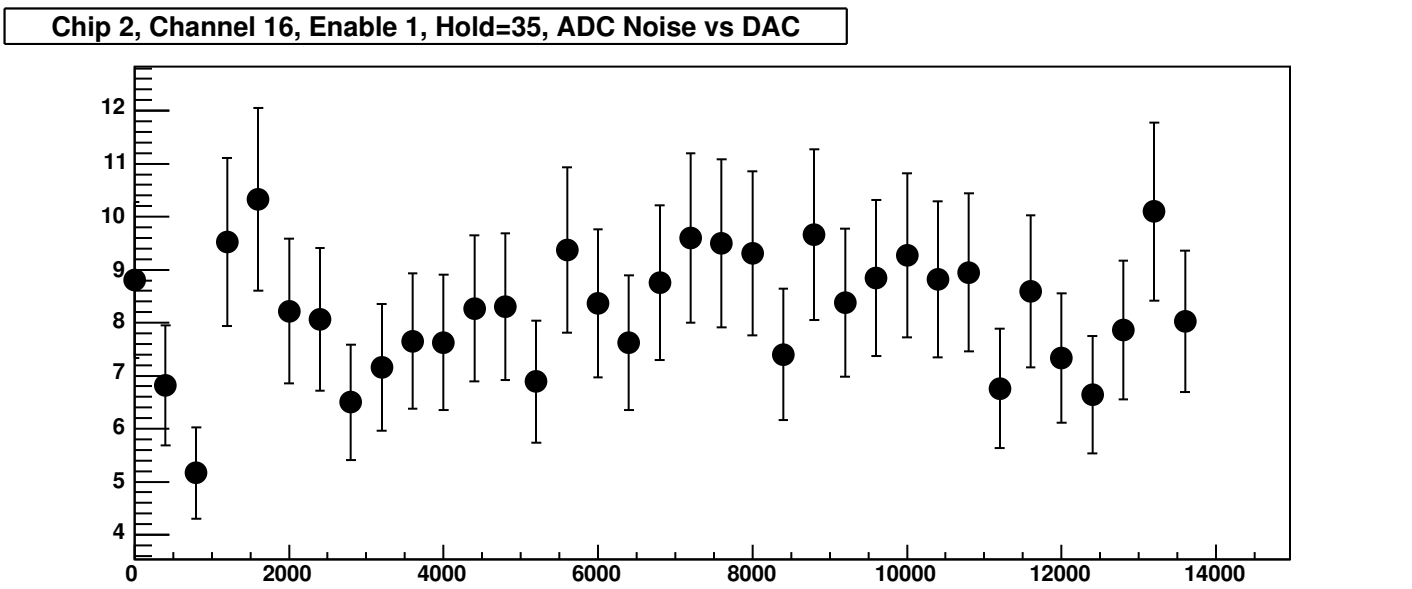
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

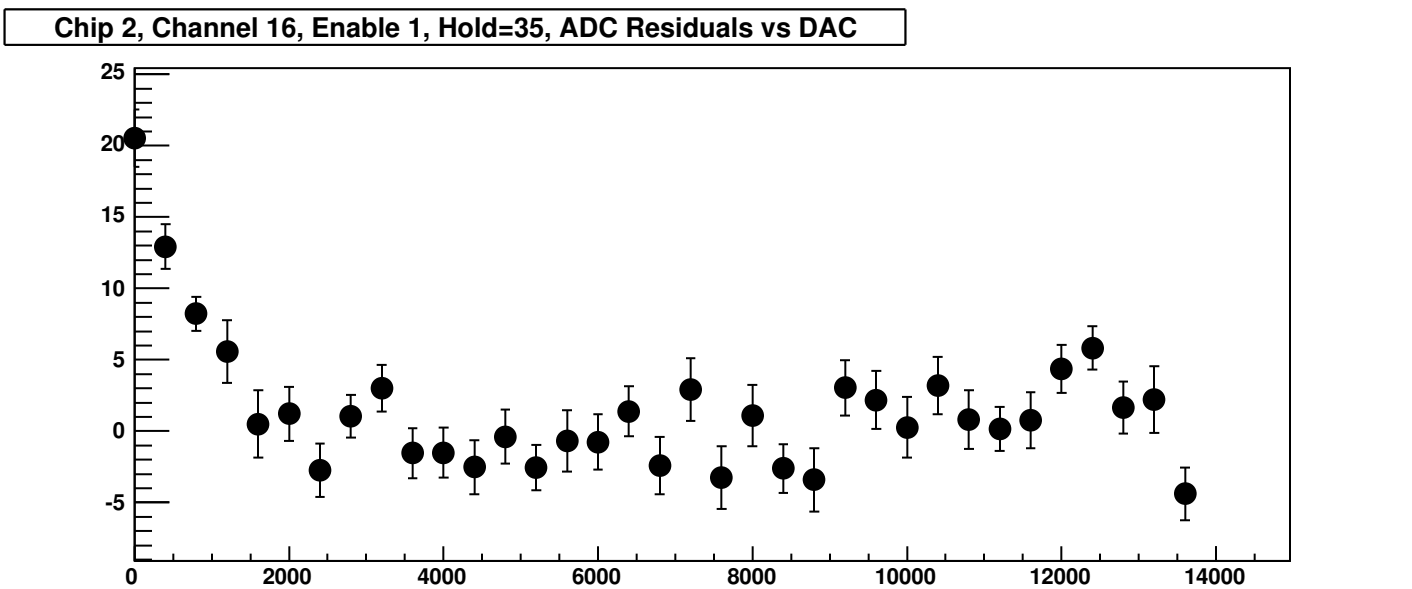


Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

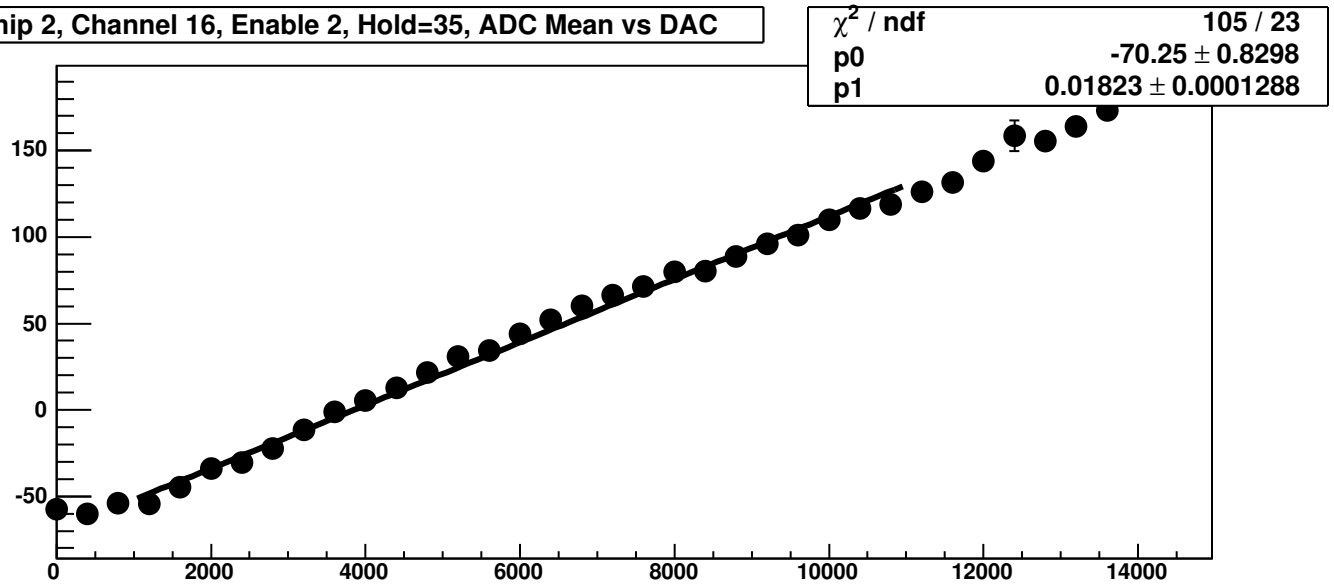



Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.59 / 23
p0 -83.79 ± 0.8784
p1 0.0161 ± 0.0001367
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

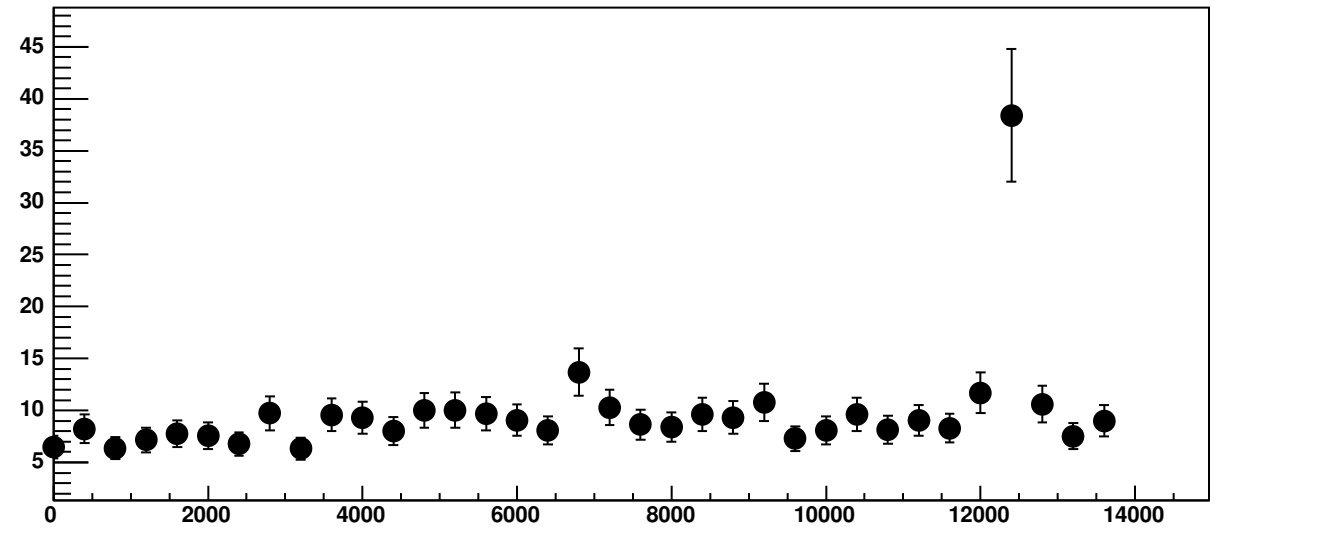

Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

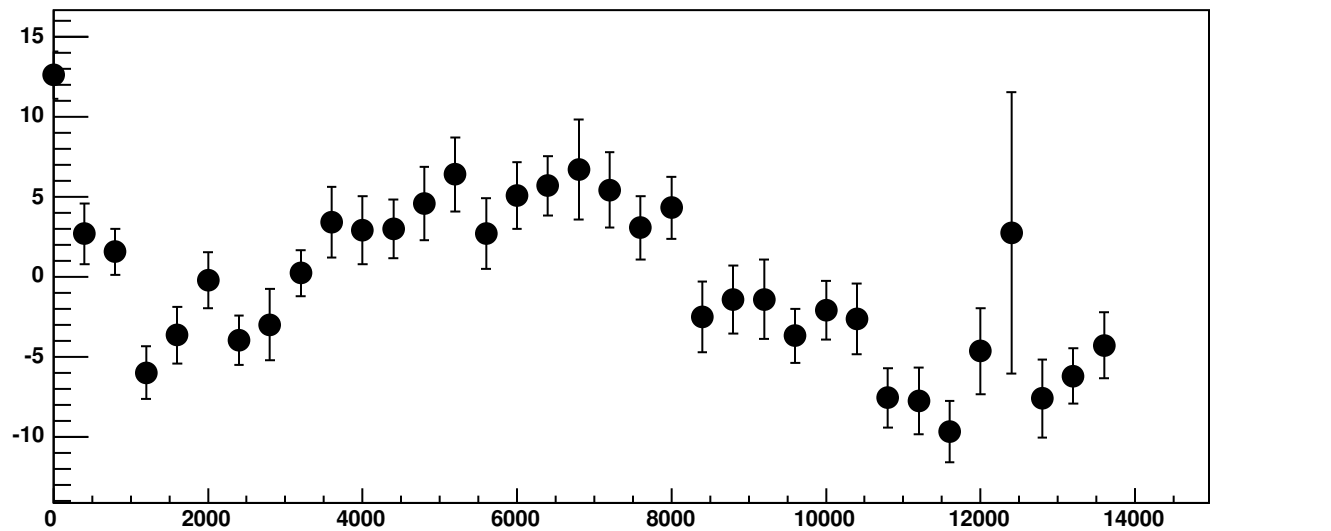
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



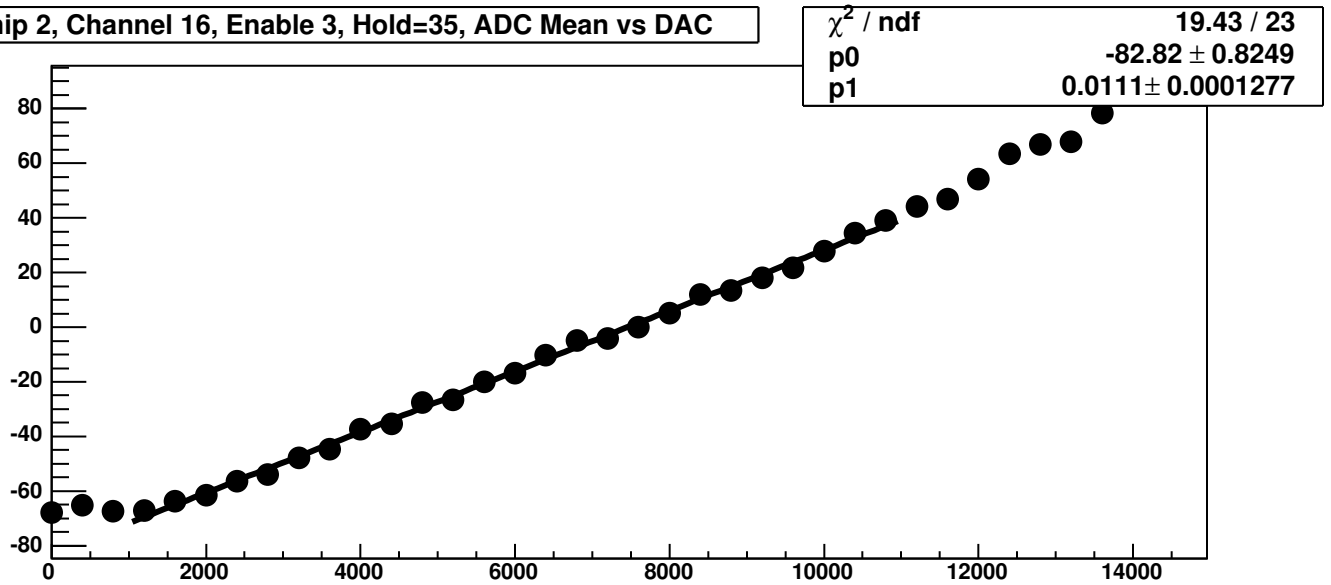
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



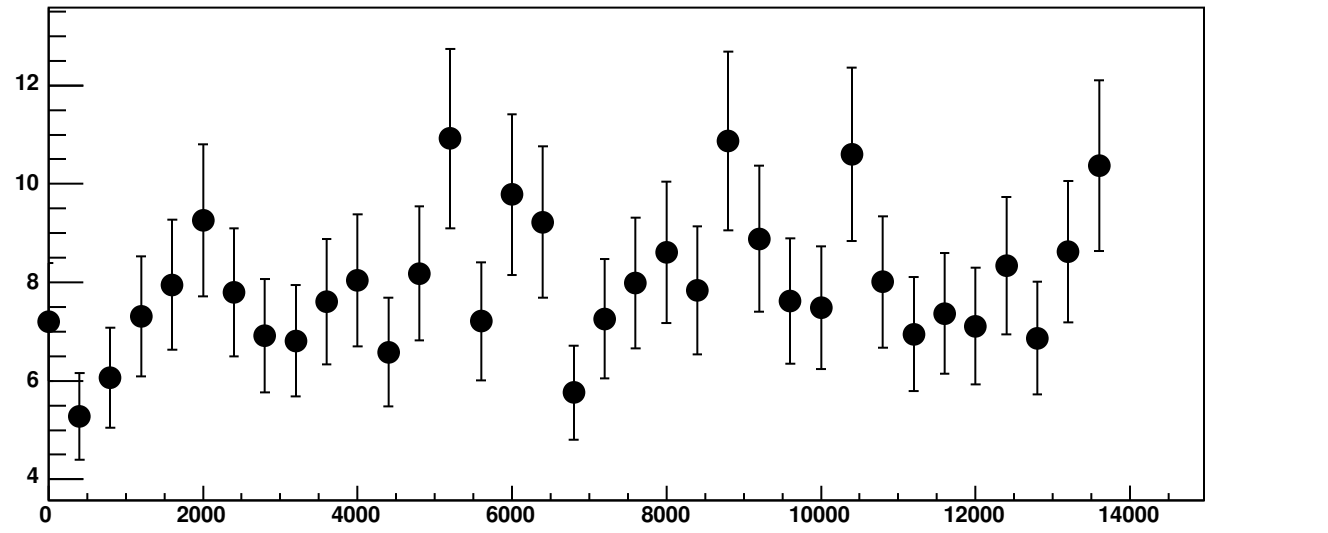
Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



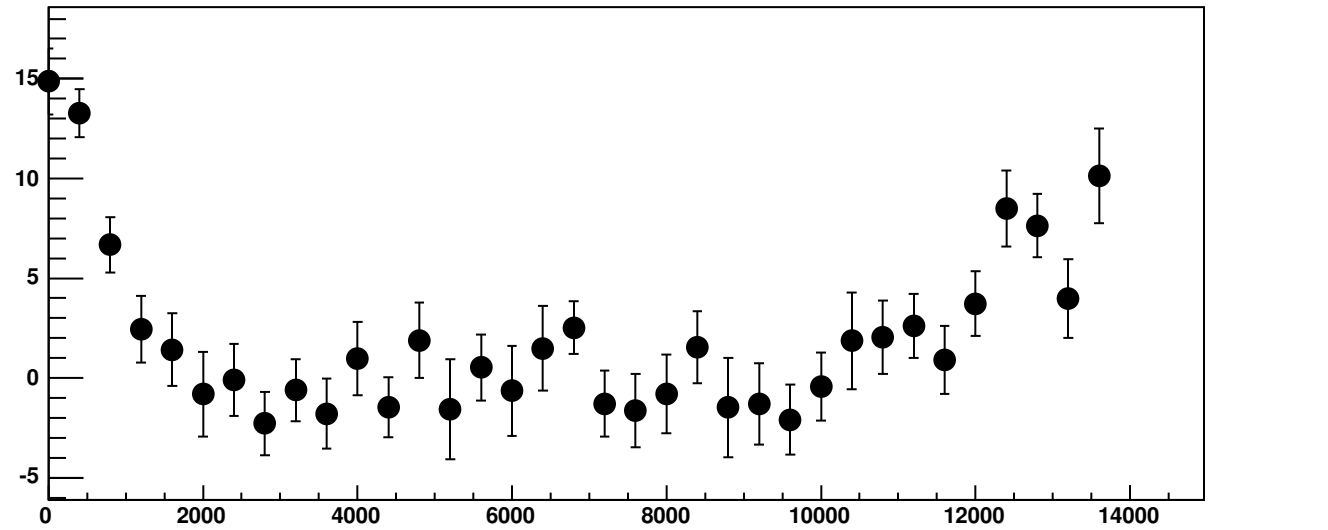
Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC



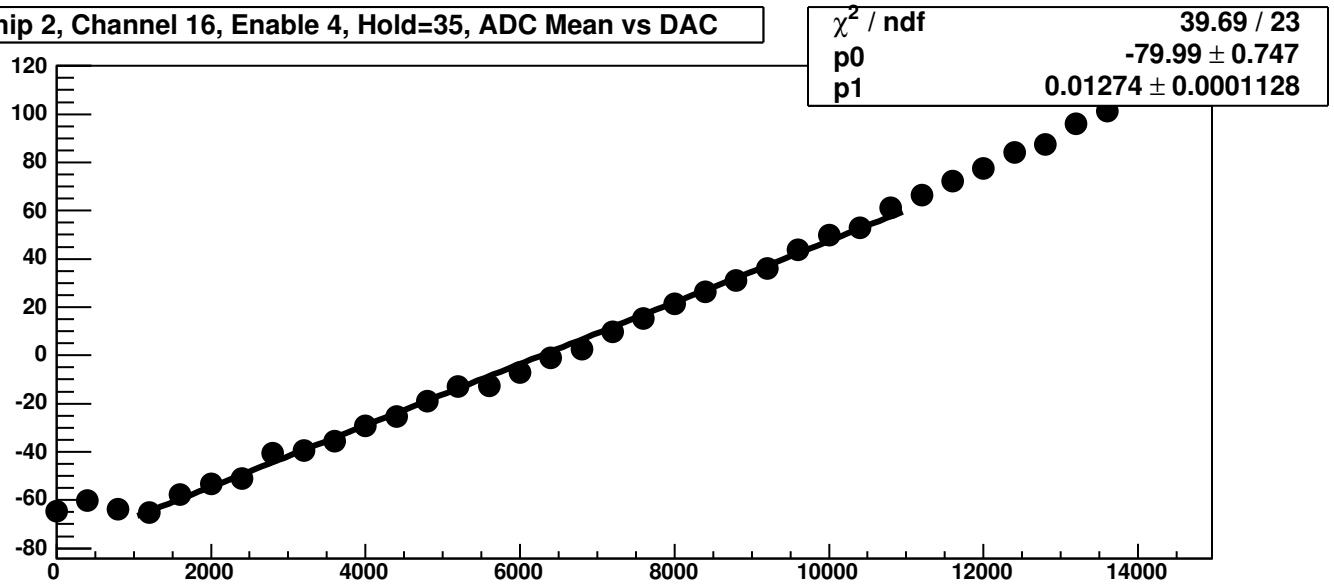
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



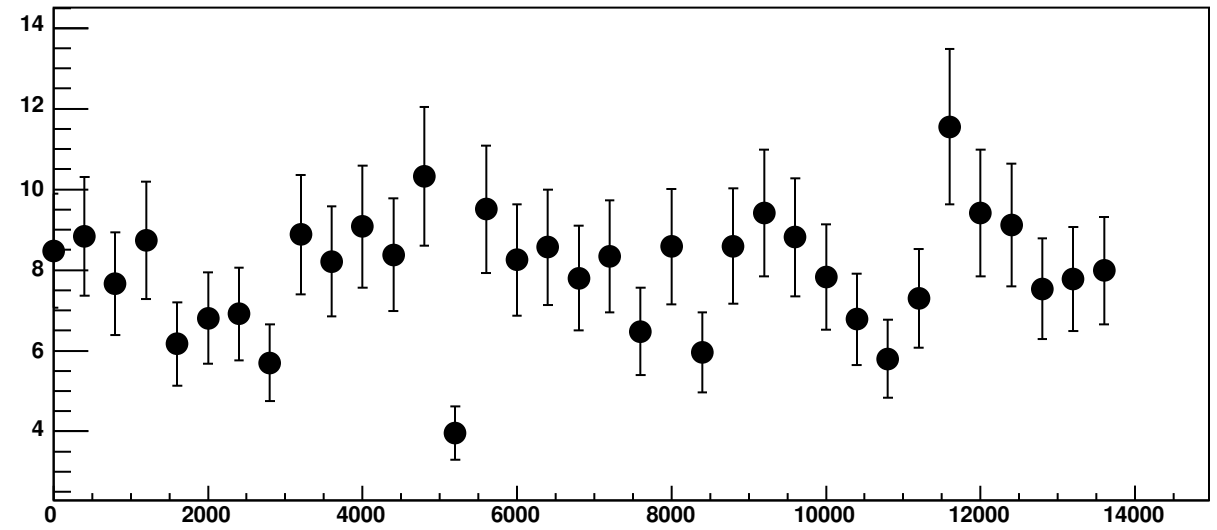
Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



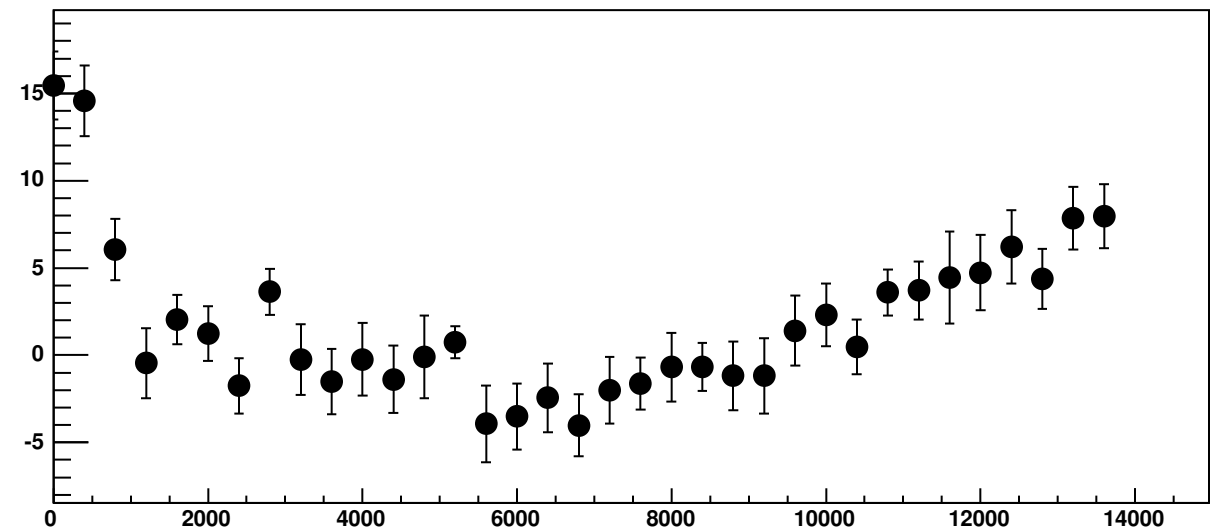
Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

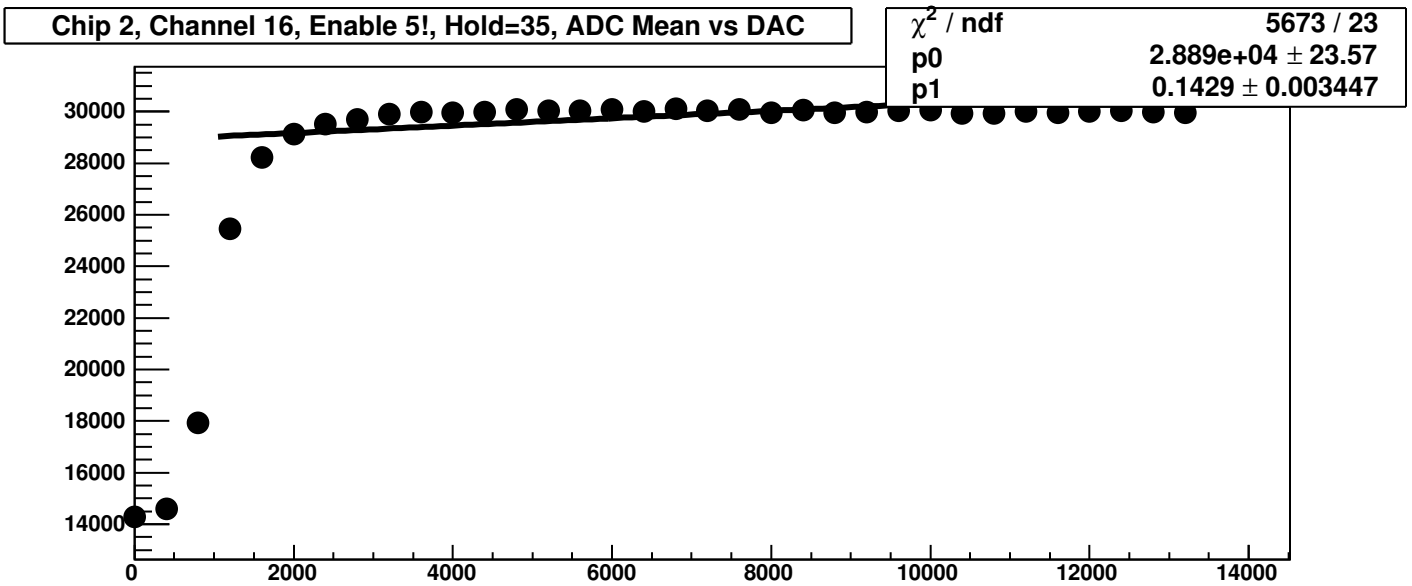


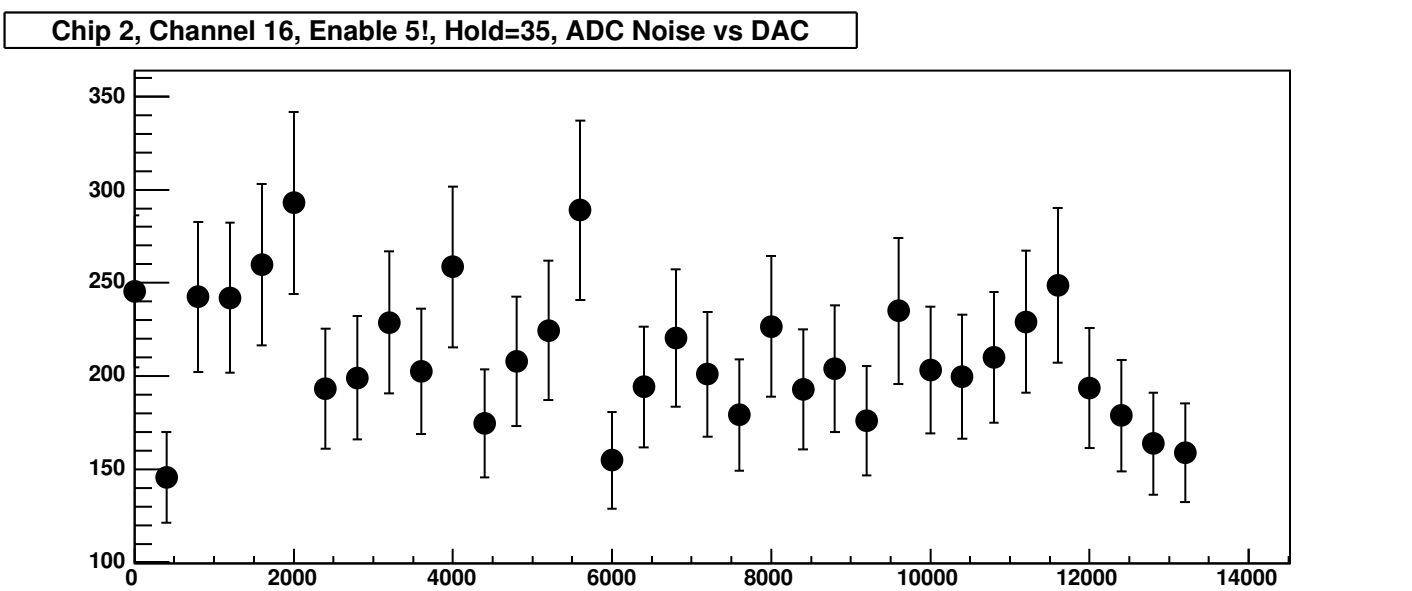
Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

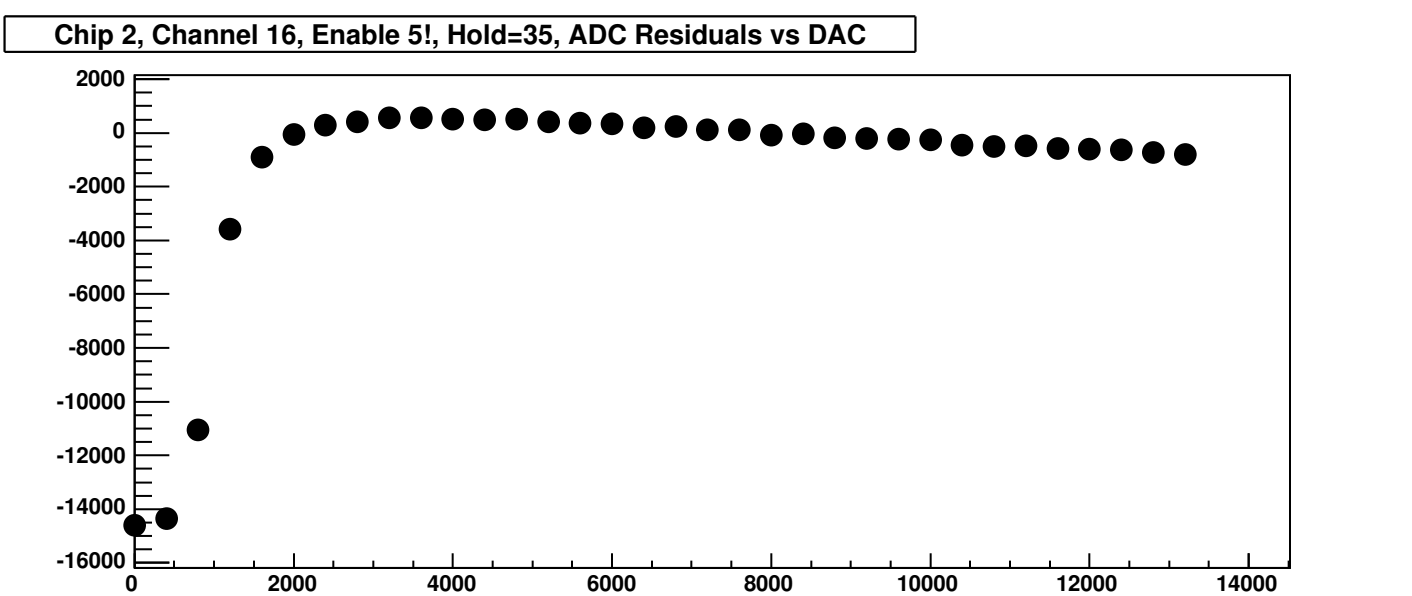


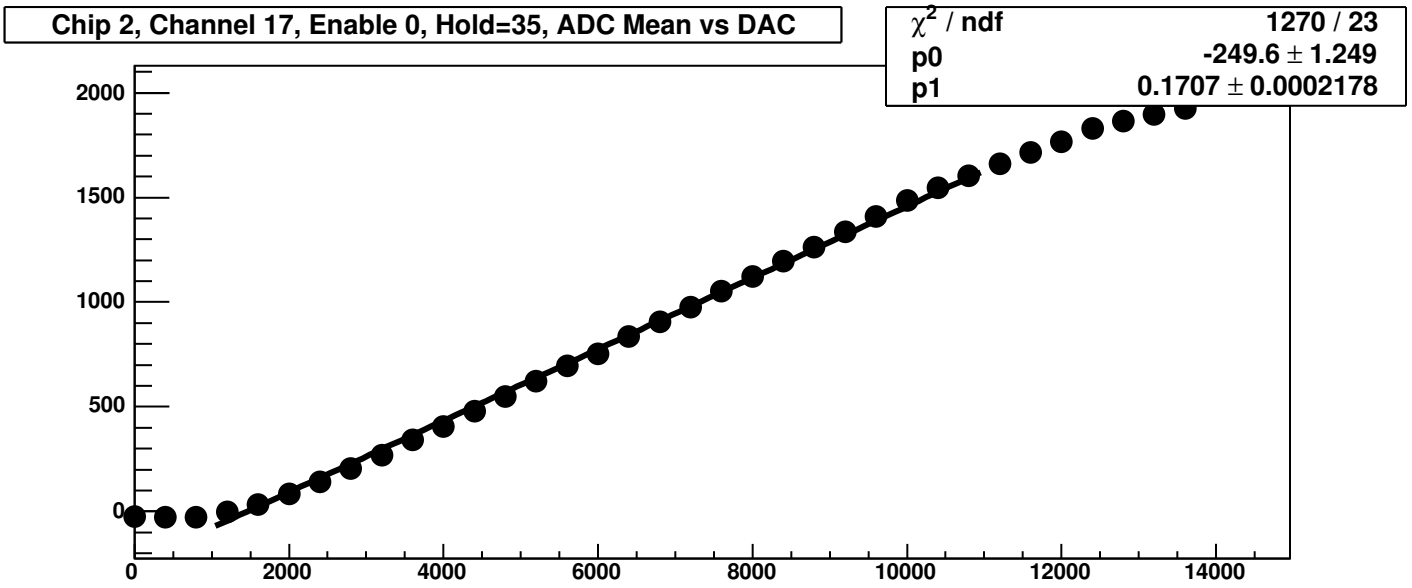
Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

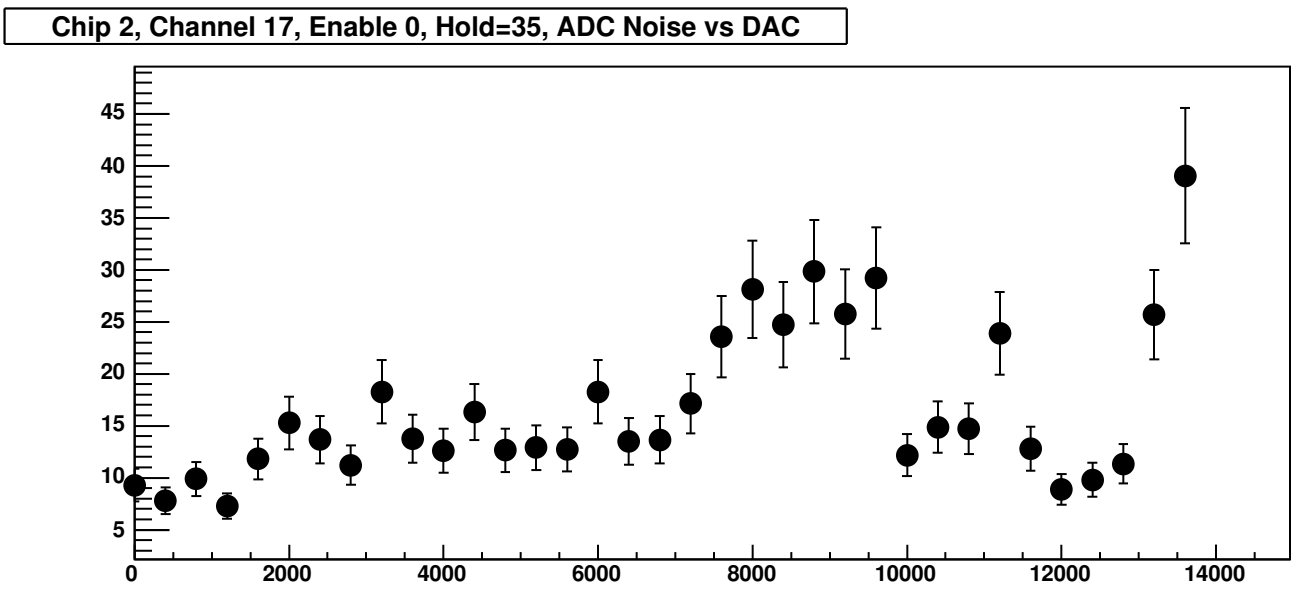


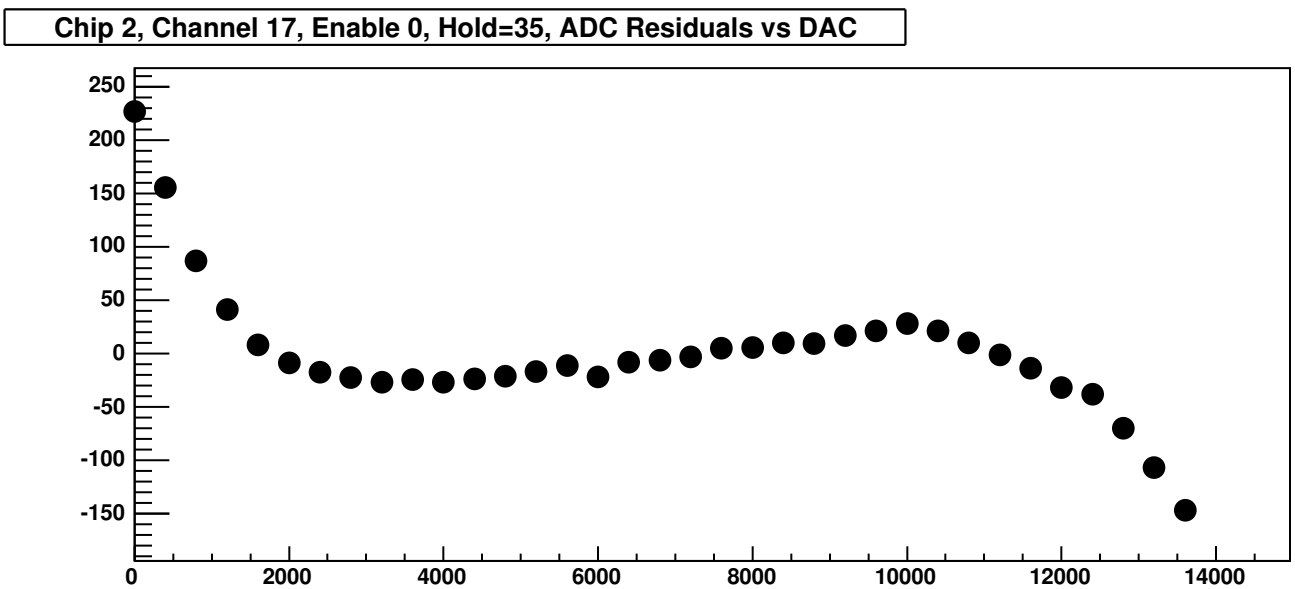

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5673 / 23
p0 2.889e+04 ± 23.57
p1 0.1429 ± 0.003447
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

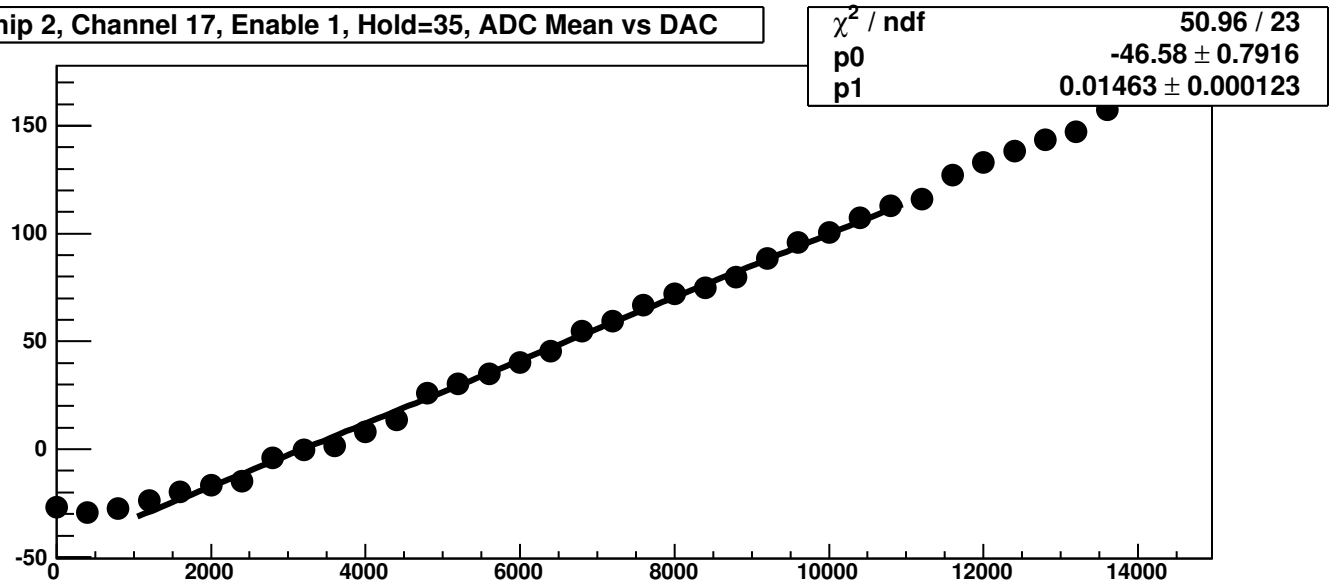

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1270 / 23
p0 -249.6 ± 1.249
p1 0.1707 ± 0.0002178
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

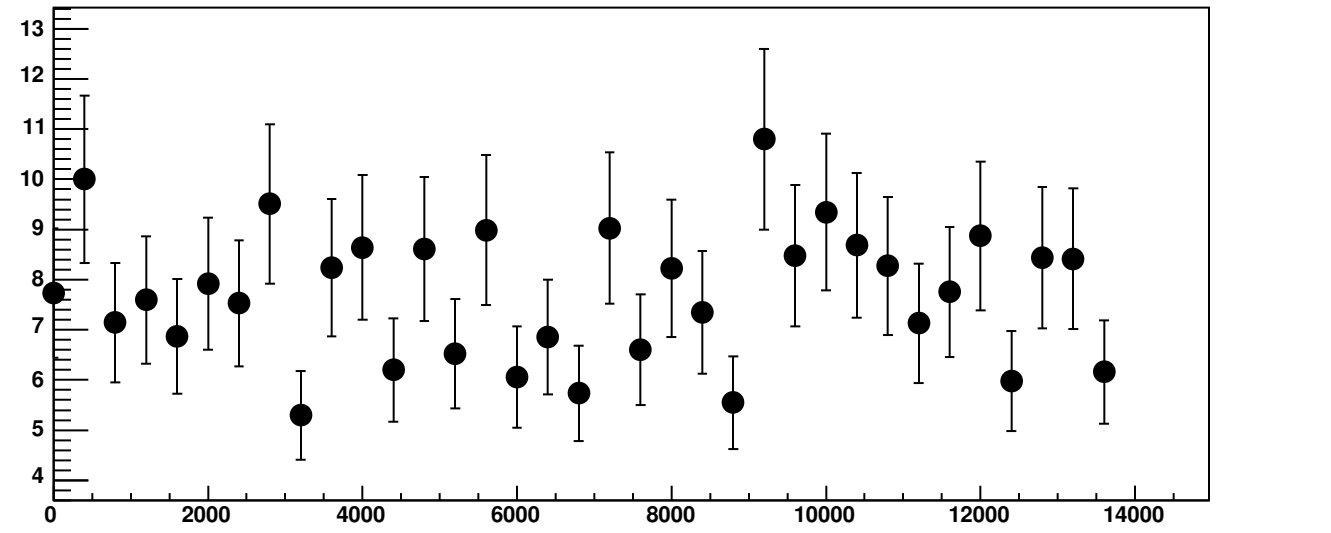

Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

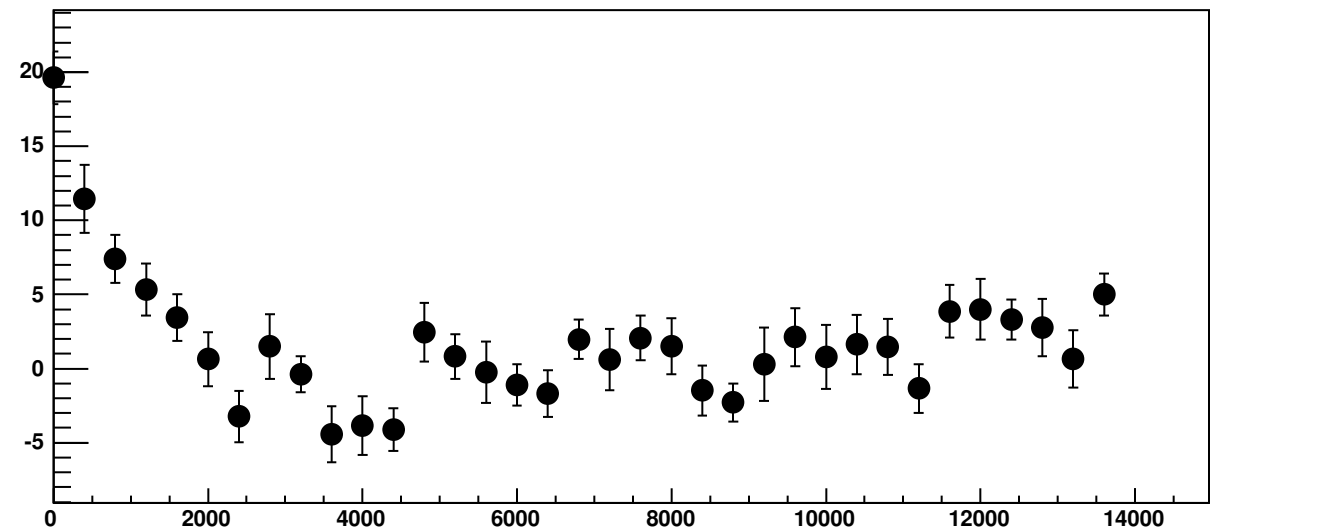
Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

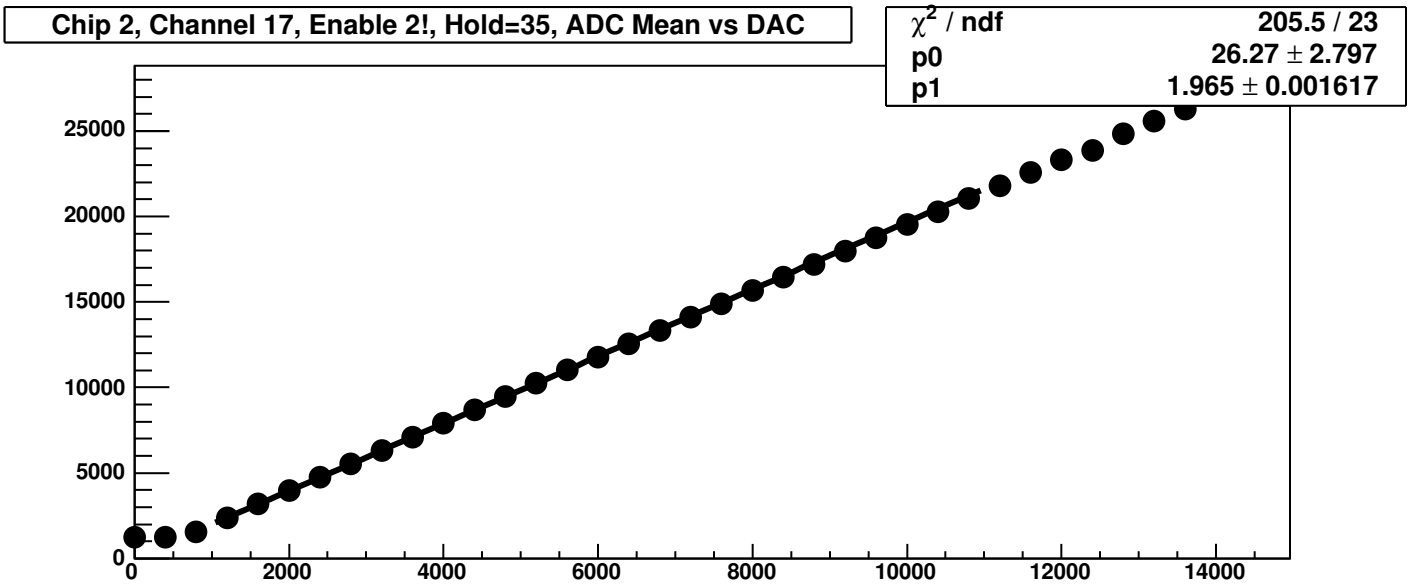


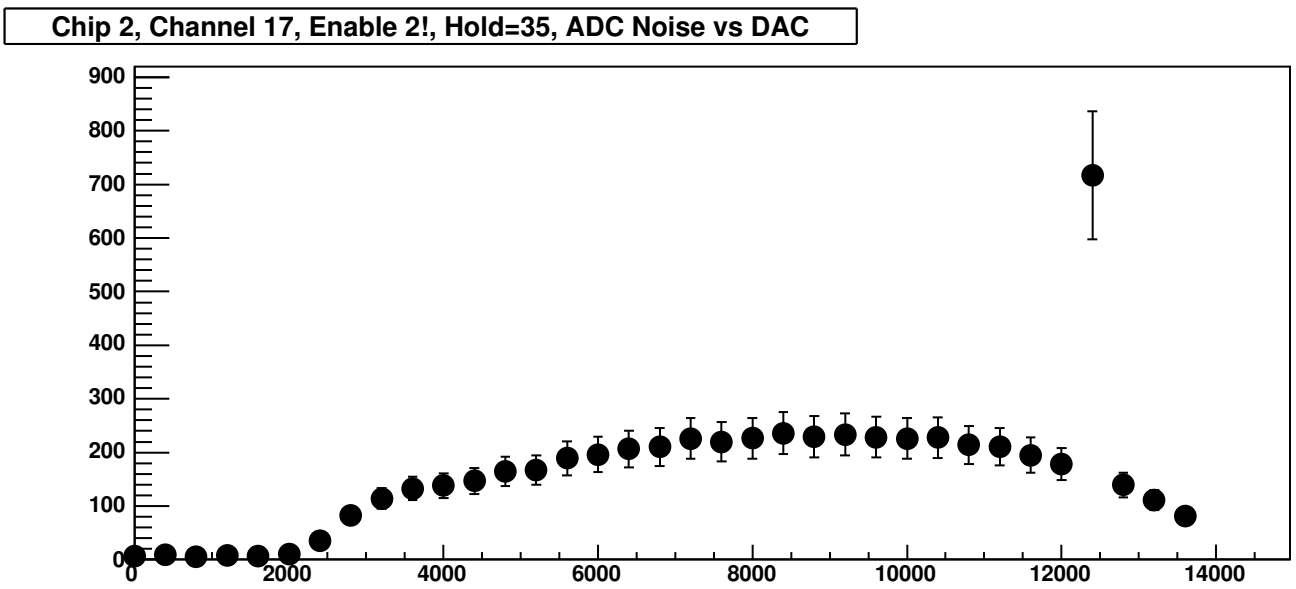
Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

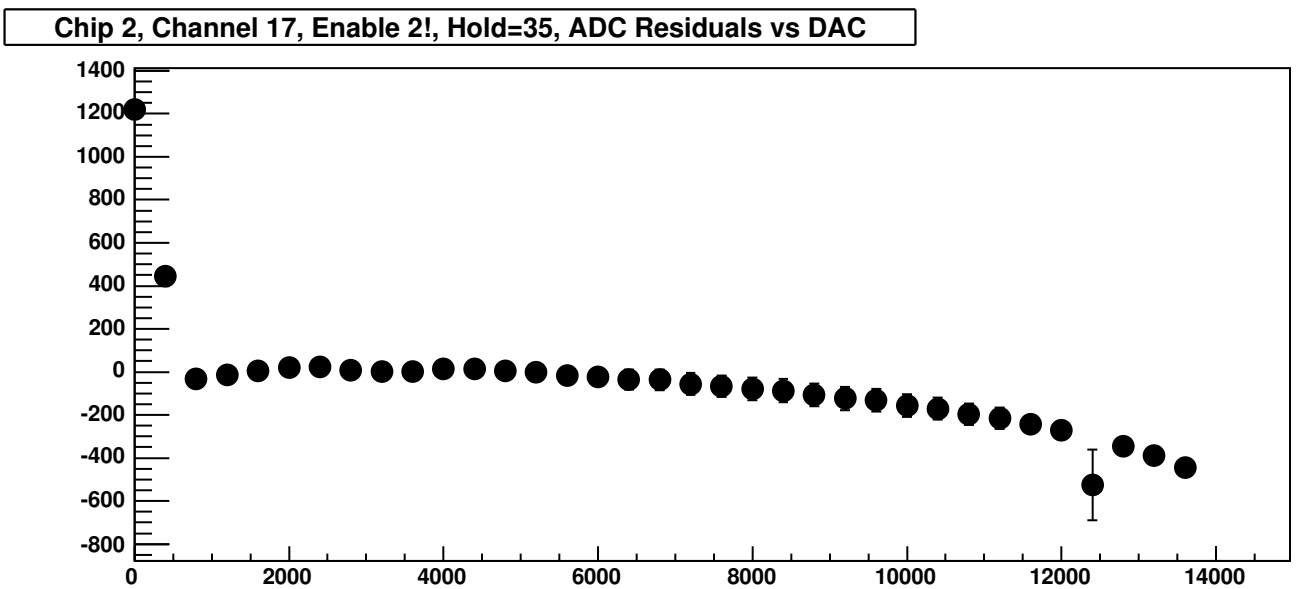


Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

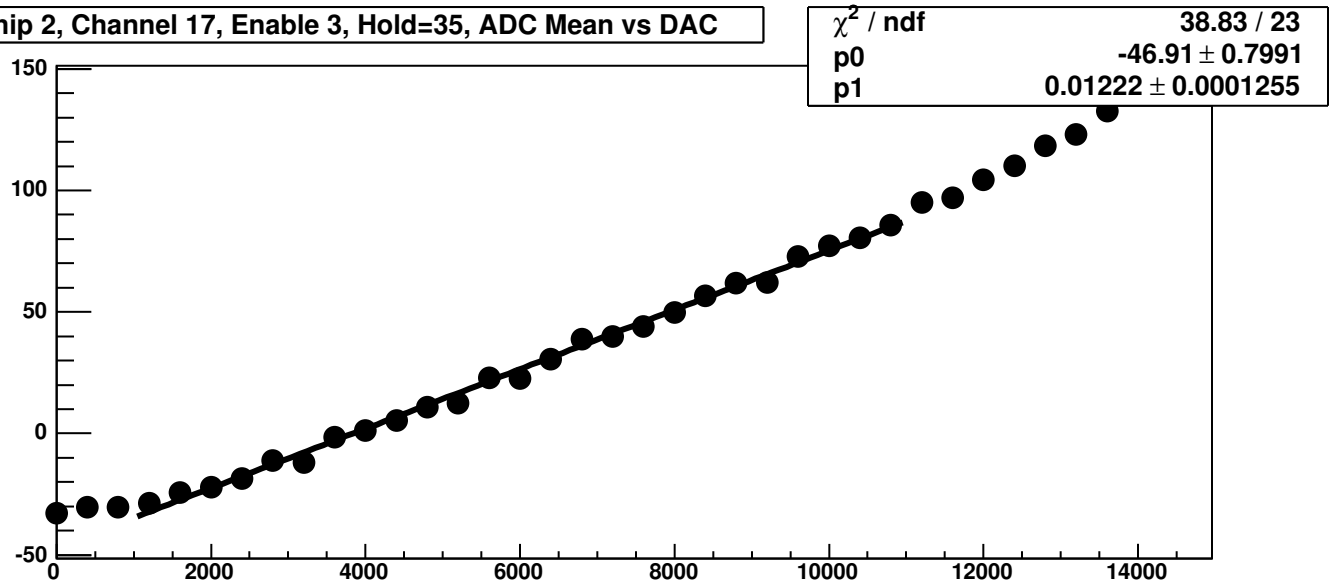



Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 205.5 / 23
p0 26.27 ± 2.797
p1 1.965 ± 0.001617
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

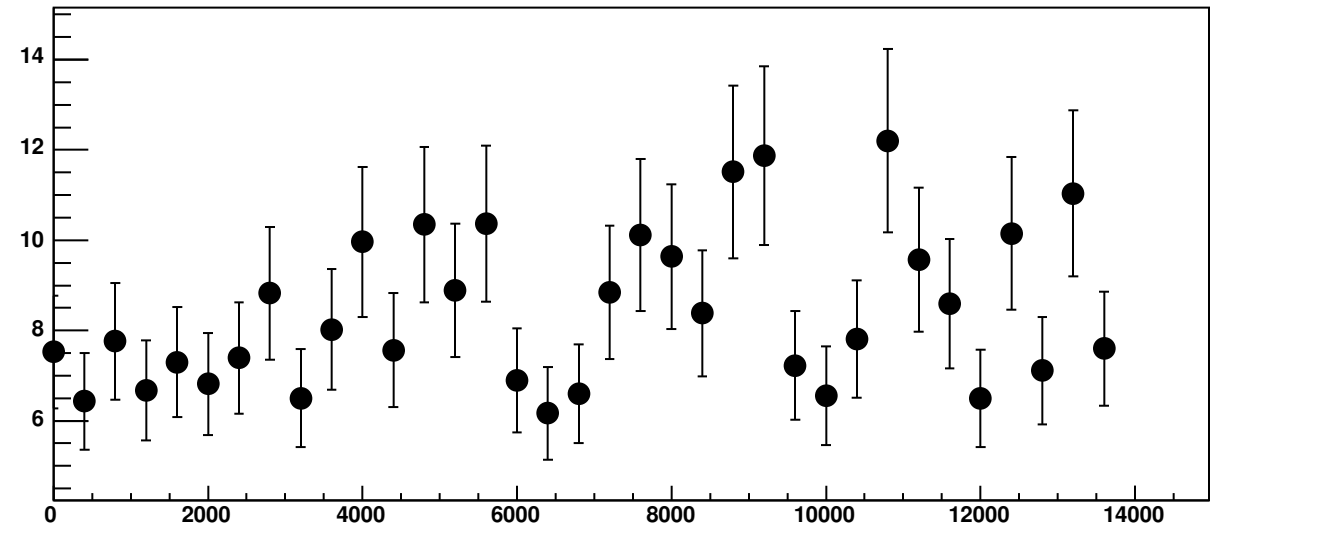

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

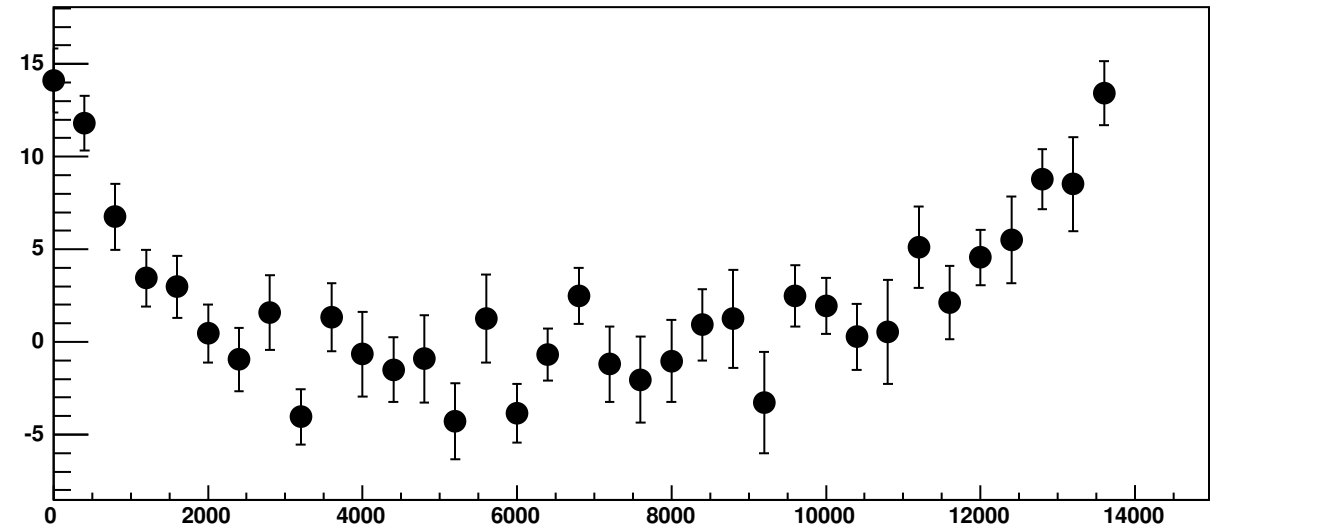
Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



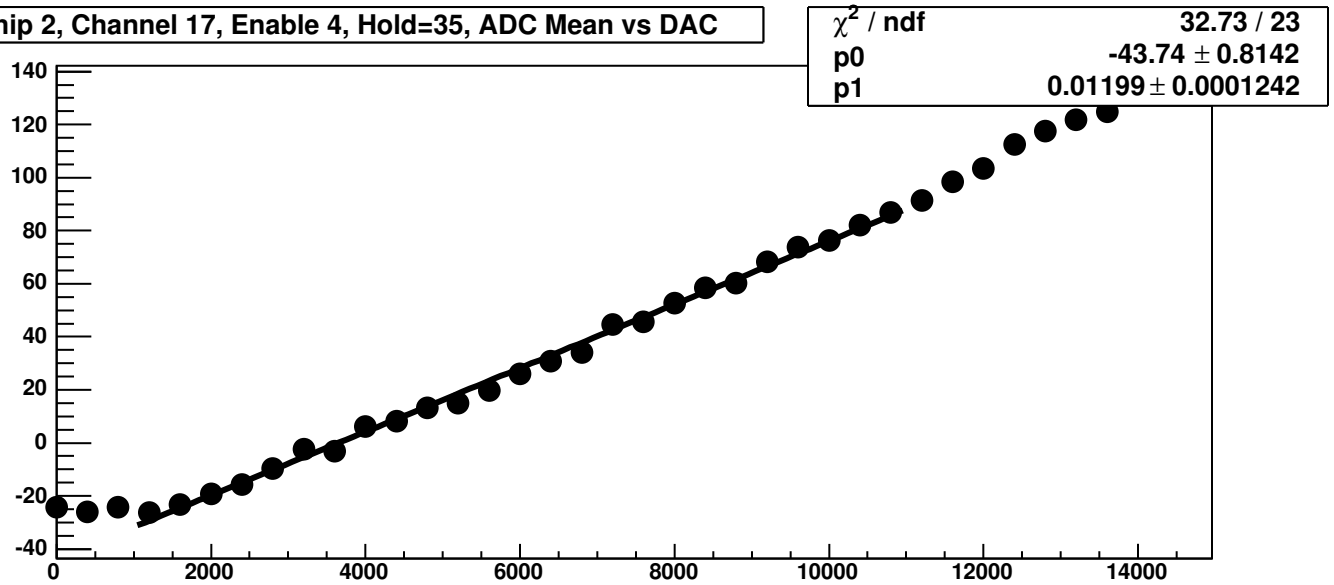
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



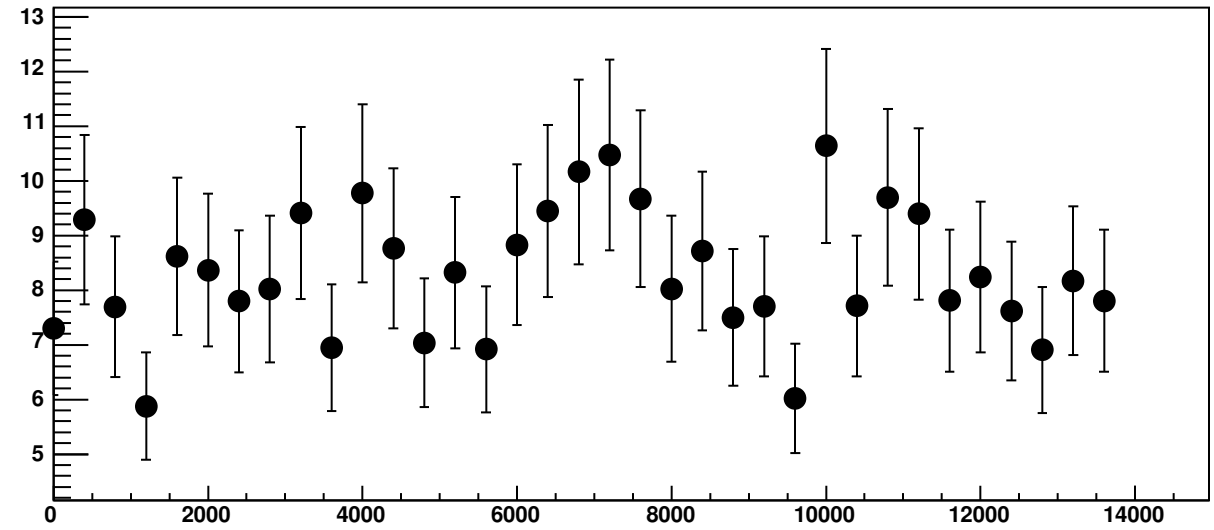
Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



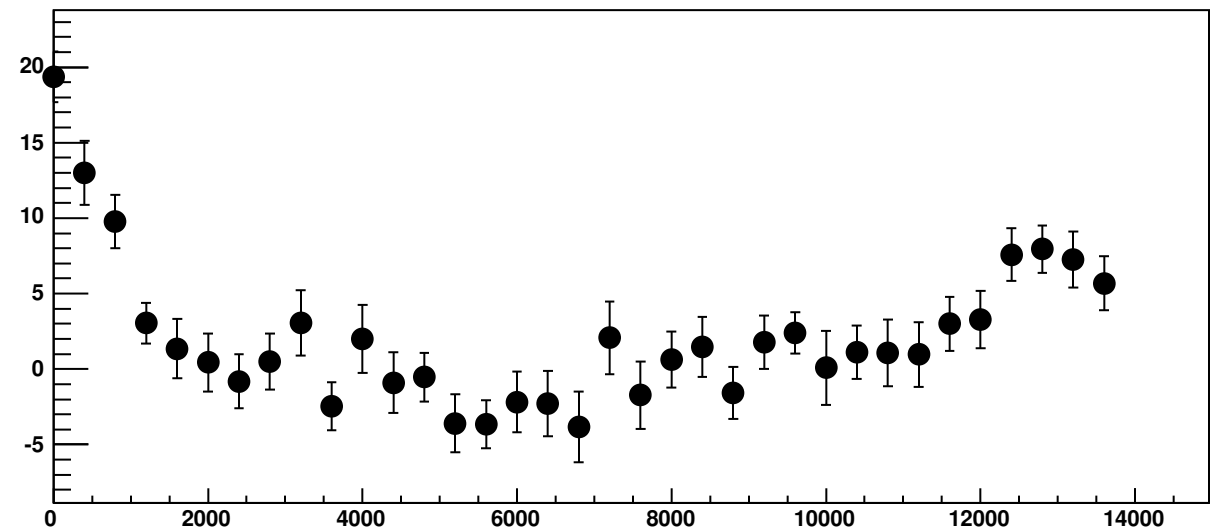
Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

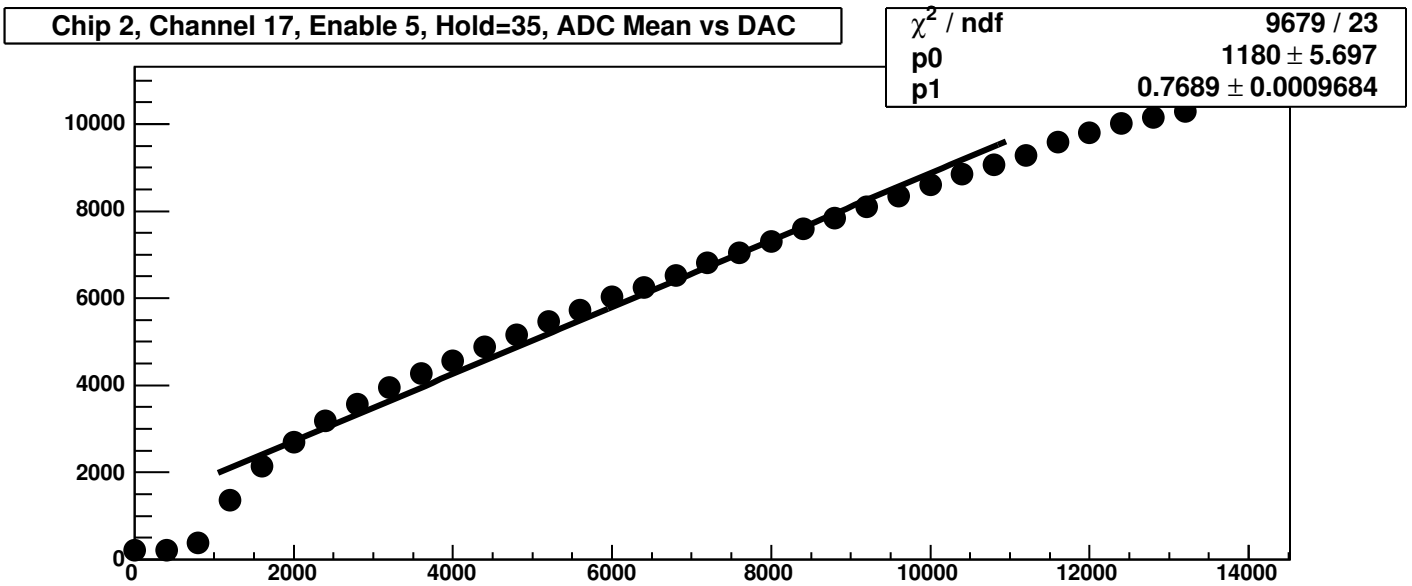


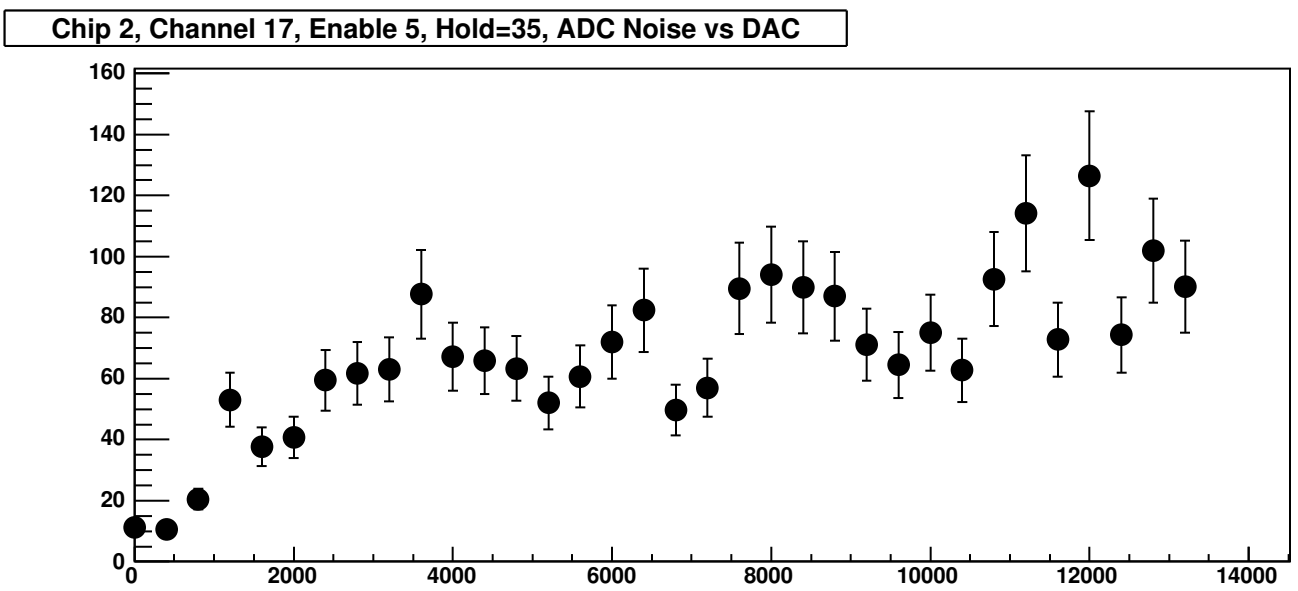
Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

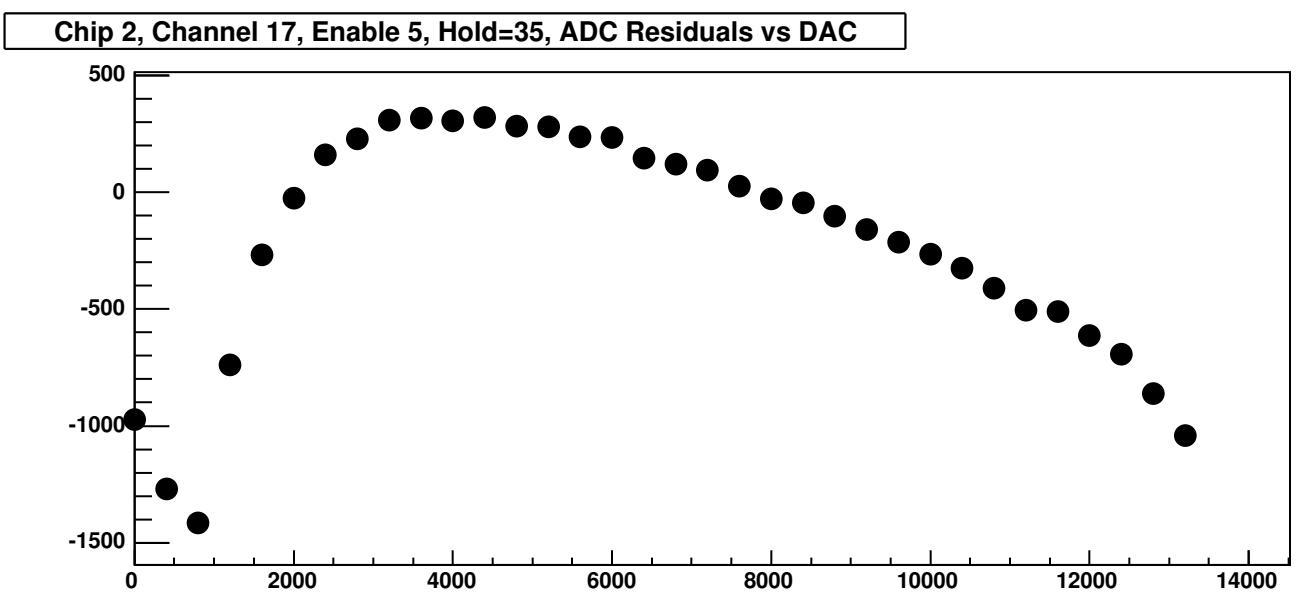


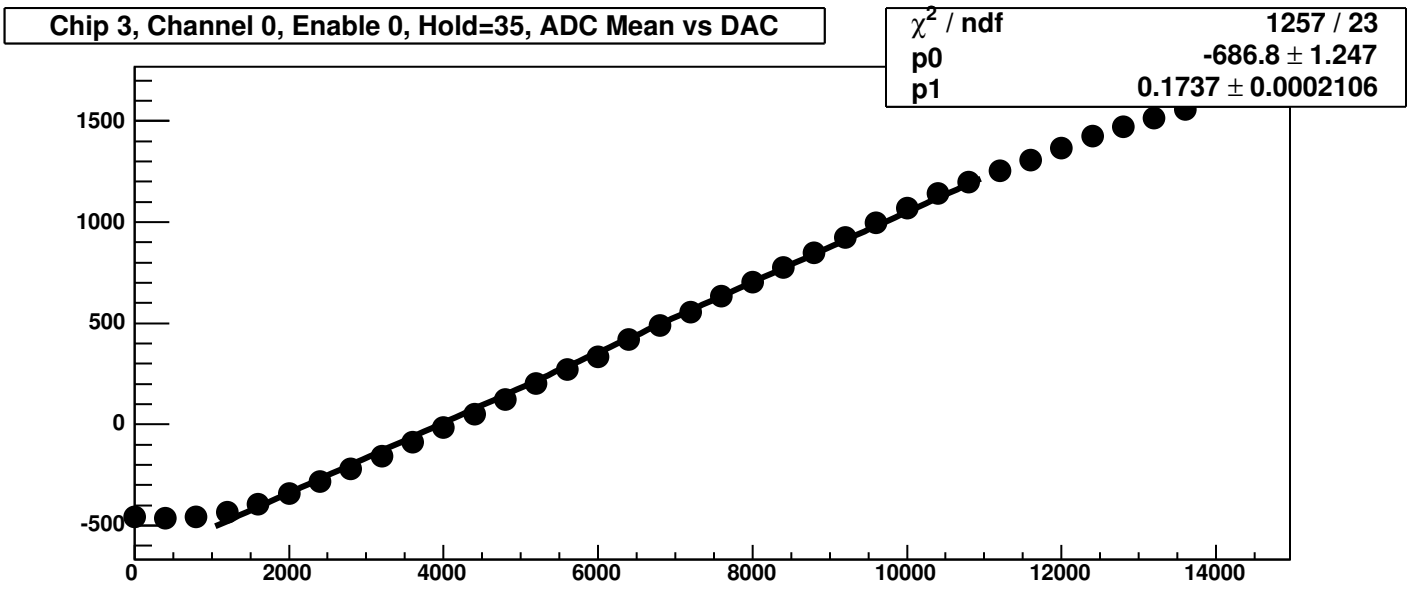
Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

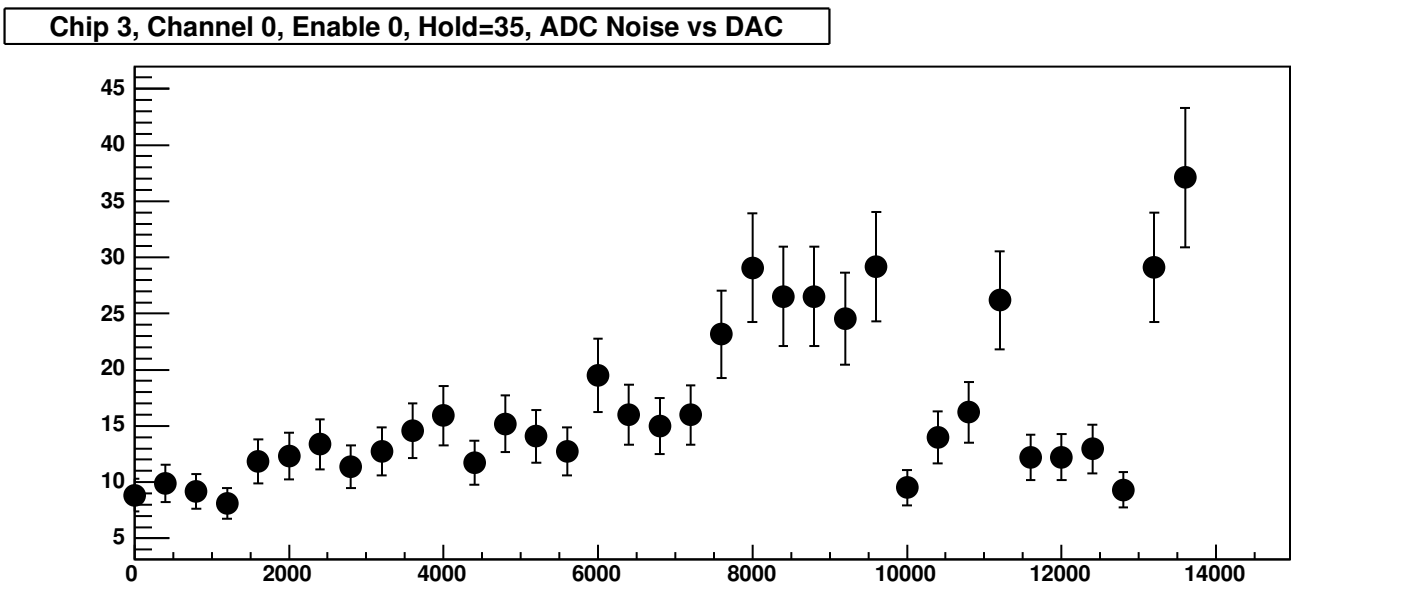


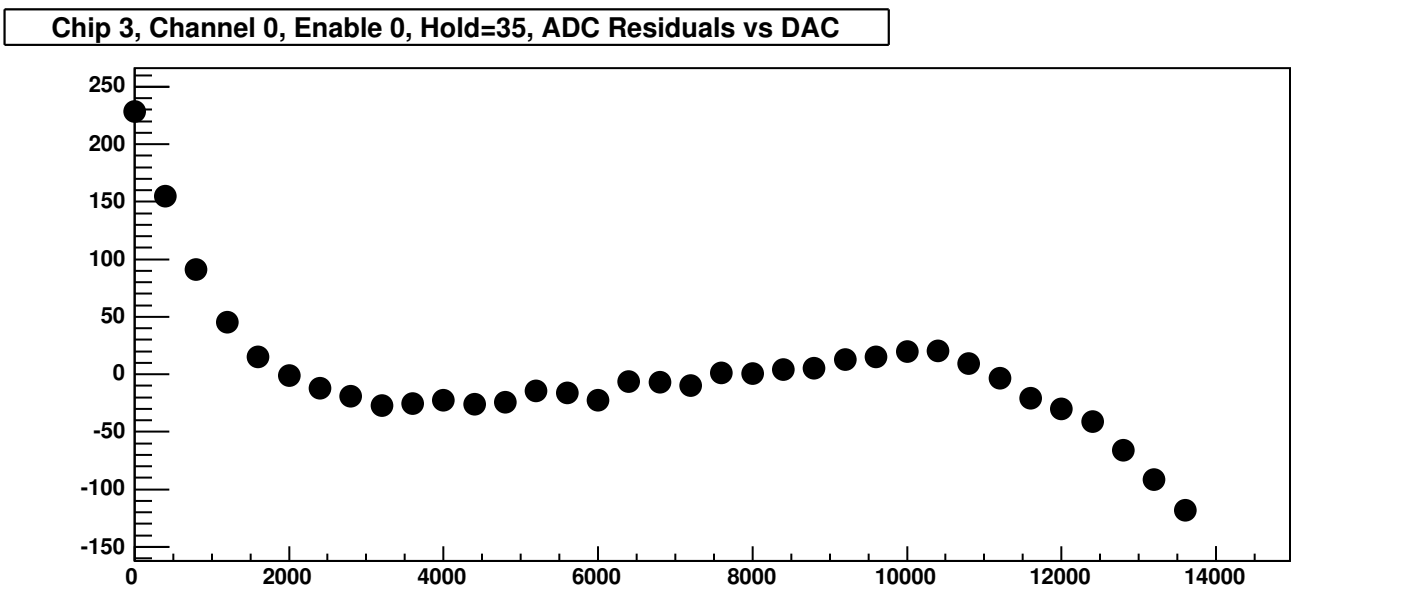

Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9679 / 23
p0 1180 ± 5.697
p1 0.7689 ± 0.0009684
0
2000
4000
6000
8000
10000
12000
14000

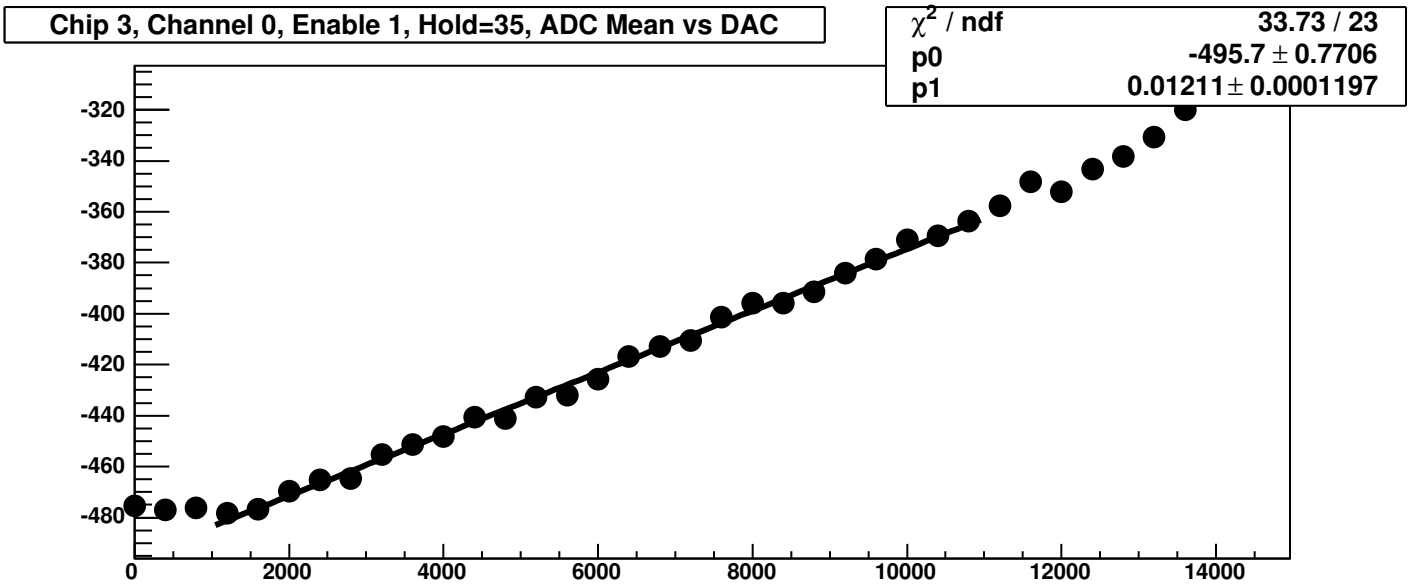

Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
0
20
40
60
80
100
120
140
160
2000
4000
6000
8000
10000
12000
14000

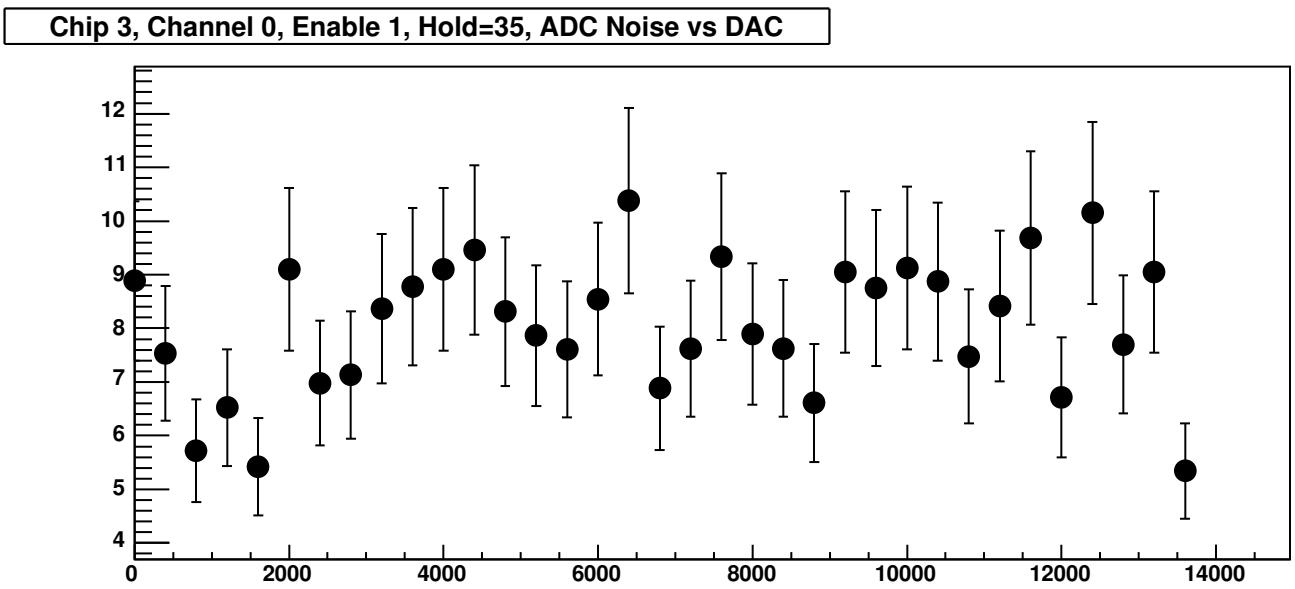

Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
500
0
-500
-1000
-1500
2000
4000
6000
8000
10000
12000
14000

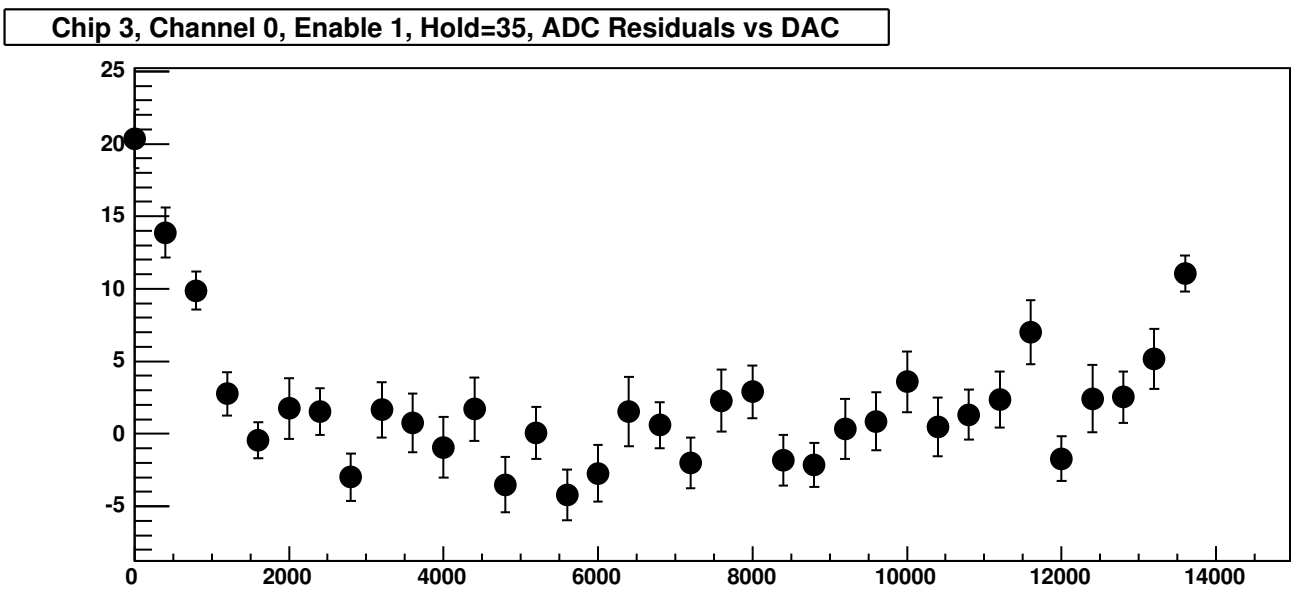

Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1257 / 23
p0 -686.8 ± 1.247
p1 0.1737 ± 0.0002106
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

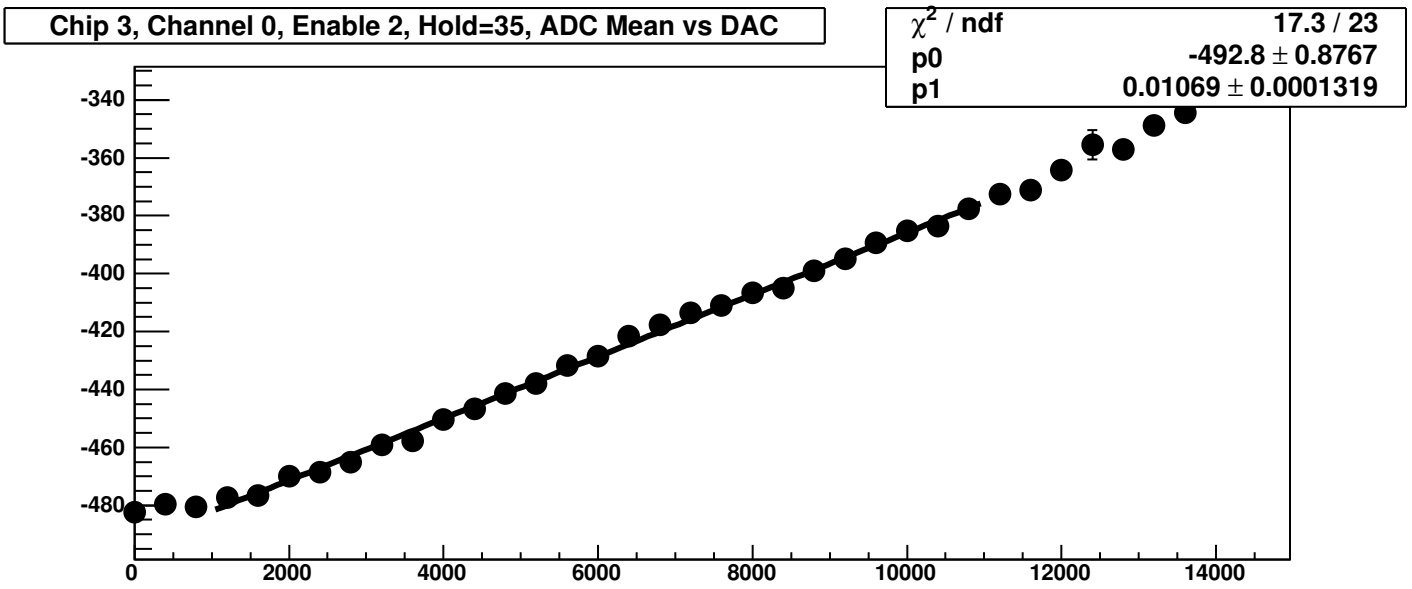

Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

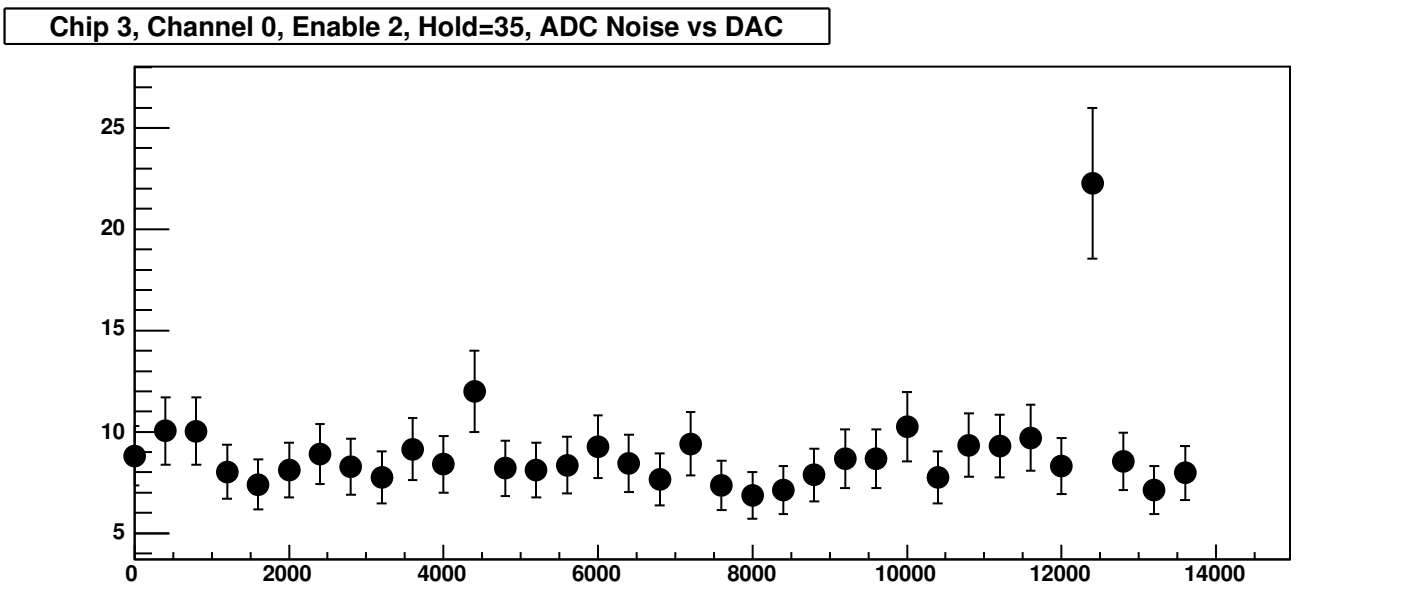

Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

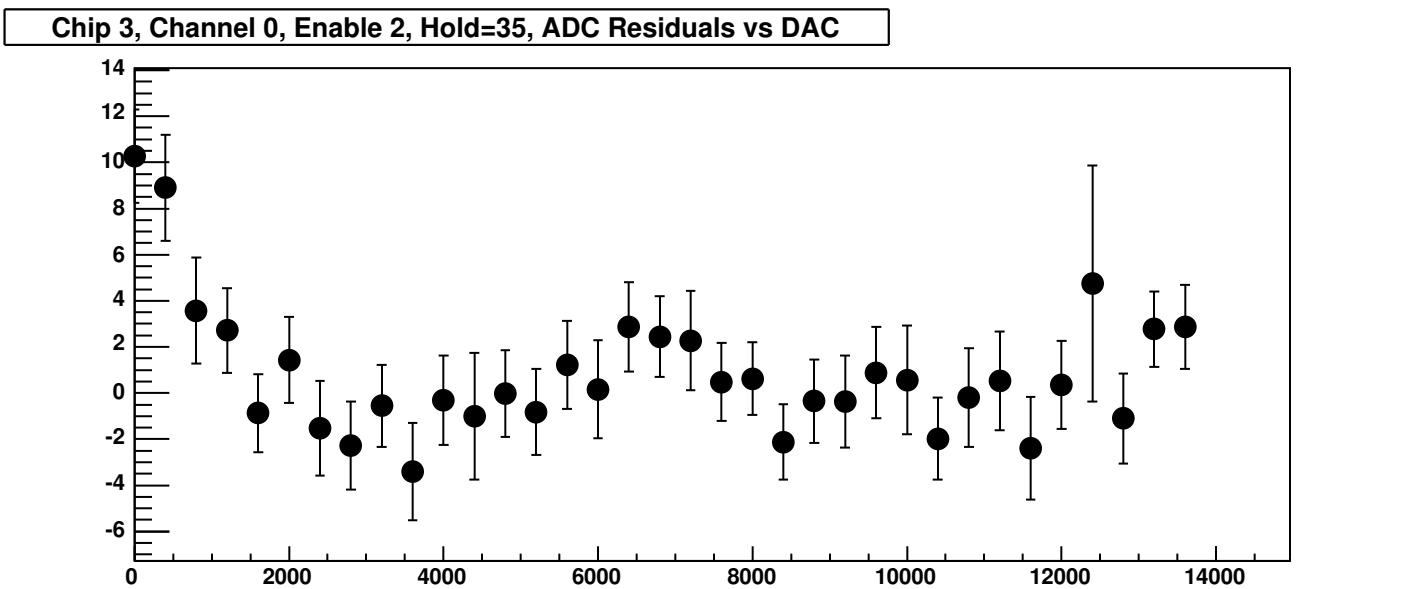

Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.73 / 23
p0 -495.7 ± 0.7706
p1 0.01211 ± 0.0001197
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

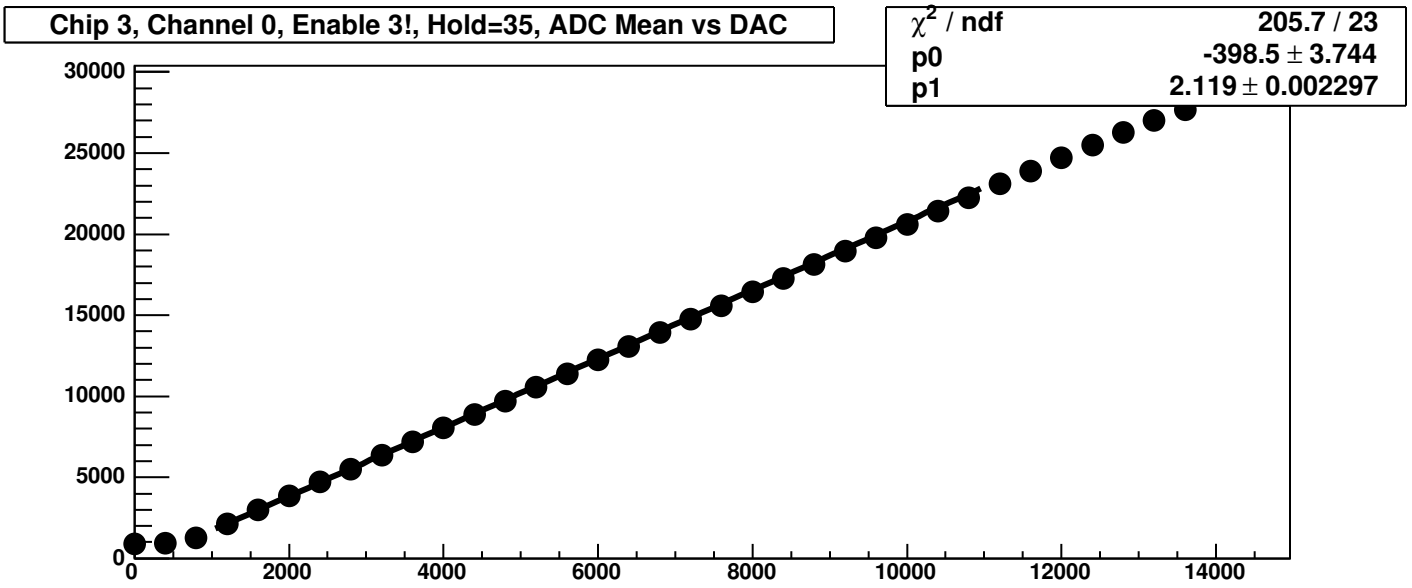

Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

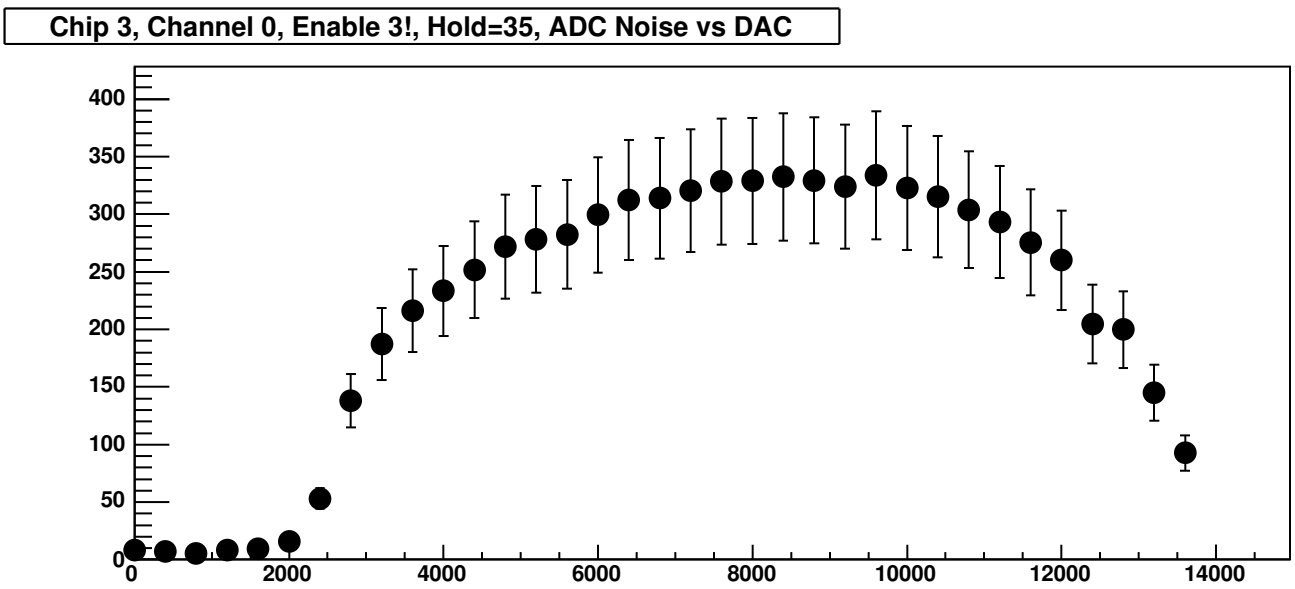

Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

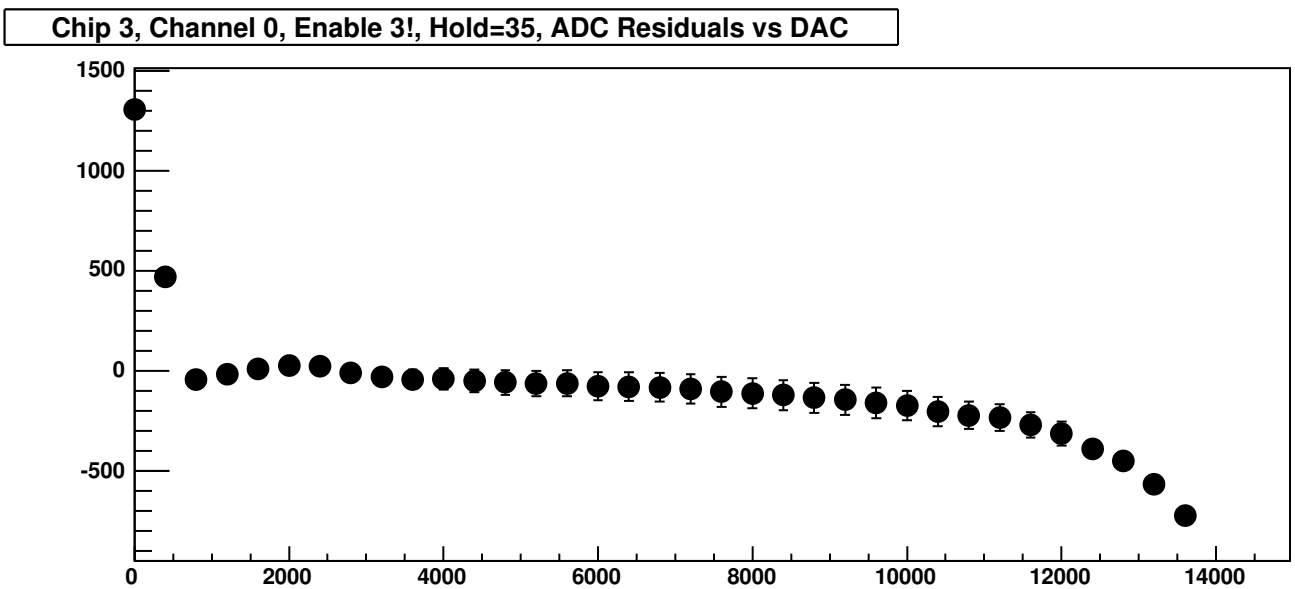

Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.3 / 23
p0 -492.8 ± 0.8767
p1 0.01069 ± 0.0001319
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

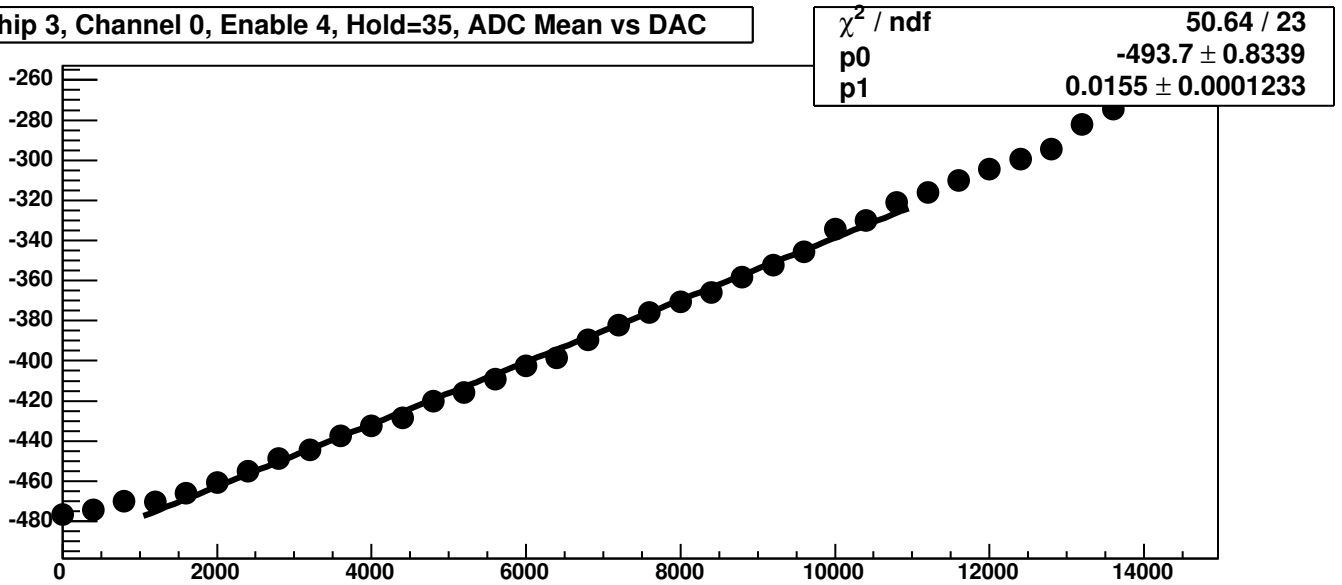

Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 205.7 / 23
p0 -398.5 ± 3.744
p1 2.119 ± 0.002297
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

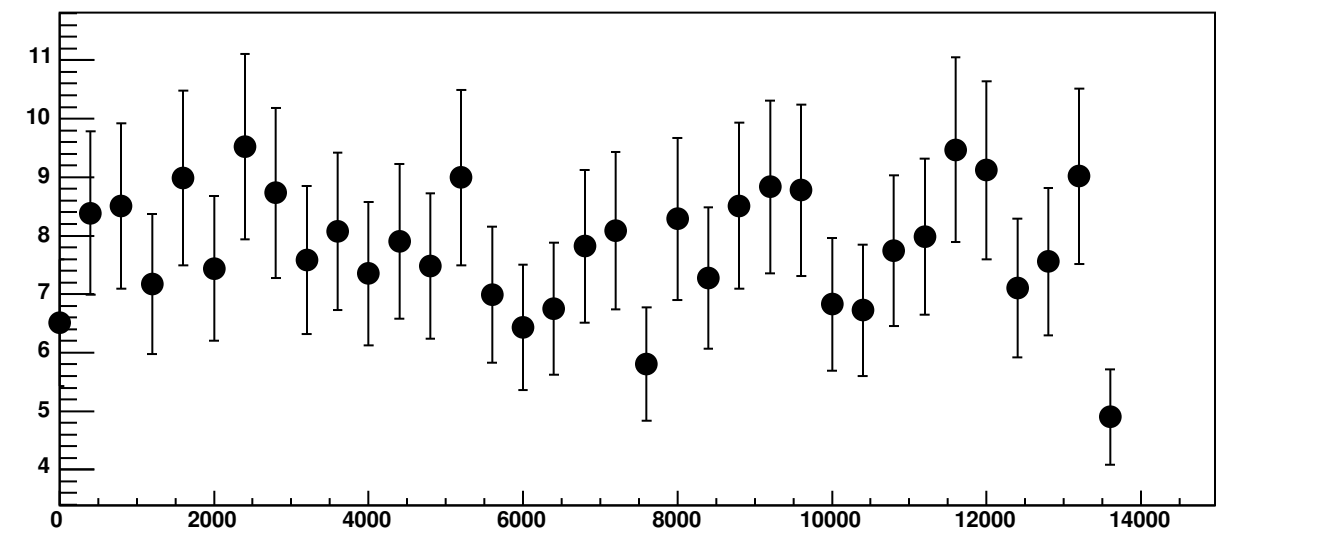

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

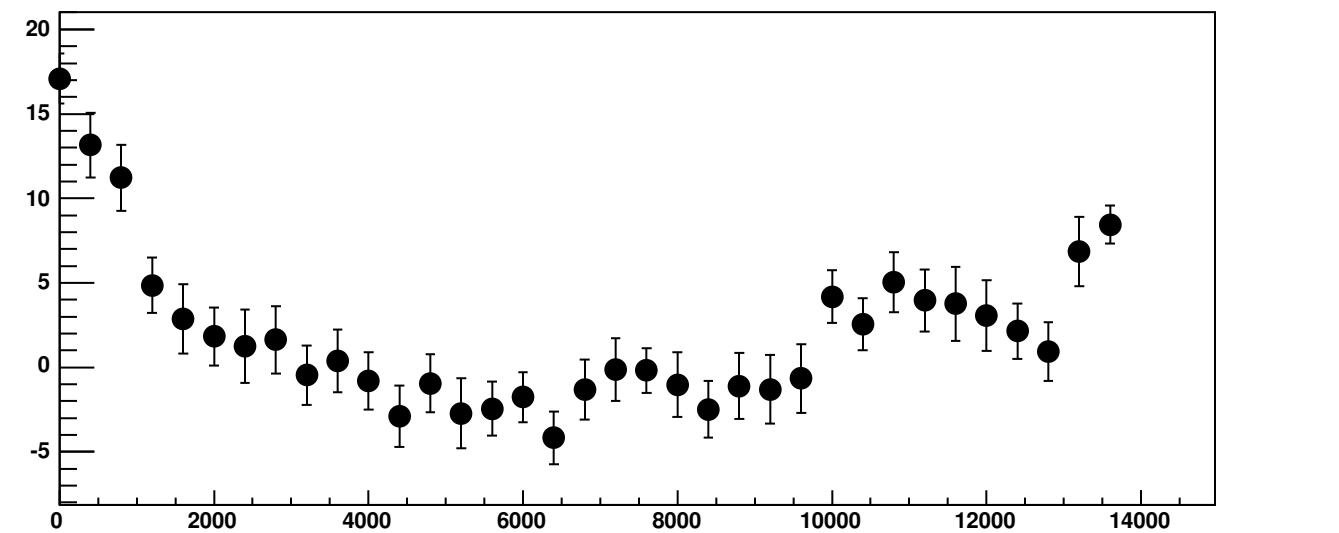
Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

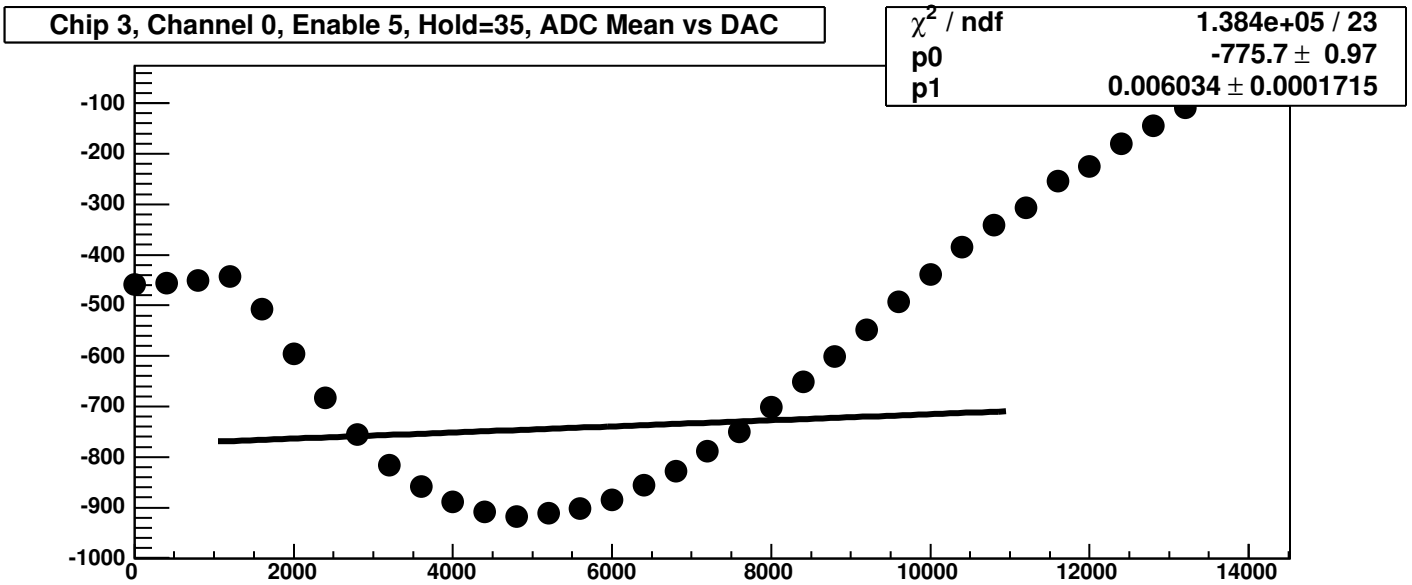


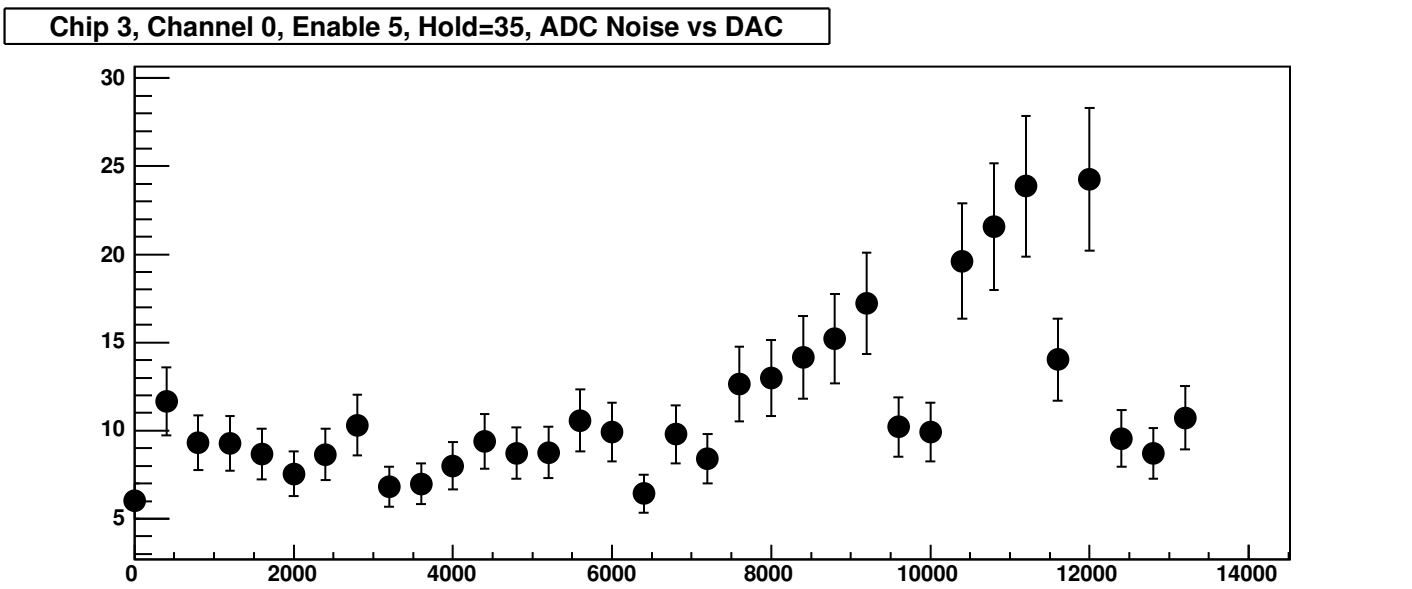
Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

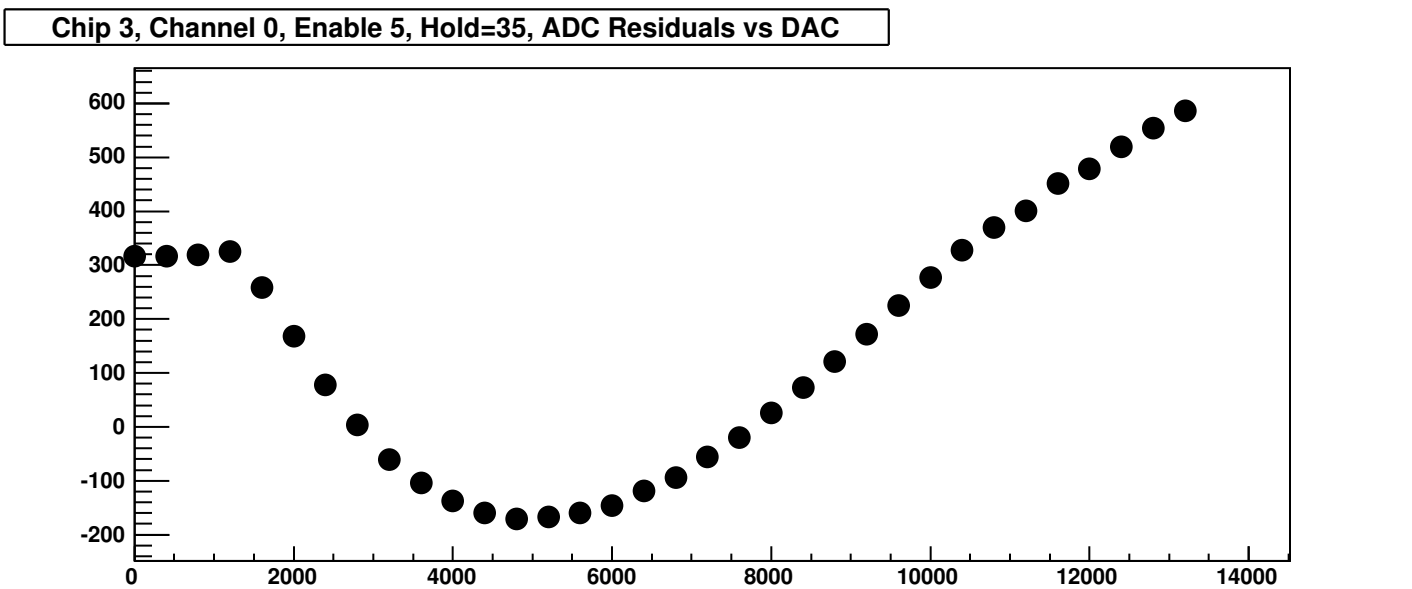


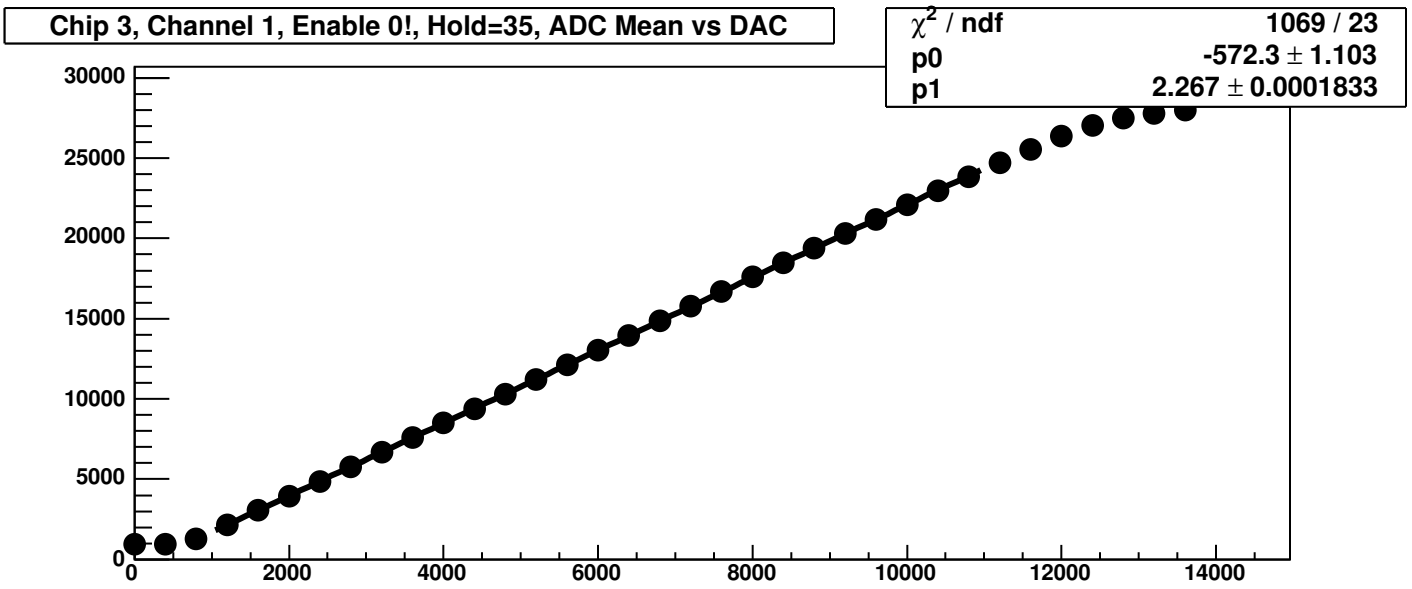
Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

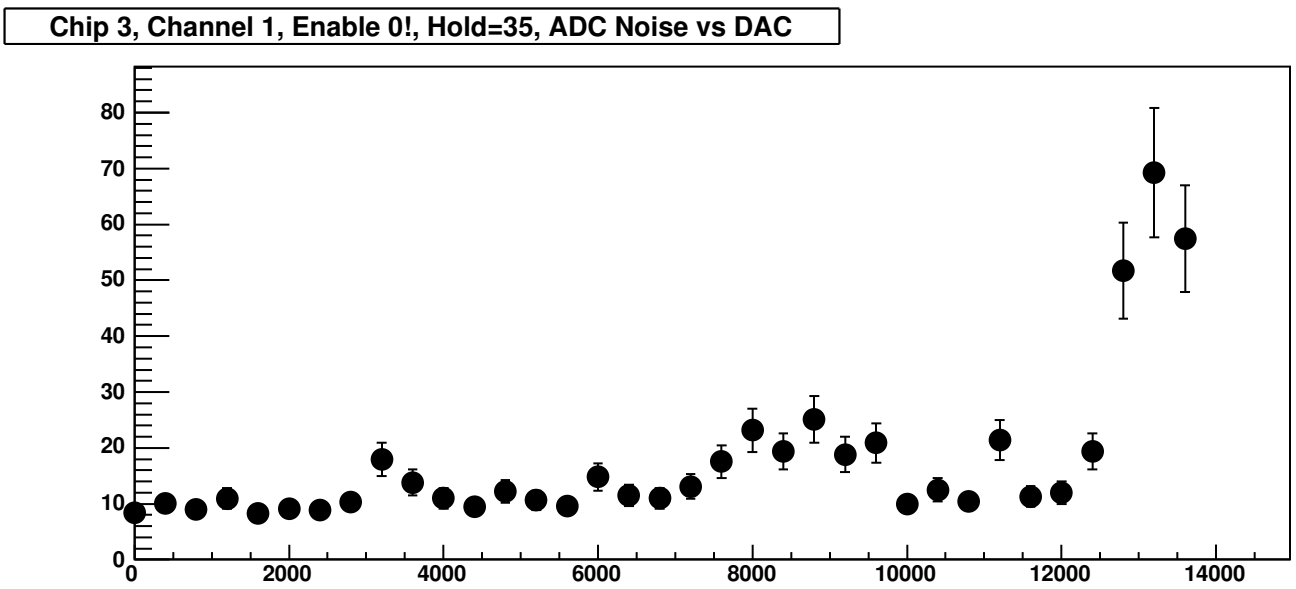


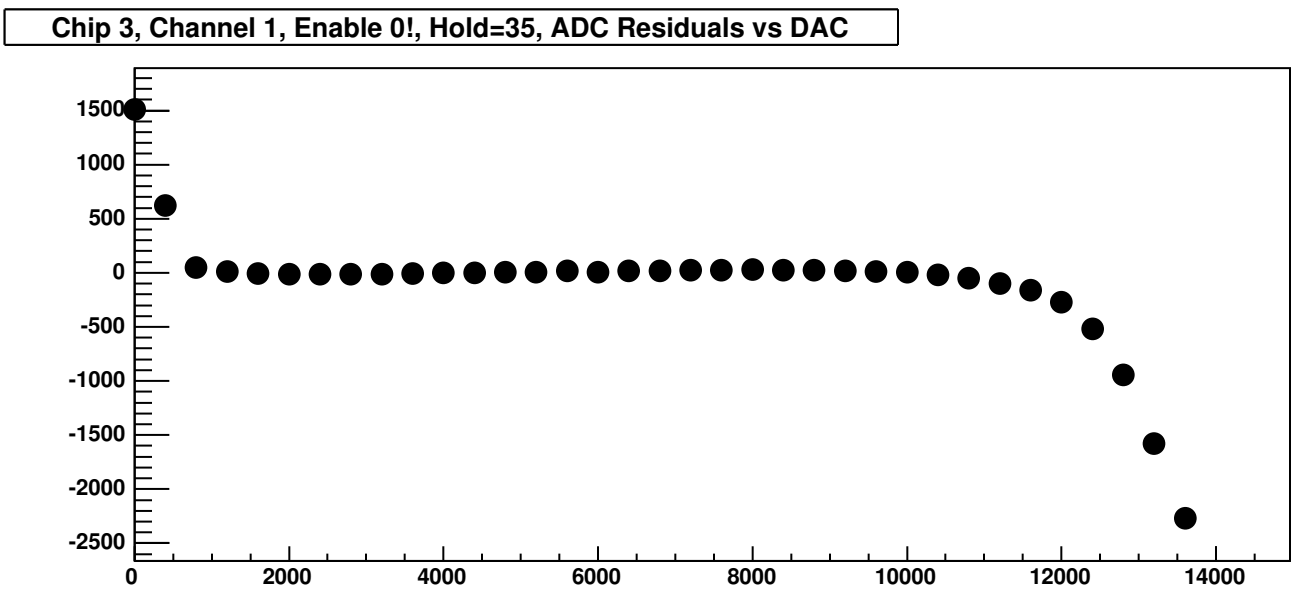

Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.384e+05 / 23
p0 -775.7 ± 0.97
p1 0.006034 ± 0.0001715
-100
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
2000
4000
6000
8000
10000
12000
14000

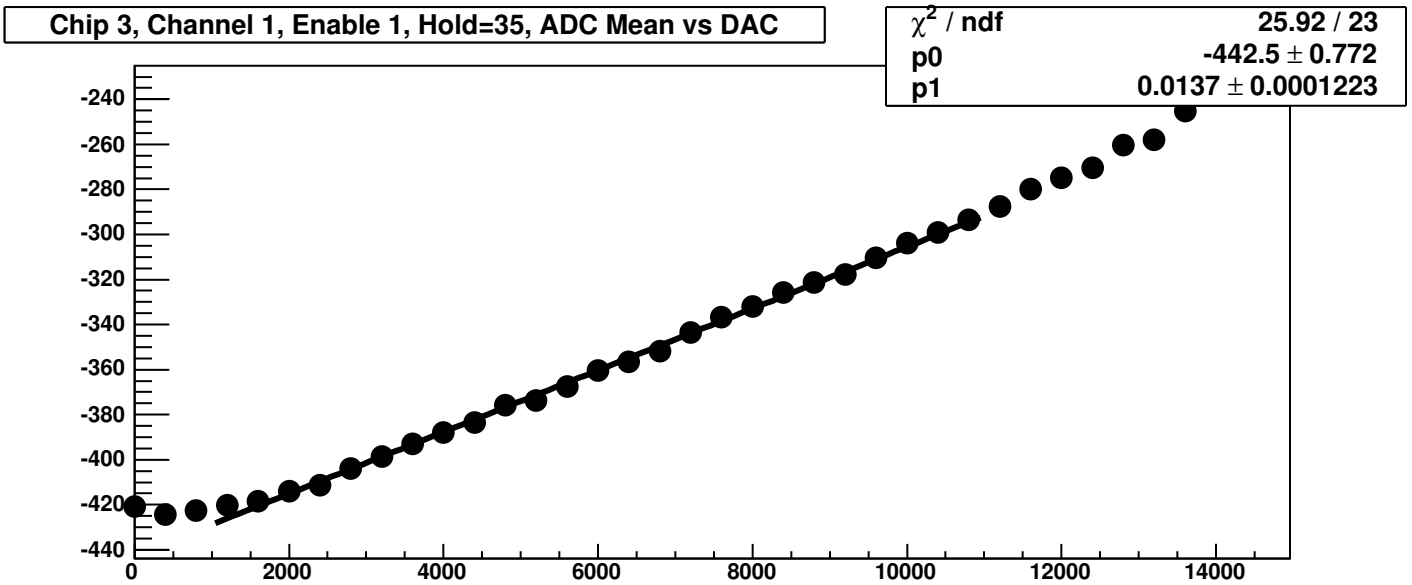

Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

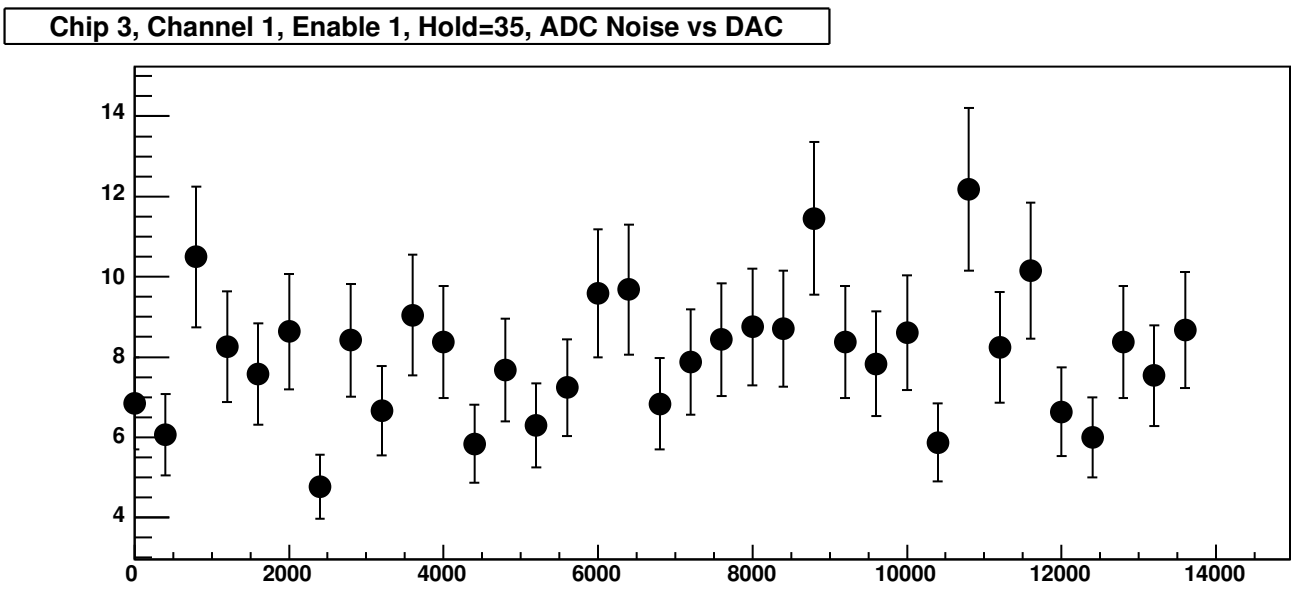

Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

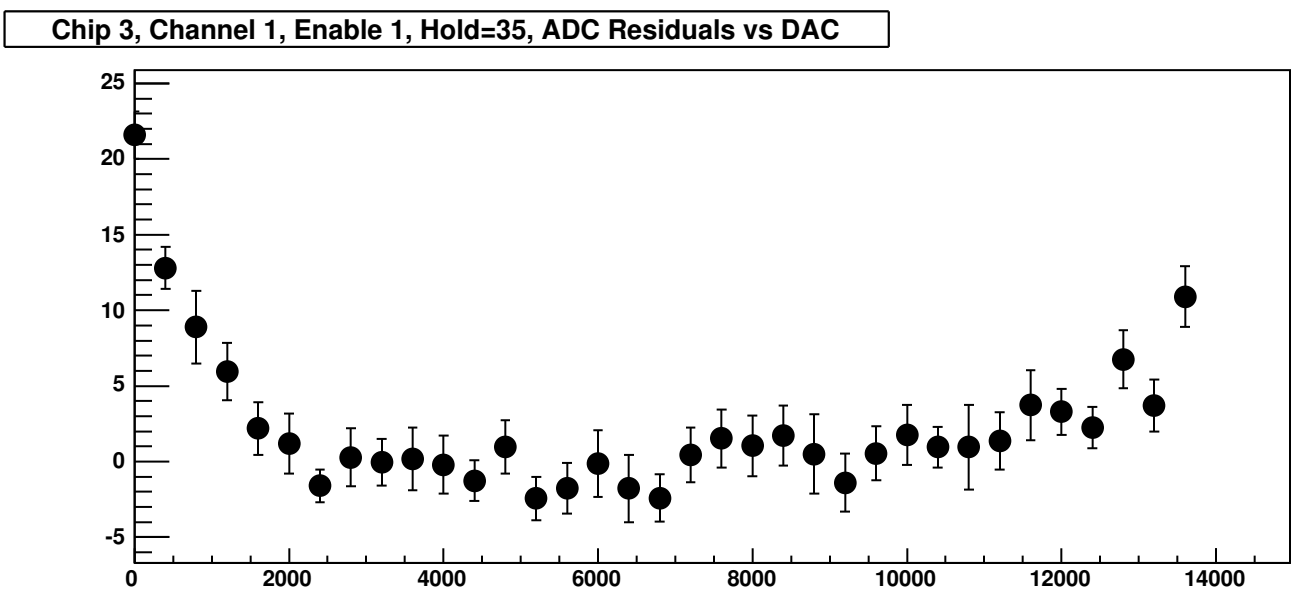

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1069 / 23
p0 -572.3 ± 1.103
p1 2.267 ± 0.0001833
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

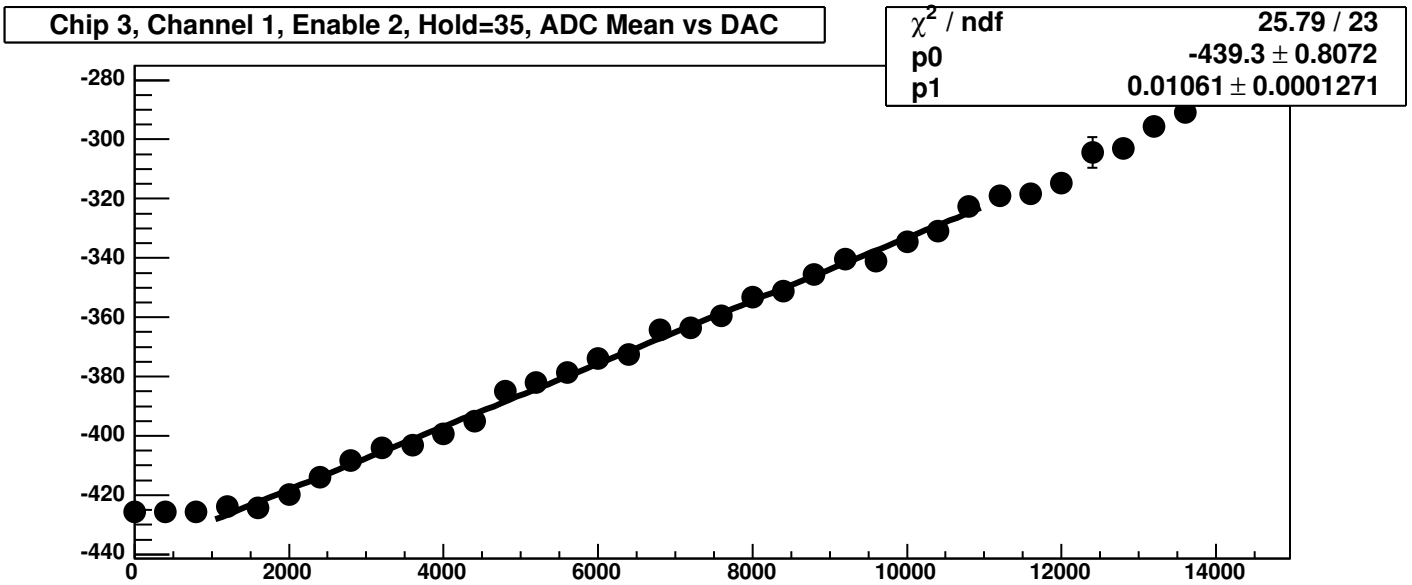

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

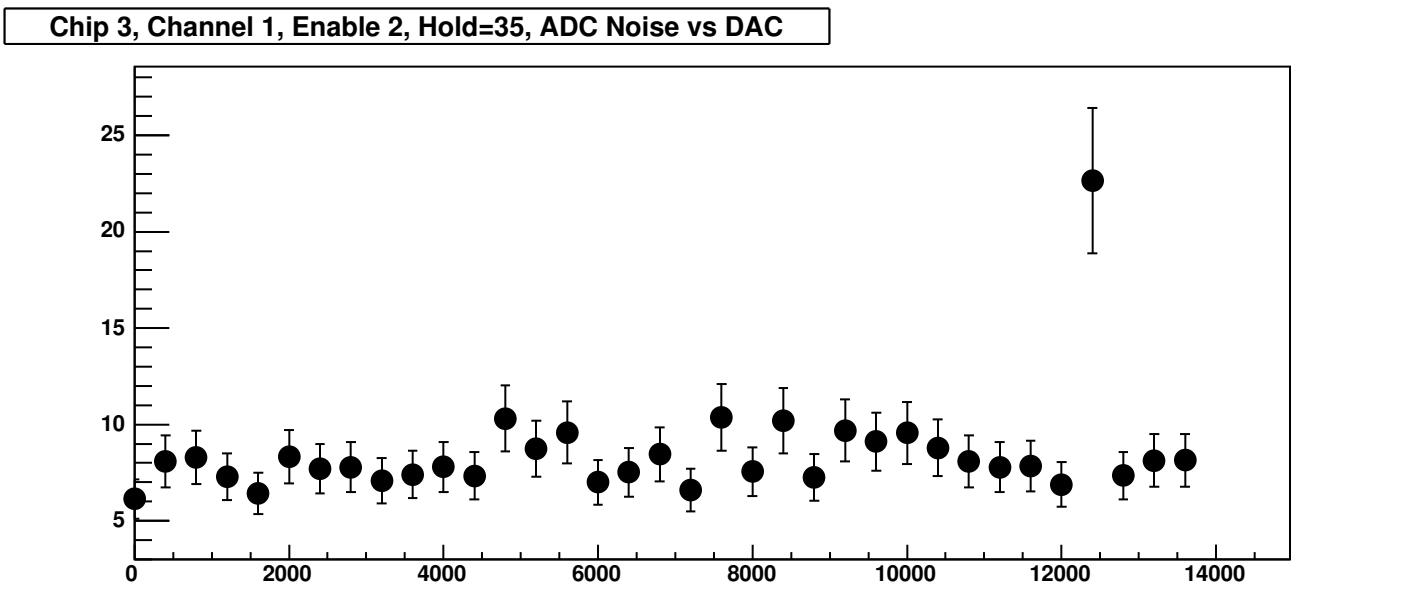

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

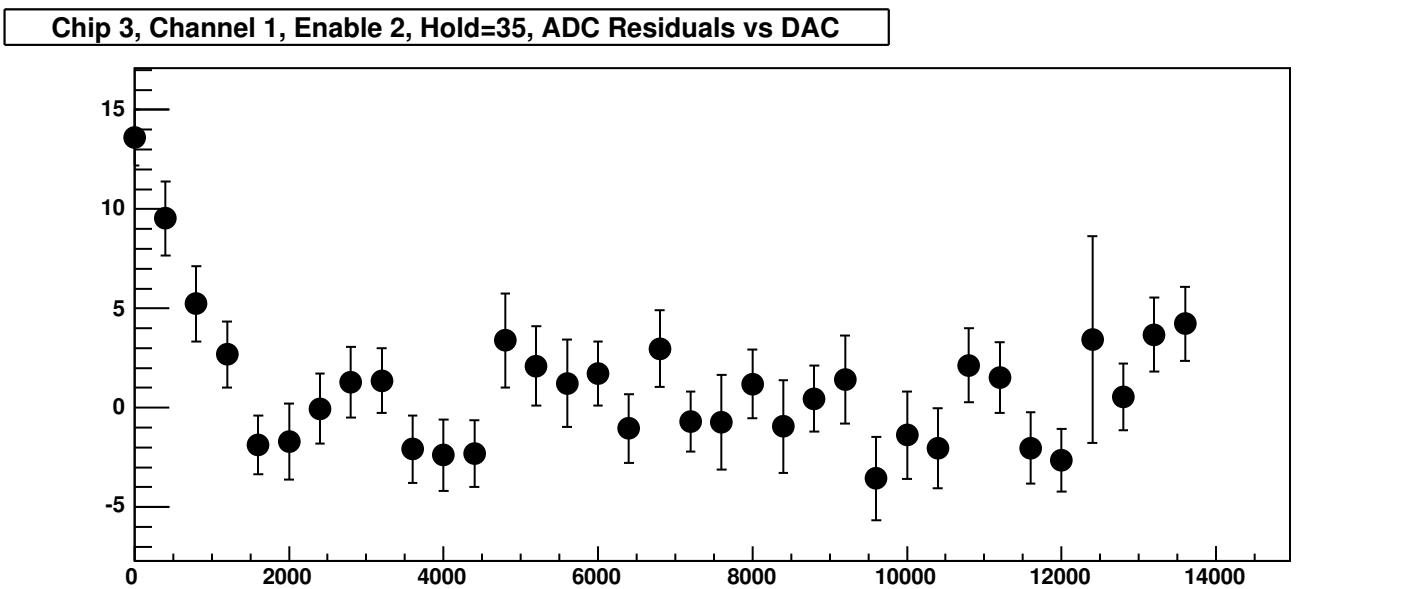

Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.92 / 23
p0 -442.5 ± 0.772
p1 0.0137 ± 0.0001223
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

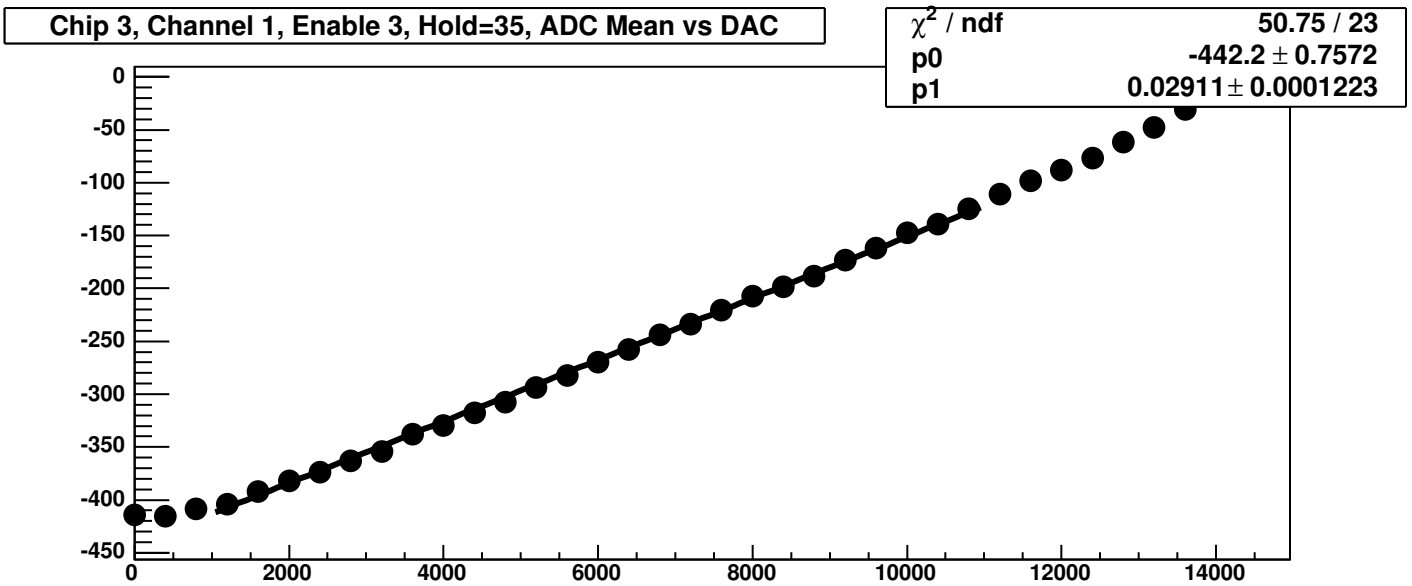

Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

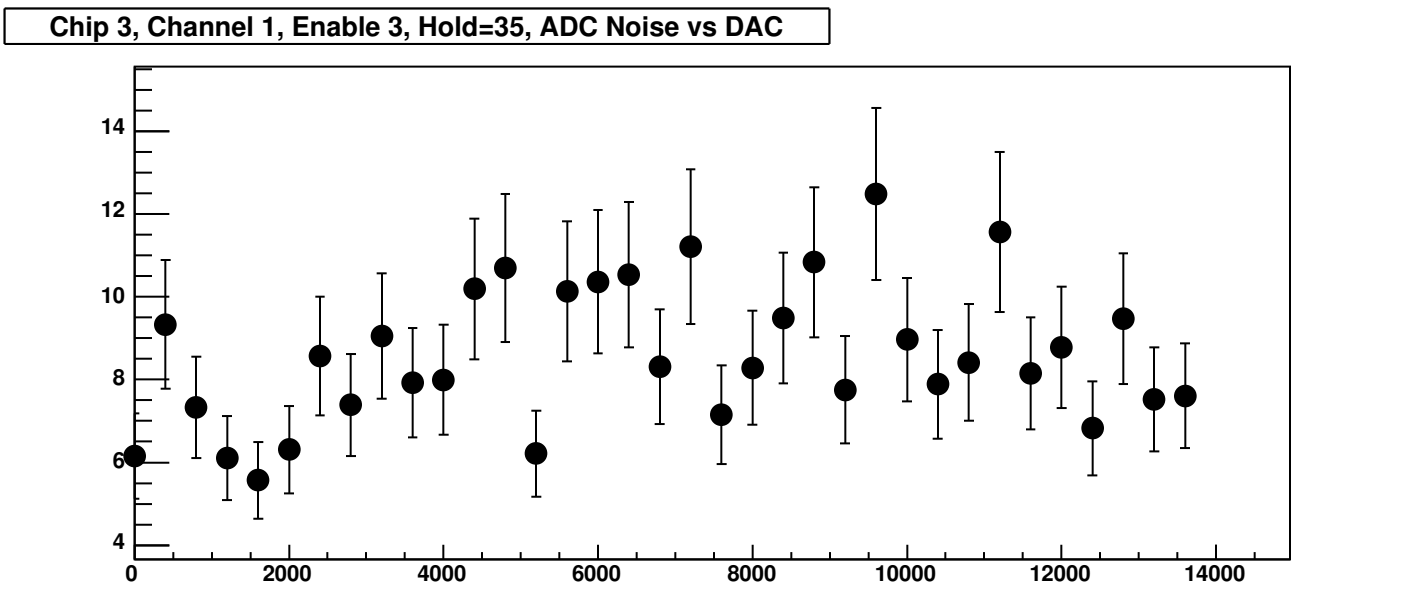

Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

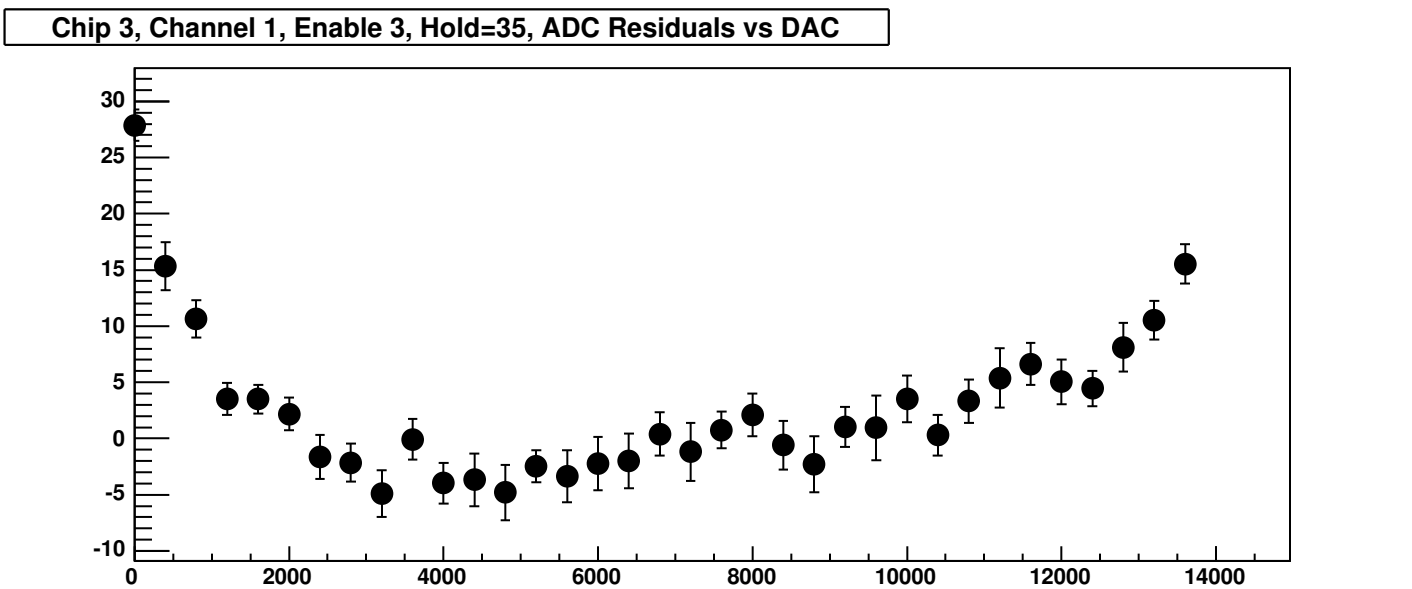

Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.79 / 23
p0 -439.3 ± 0.8072
p1 0.01061 ± 0.0001271
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

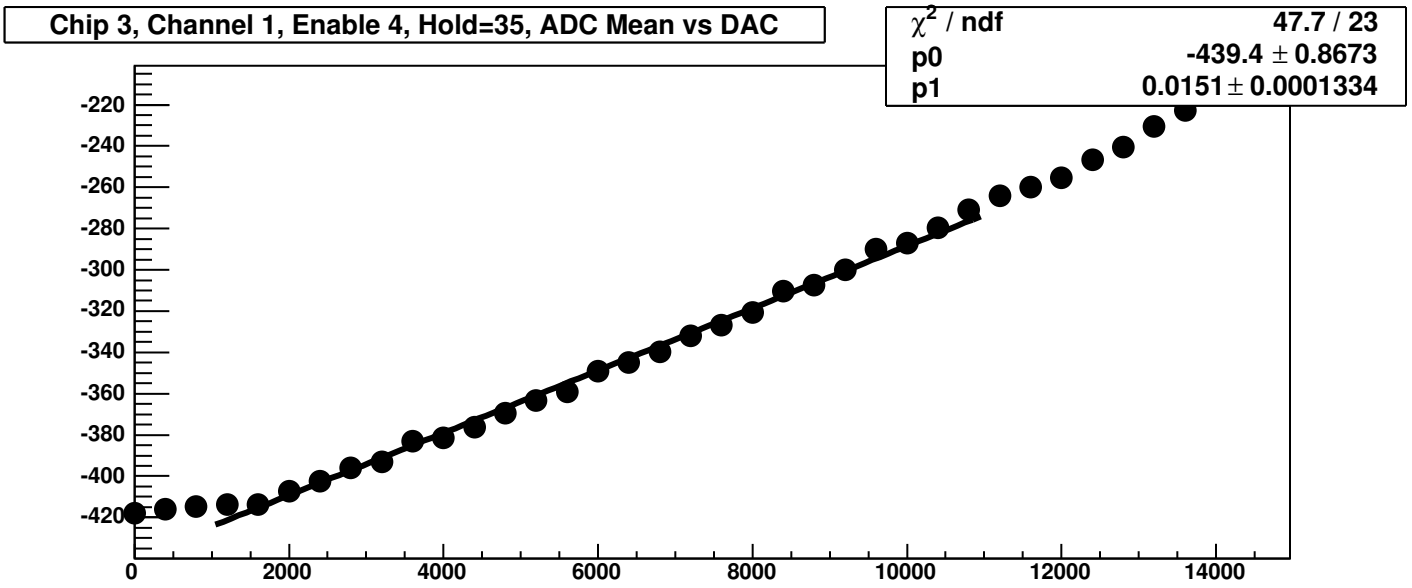

Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

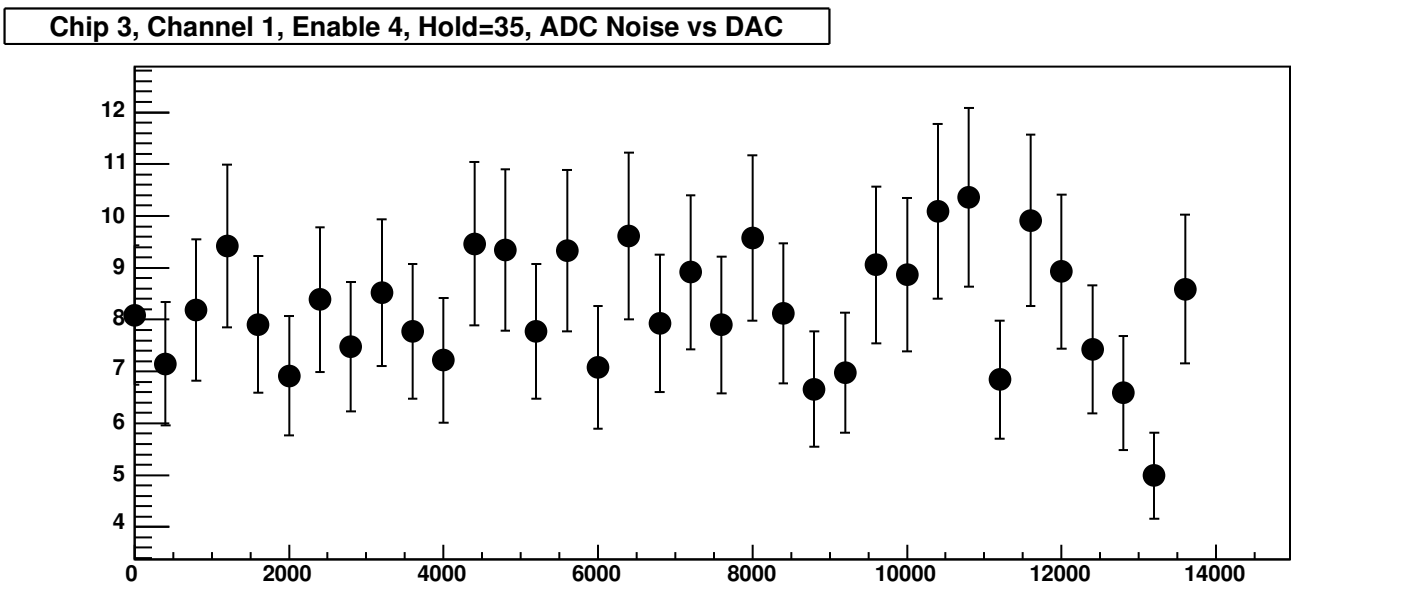

Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

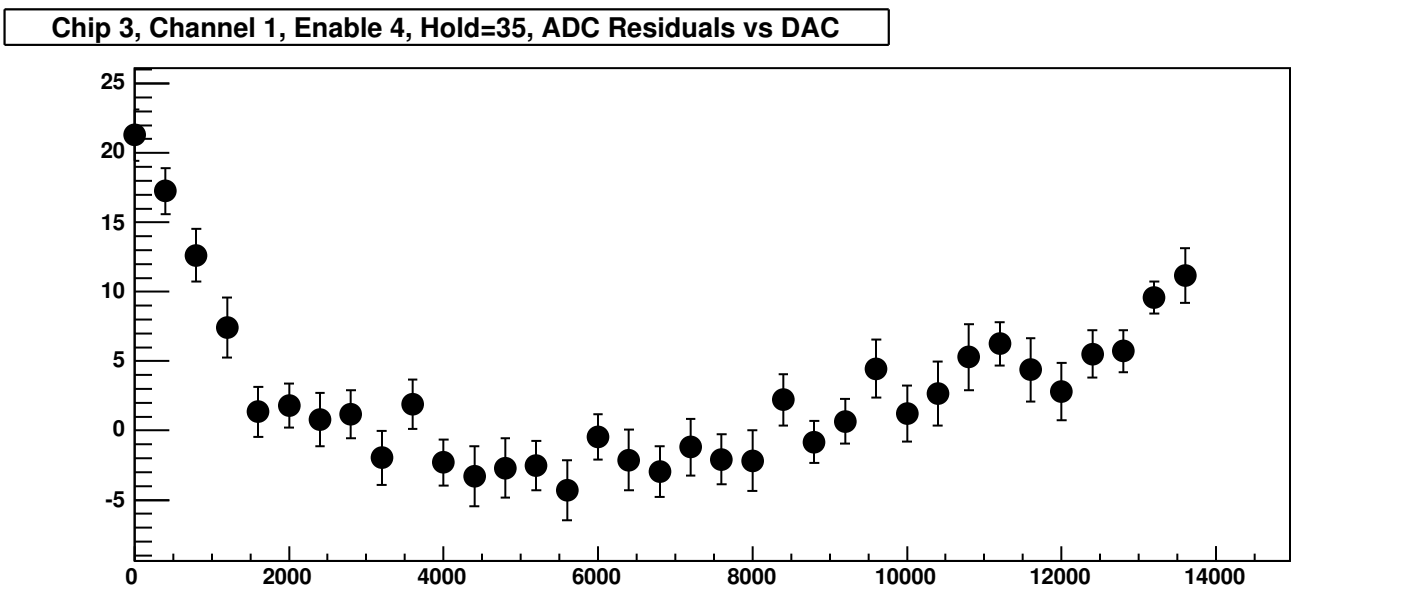

Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.75 / 23
p0 -442.2 ± 0.7572
p1 0.02911 ± 0.0001223
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

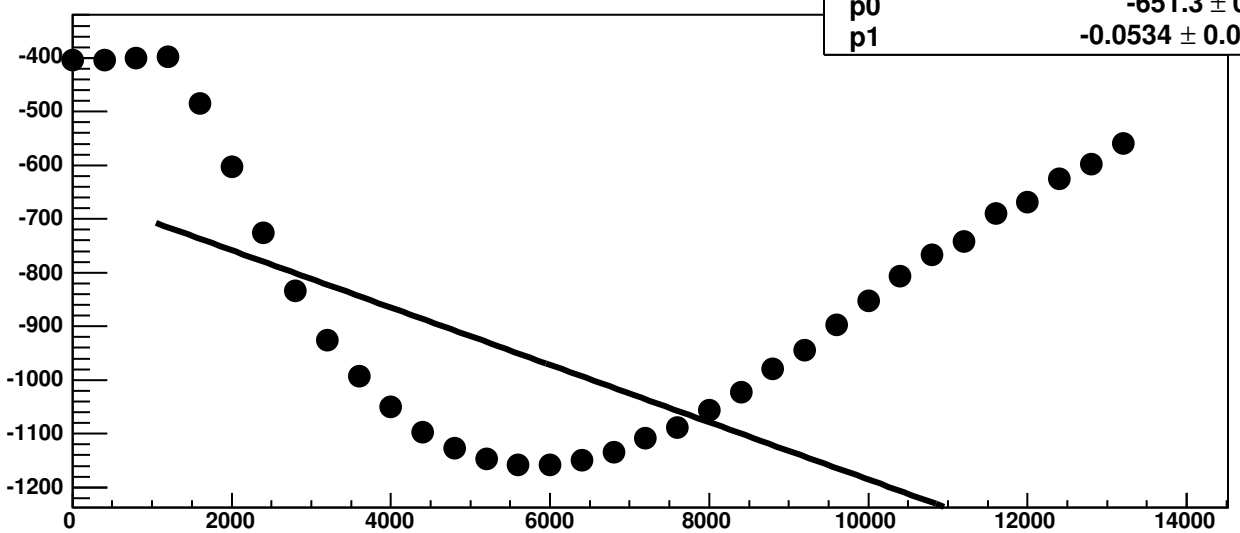

Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.7 / 23
p0 -439.4 ± 0.8673
p1 0.0151 ± 0.0001334
-220
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

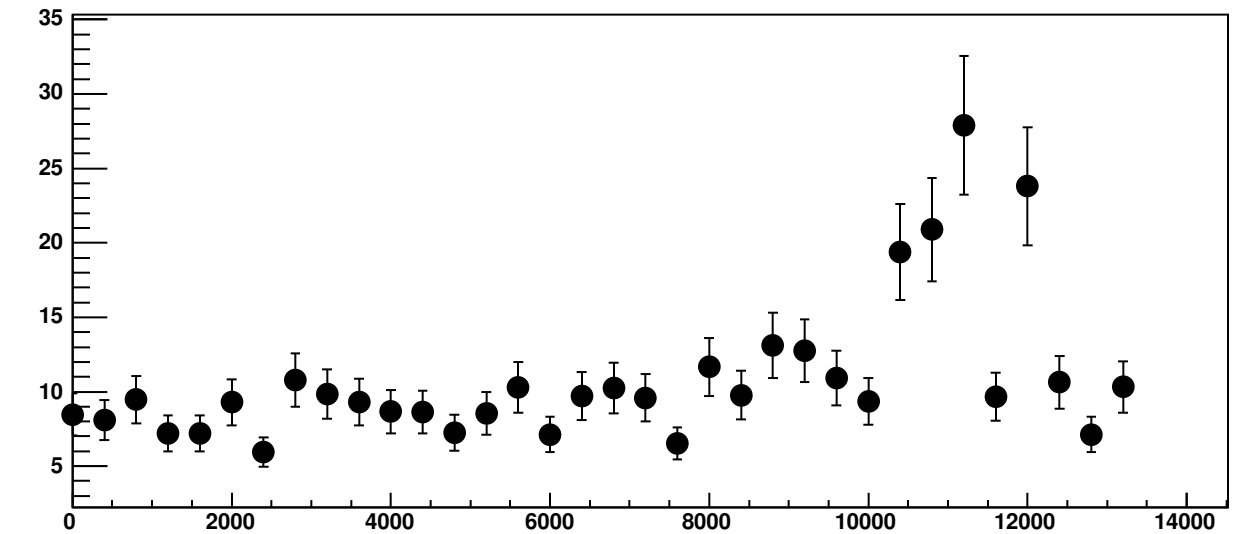

Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

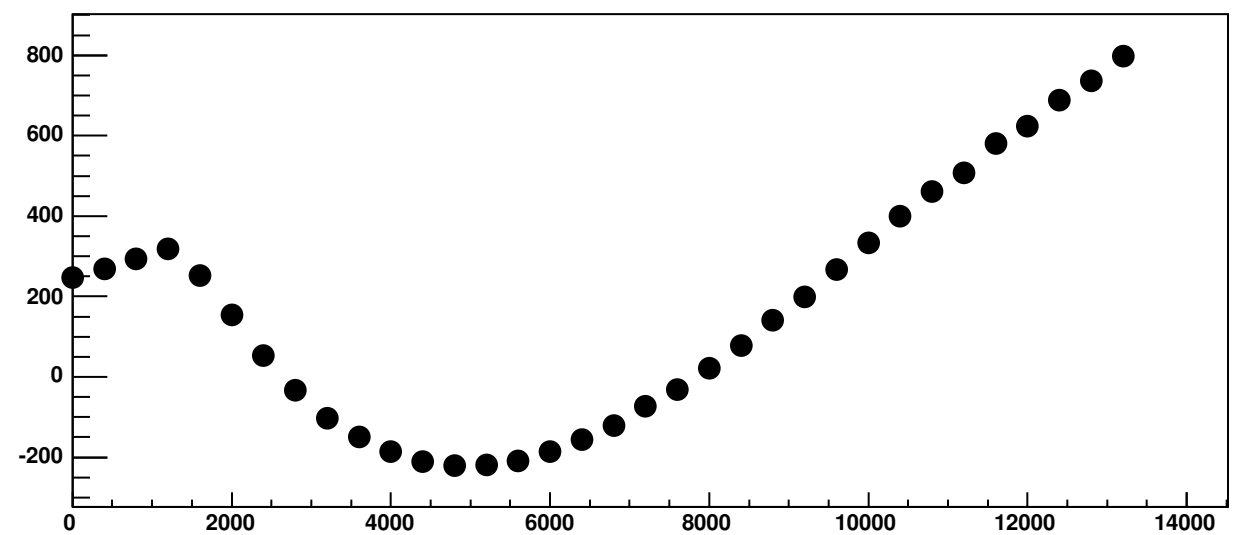
χ^2 / ndf	2.149e+05 / 23
p0	-651.3 ± 0.9006
p1	-0.0534 ± 0.000155



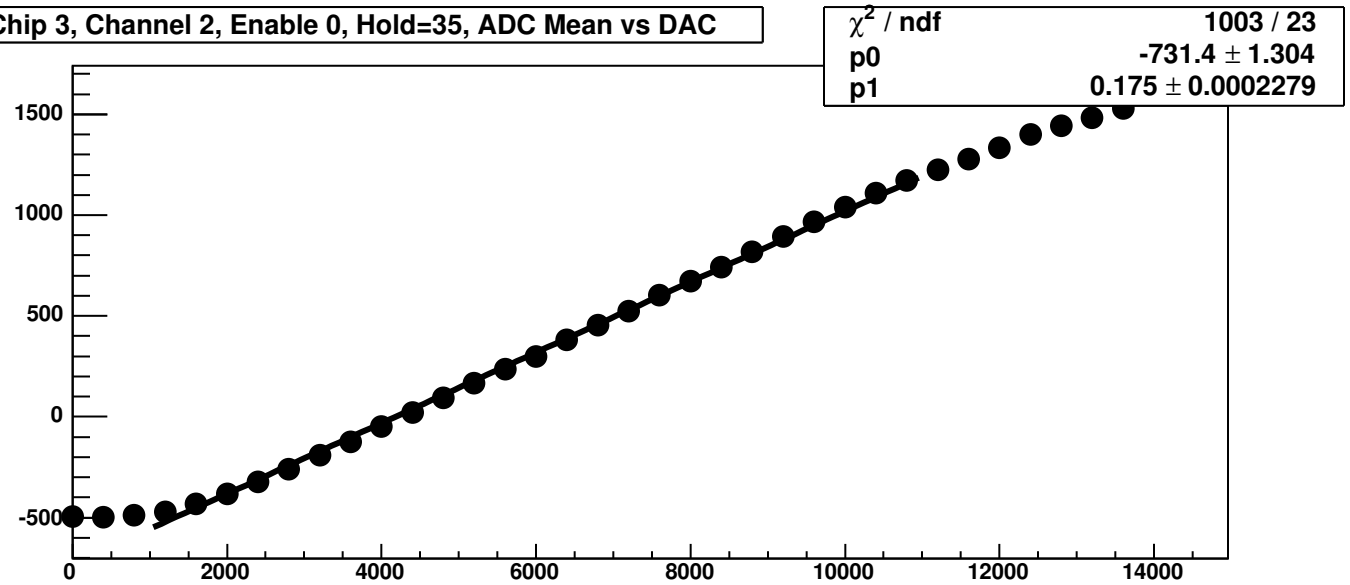
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



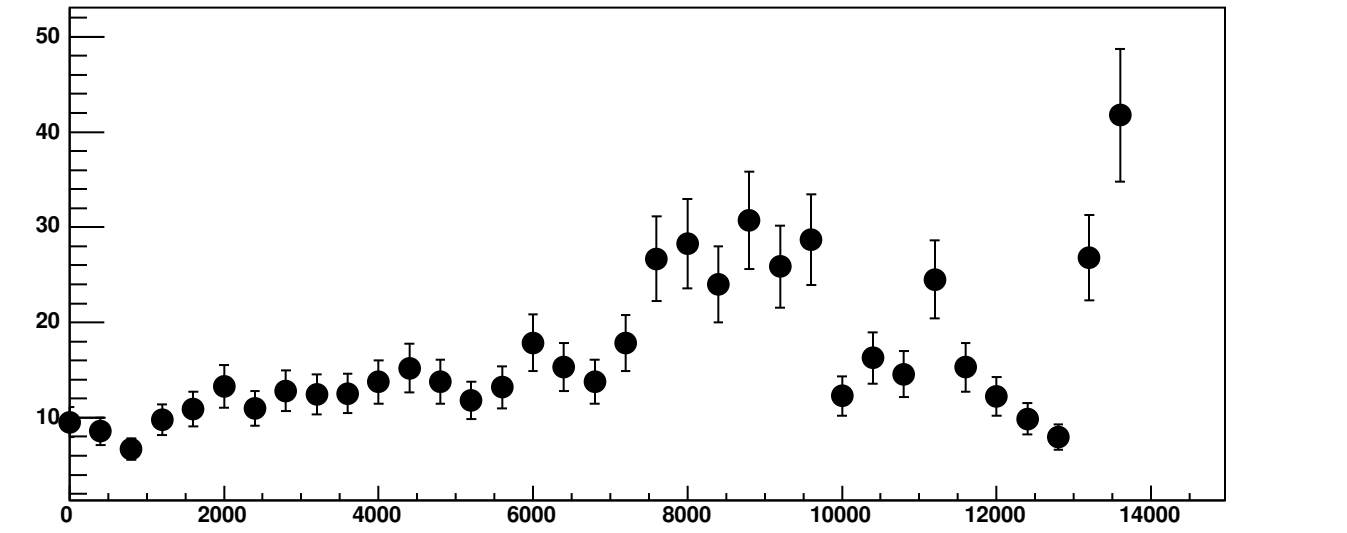
Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



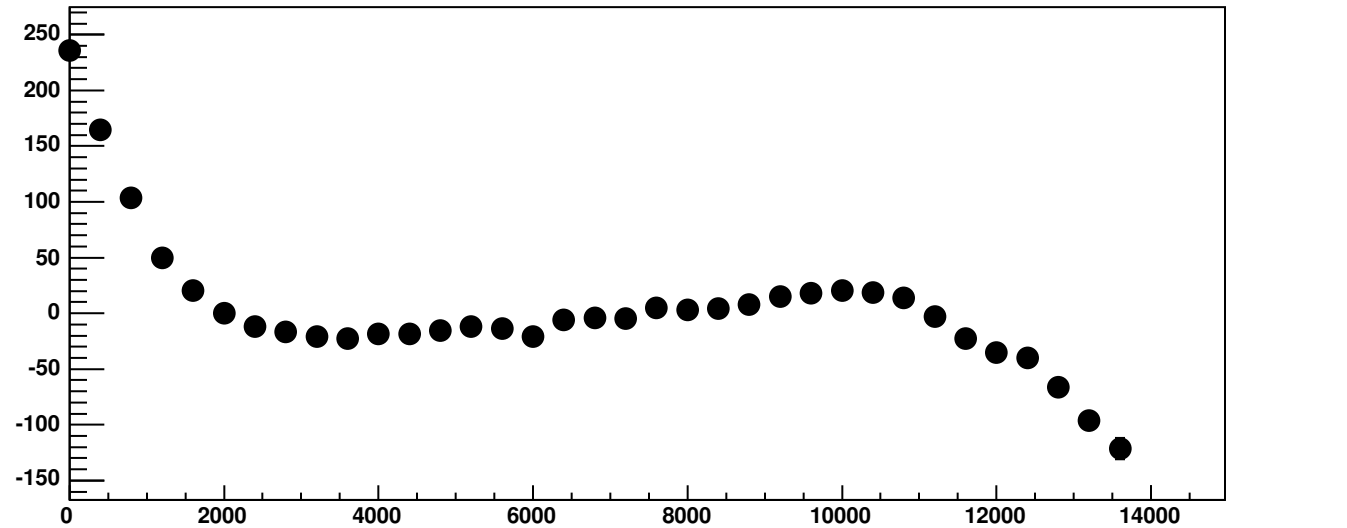
Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

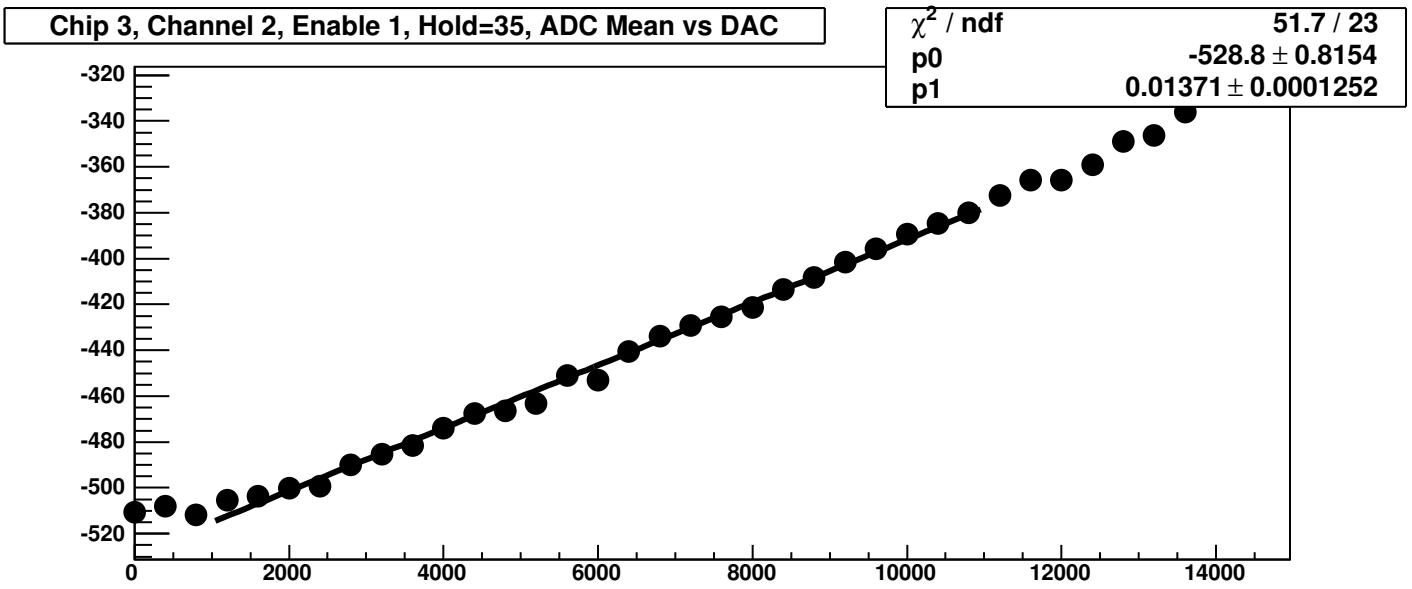


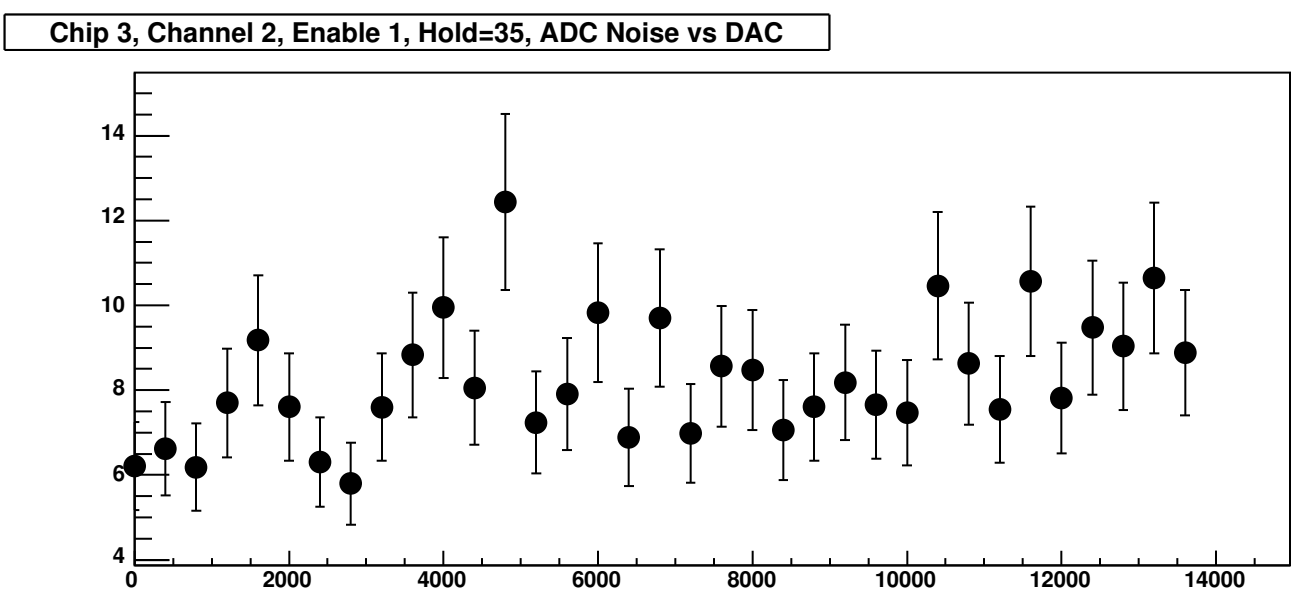
Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC

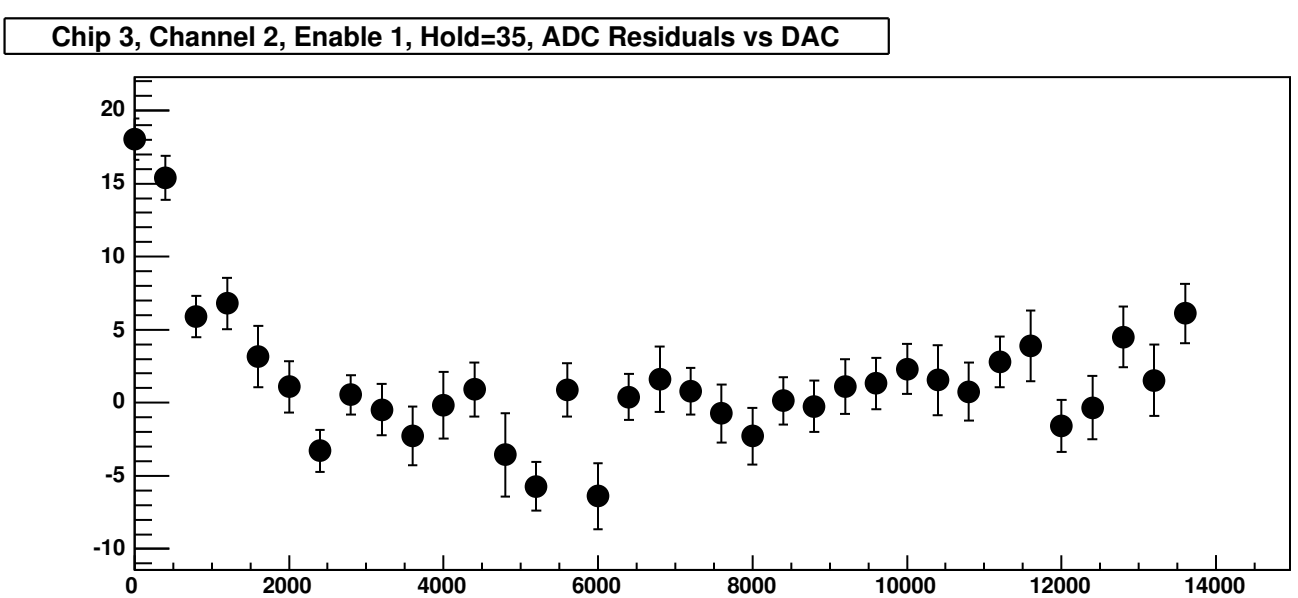


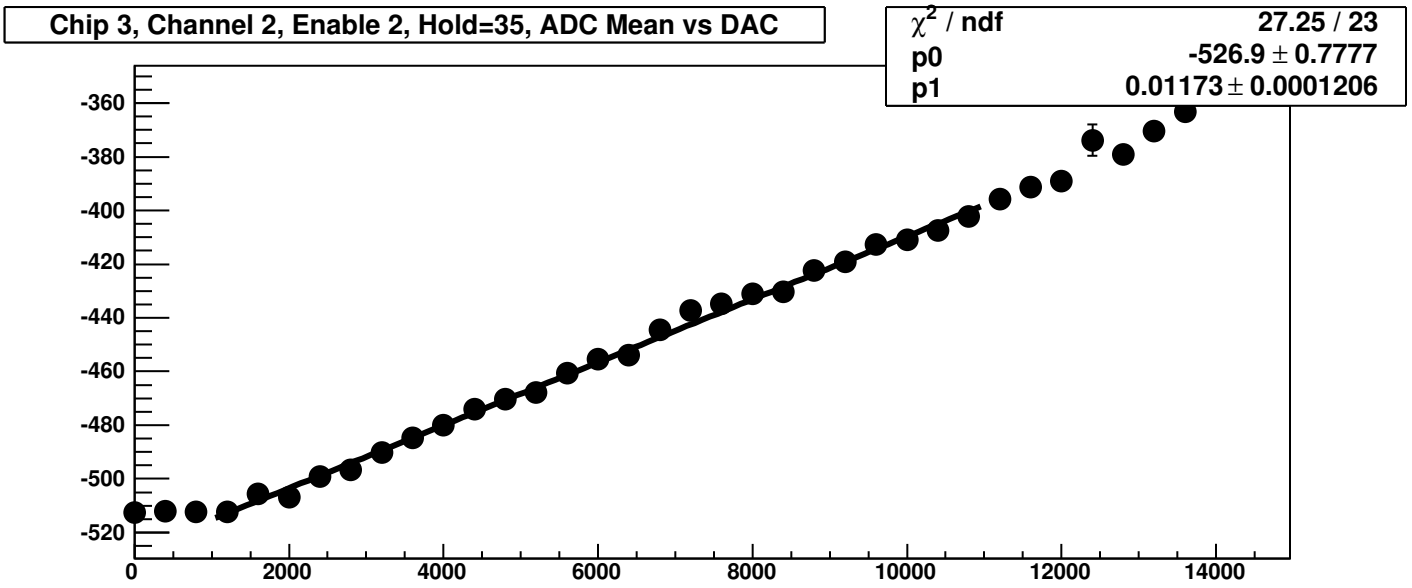
Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

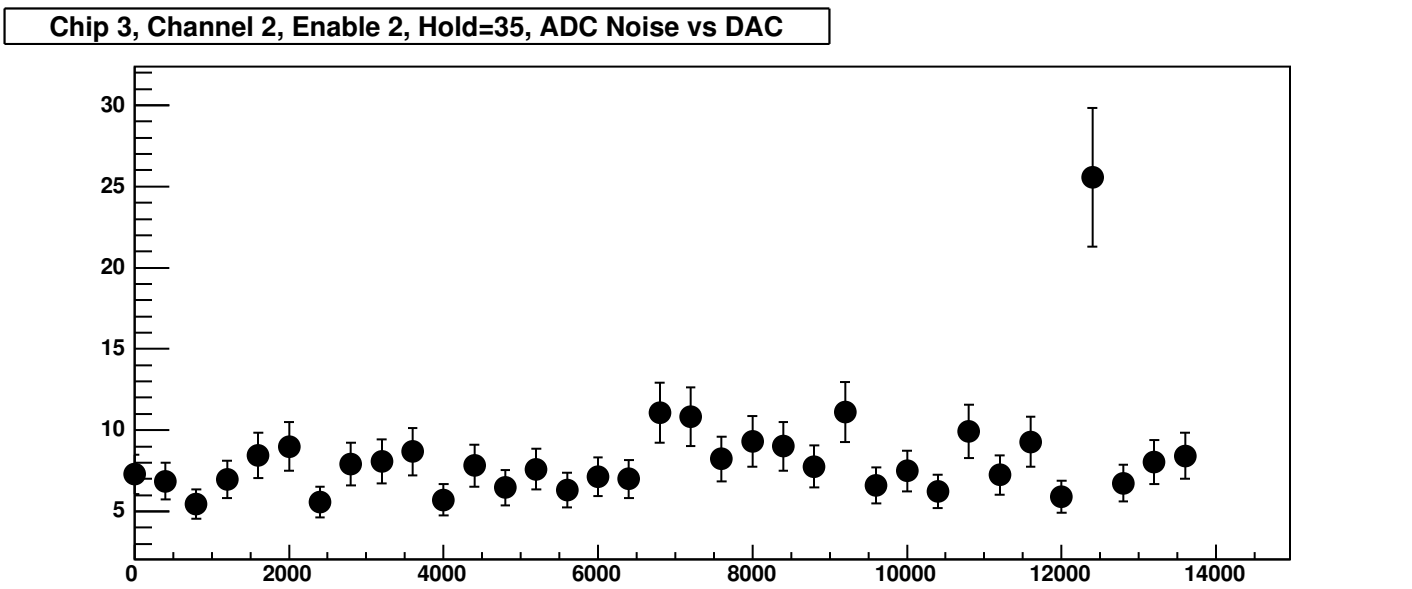


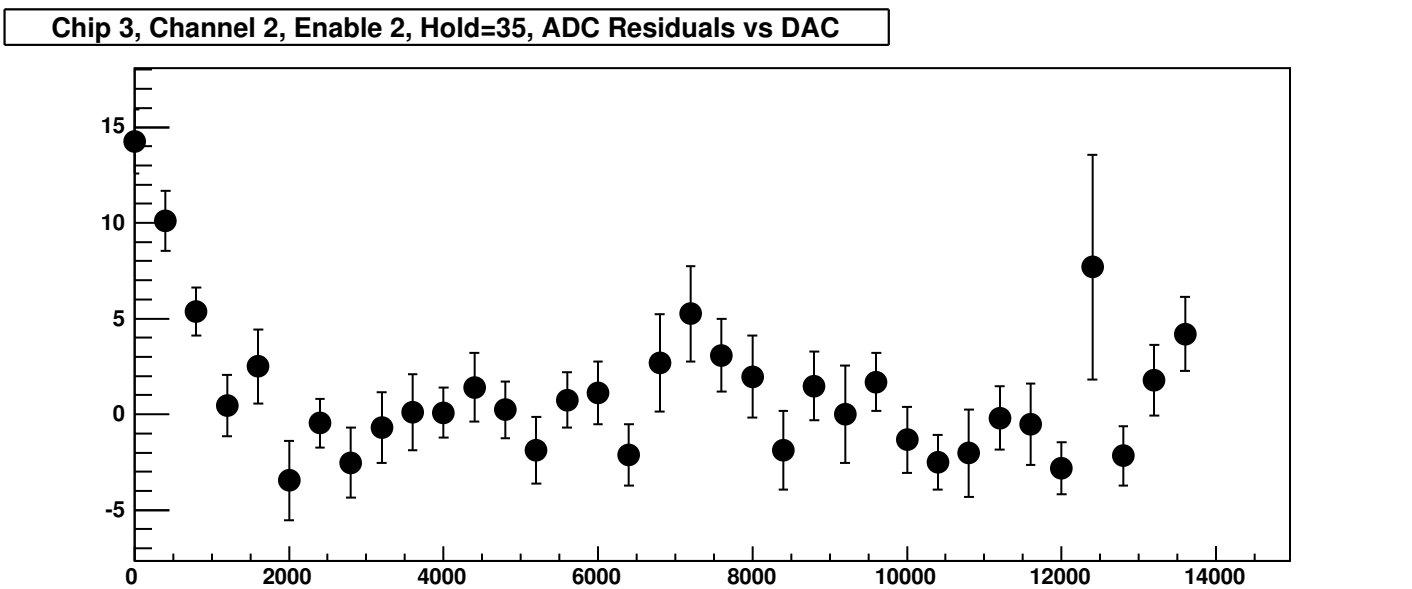

Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 51.7 / 23
p0 -528.8 ± 0.8154
p1 0.01371 ± 0.0001252
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000

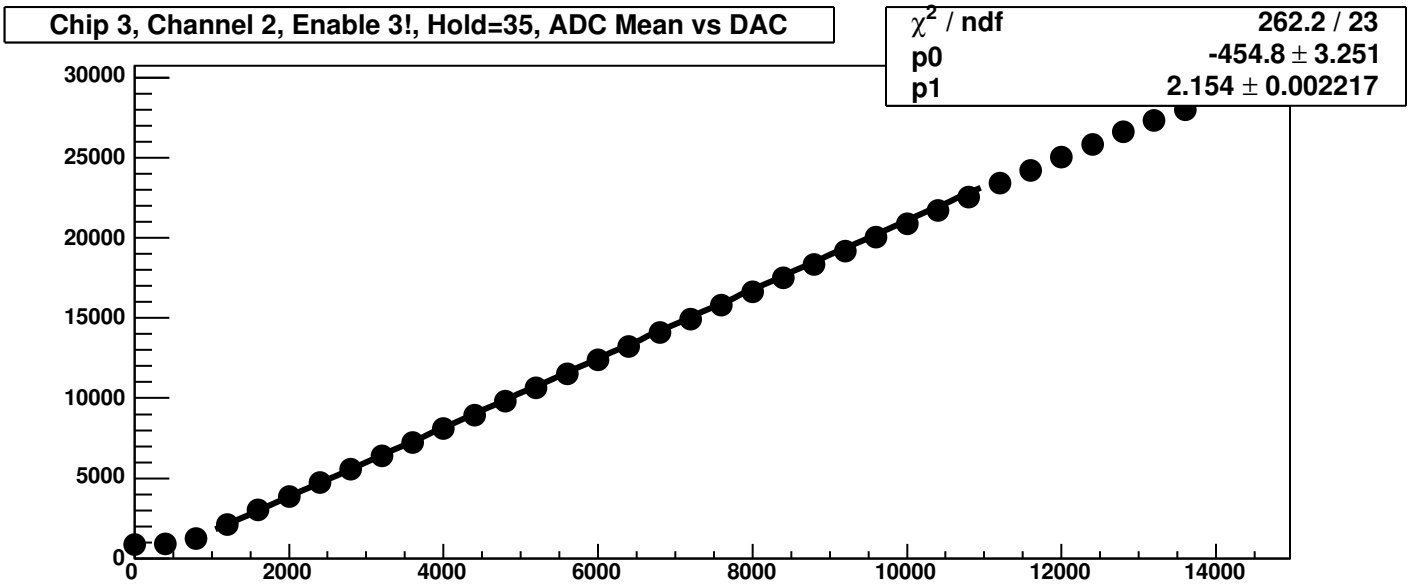

Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

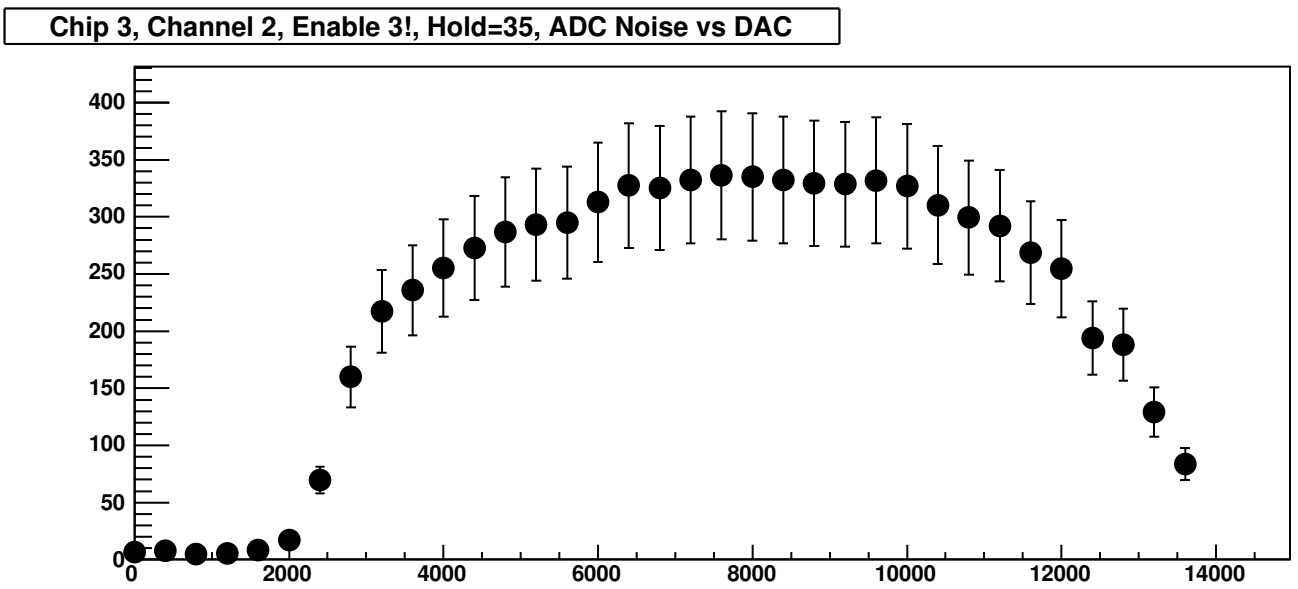

Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

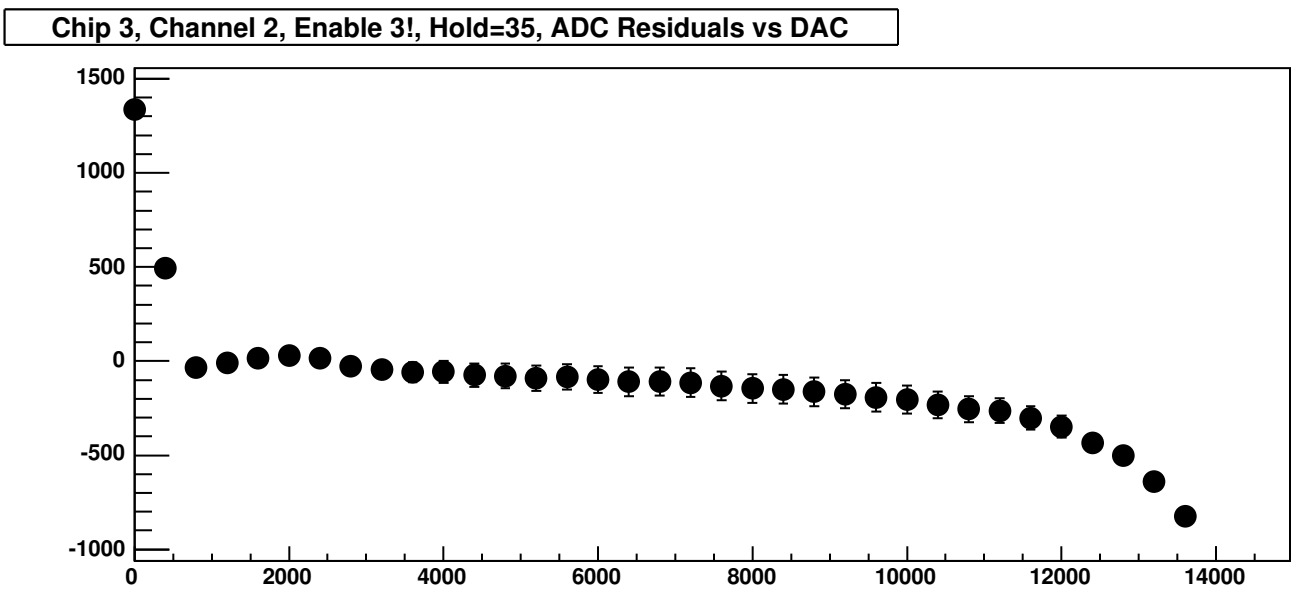

Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.25 / 23
p0 -526.9 ± 0.7777
p1 0.01173 ± 0.0001206
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000

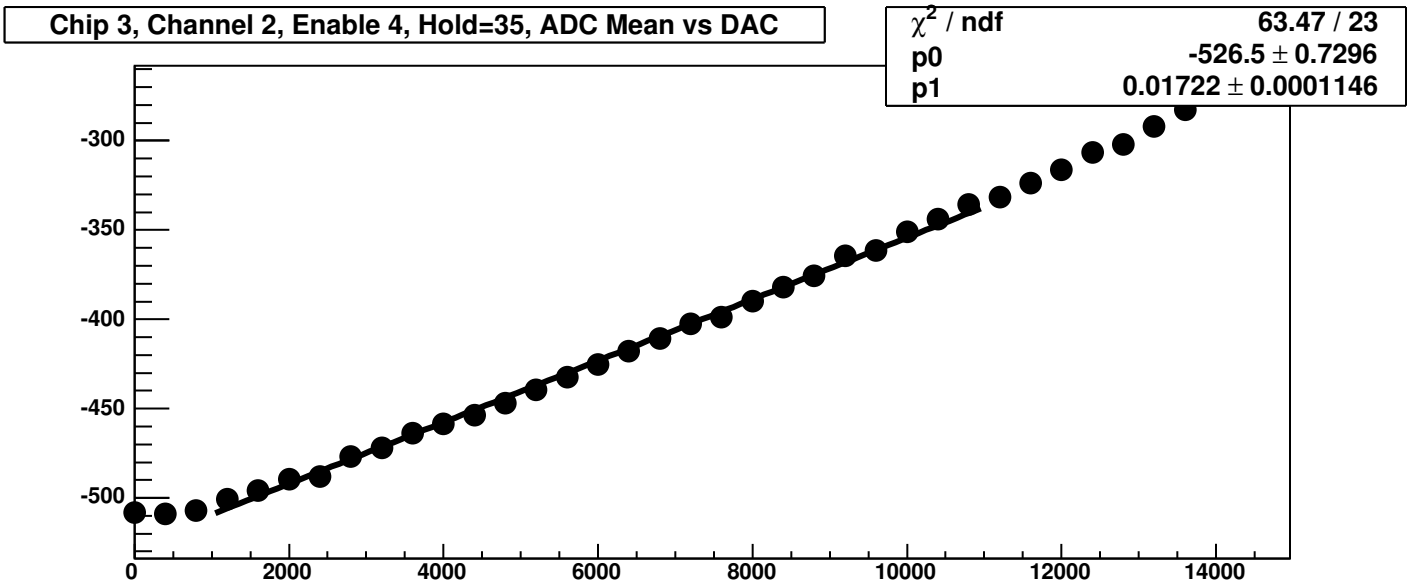

Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

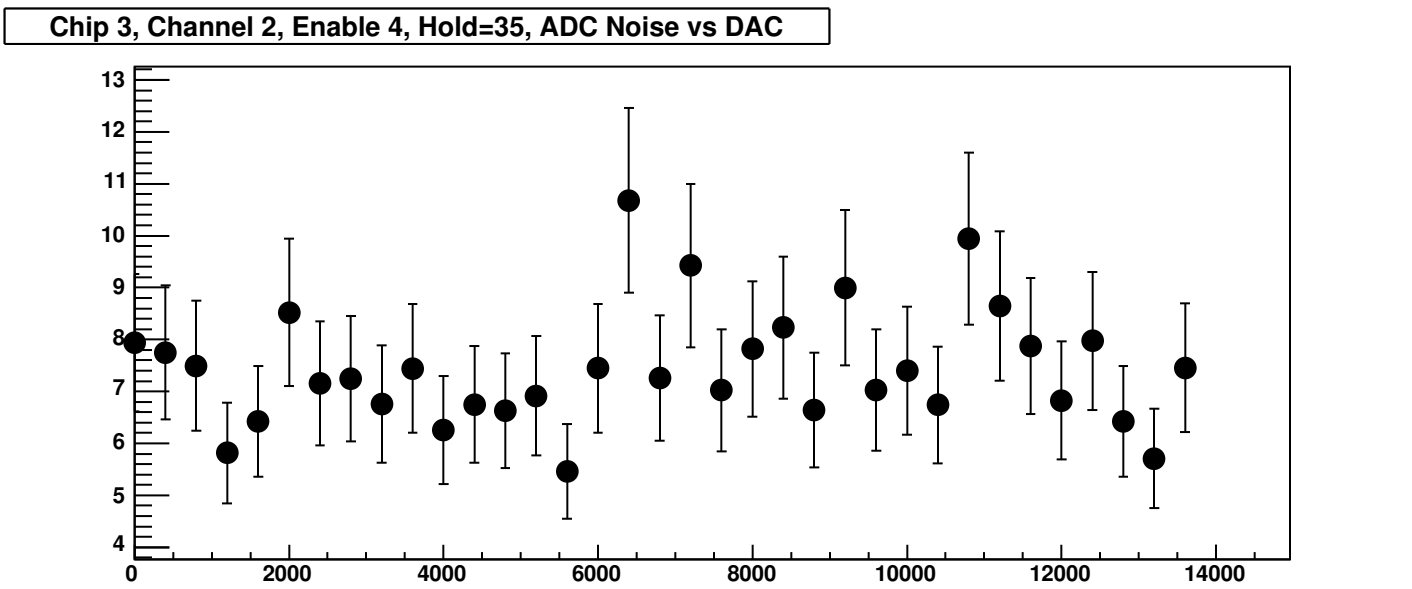

Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

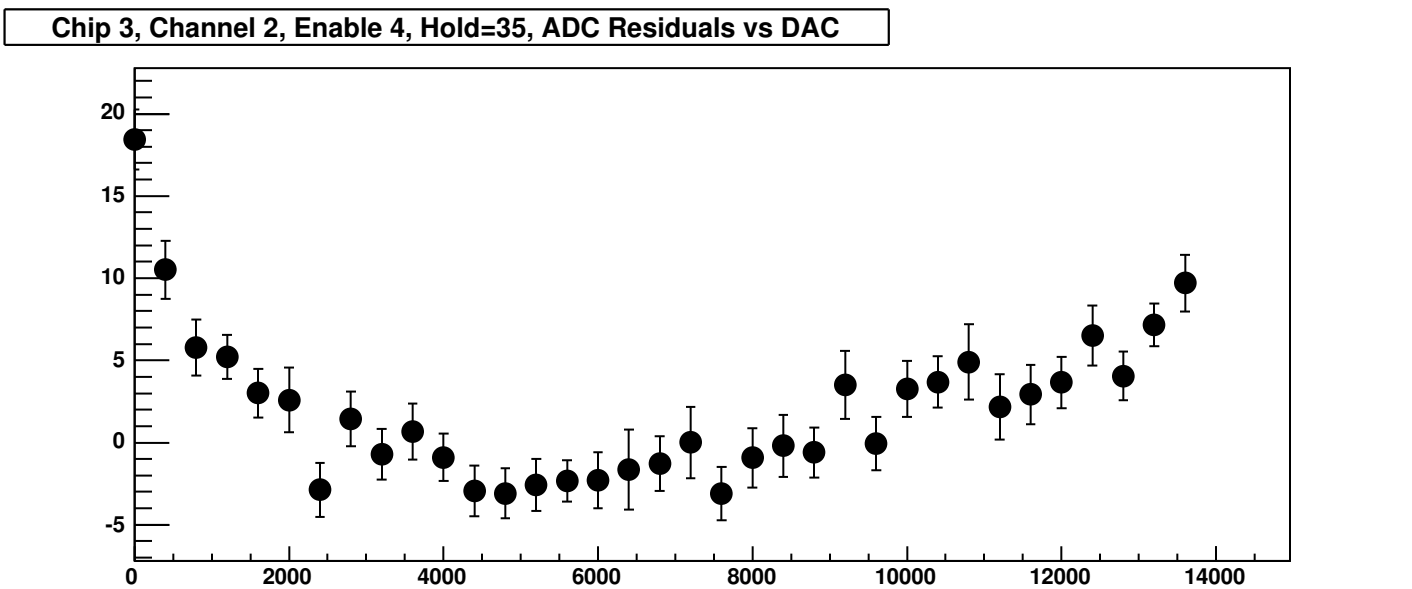

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 262.2 / 23
p0 -454.8 ± 3.251
p1 2.154 ± 0.002217
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

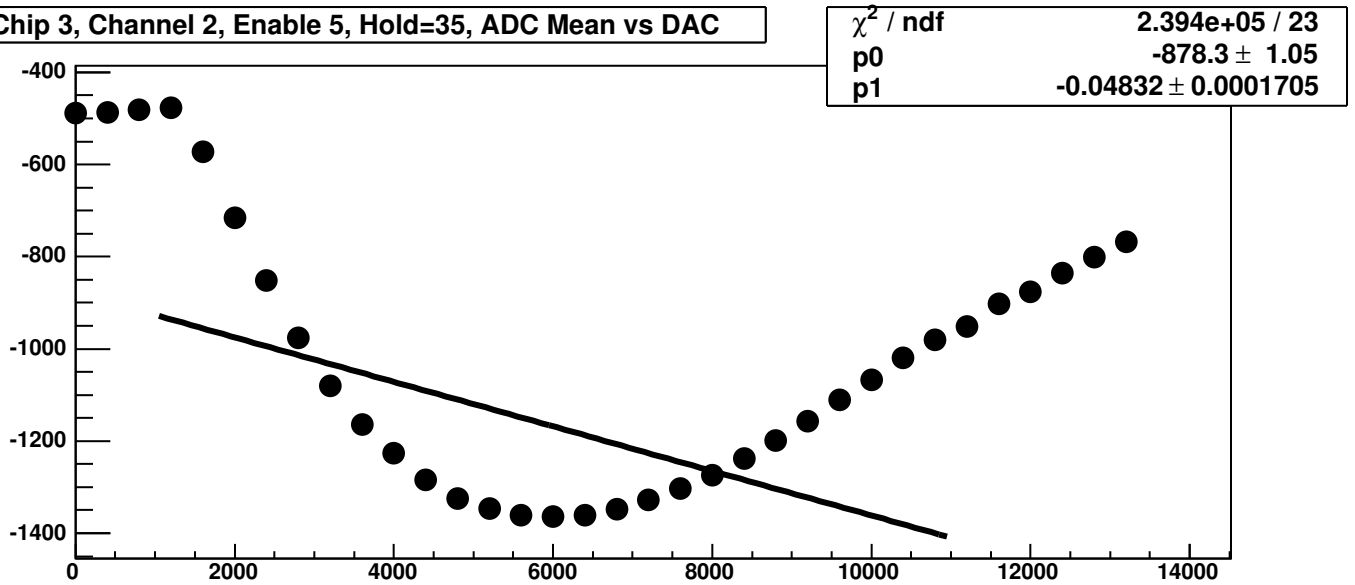

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 63.47 / 23
p0 -526.5 ± 0.7296
p1 0.01722 ± 0.0001146
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

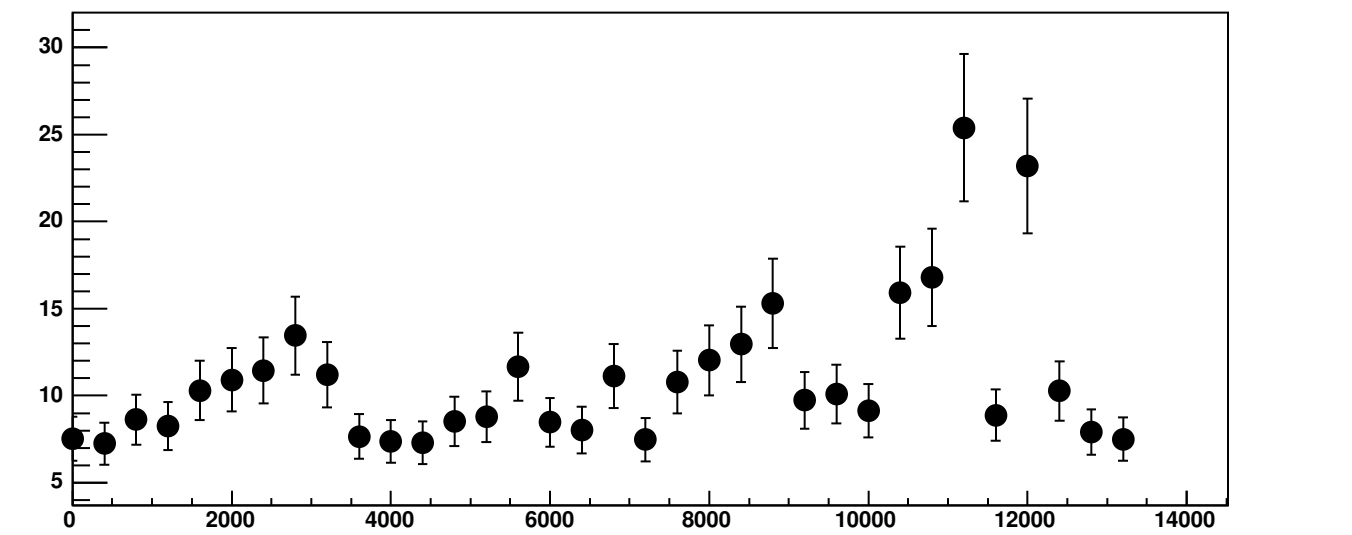

Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

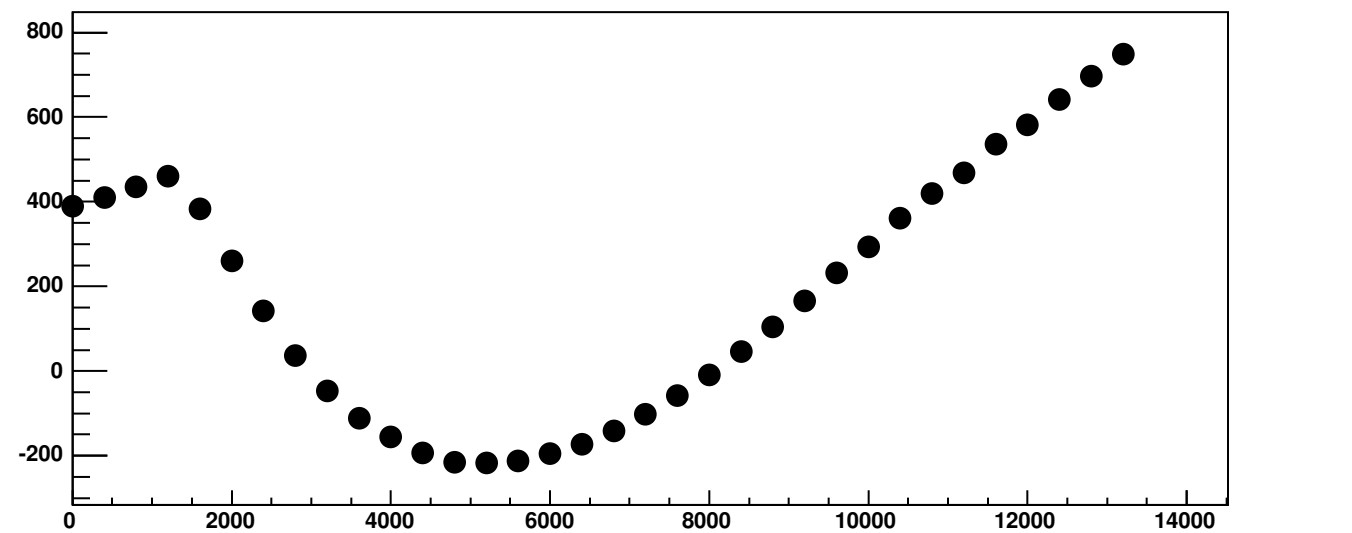
Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

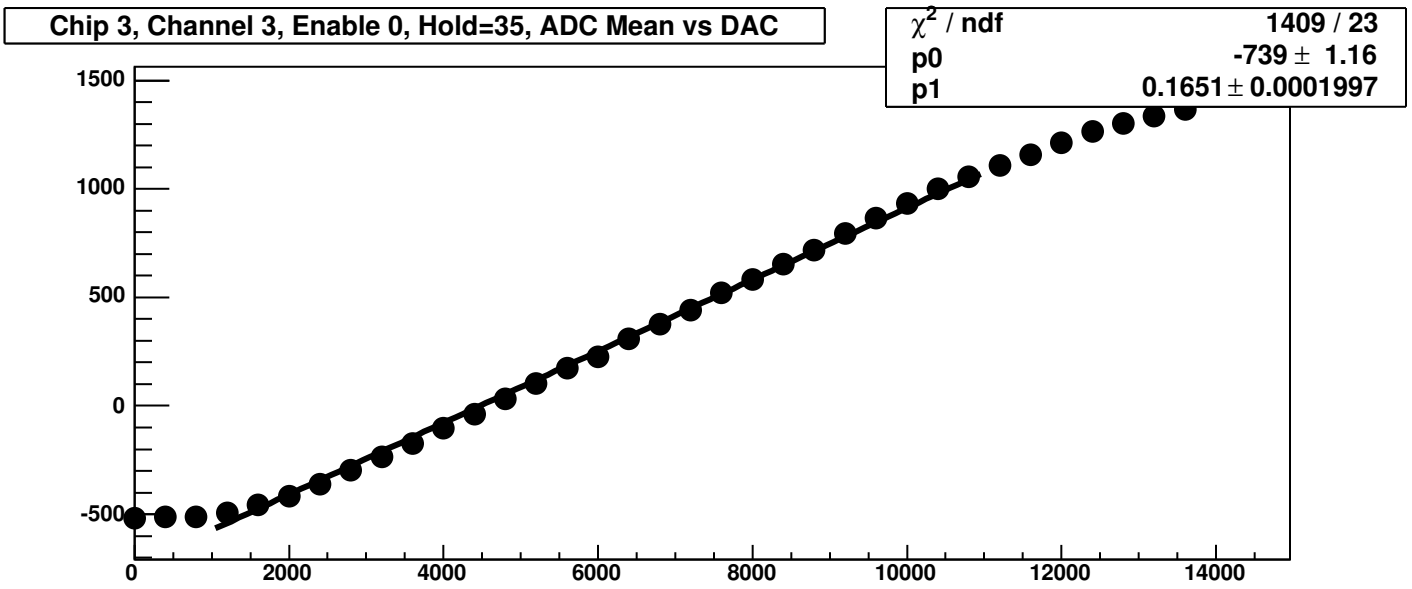


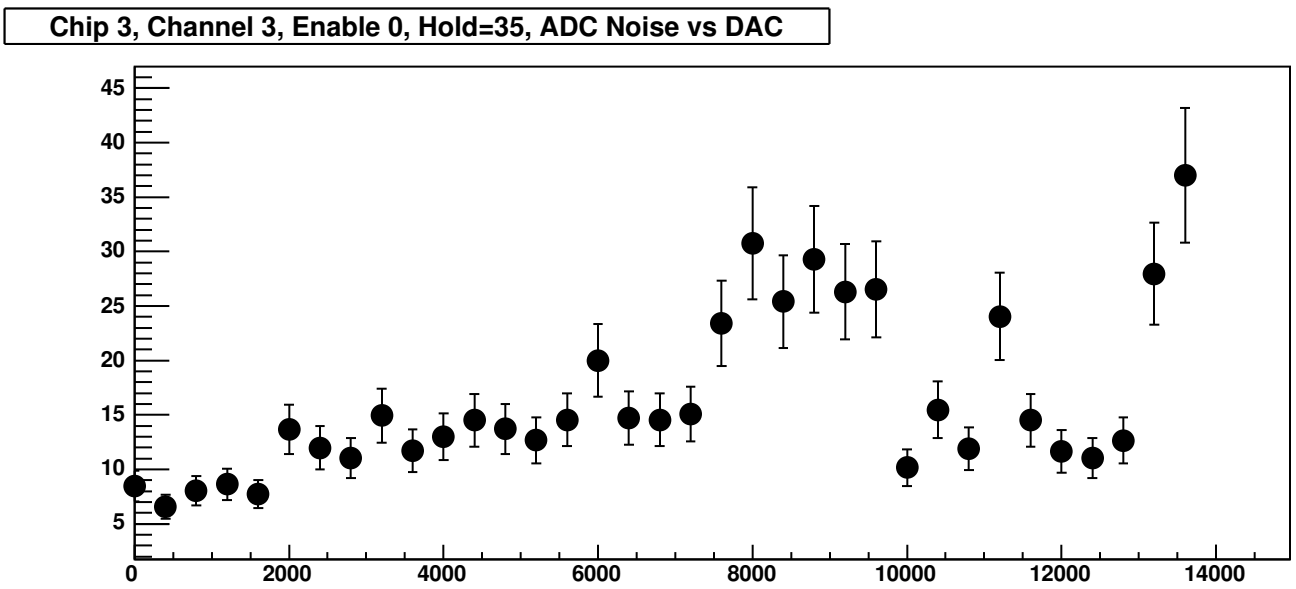
Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

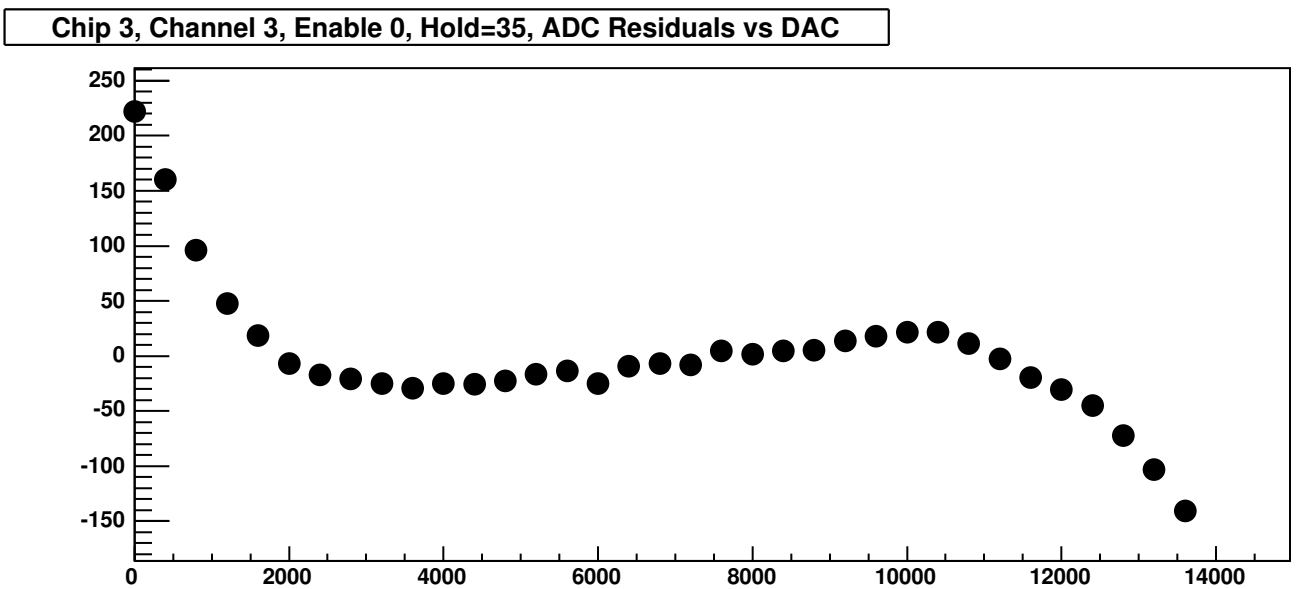


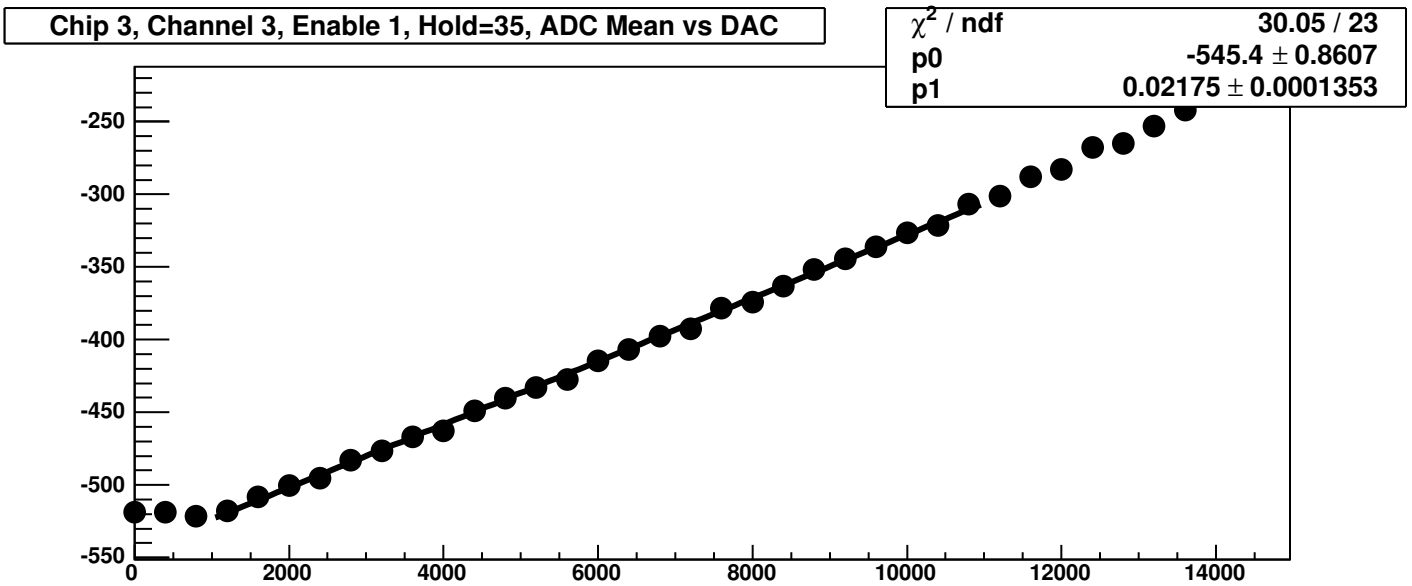
Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

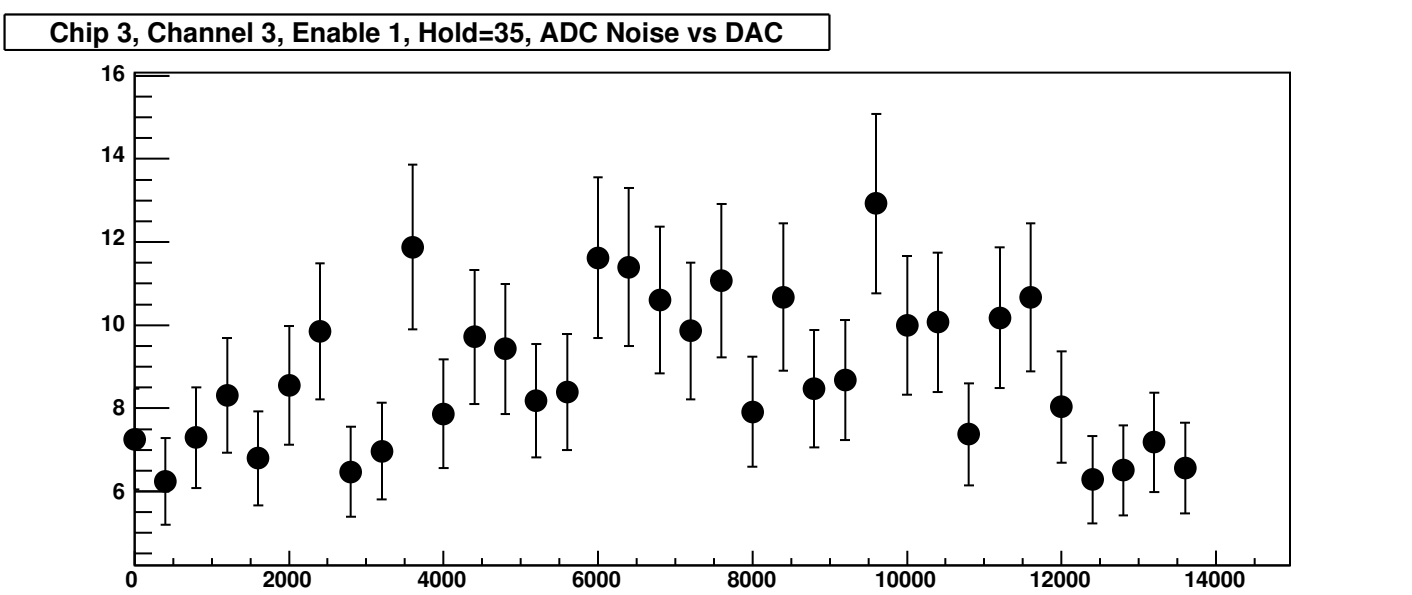


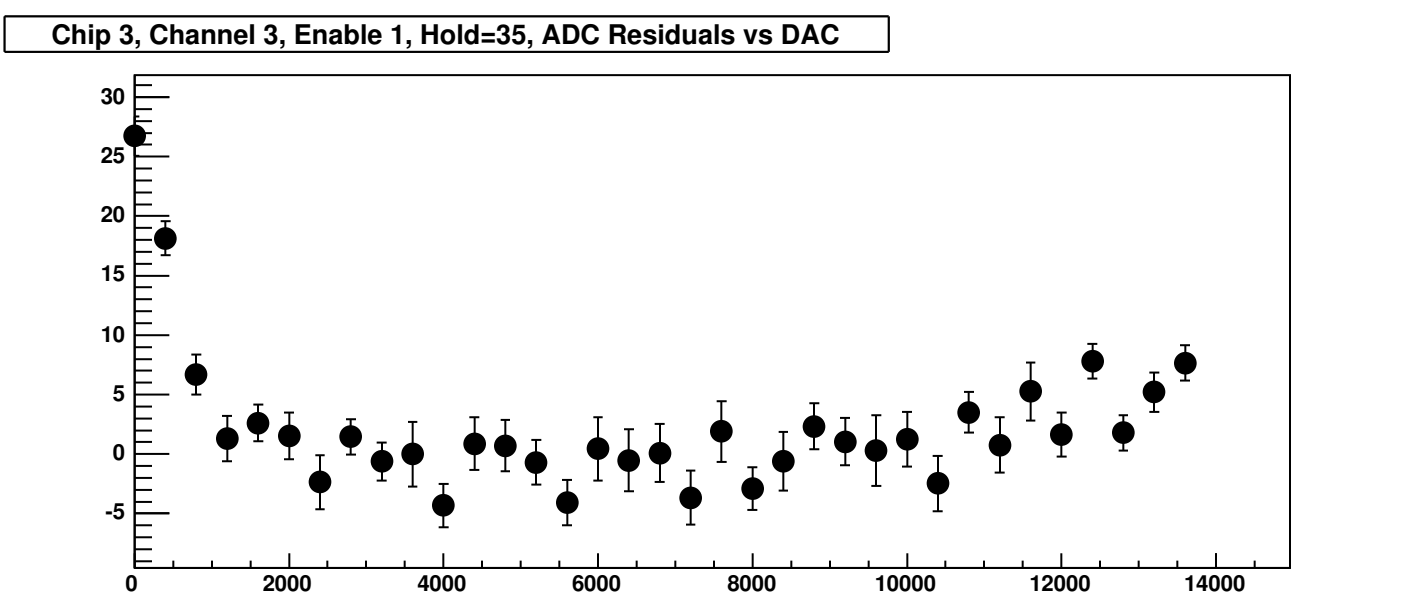

Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1409 / 23
p0 -739 ± 1.16
p1 0.1651 ± 0.0001997
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

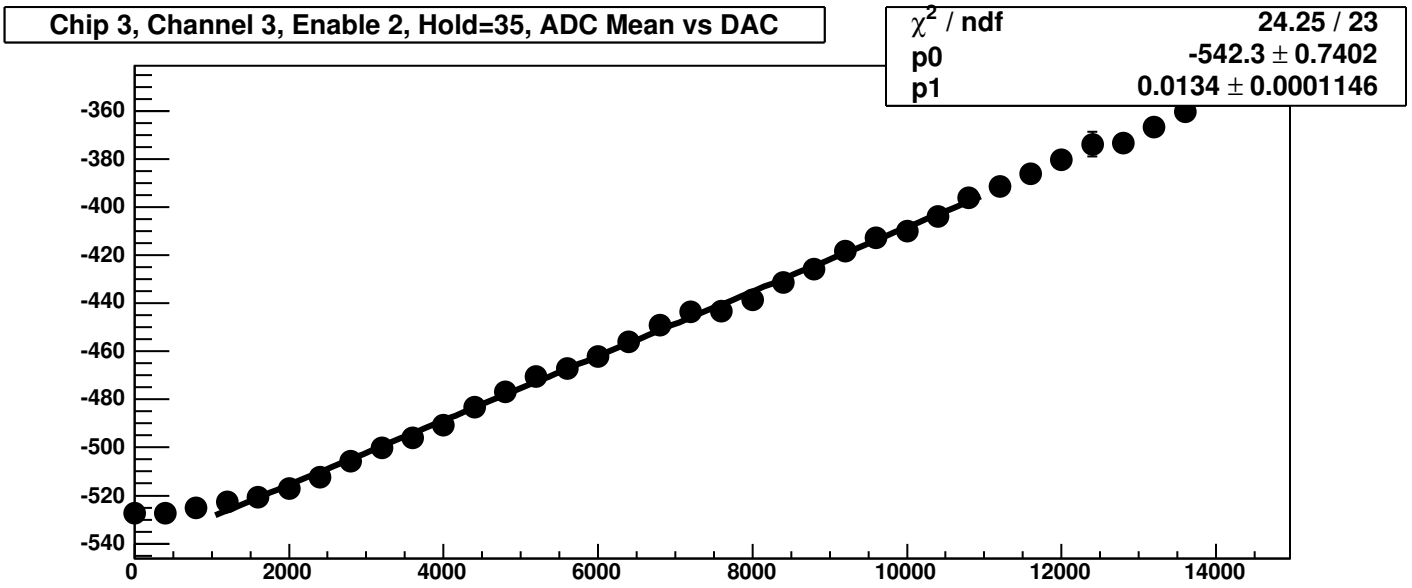

Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

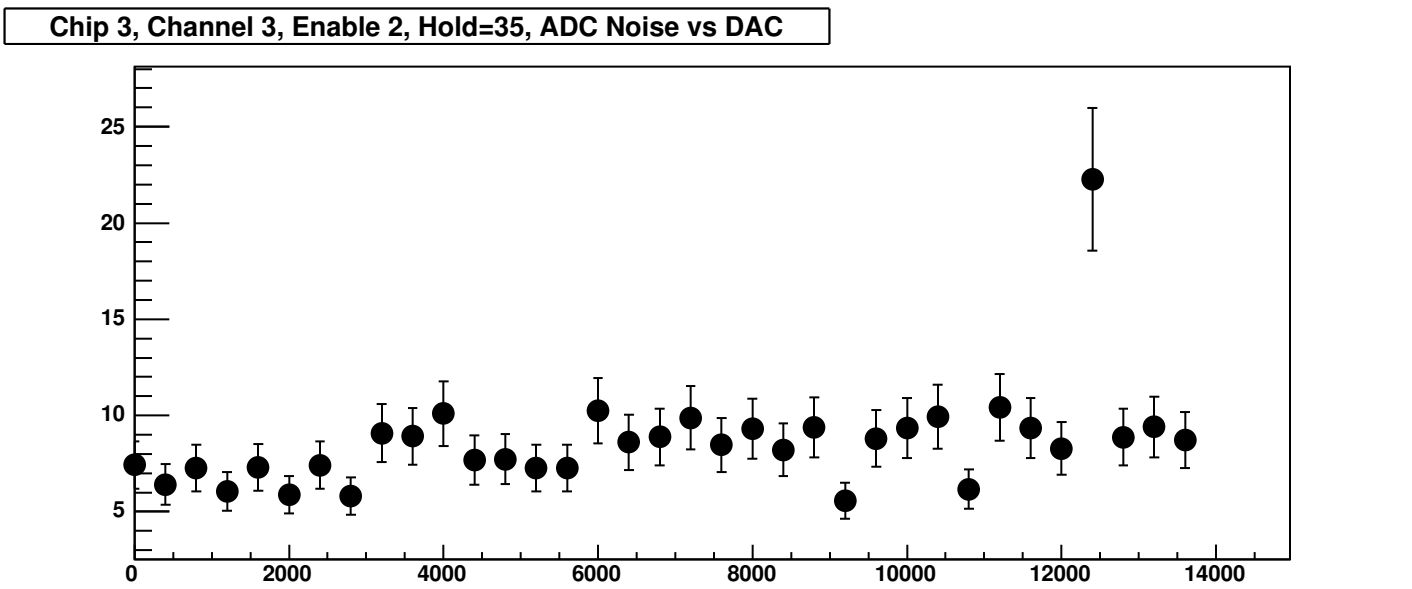

Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

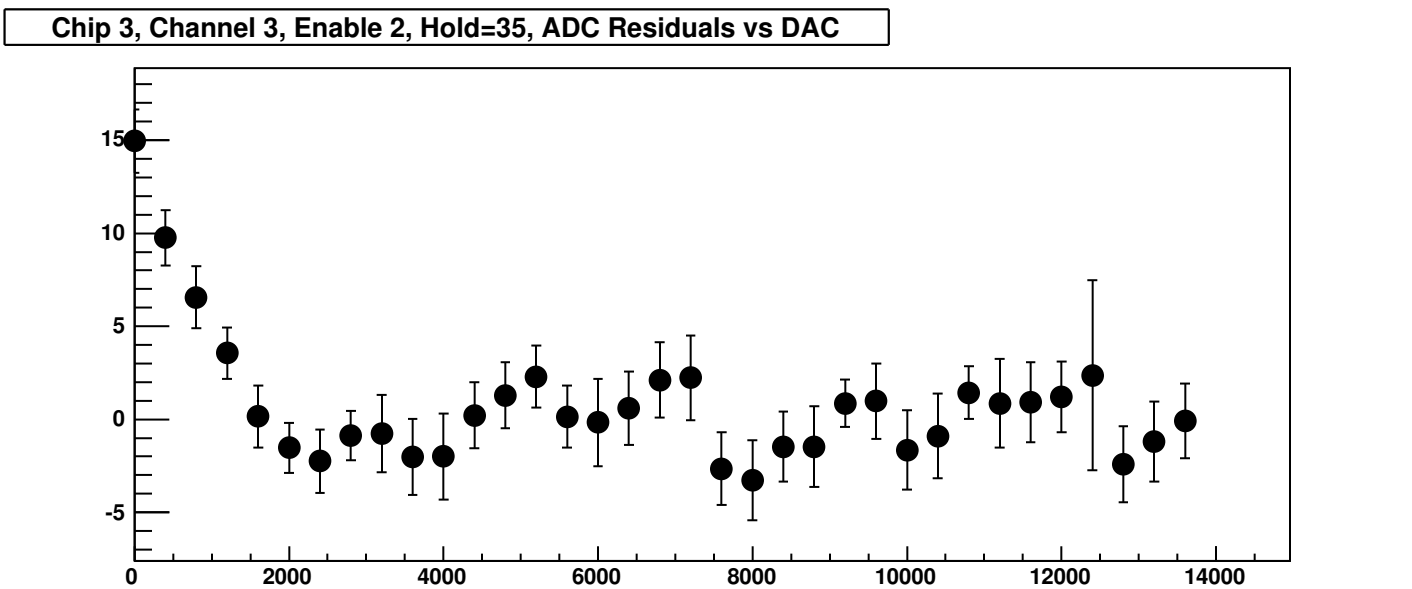

Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.05 / 23
p0 -545.4 ± 0.8607
p1 0.02175 ± 0.0001353
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

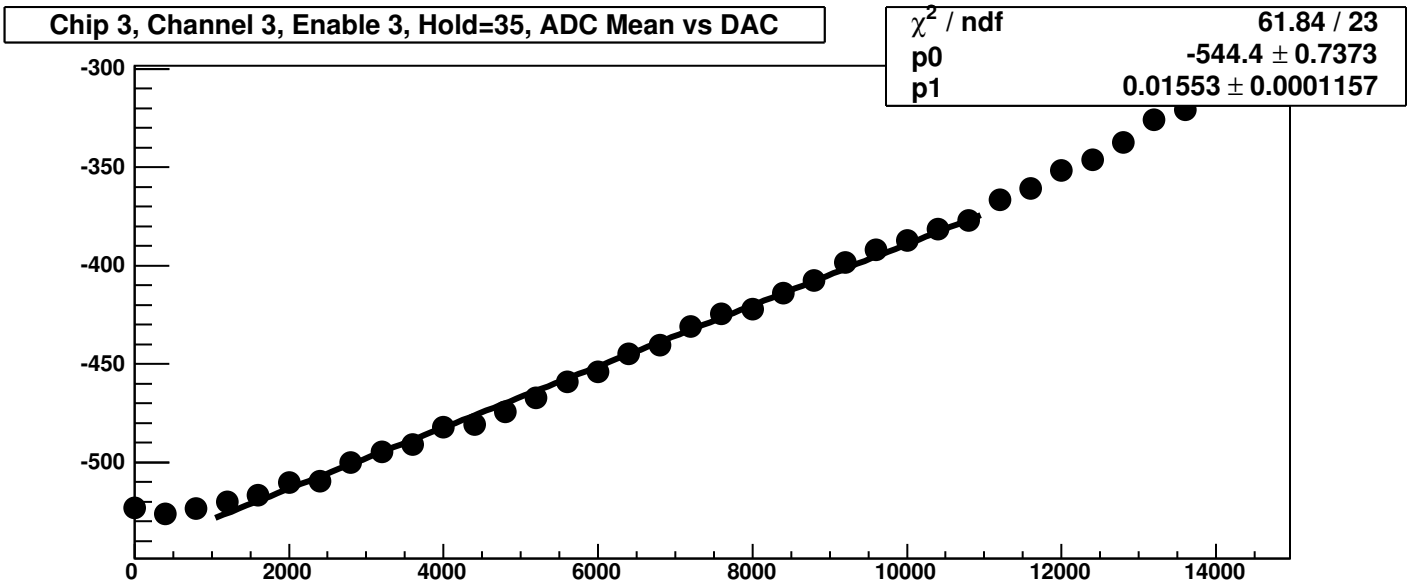

Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

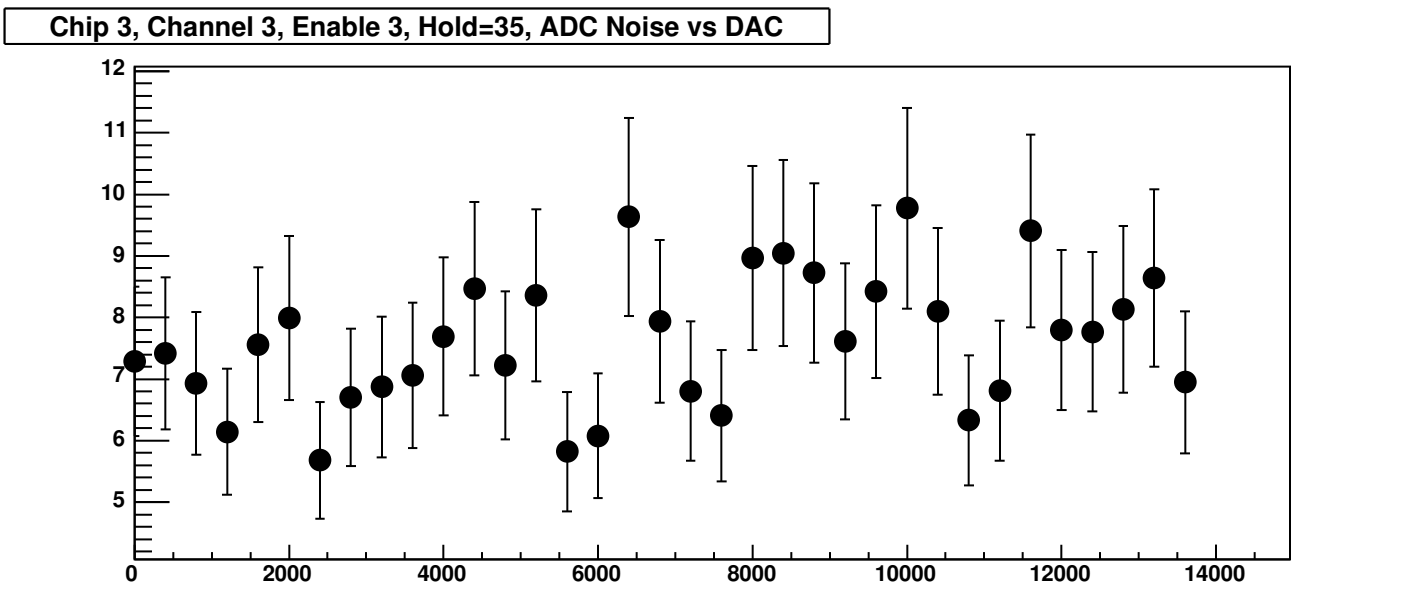

Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

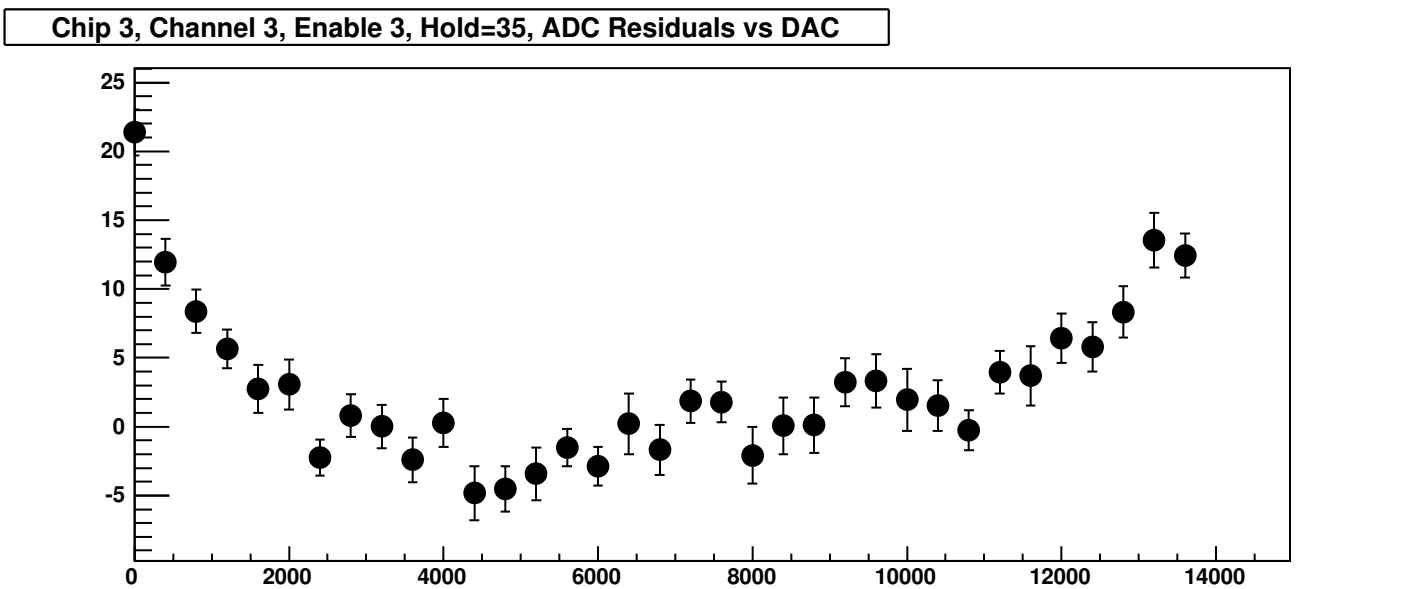

Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.25 / 23
p0 -542.3 ± 0.7402
p1 0.0134 ± 0.0001146
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

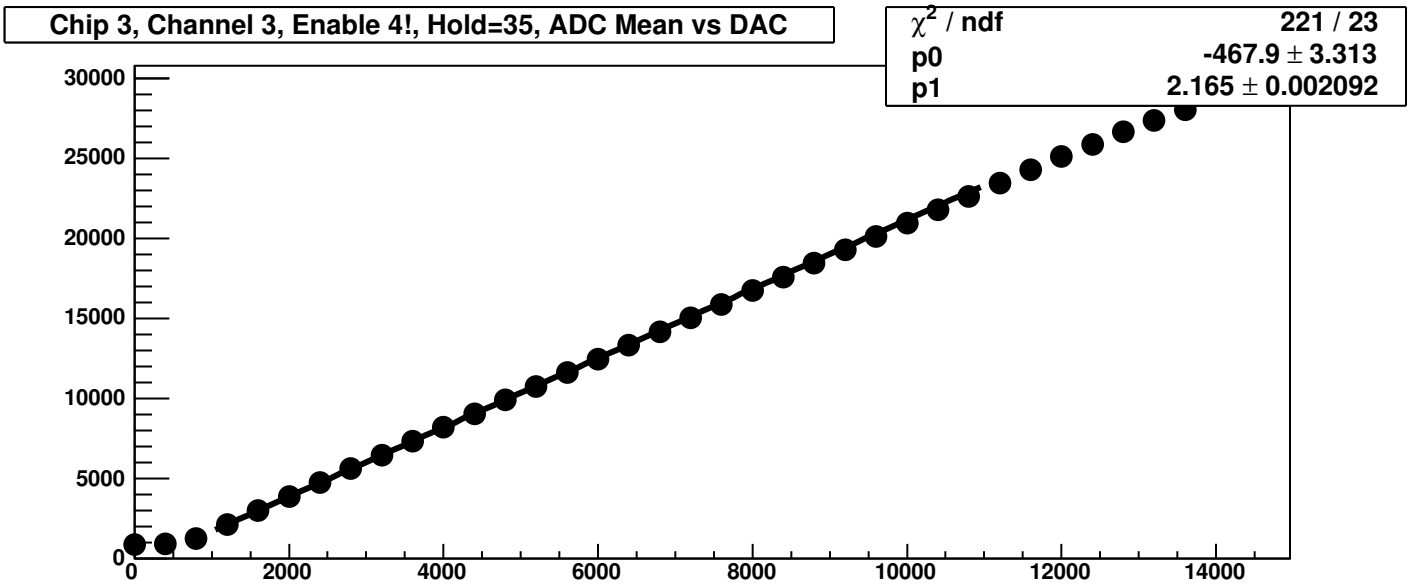

Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

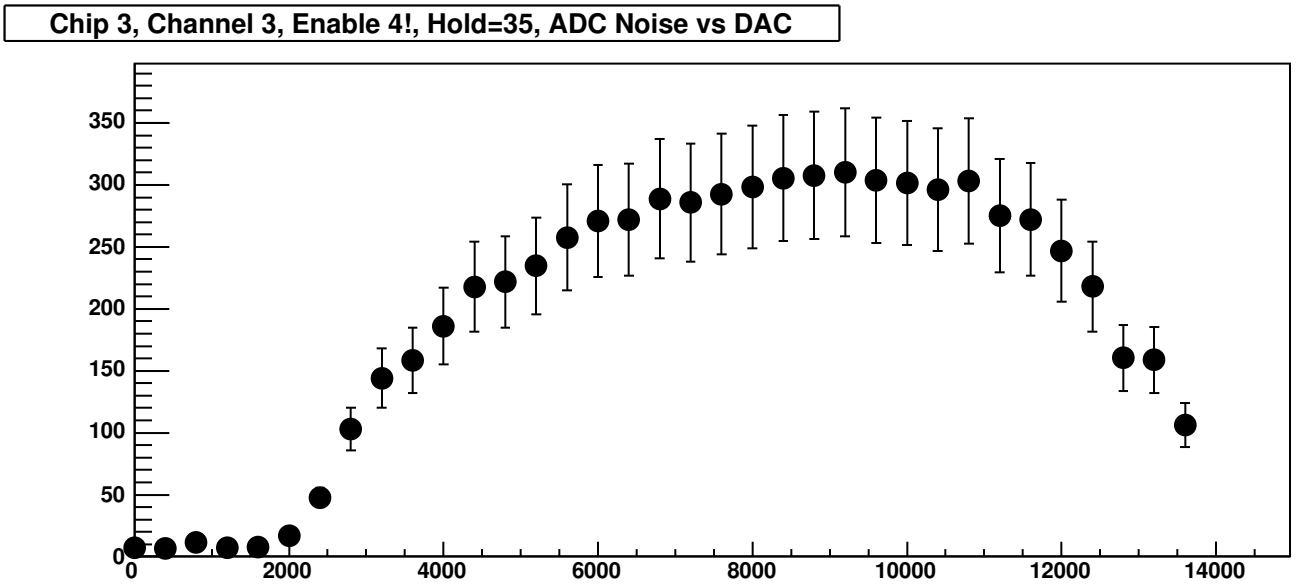

Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

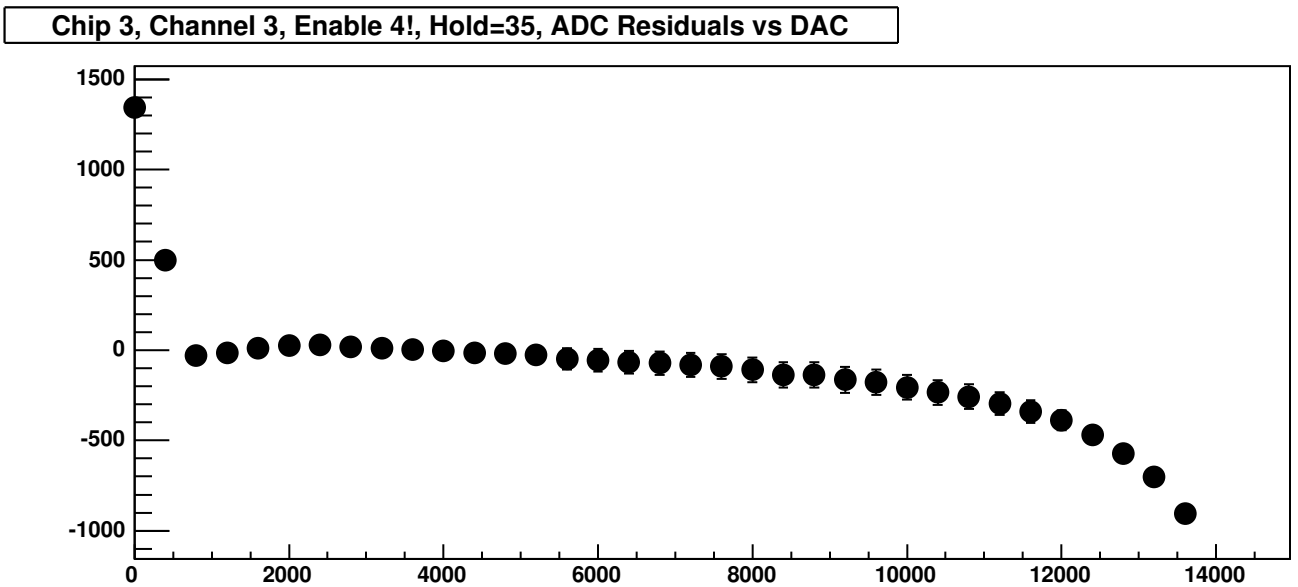

Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 61.84 / 23
p0 -544.4 ± 0.7373
p1 0.01553 ± 0.0001157
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

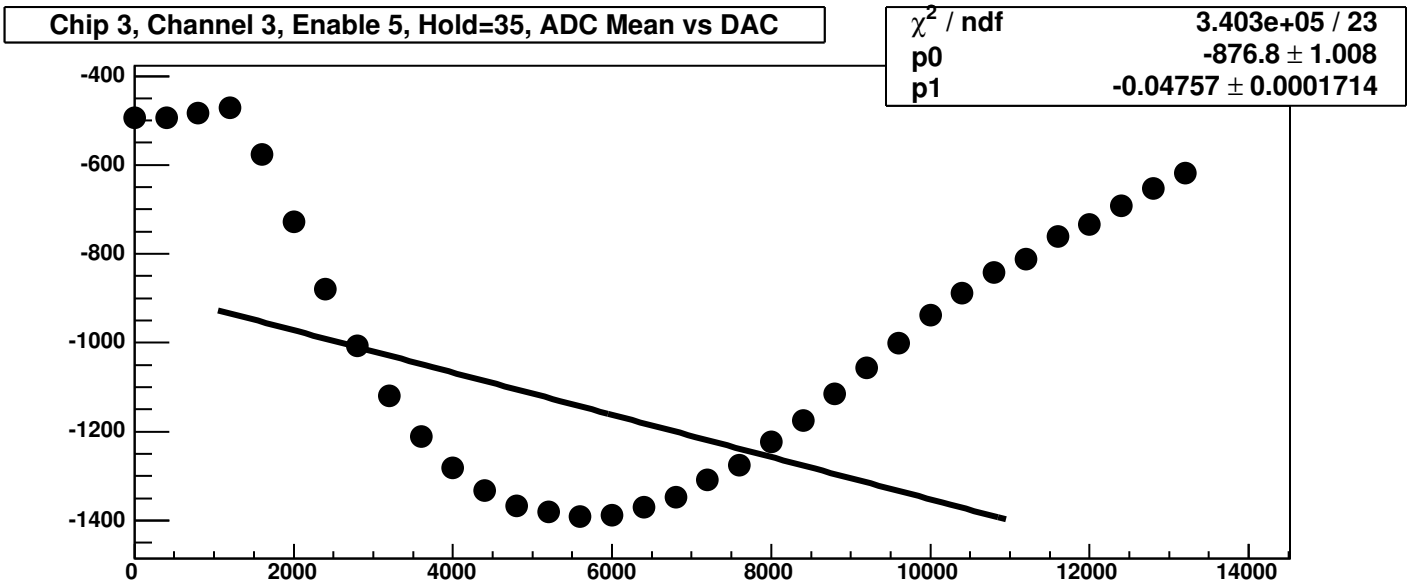

Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

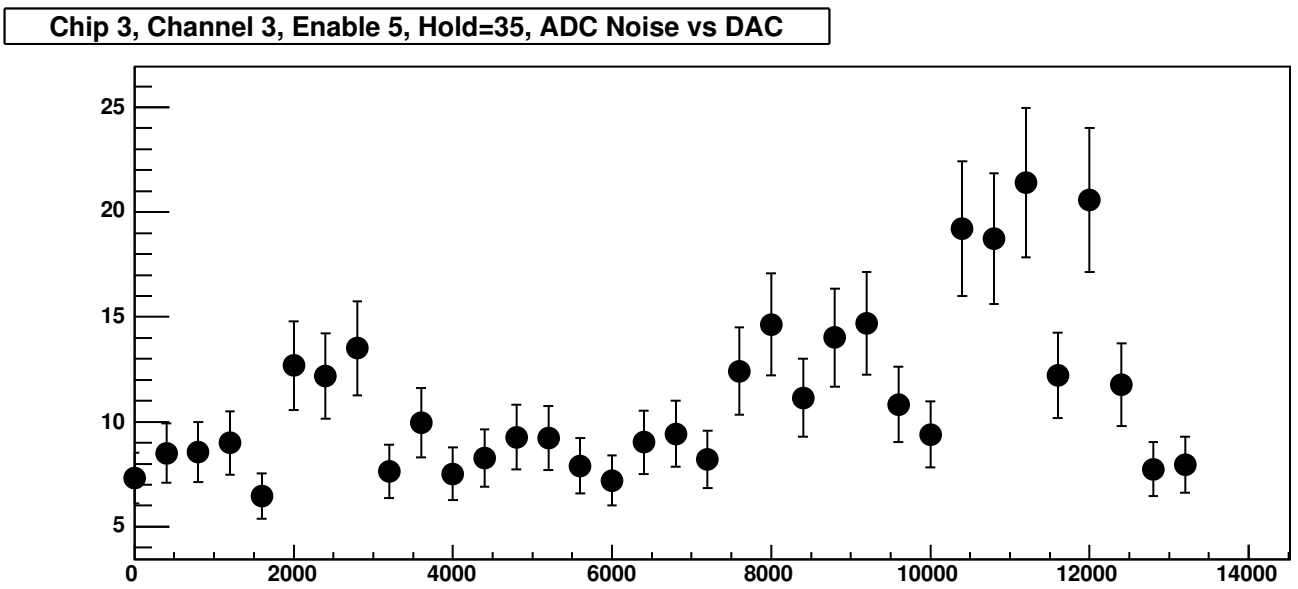

Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

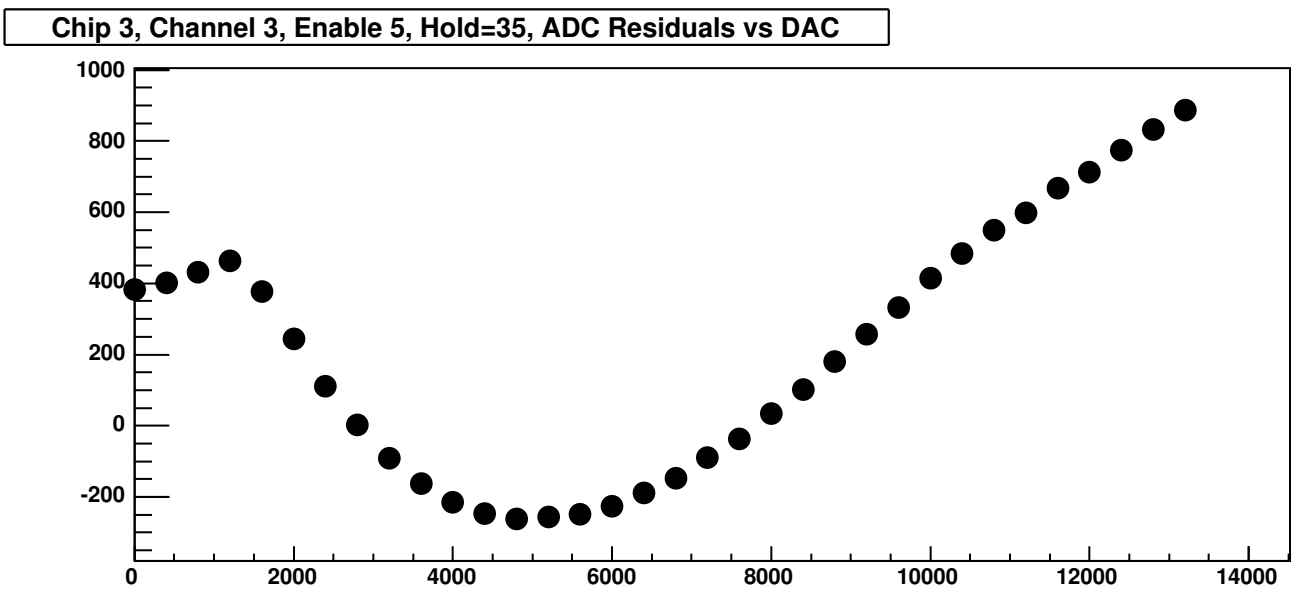

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 221 / 23
p0 -467.9 ± 3.313
p1 2.165 ± 0.002092
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

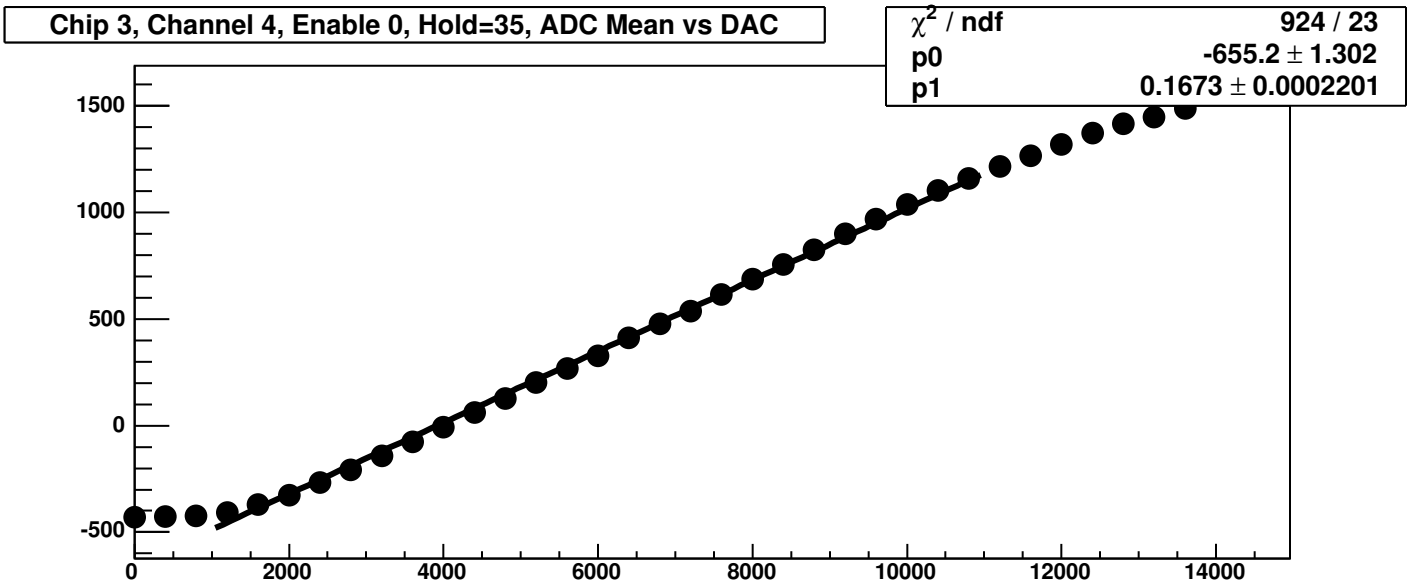

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

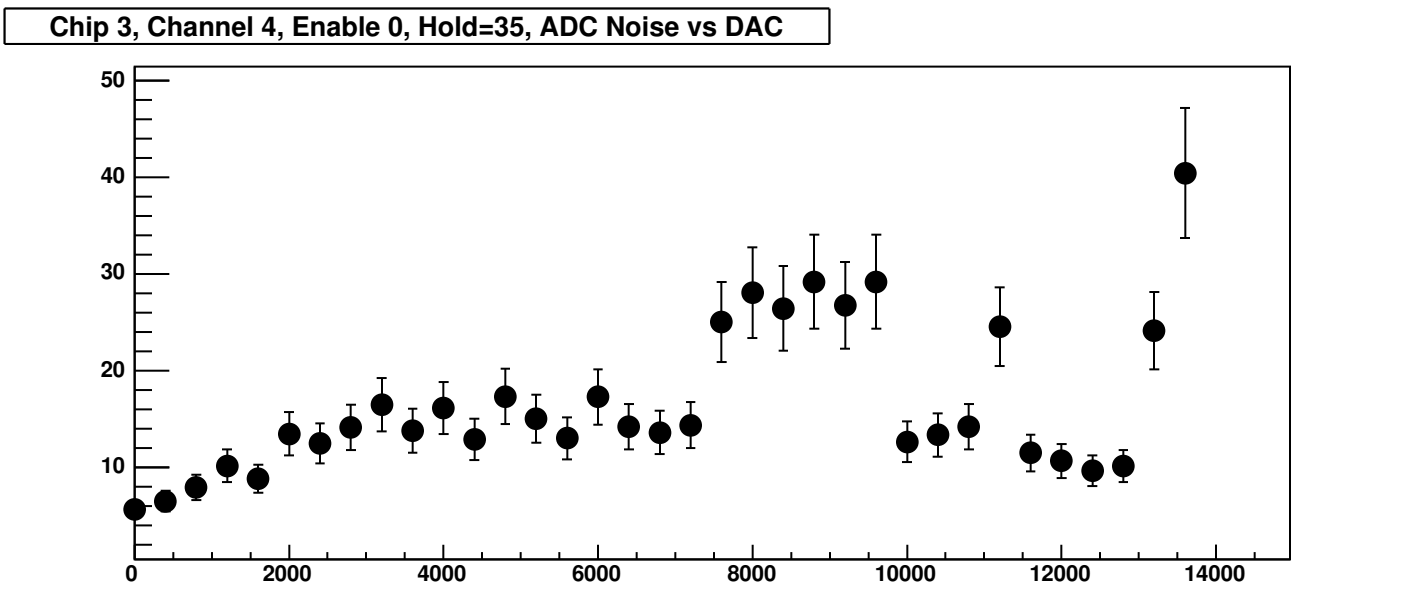

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

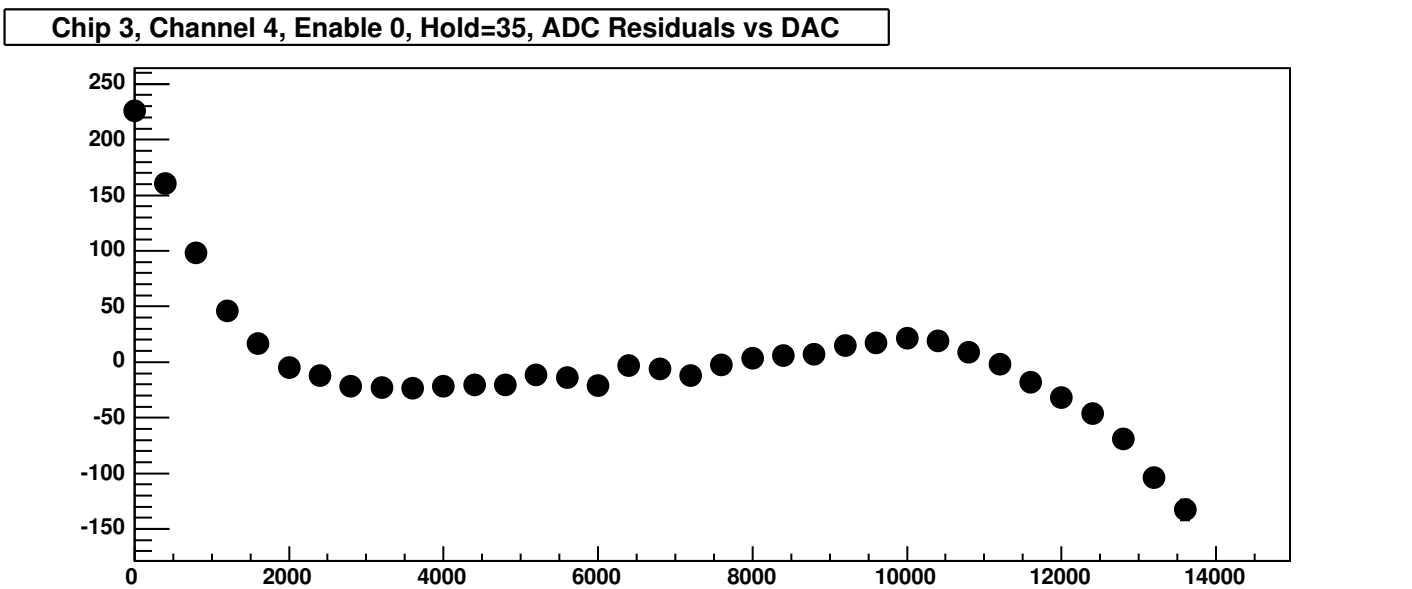

Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.403e+05 / 23
p0 -876.8 ± 1.008
p1 -0.04757 ± 0.0001714
-400
-600
-800
-1000
-1200
-1400
0
2000
4000
6000
8000
10000
12000
14000

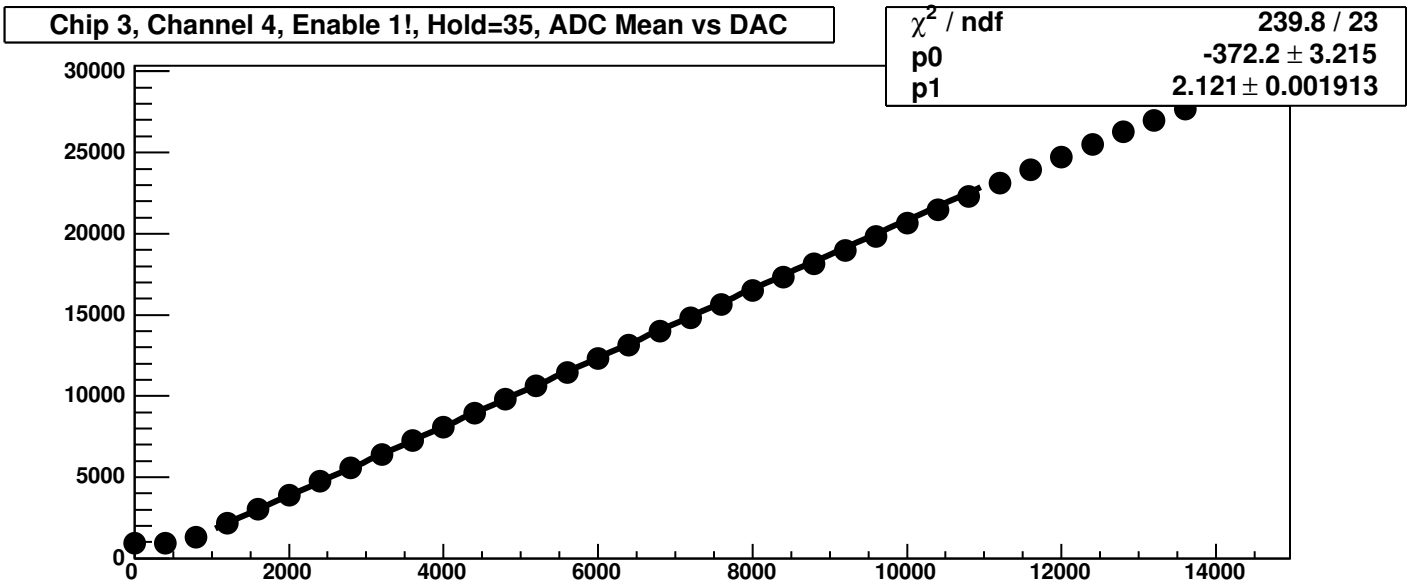

Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

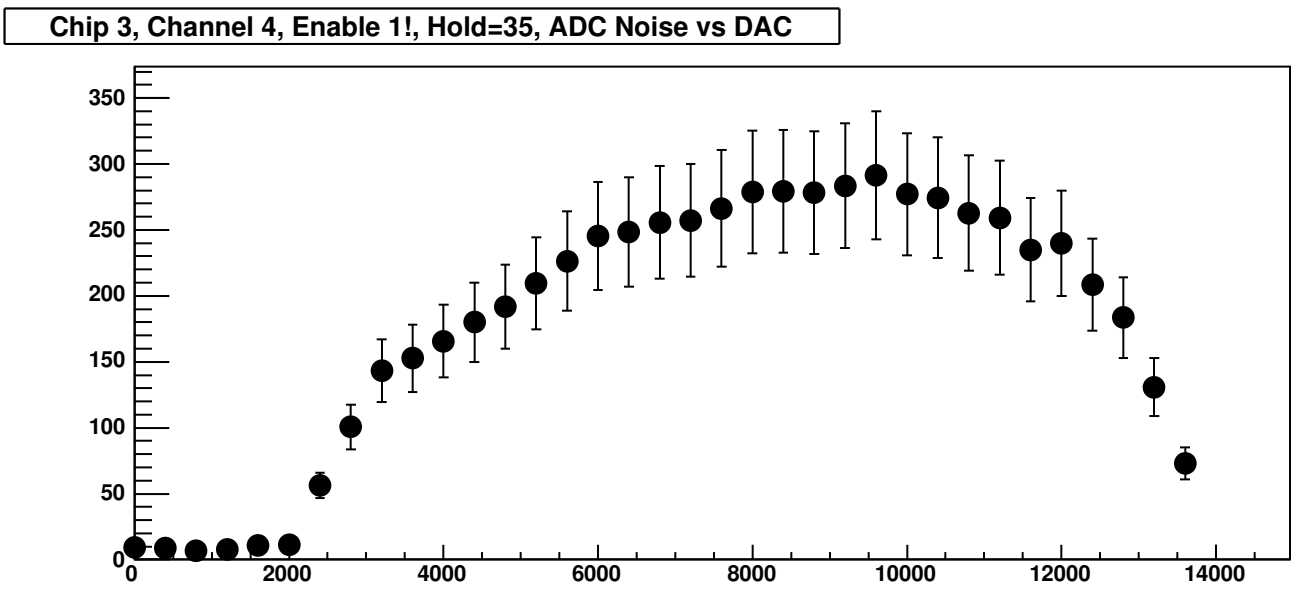

Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

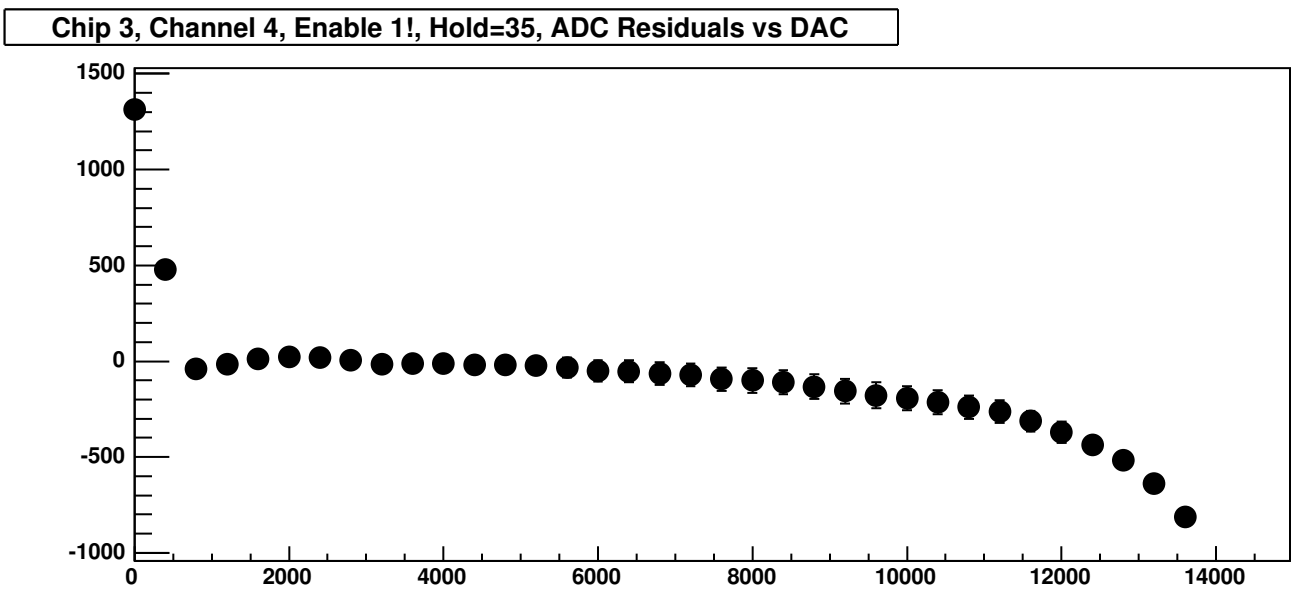

Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 924 / 23
p0 -655.2 ± 1.302
p1 0.1673 ± 0.0002201
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

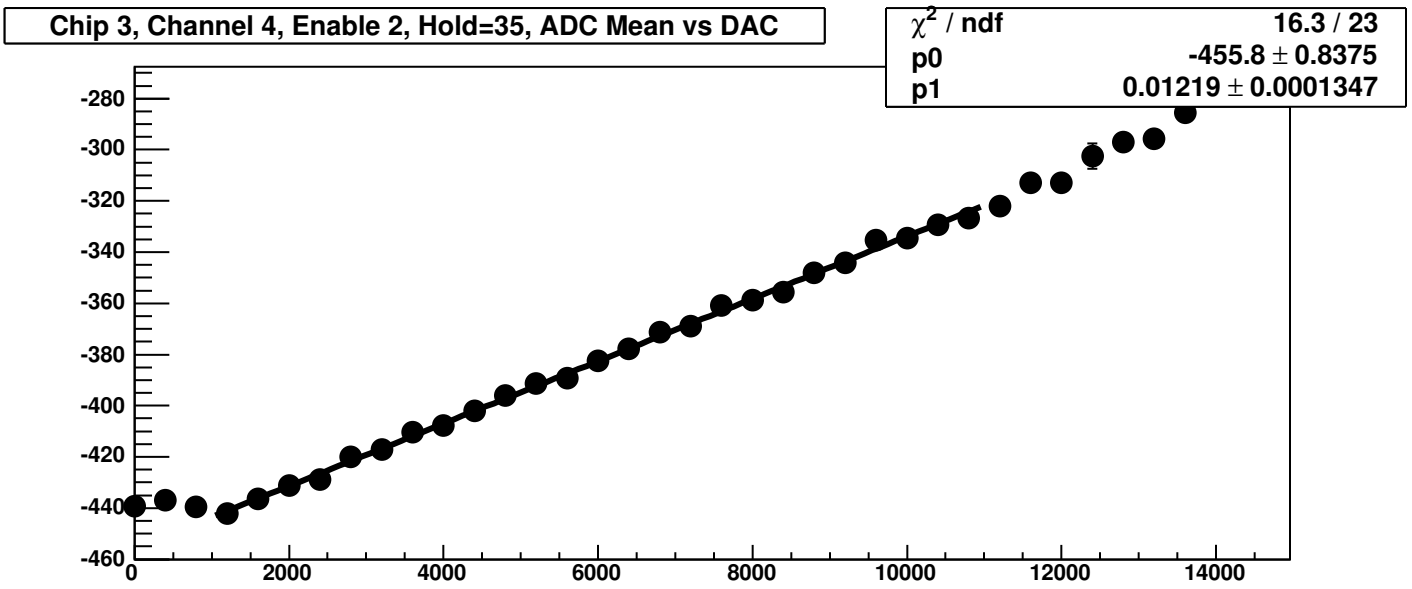

Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

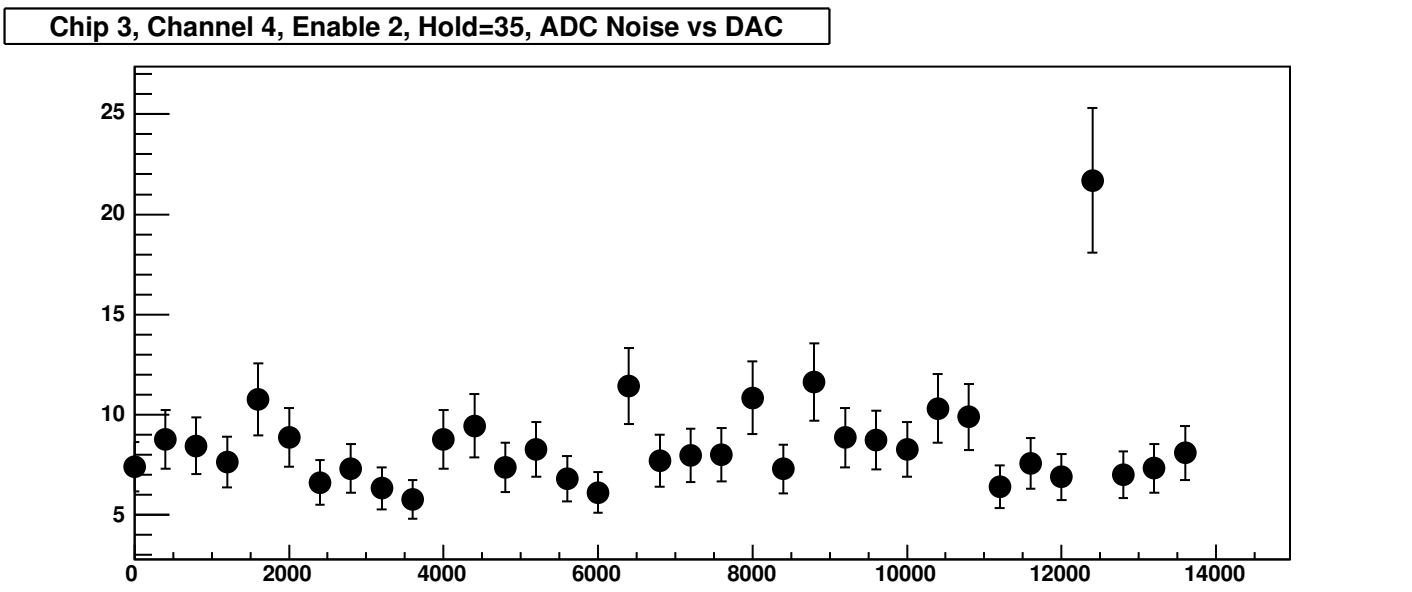

Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

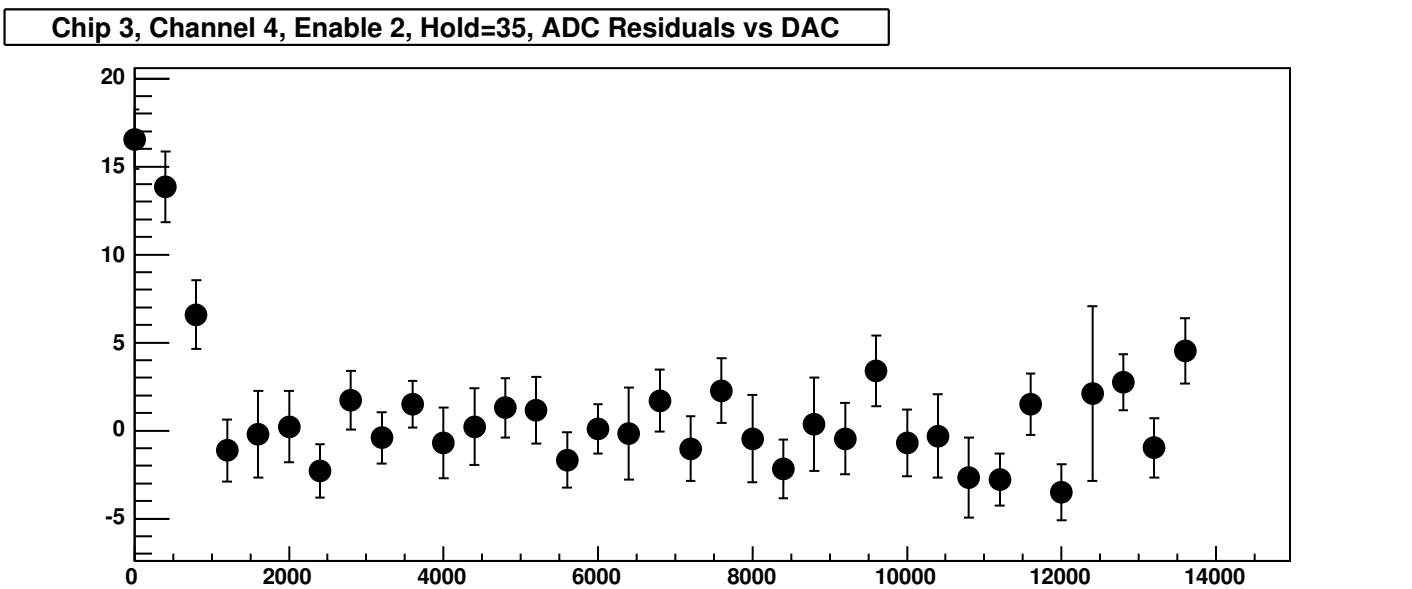

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 239.8 / 23
p0 -372.2 ± 3.215
p1 2.121 ± 0.001913
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

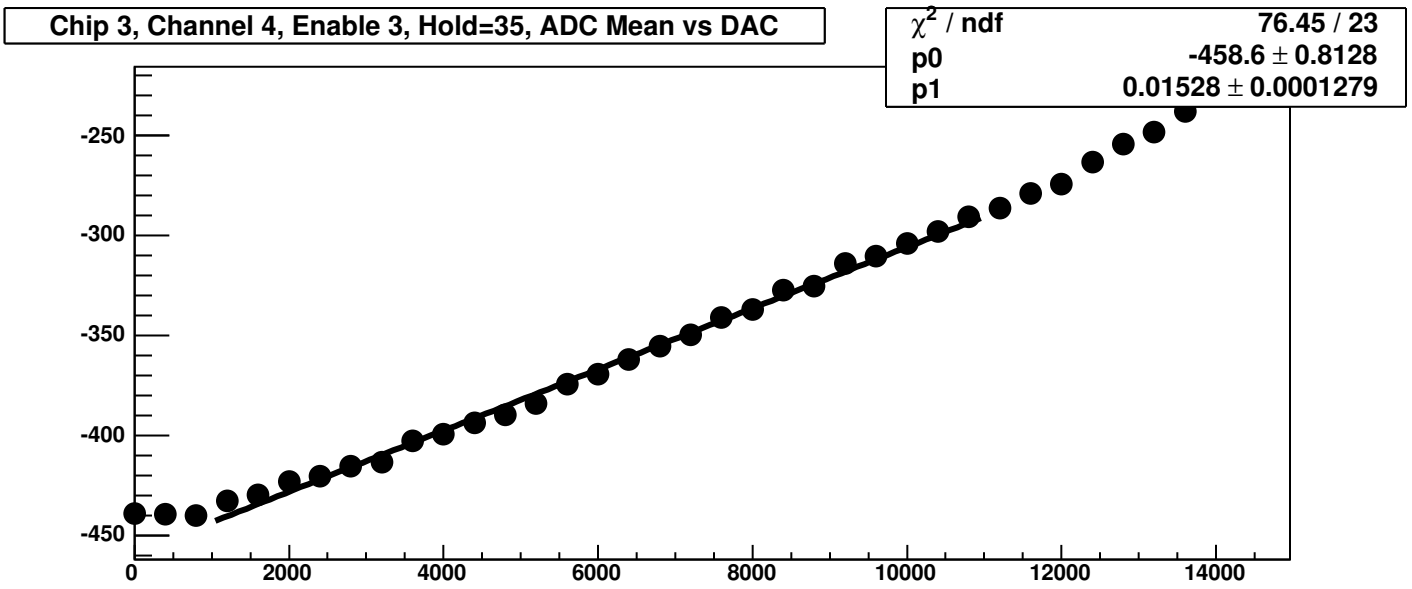

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

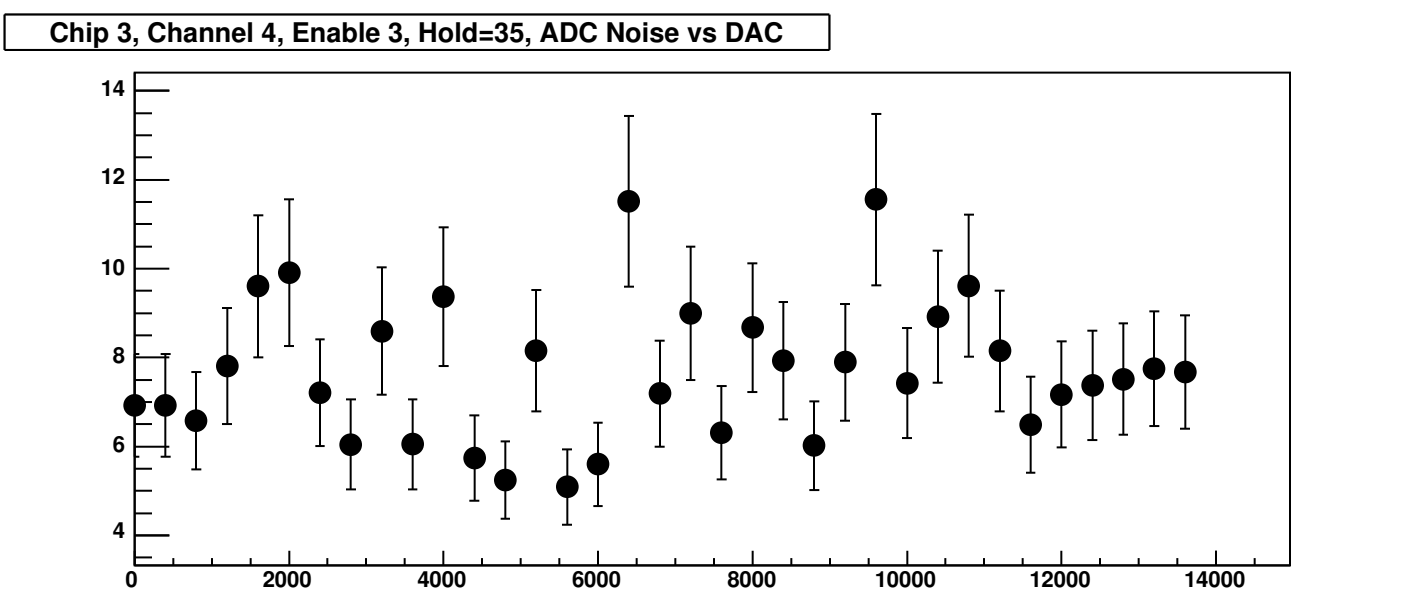

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

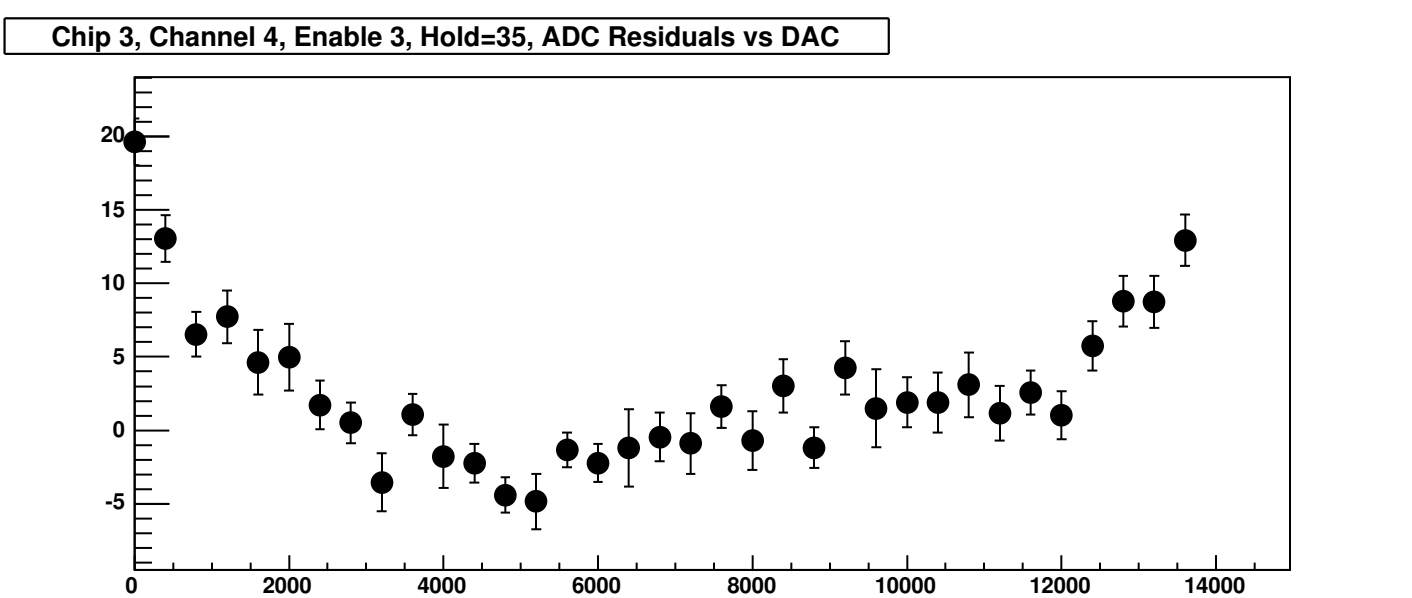

Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 16.3 / 23
p0 -455.8 ± 0.8375
p1 0.01219 ± 0.0001347
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
2000
4000
6000
8000
10000
12000
14000

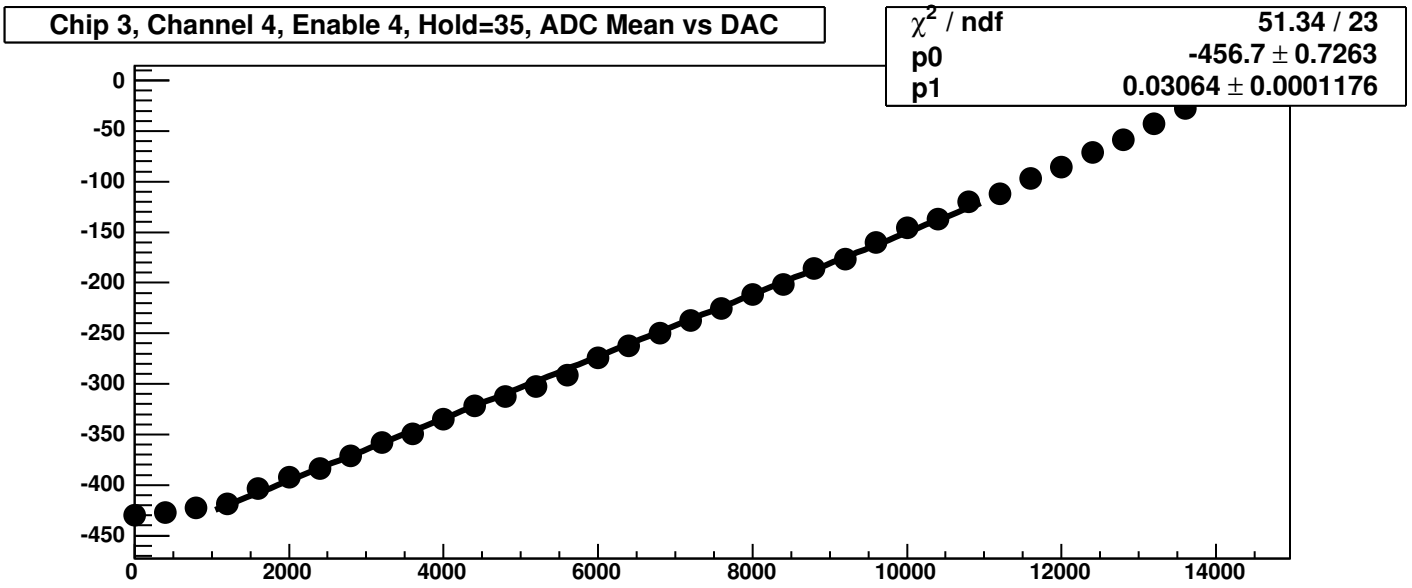

Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

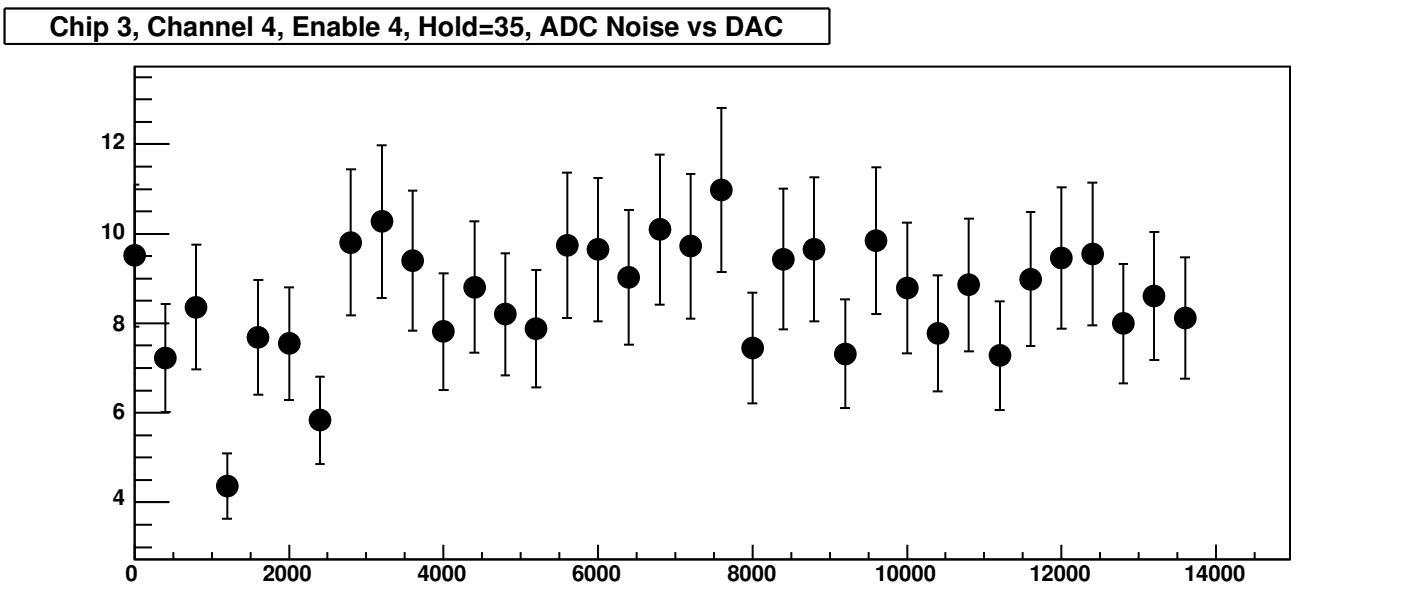

Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

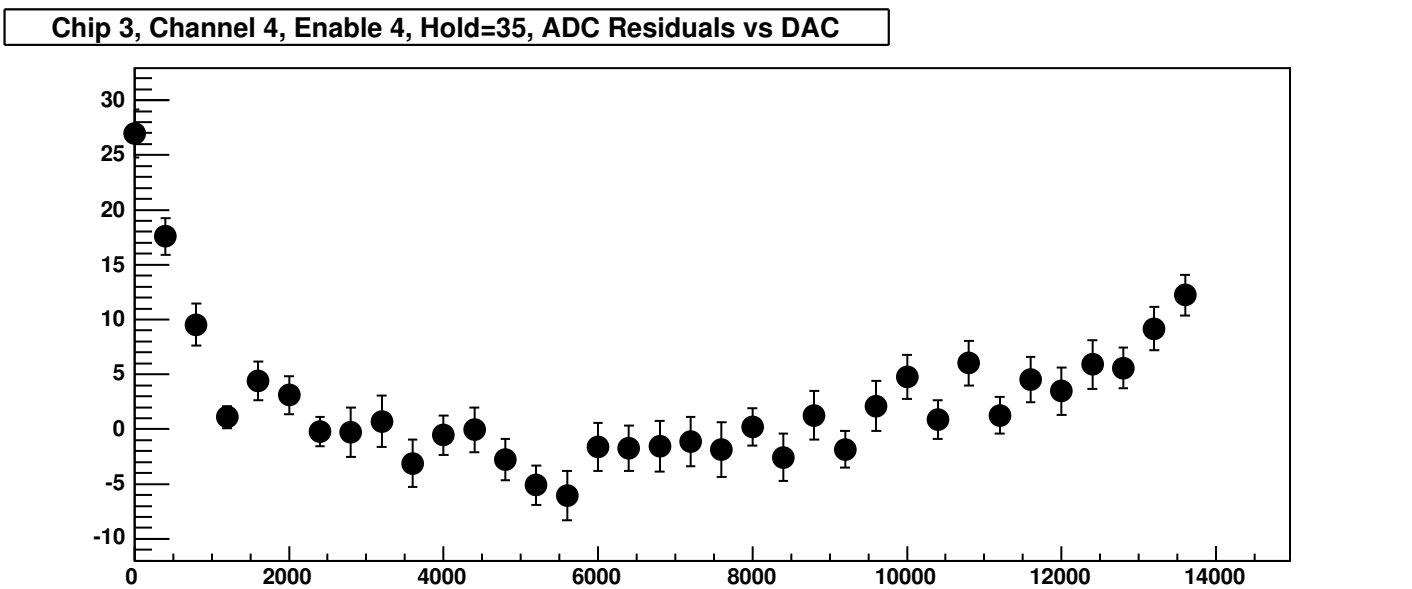

Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 76.45 / 23
p0 -458.6 ± 0.8128
p1 0.01528 ± 0.0001279
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

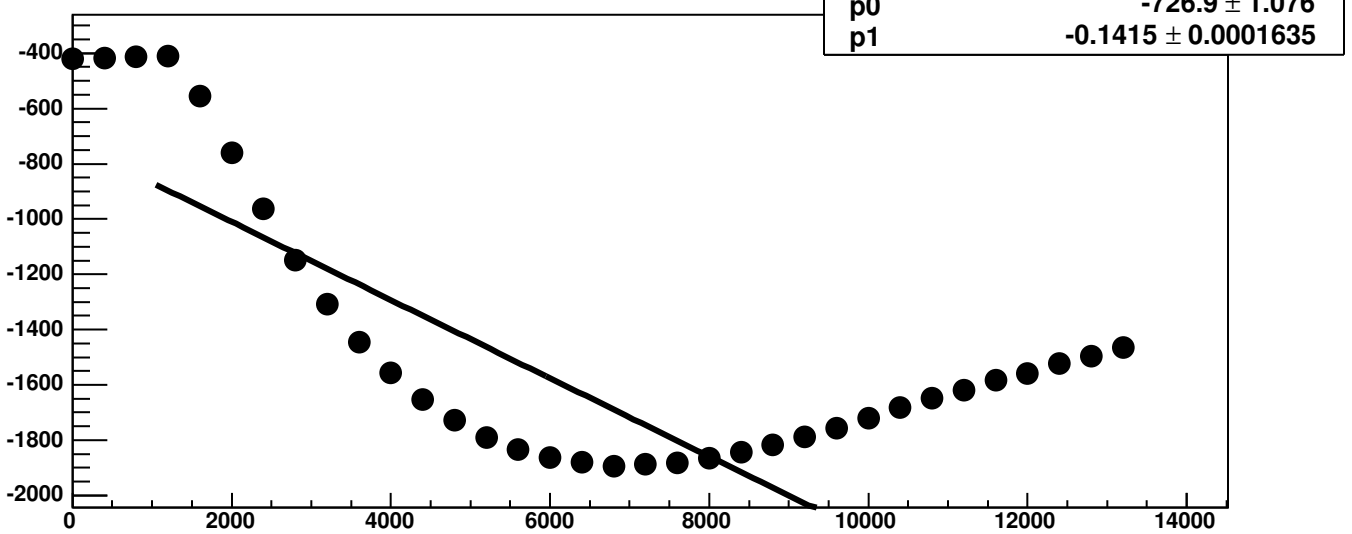

Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 51.34 / 23
p0 -456.7 ± 0.7263
p1 0.03064 ± 0.0001176
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

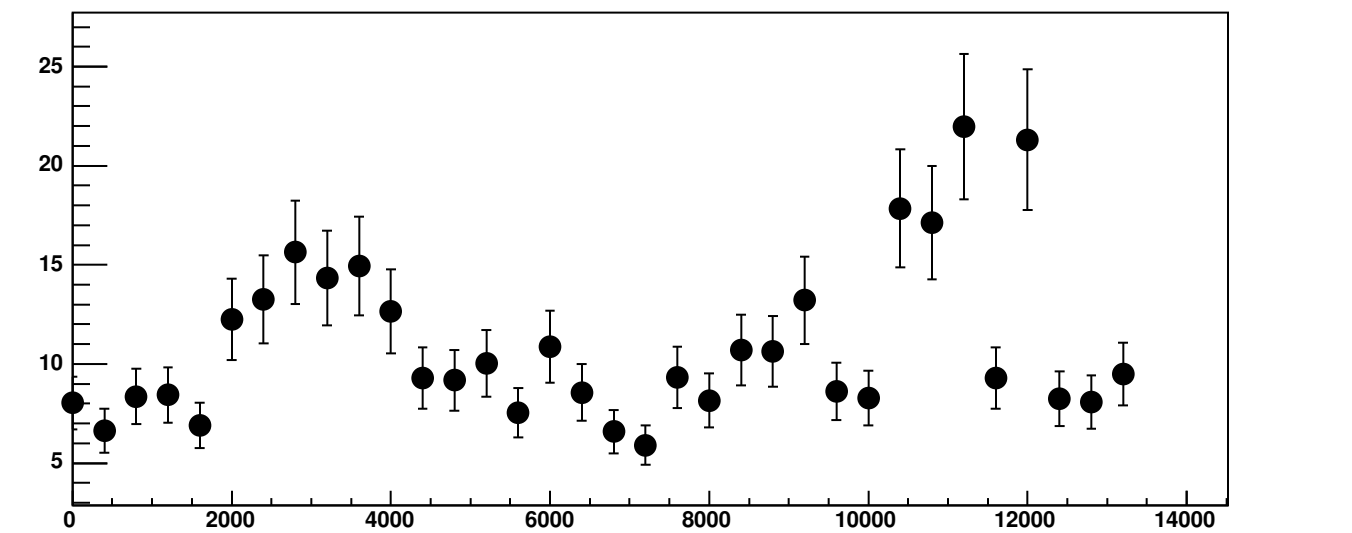

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

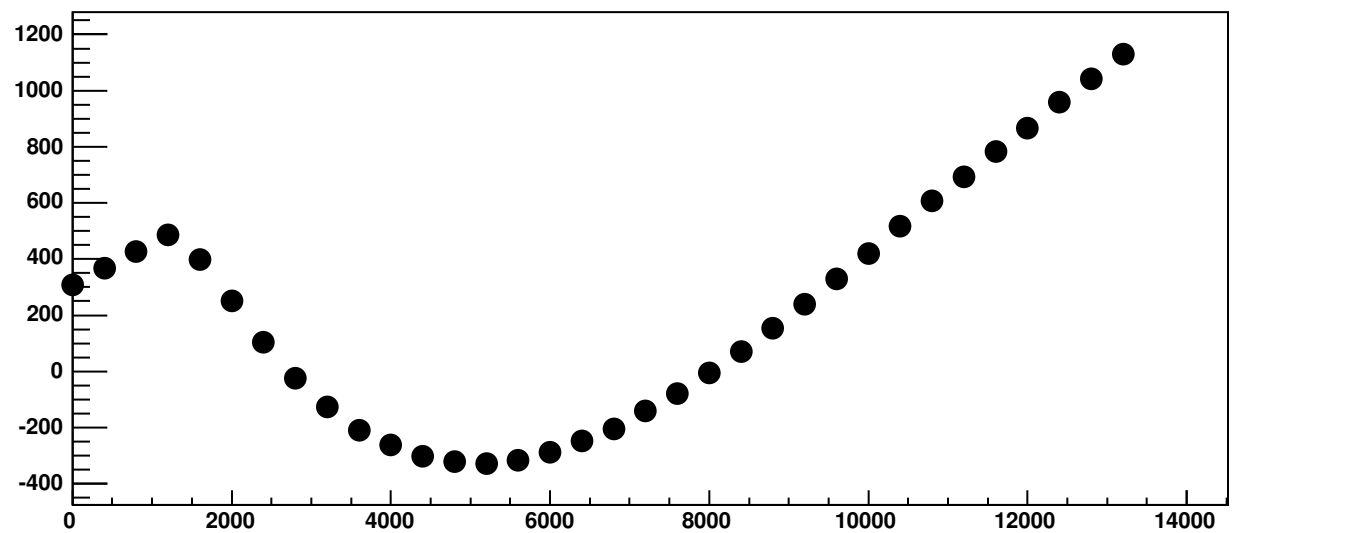
Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

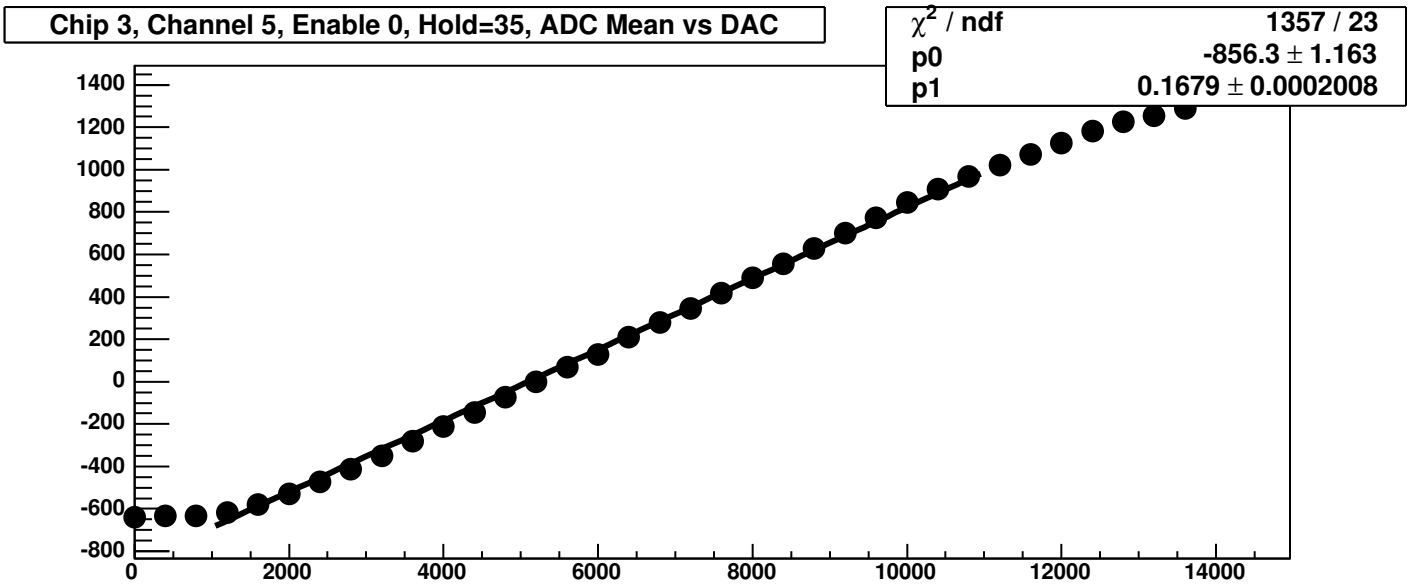


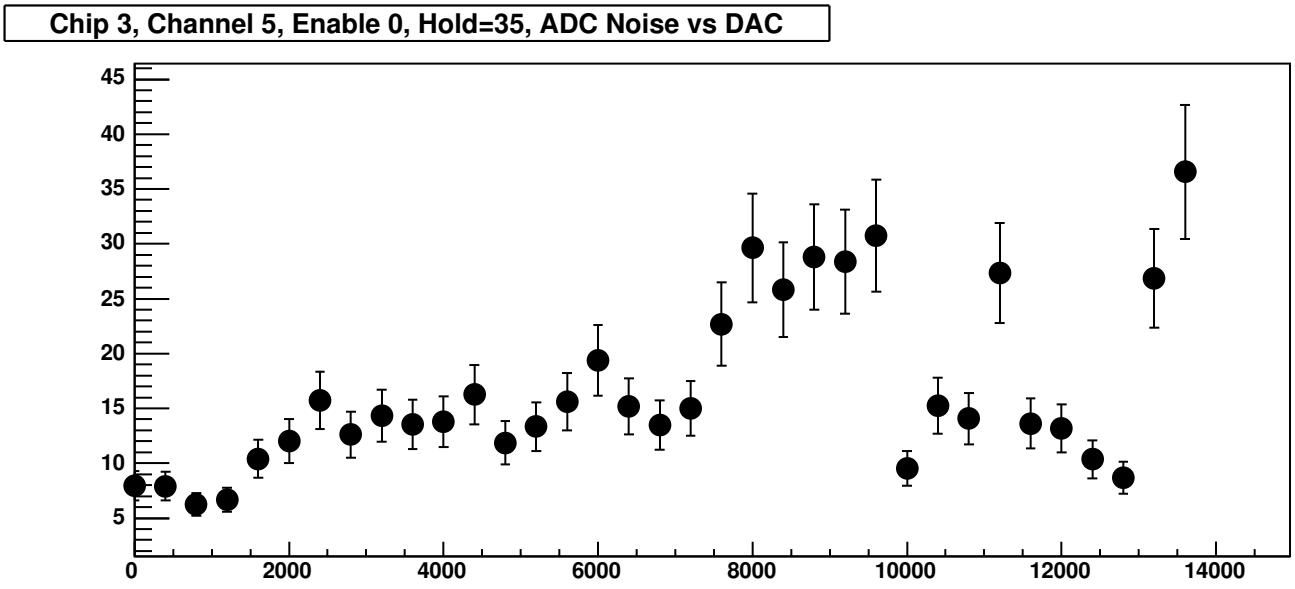
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

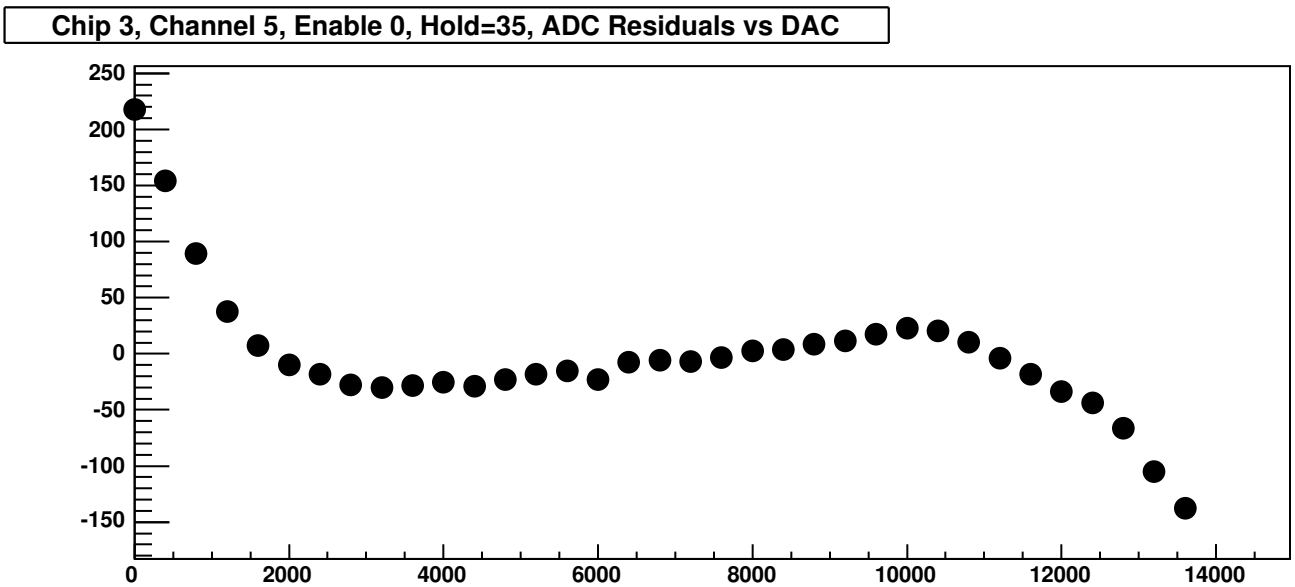


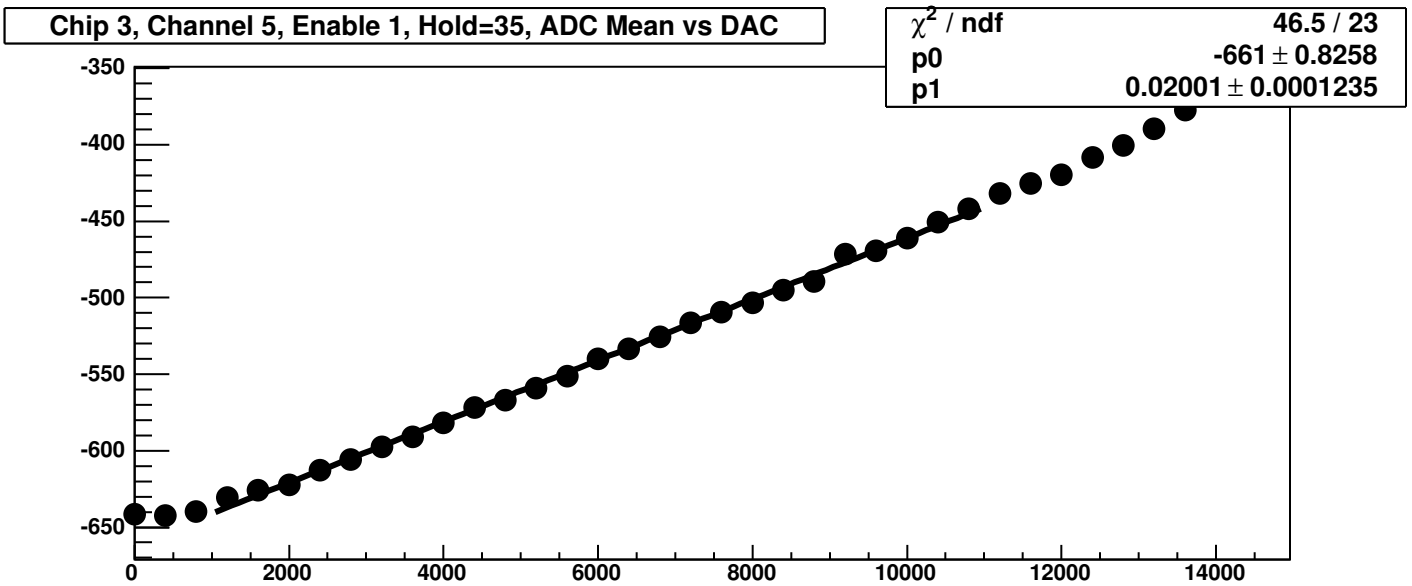
Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

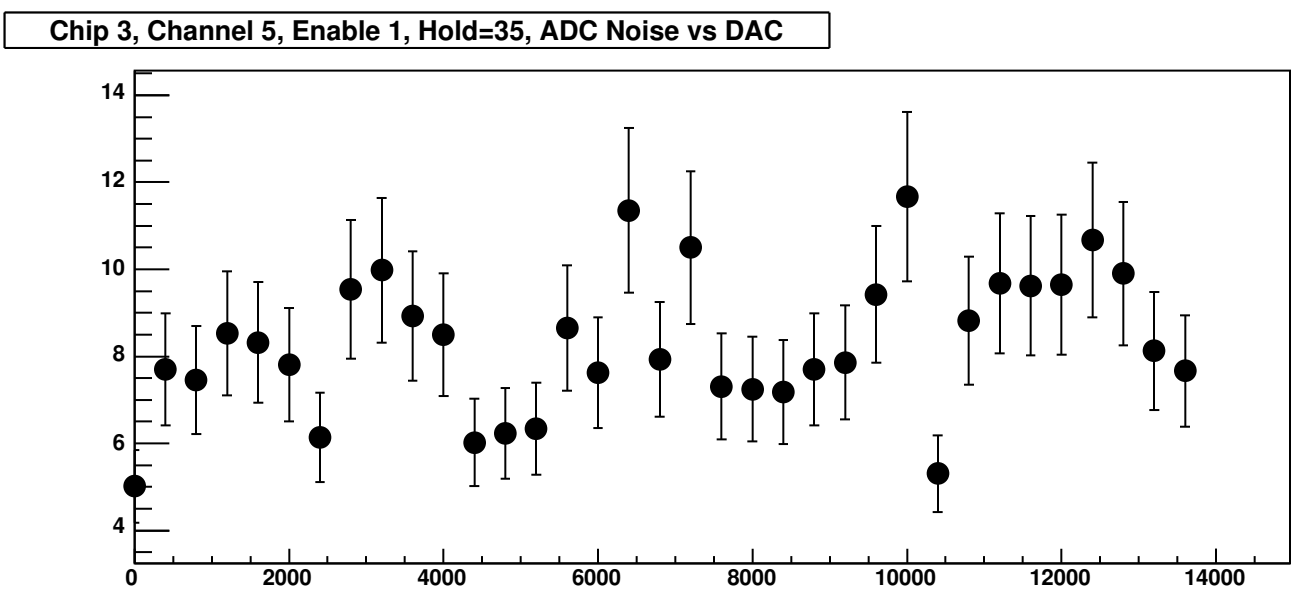


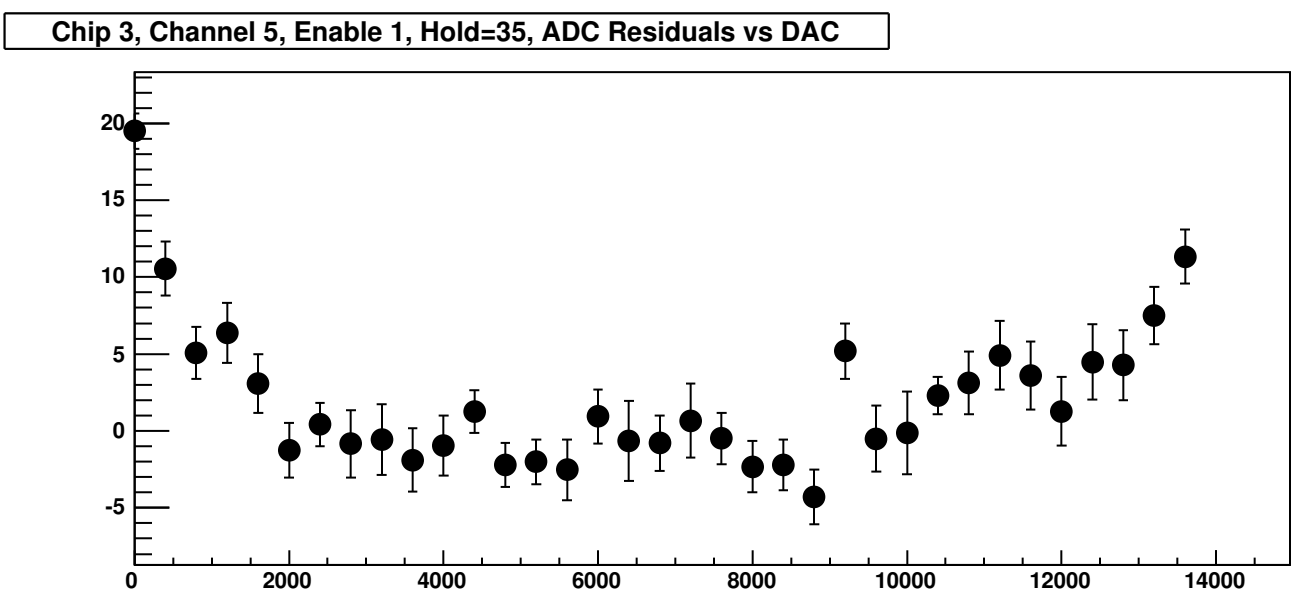

Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1357 / 23
p0 -856.3 ± 1.163
p1 0.1679 ± 0.0002008
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

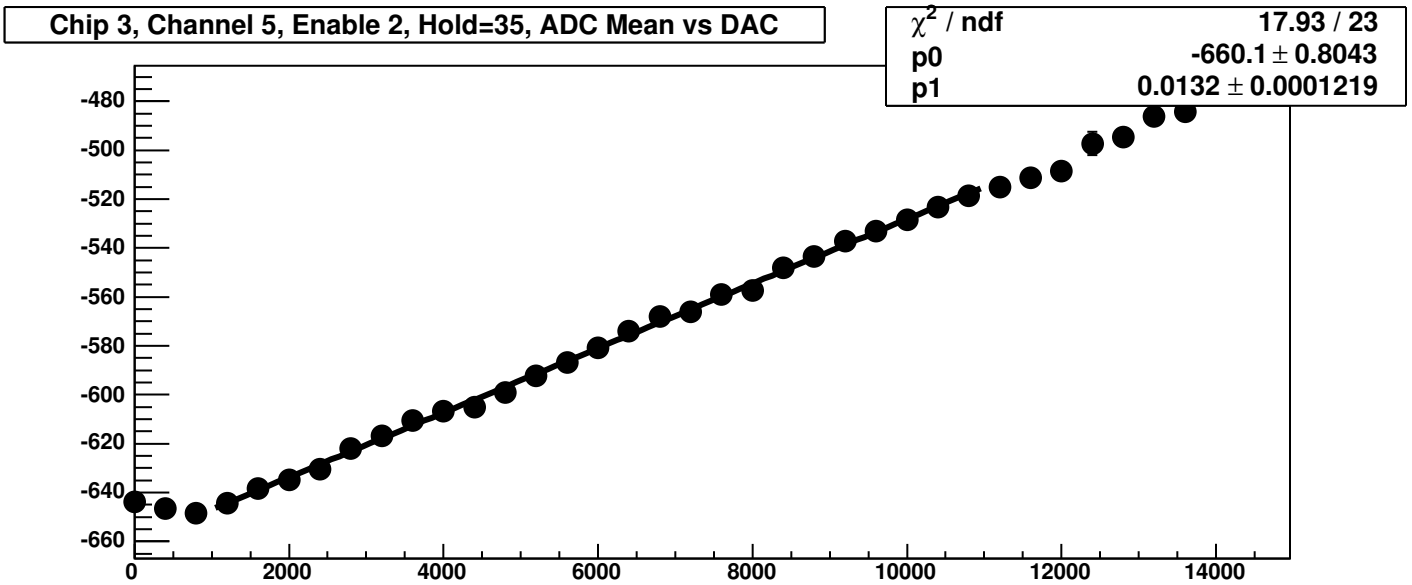

Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

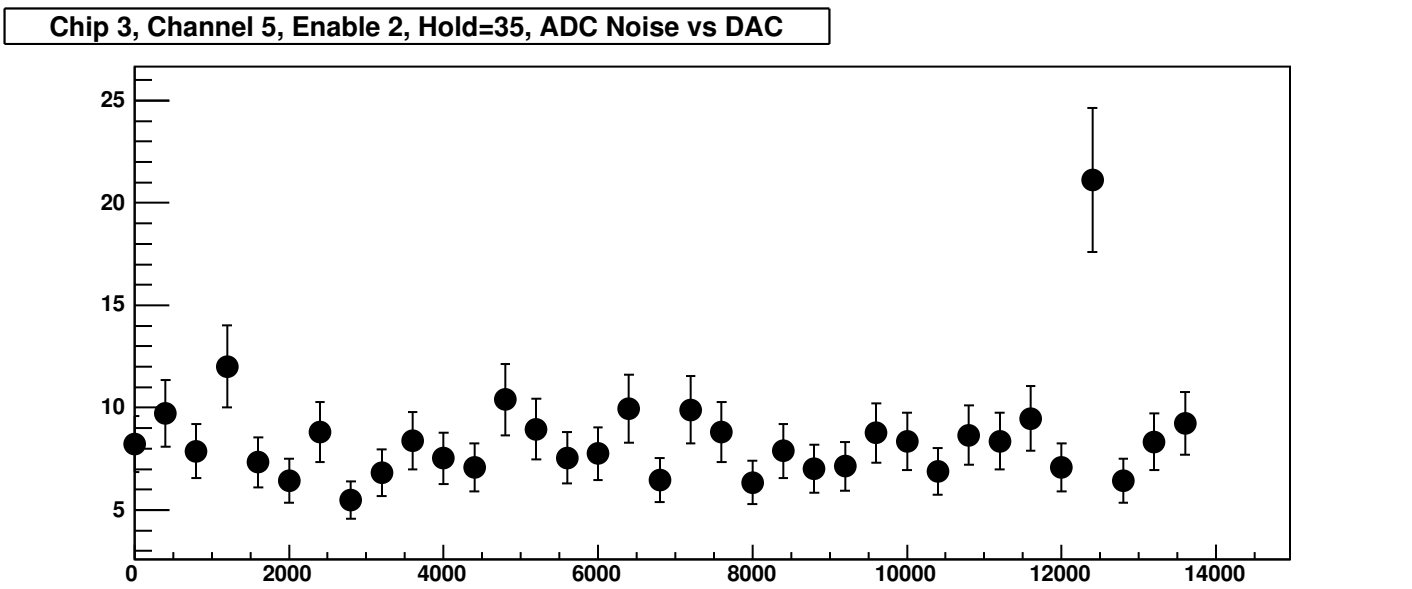

Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

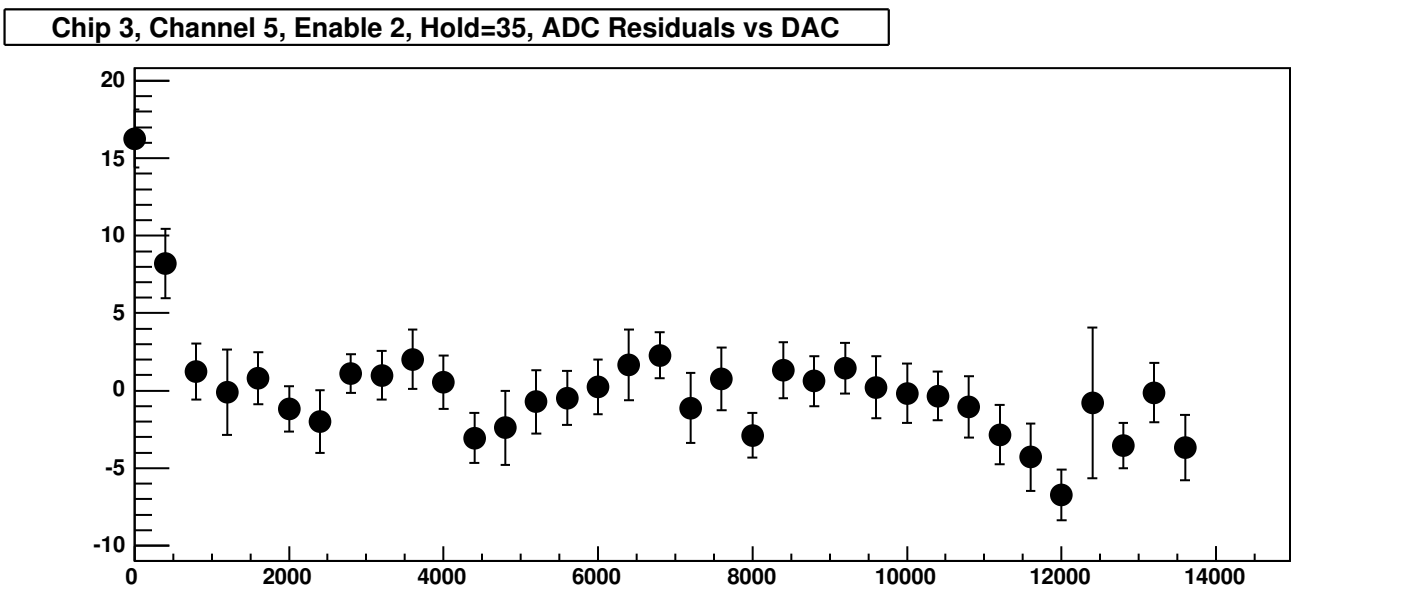

Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.5 / 23
p0 -661 ± 0.8258
p1 0.02001 ± 0.0001235
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

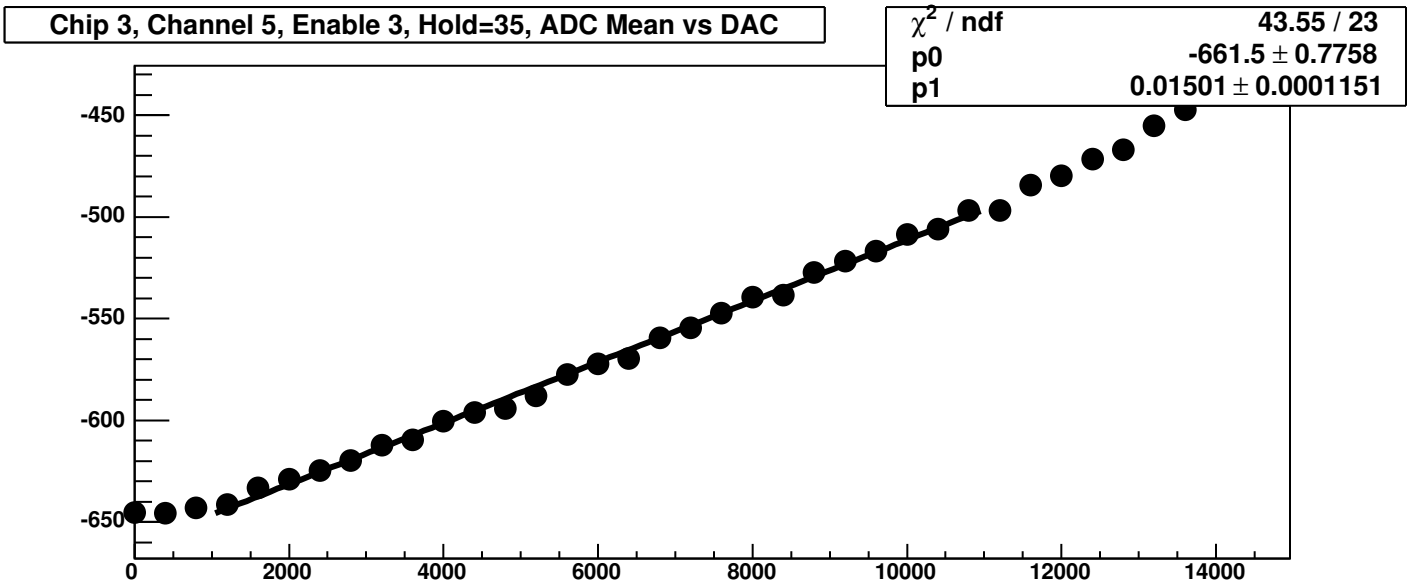

Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

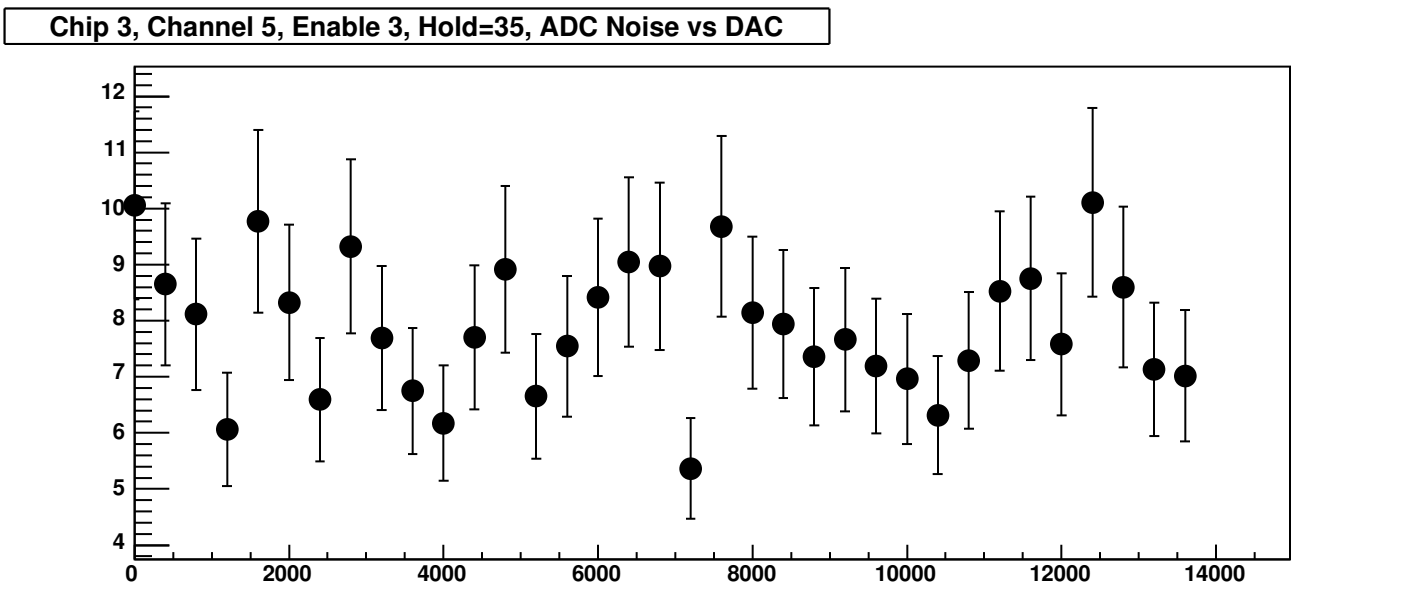

Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

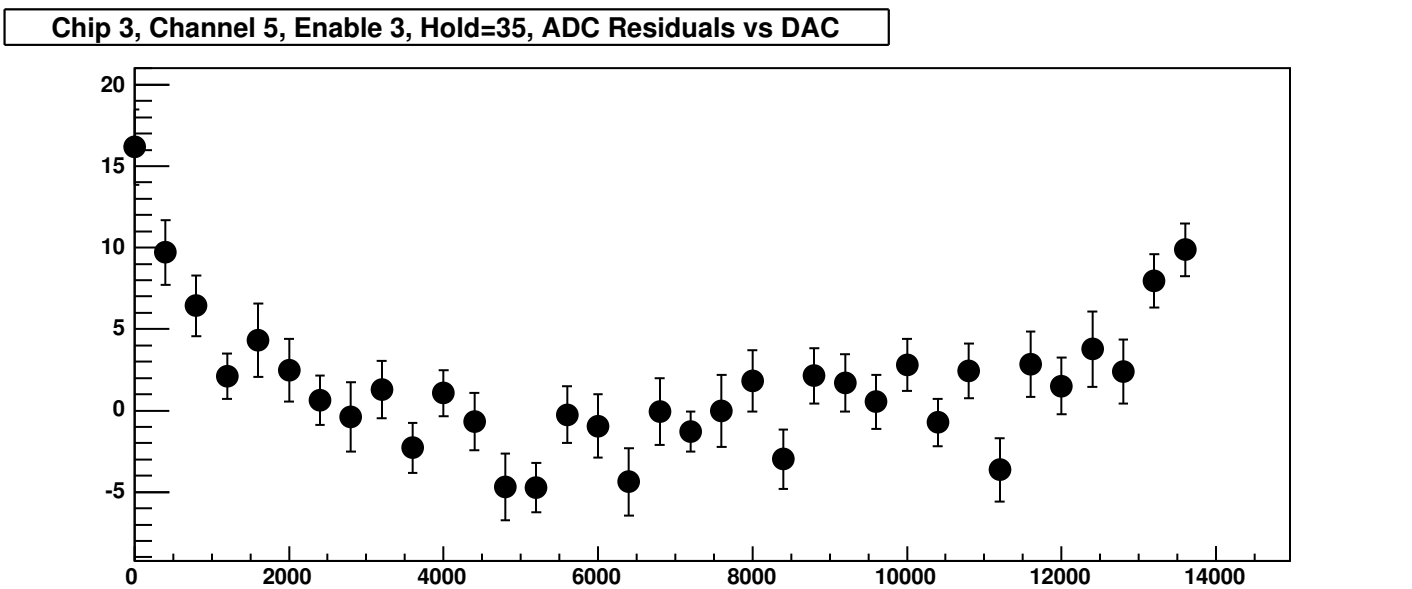

Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.93 / 23
p0 -660.1 ± 0.8043
p1 0.0132 ± 0.0001219
-480
-500
-520
-540
-560
-580
-600
-620
-640
-660
0
2000
4000
6000
8000
10000
12000
14000

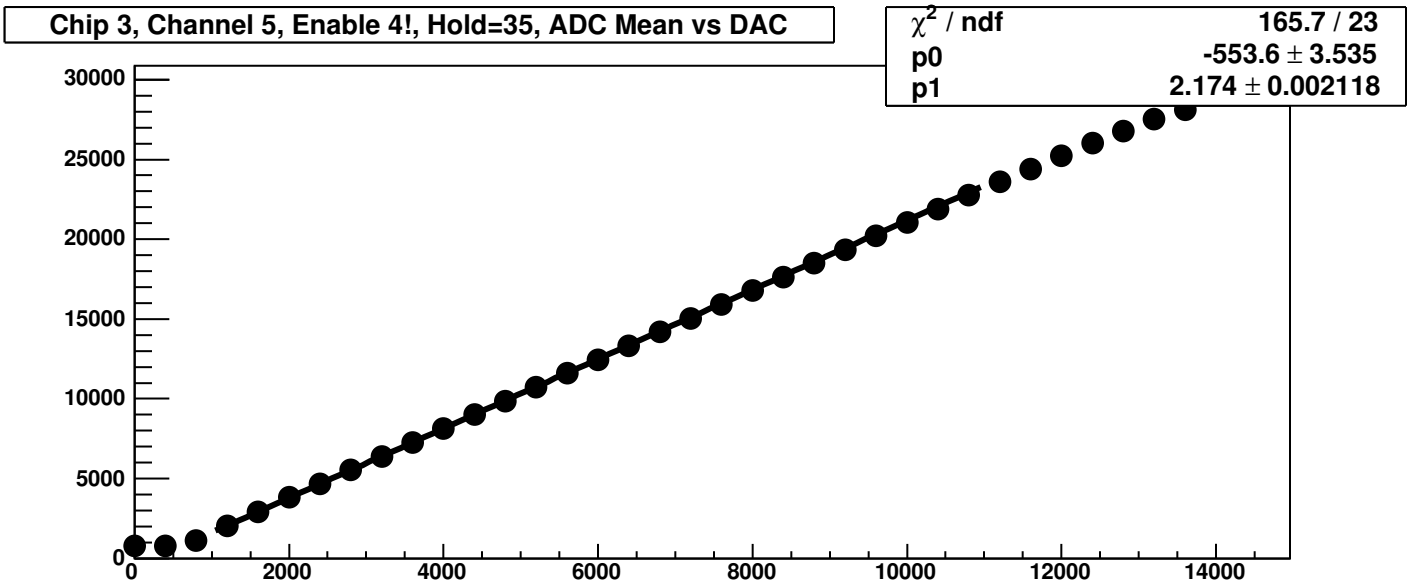

Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

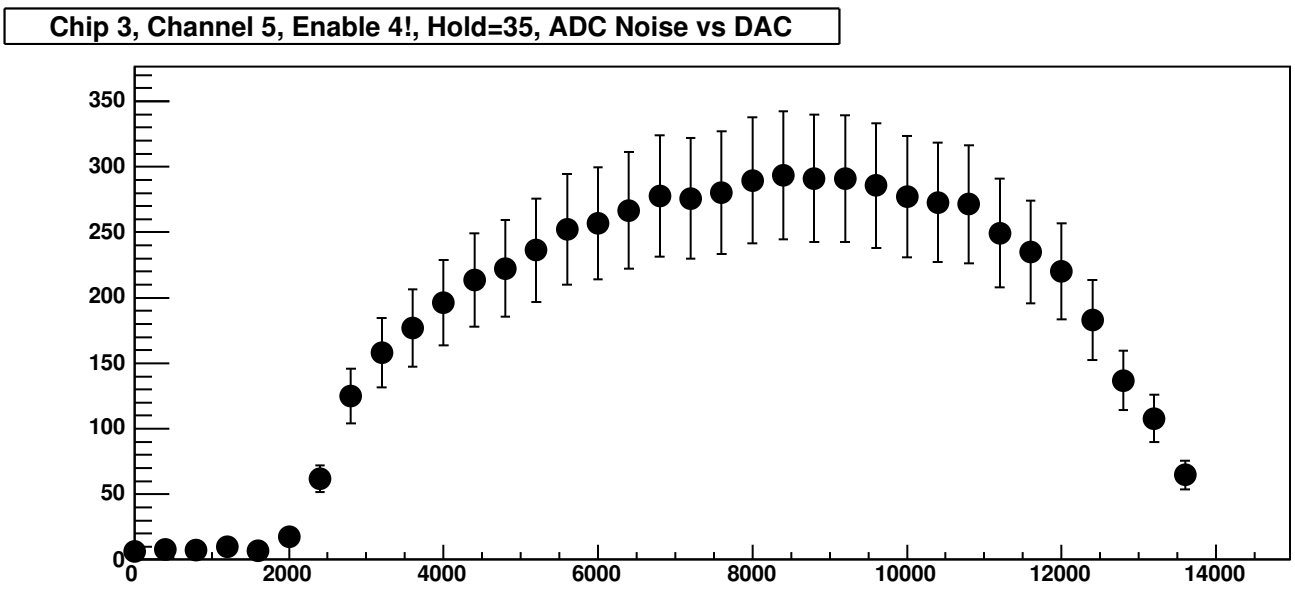

Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

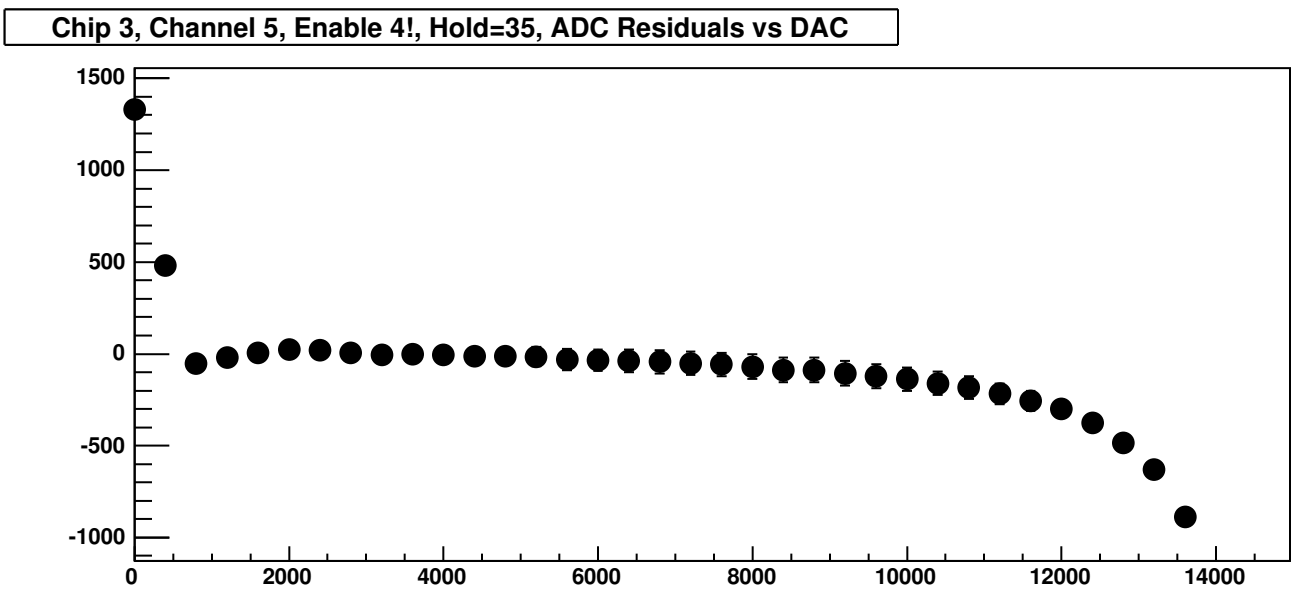

Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.55 / 23
p0 -661.5 ± 0.7758
p1 0.01501 ± 0.0001151
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

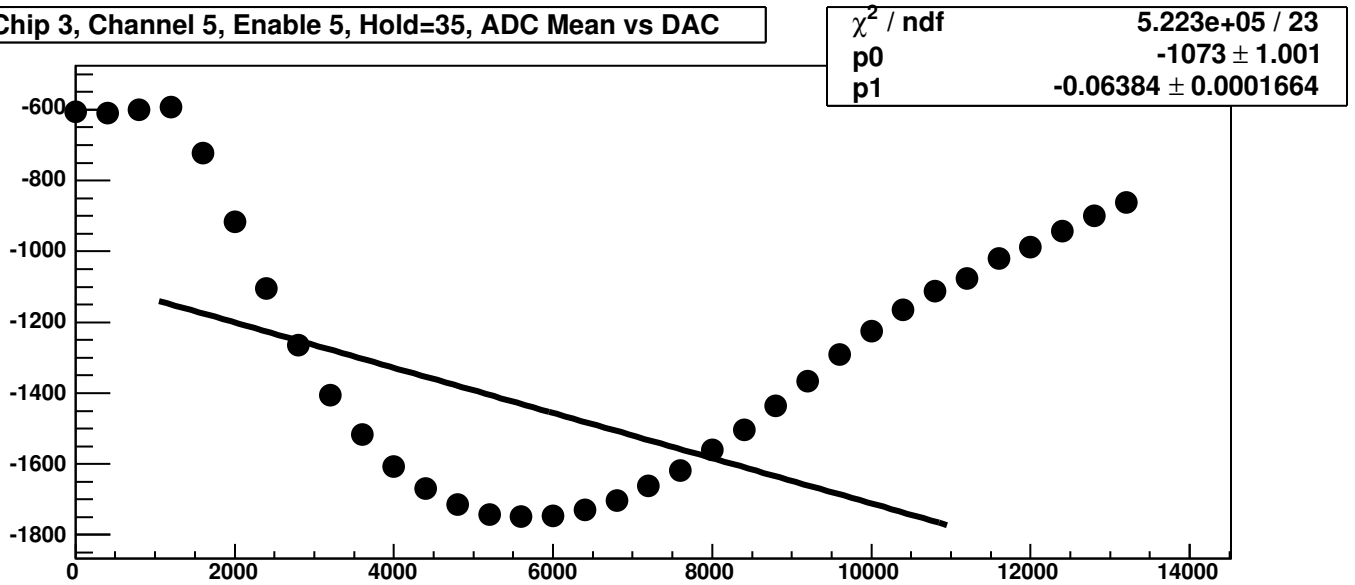

Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 165.7 / 23
p0 -553.6 ± 3.535
p1 2.174 ± 0.002118
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

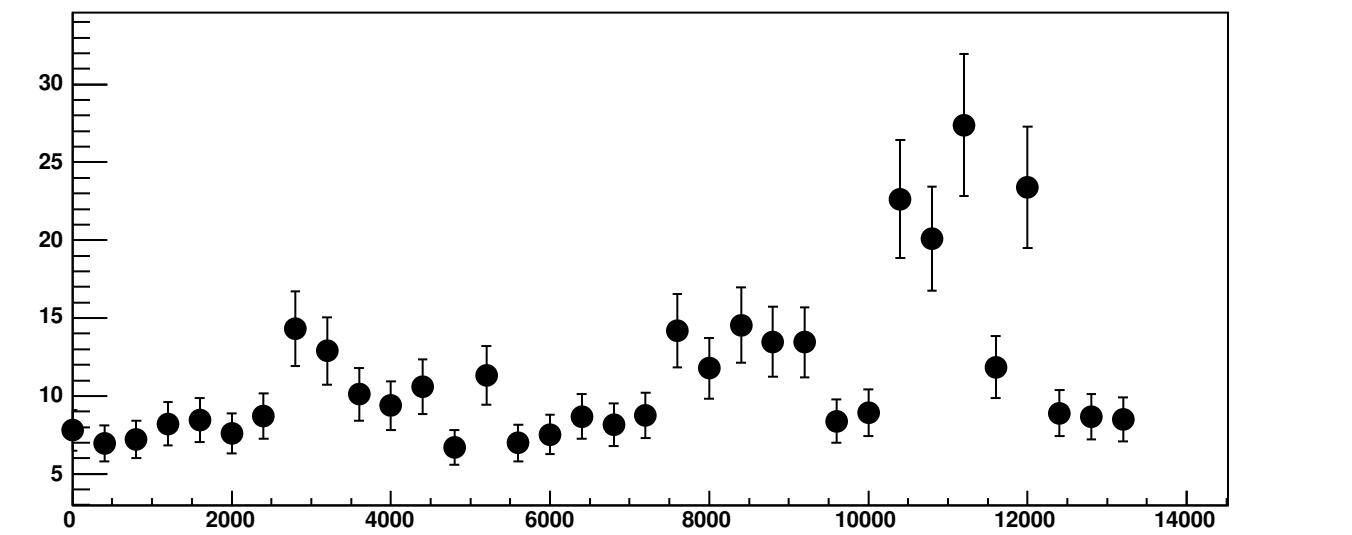

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

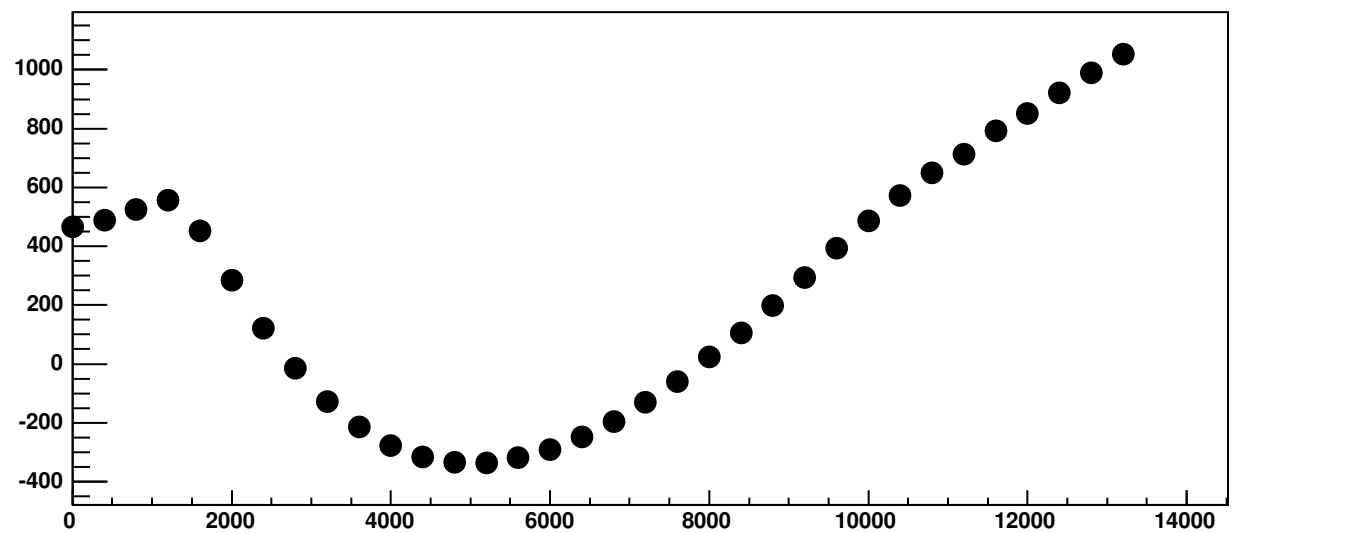
Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

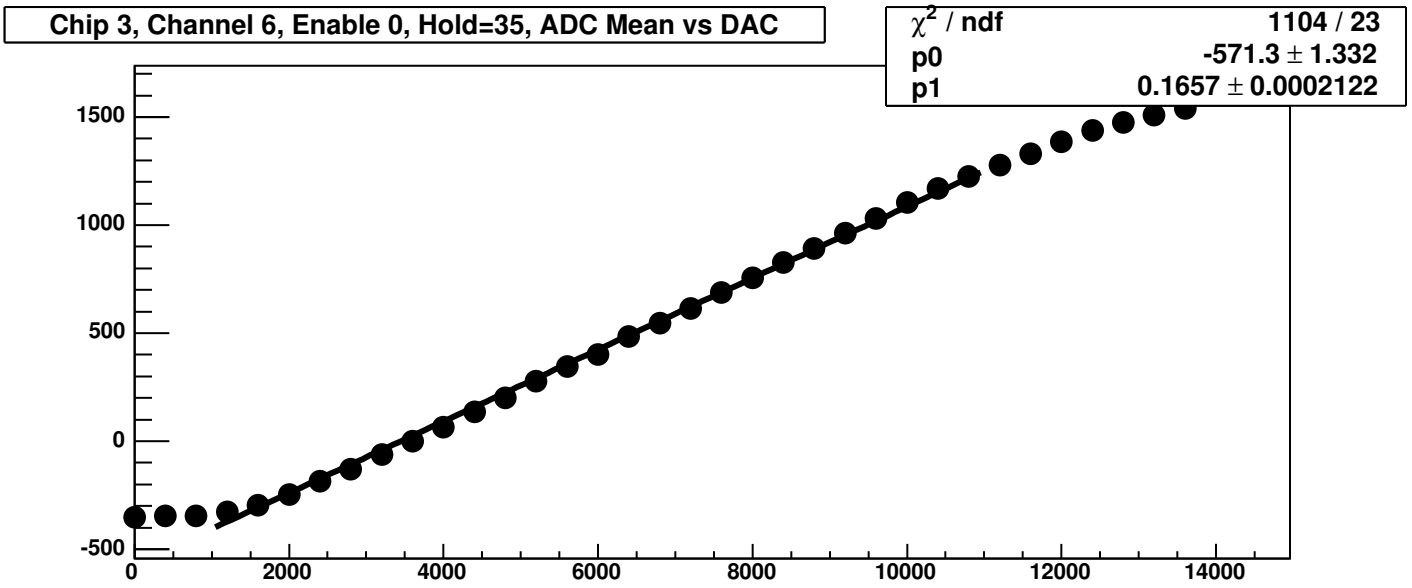


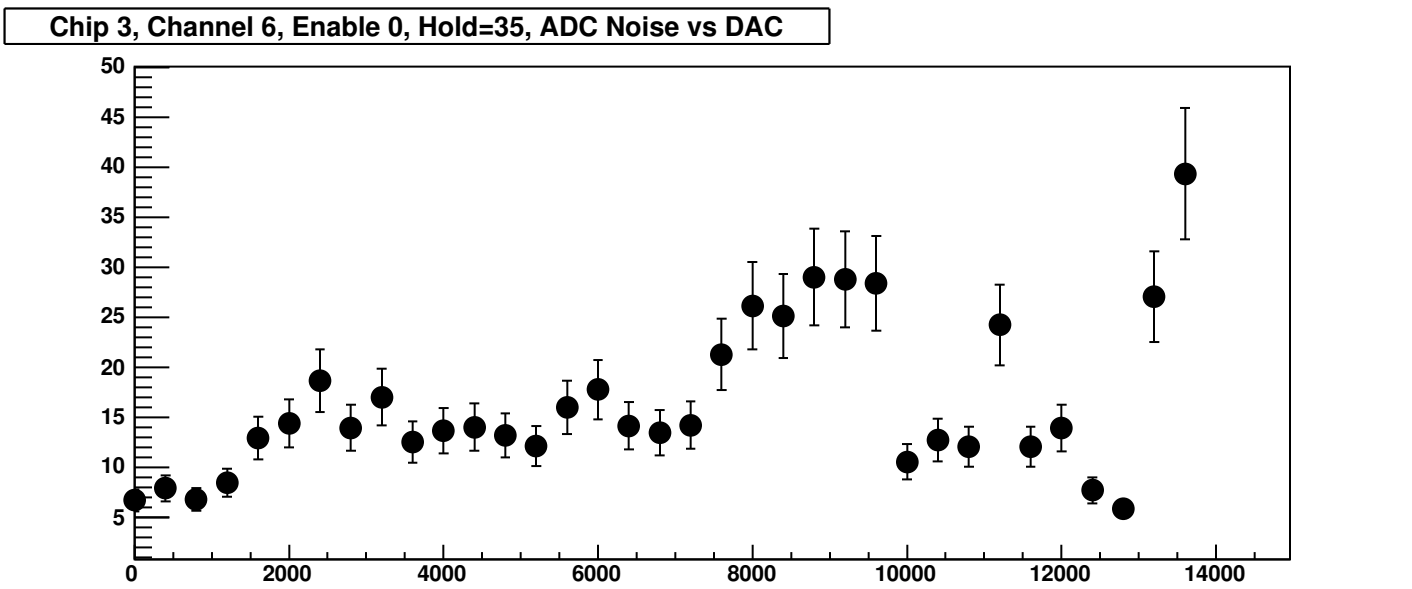
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

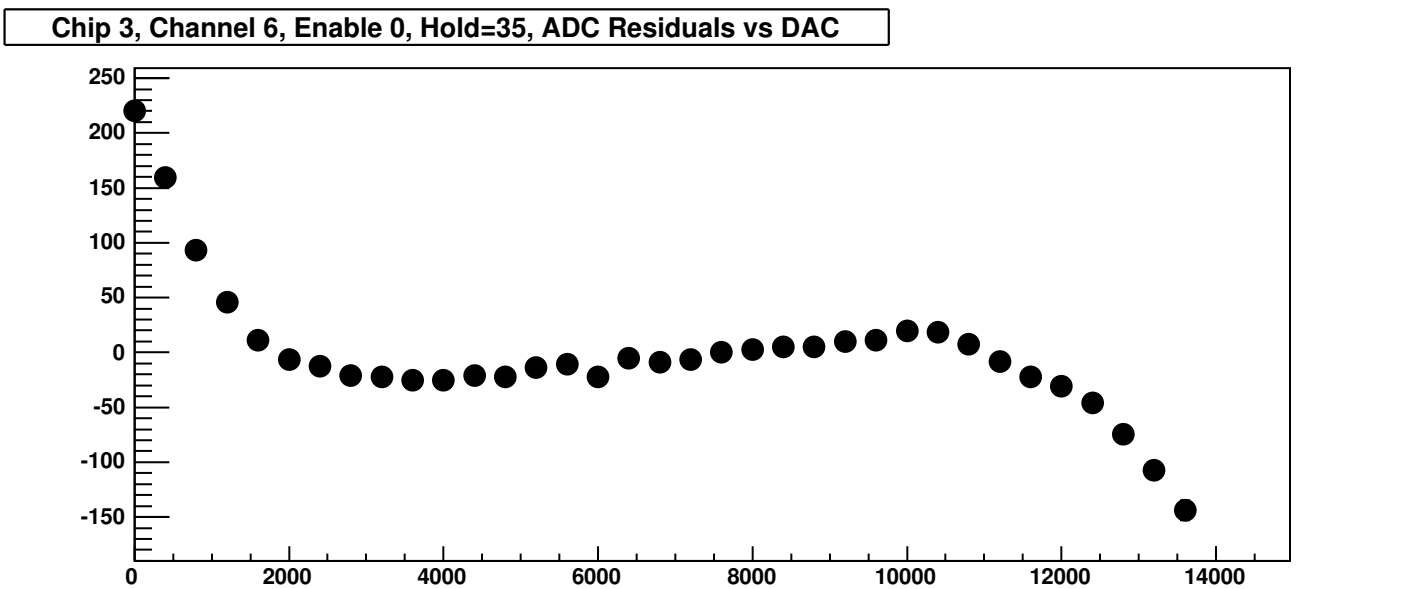


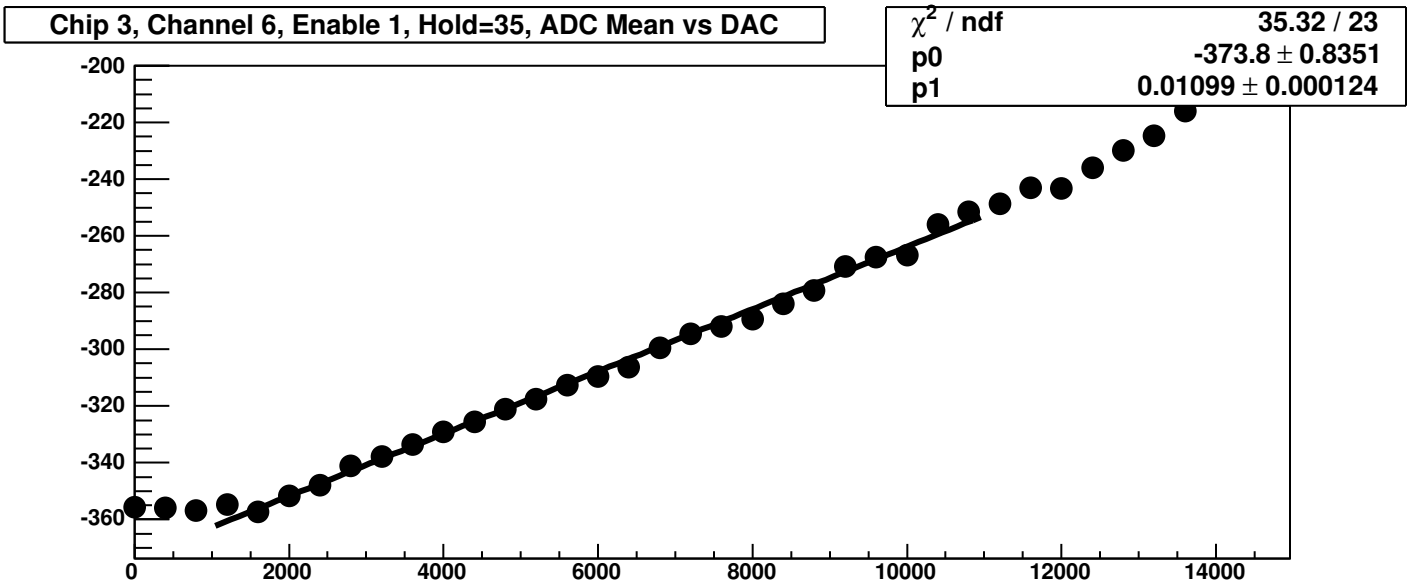
Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

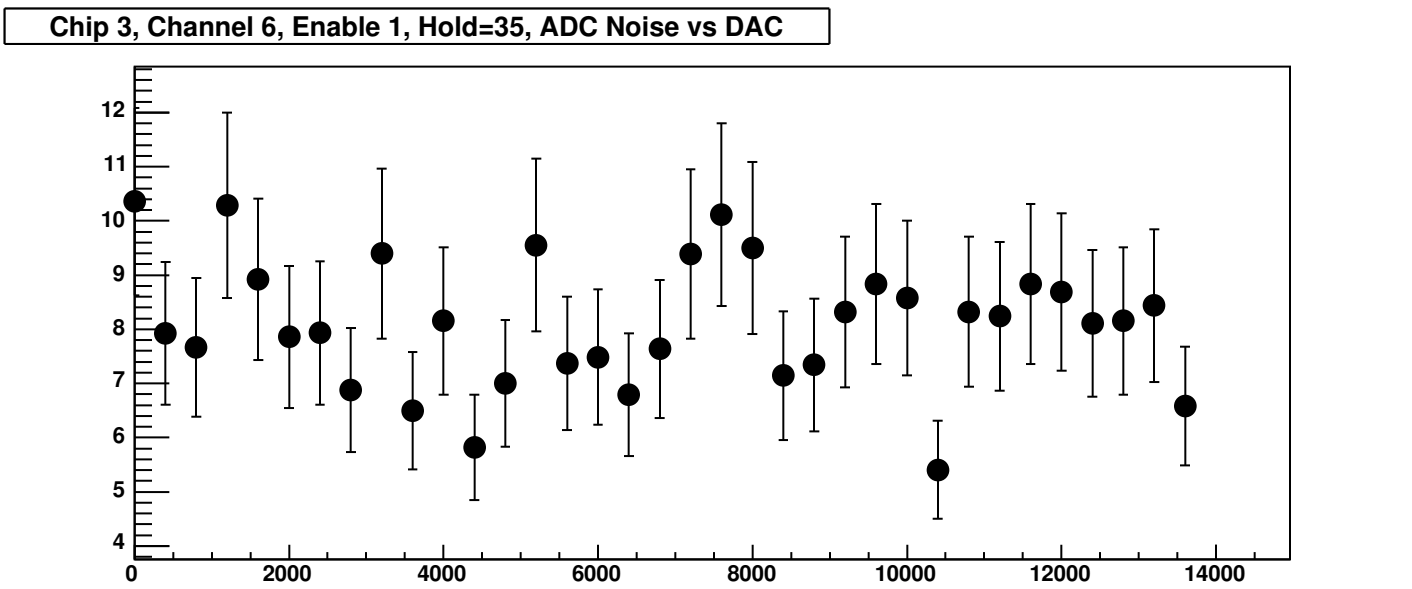


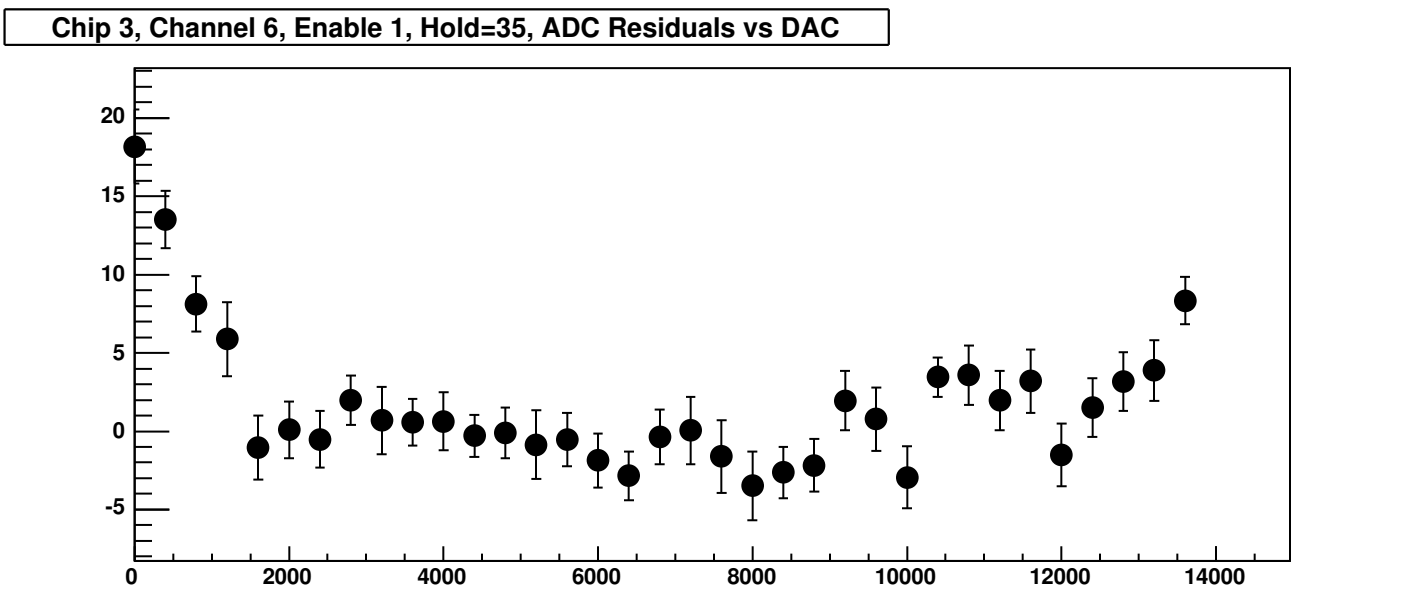

Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1104 / 23
p0 -571.3 ± 1.332
p1 0.1657 ± 0.0002122
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

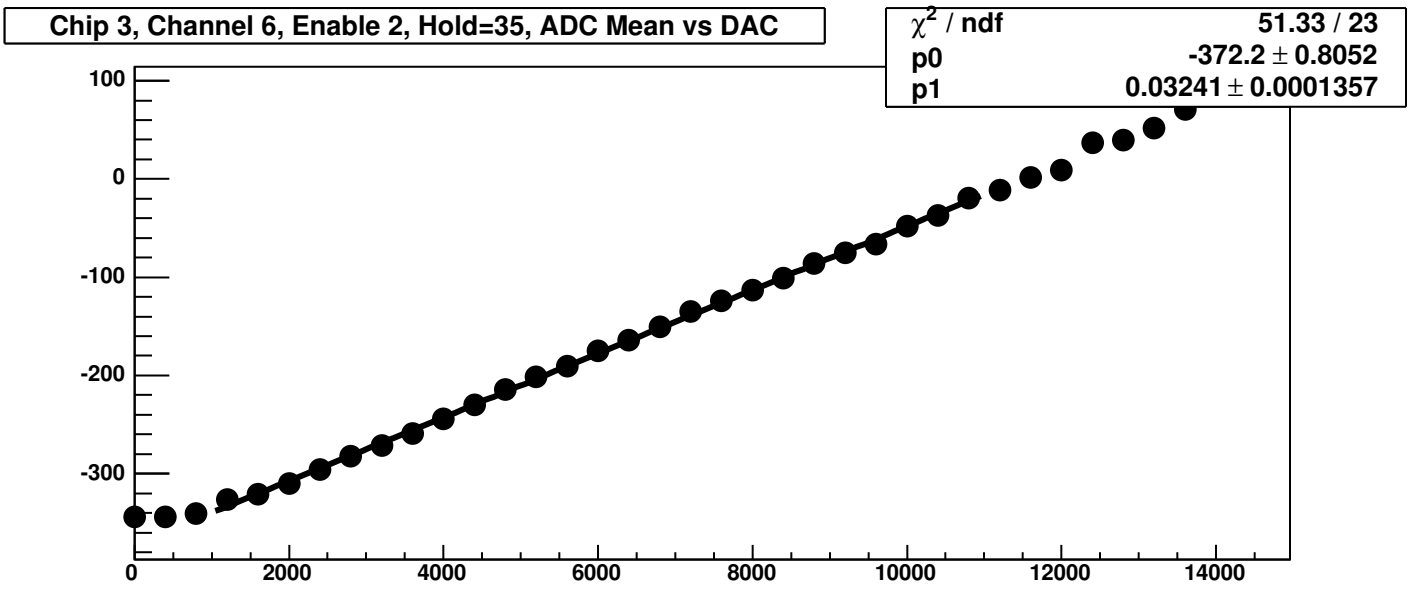

Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

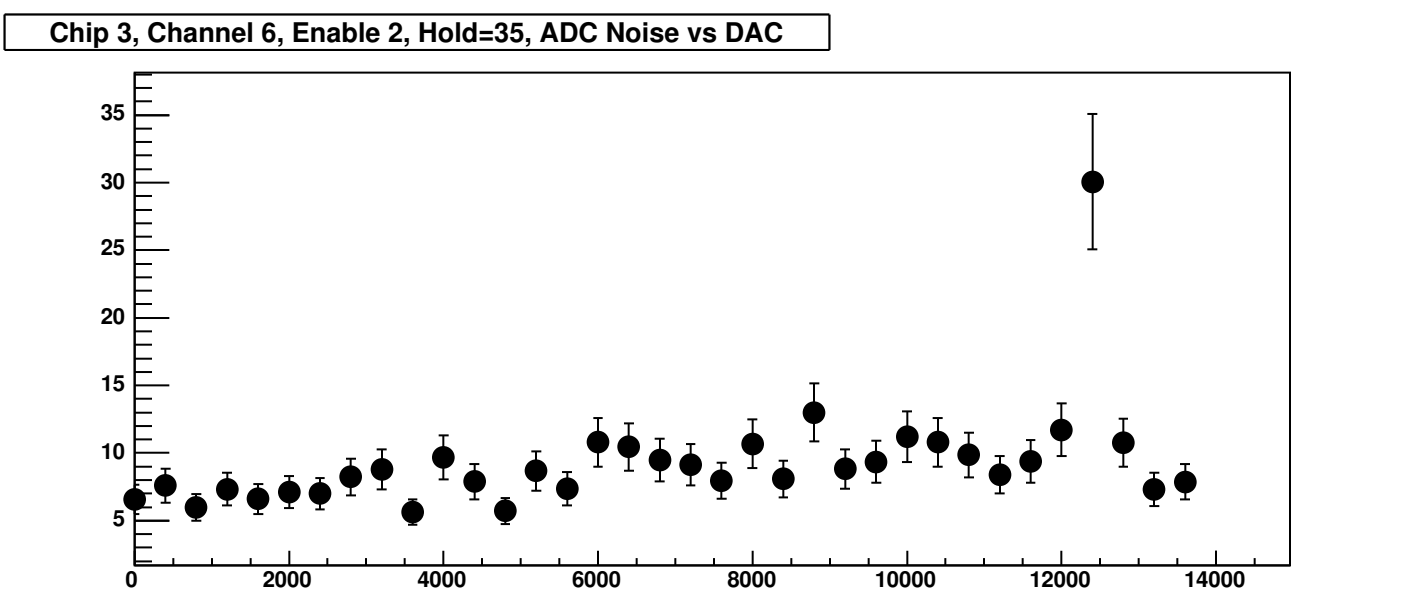

Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

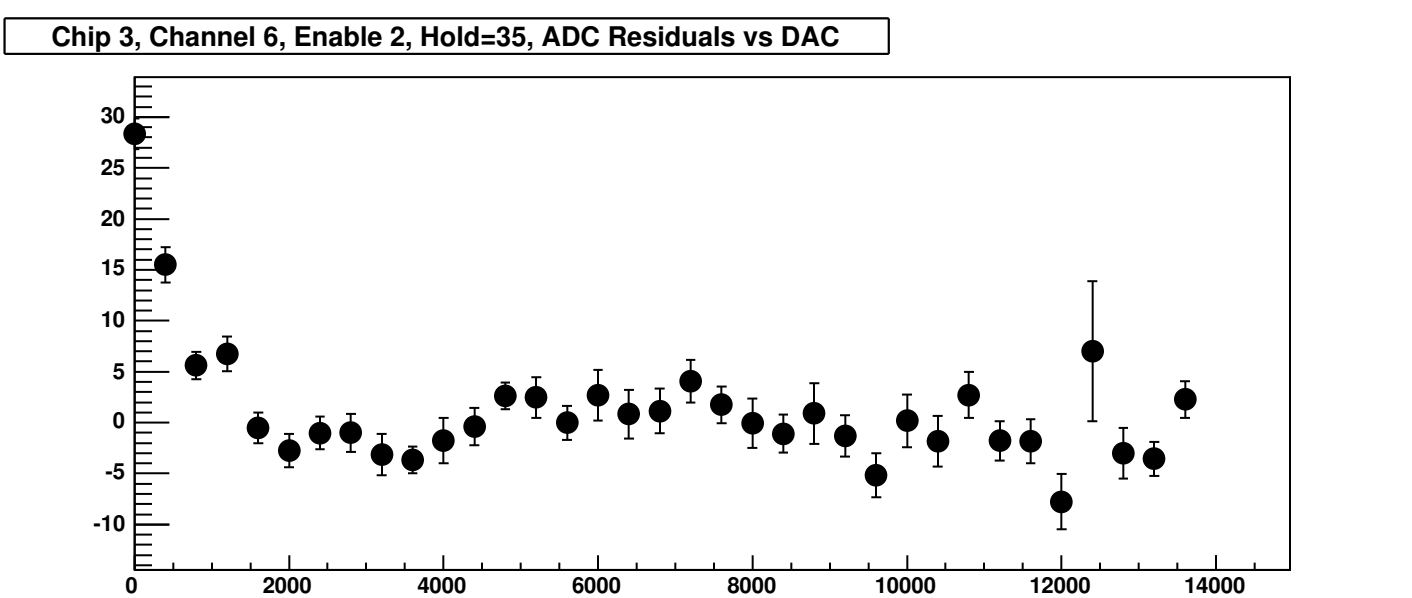

Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.32 / 23
p0 -373.8 ± 0.8351
p1 0.01099 ± 0.000124
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
2000
4000
6000
8000
10000
12000
14000

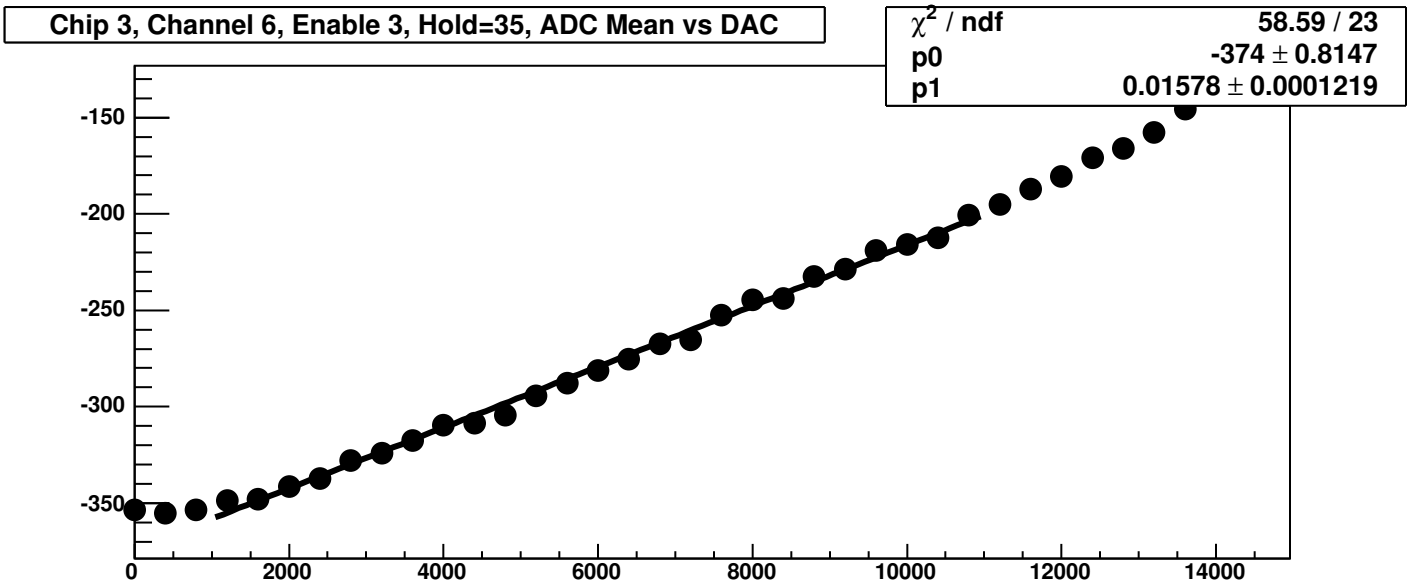

Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

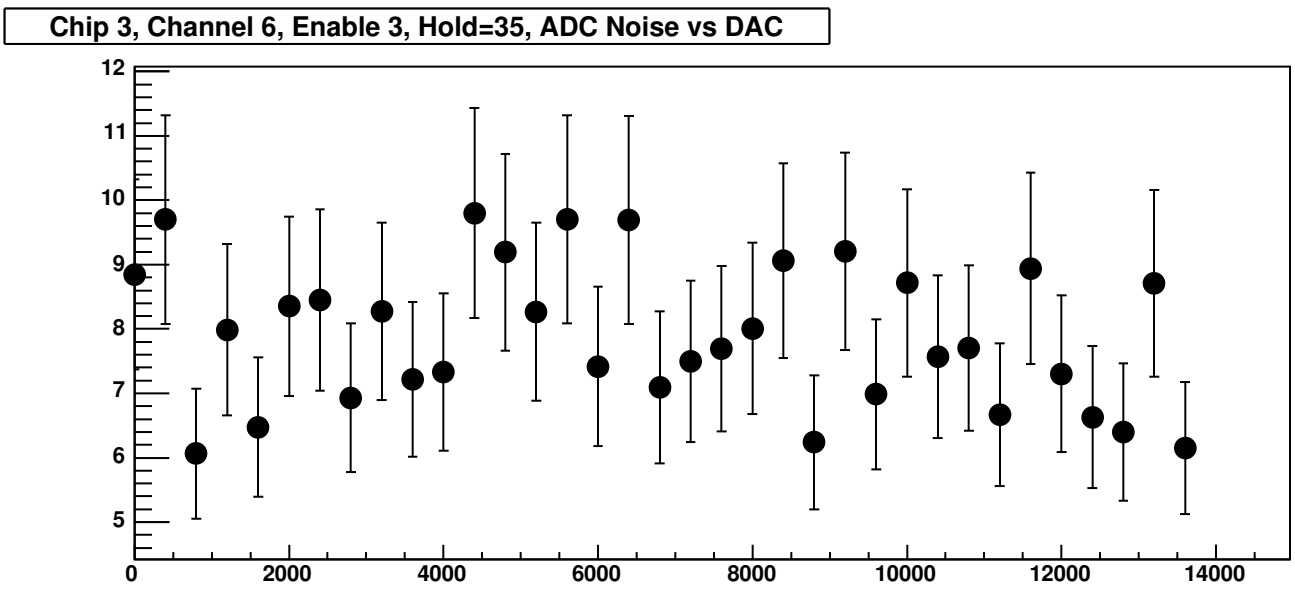

Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

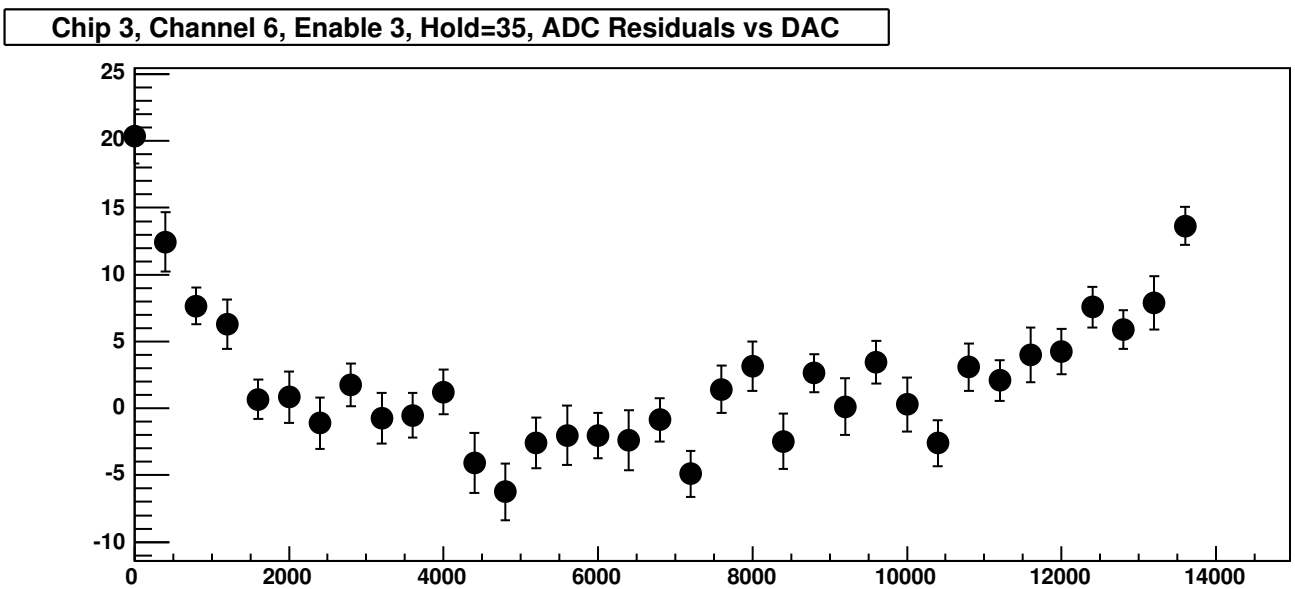

Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 51.33 / 23
p0 -372.2 ± 0.8052
p1 0.03241 ± 0.0001357
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

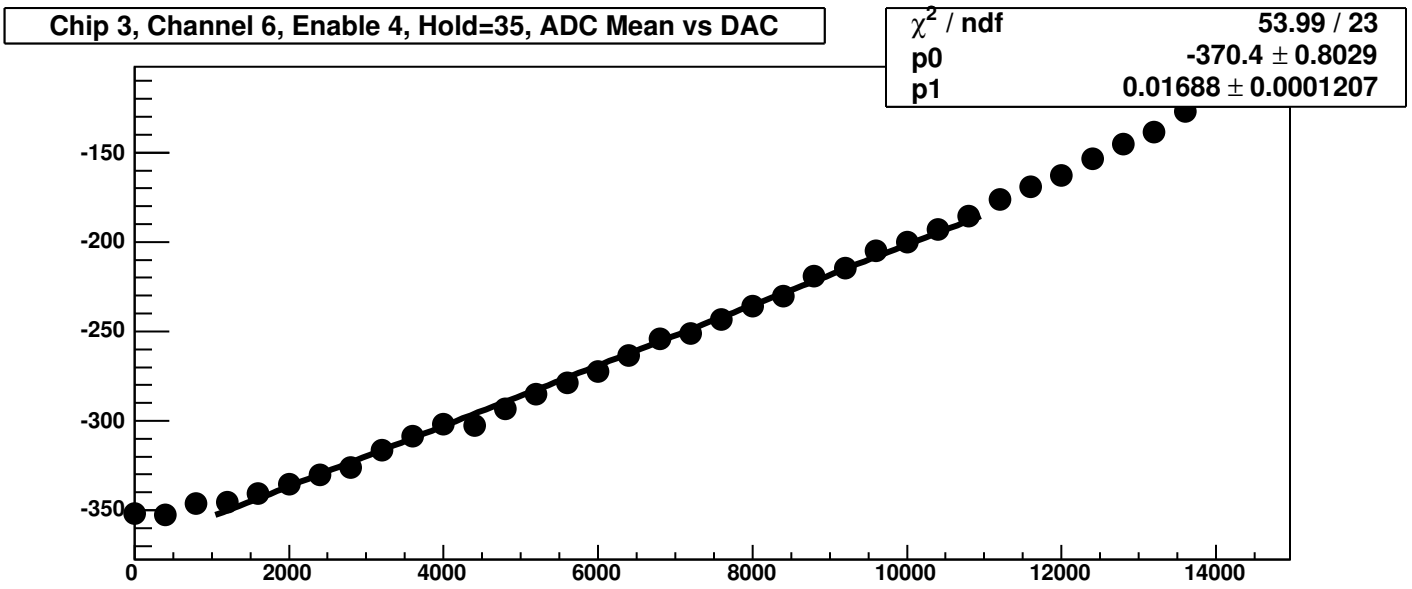

Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

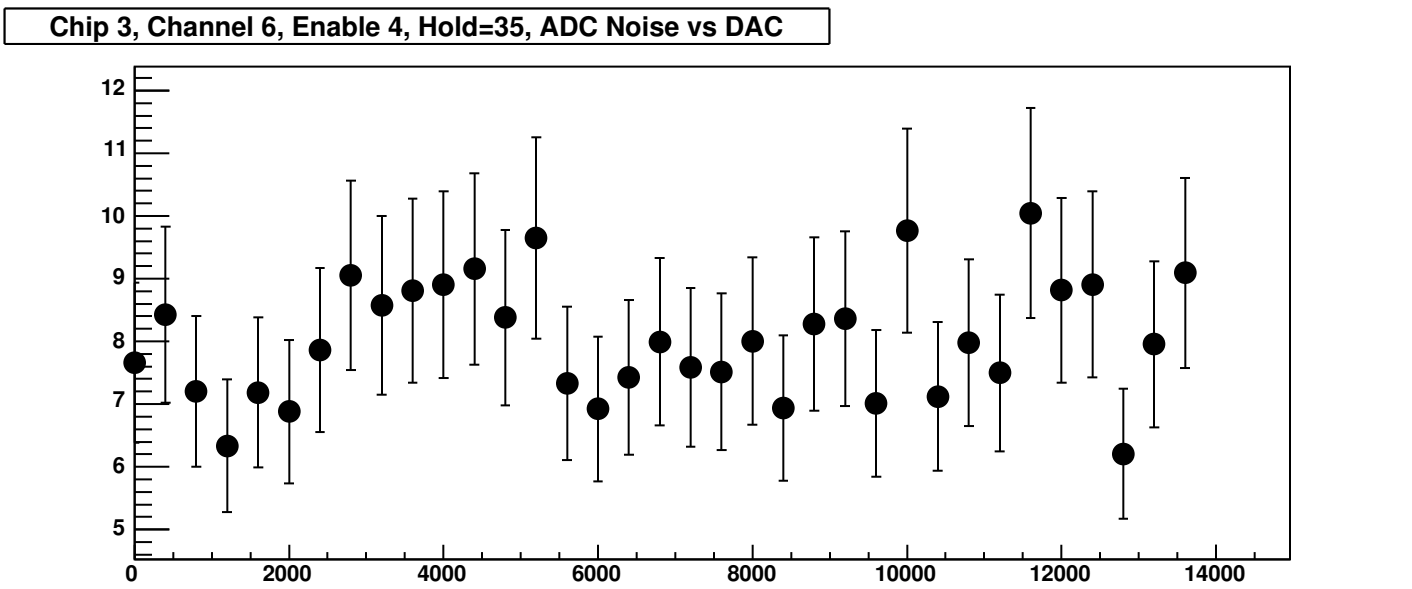

Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

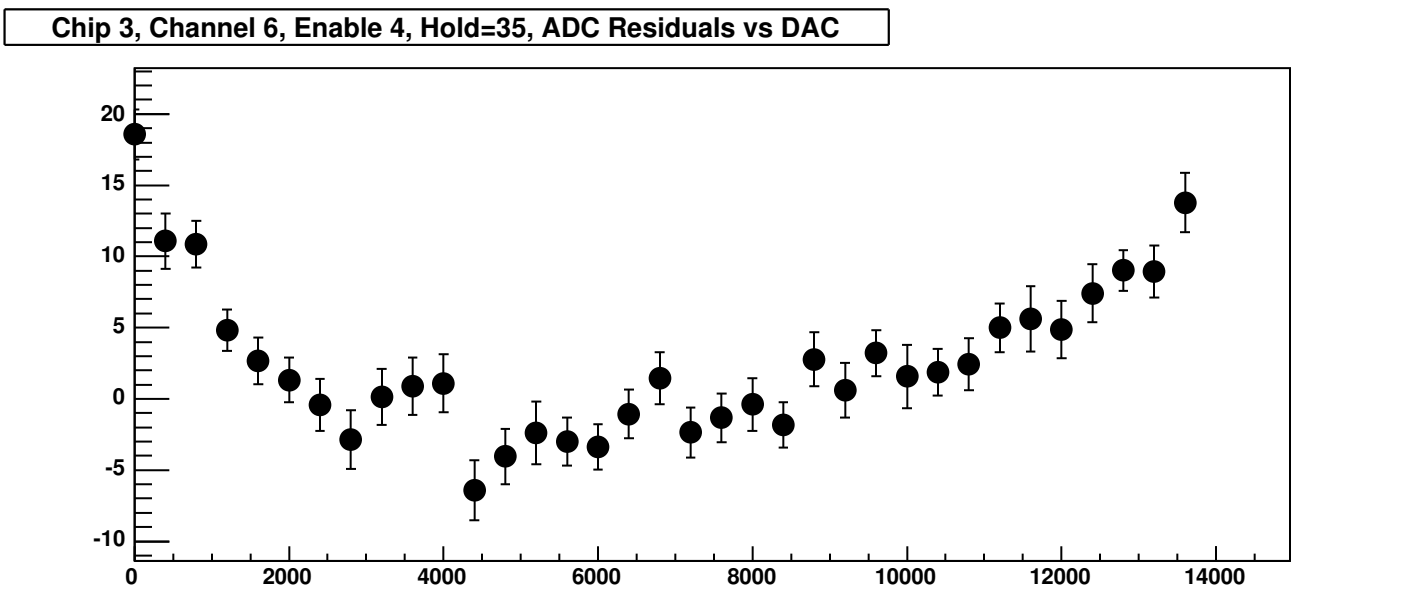

Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 58.59 / 23
p0 -374 ± 0.8147
p1 0.01578 ± 0.0001219
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

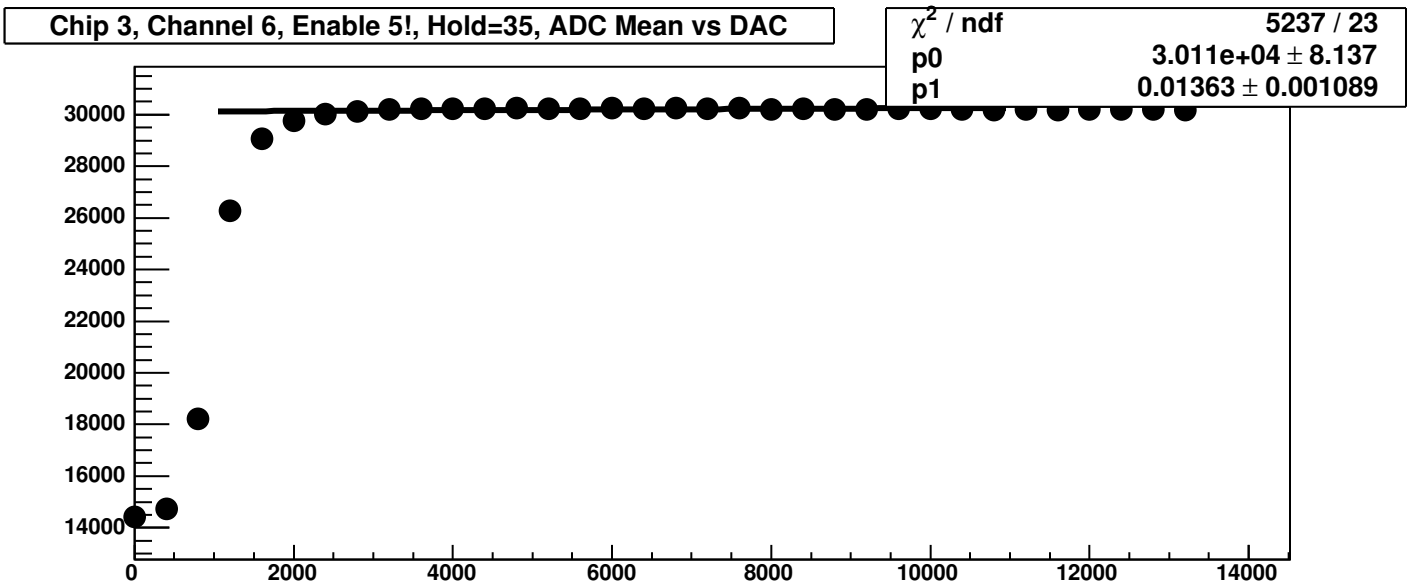

Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

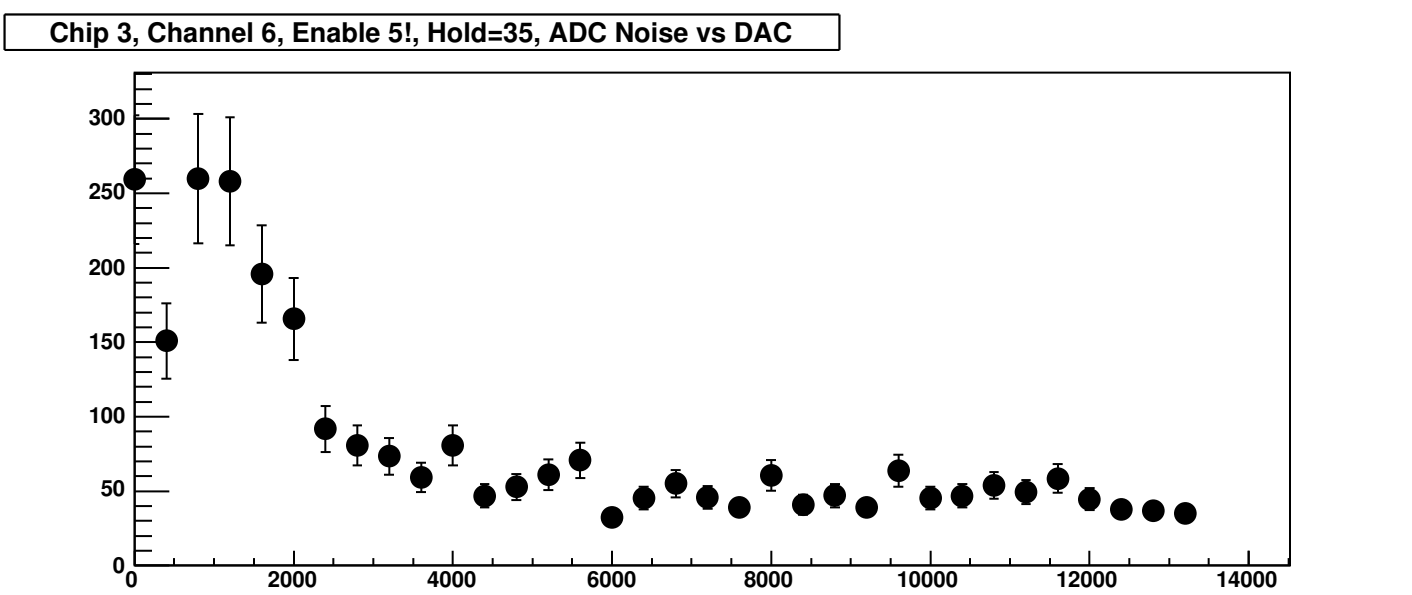

Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

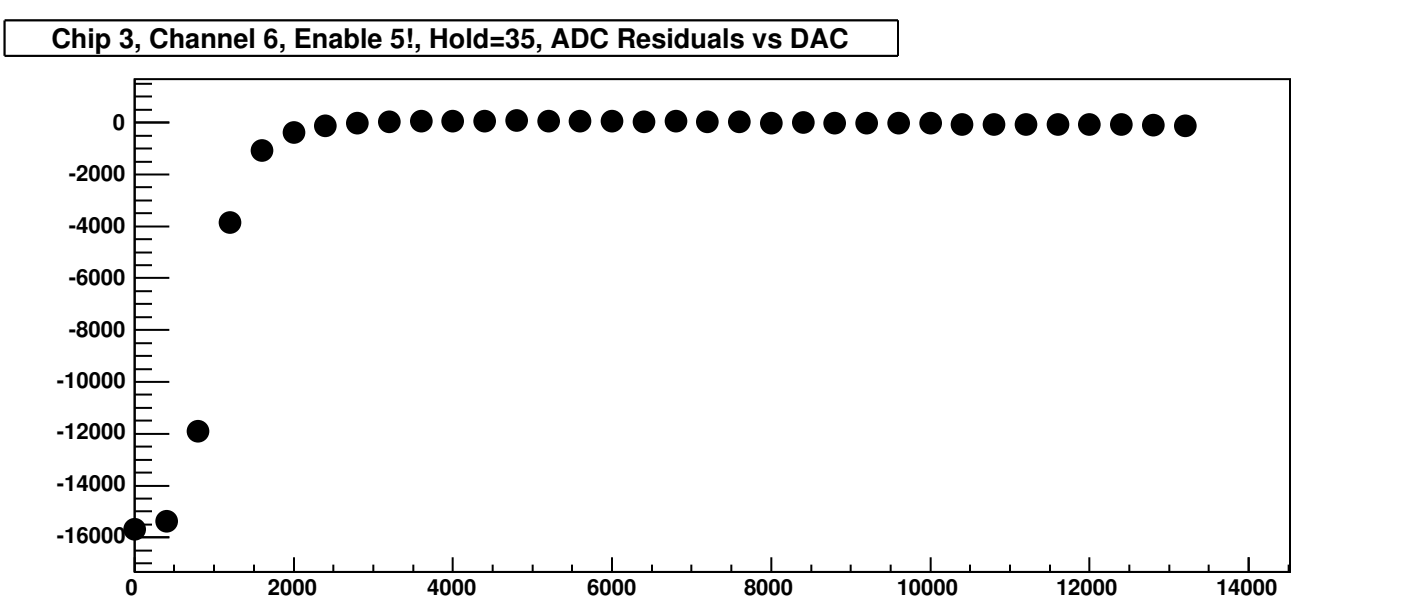

Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 53.99 / 23
p0 -370.4 ± 0.8029
p1 0.01688 ± 0.0001207
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

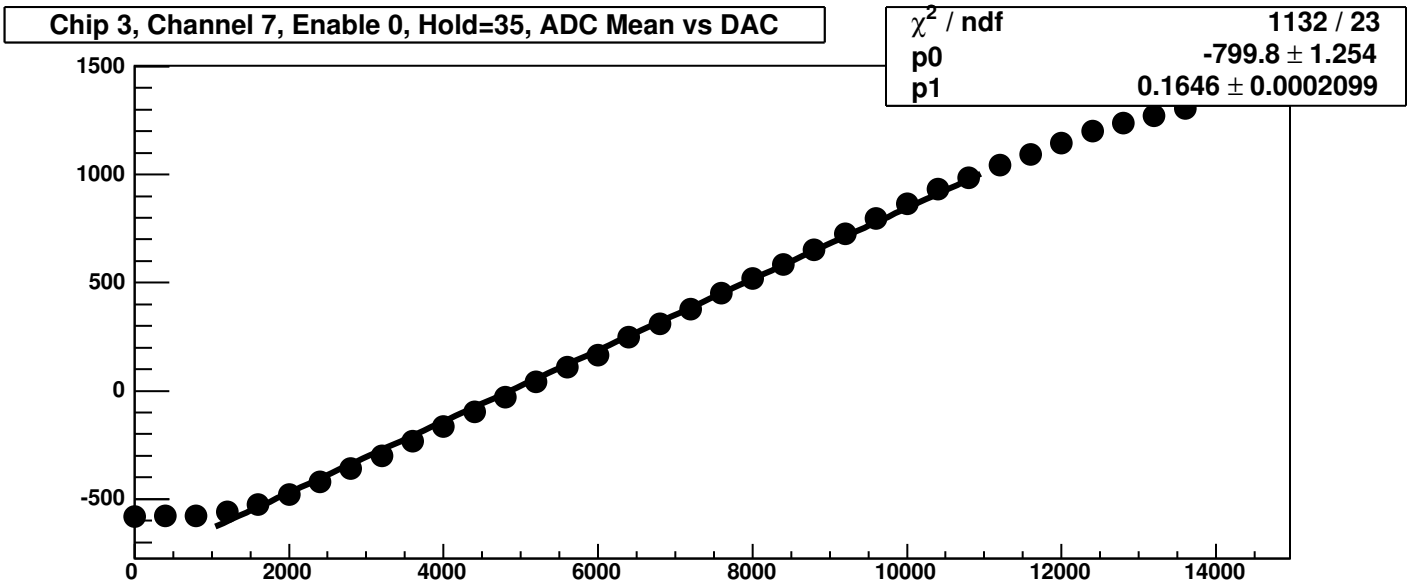

Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

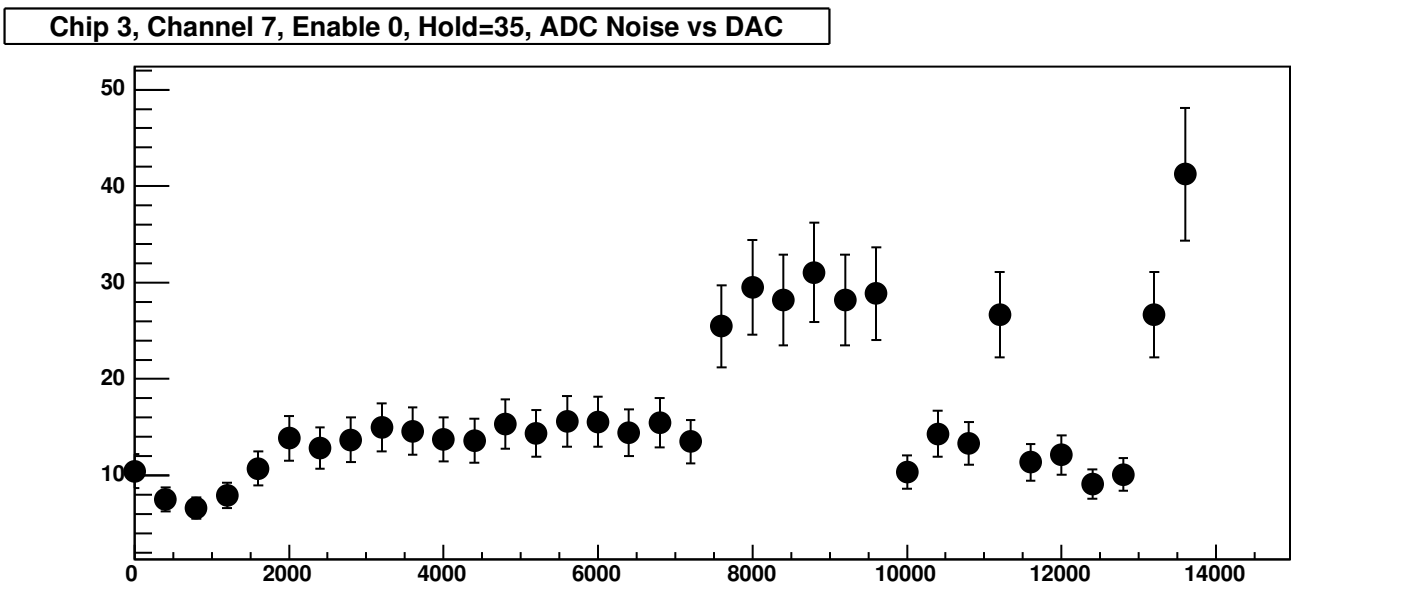

Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

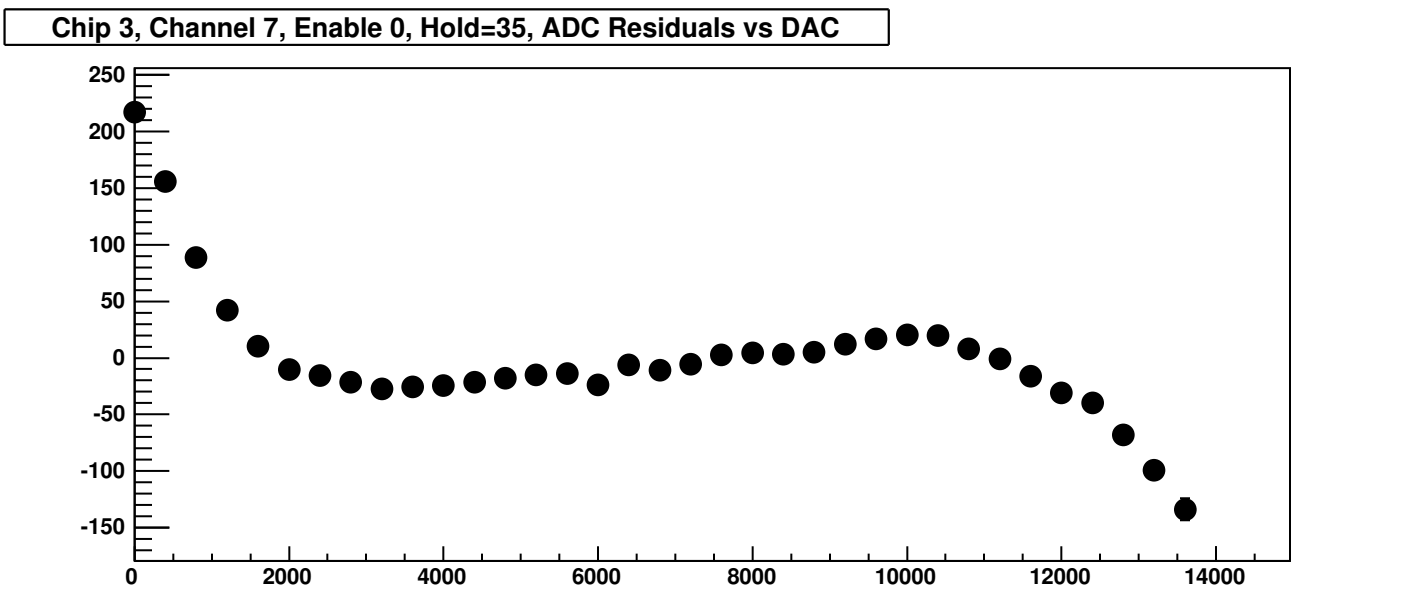

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5237 / 23
p0 3.011e+04 ± 8.137
p1 0.01363 ± 0.001089
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

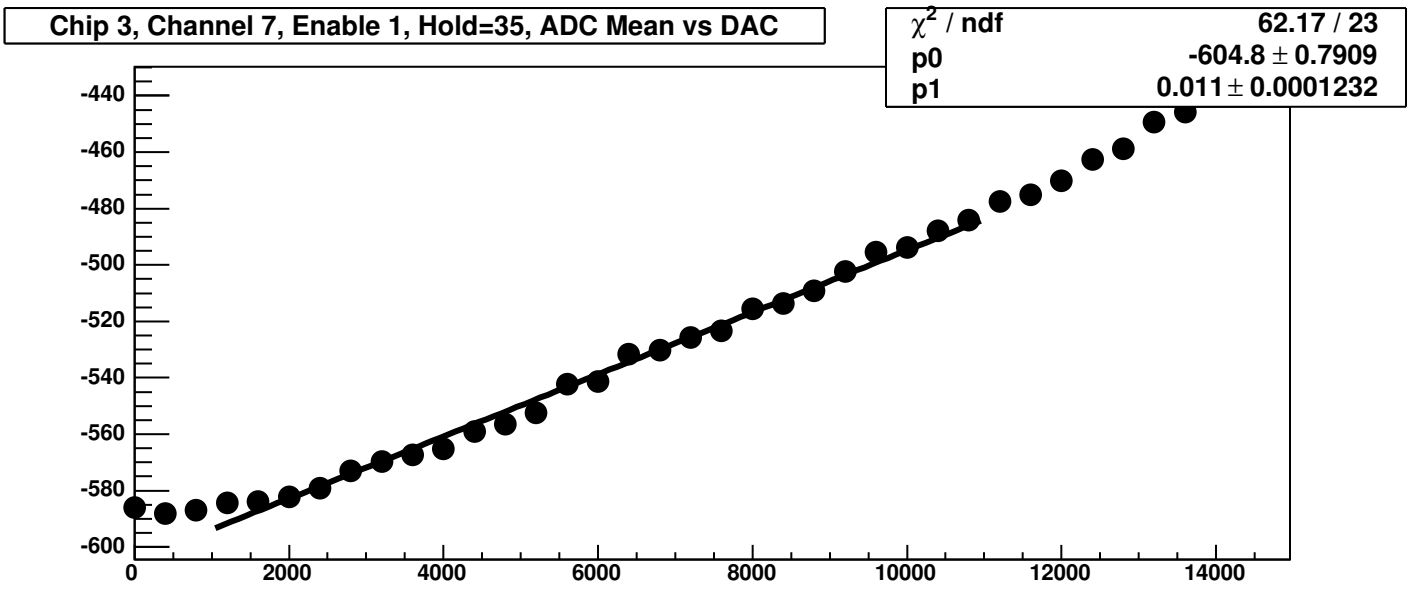

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

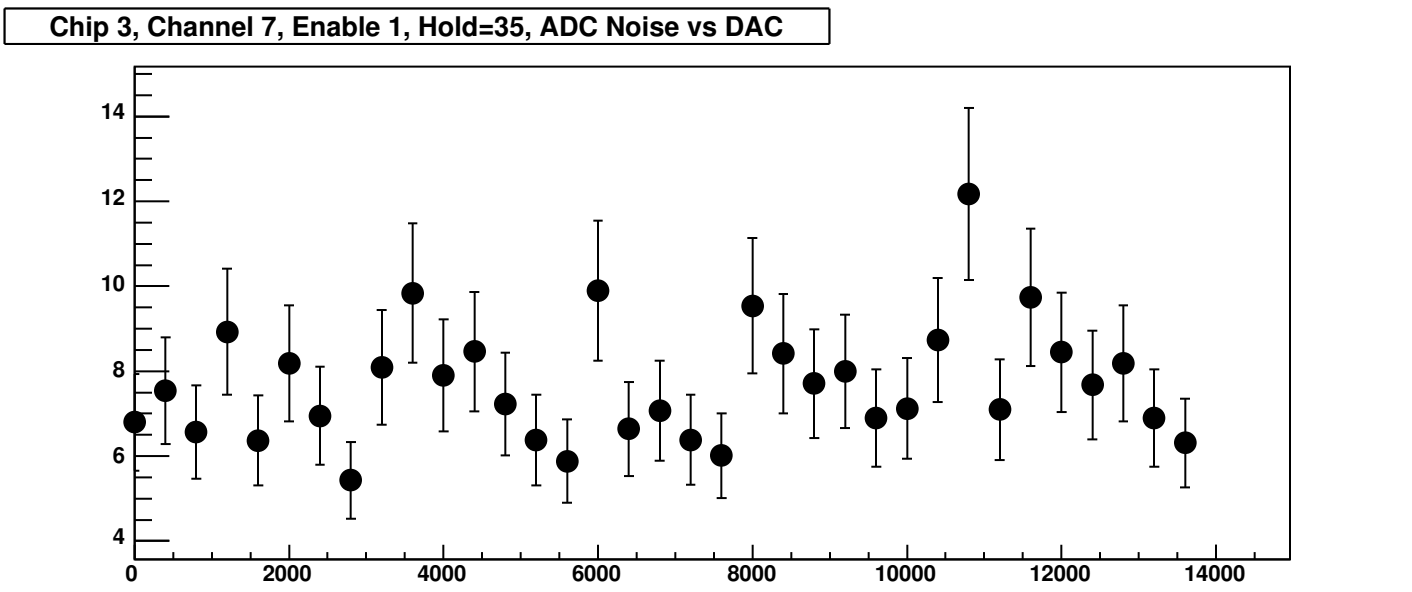

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

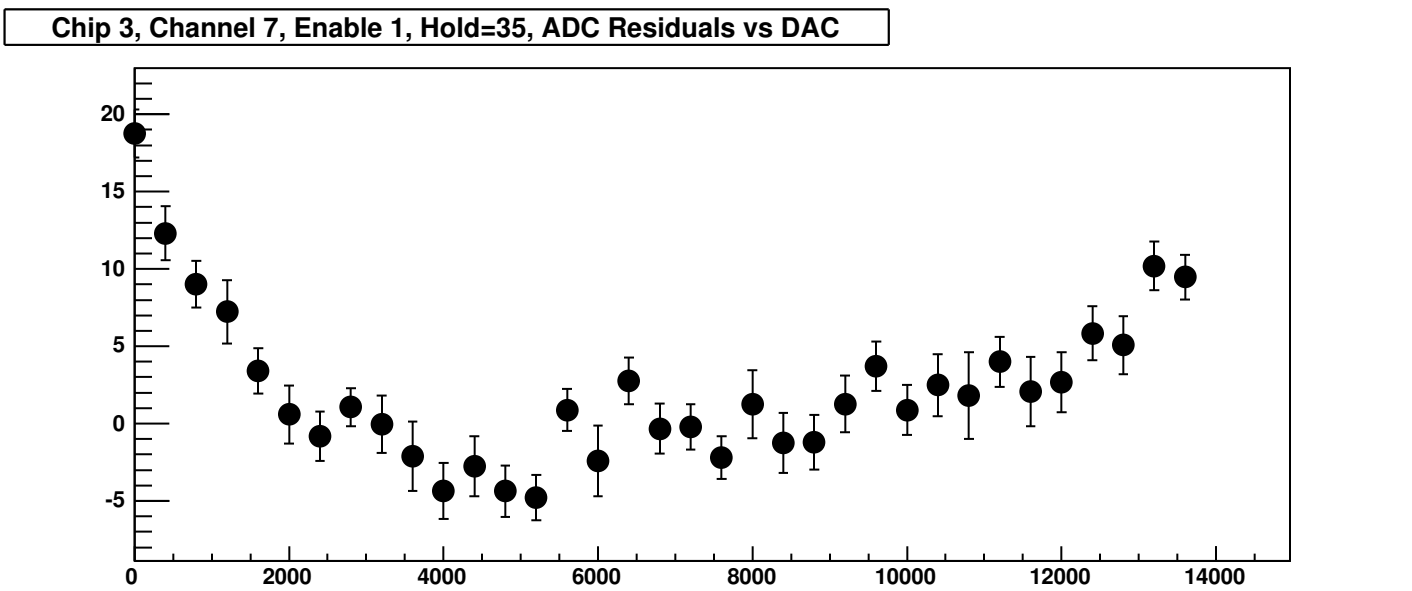

Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1132 / 23
p0 -799.8 ± 1.254
p1 0.1646 ± 0.0002099
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

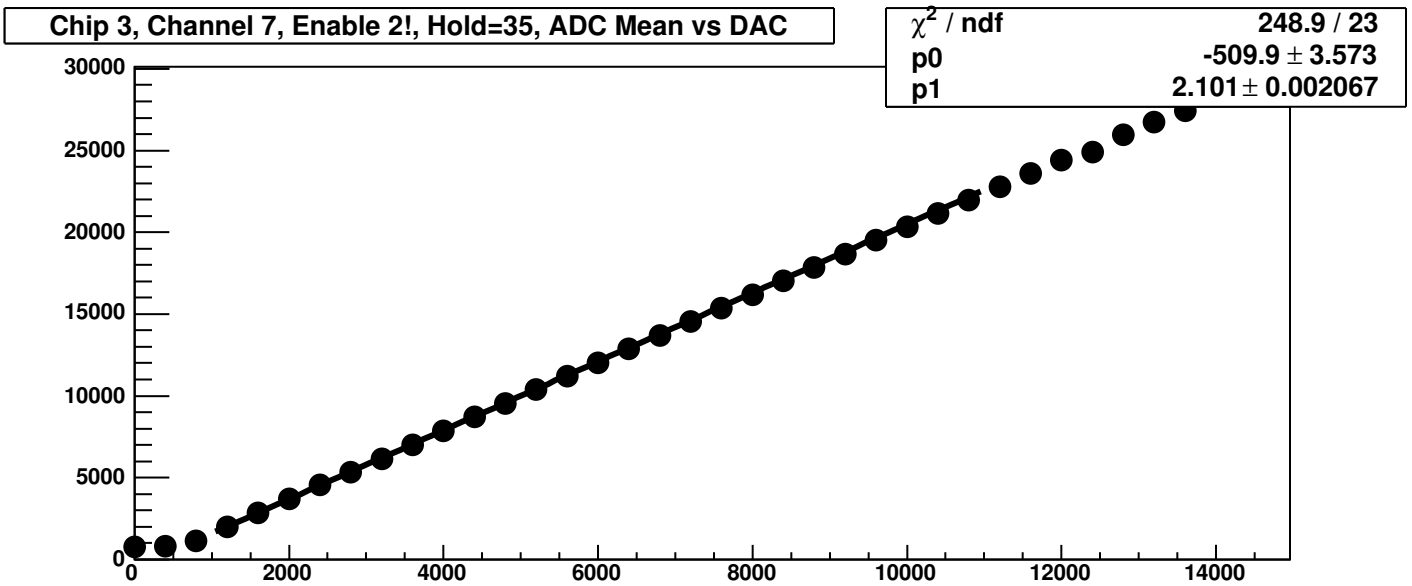

Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

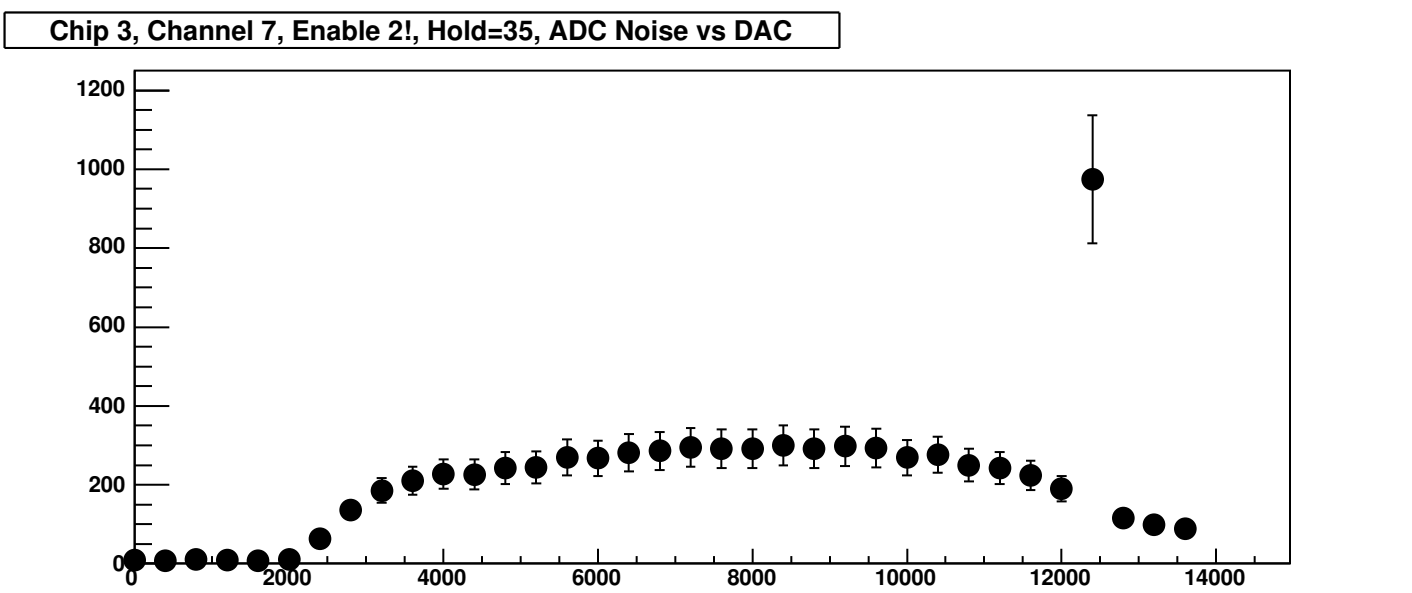

Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

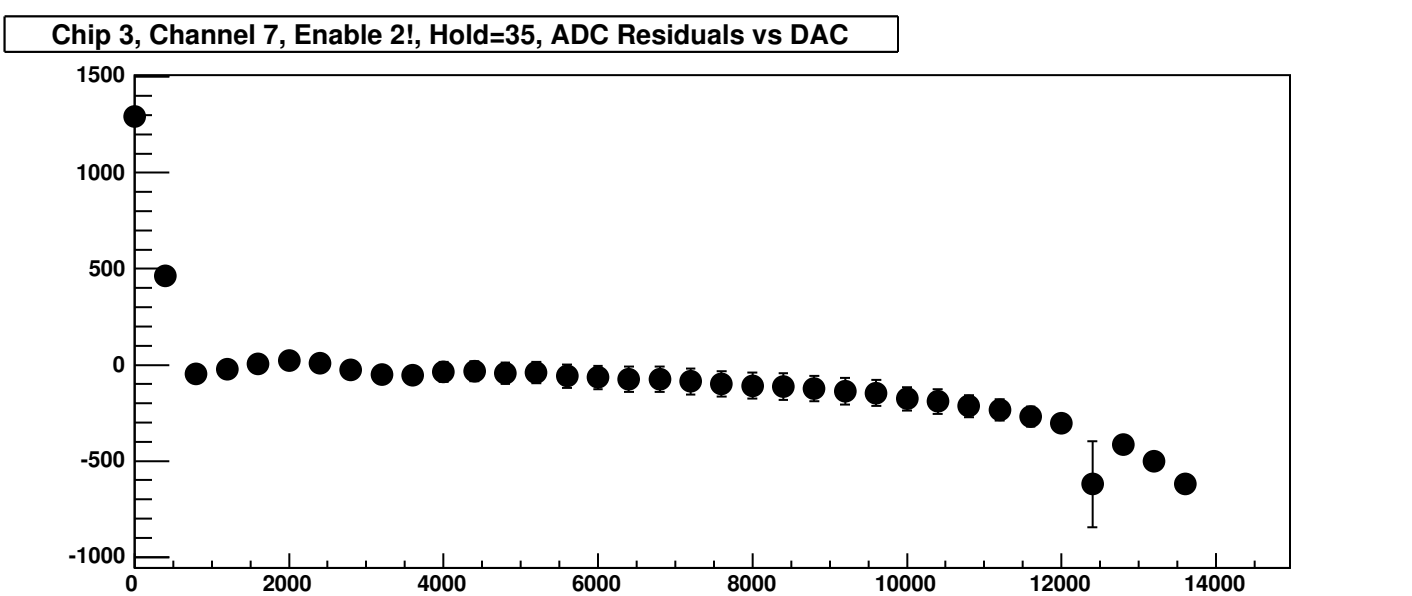

Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 62.17 / 23
p0 -604.8 ± 0.7909
p1 0.011 ± 0.0001232
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

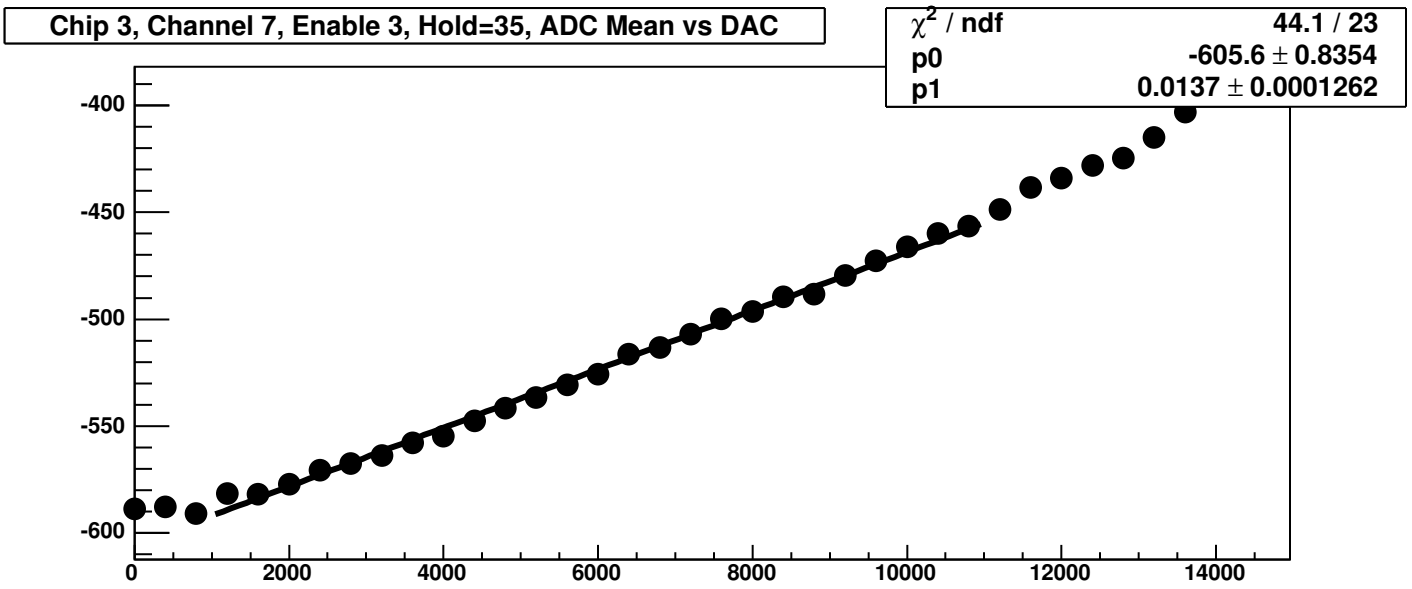

Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

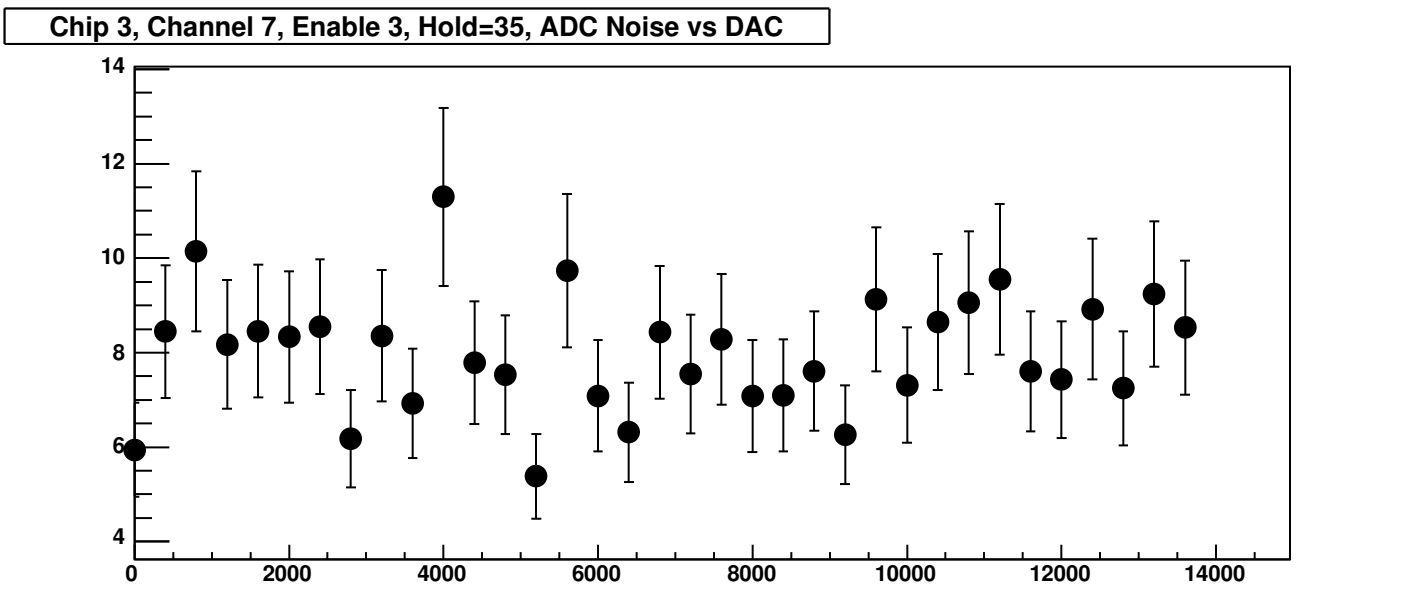

Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

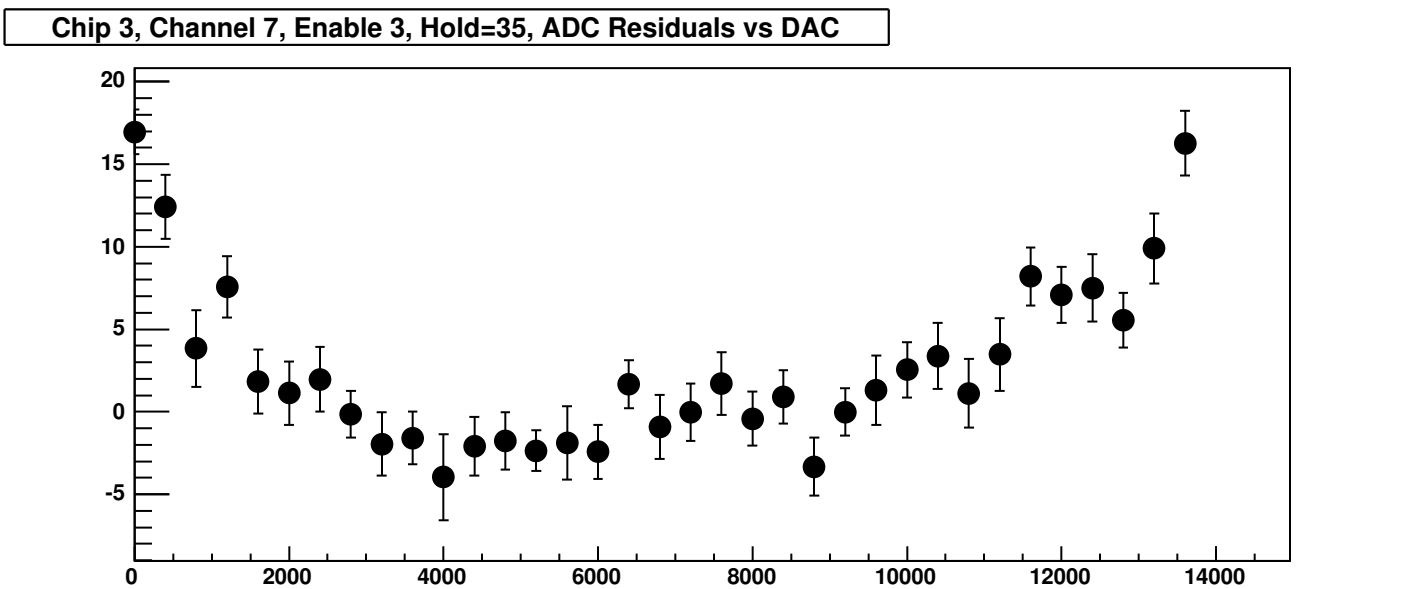

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 248.9 / 23
p0 -509.9 ± 3.573
p1 2.101 ± 0.002067
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

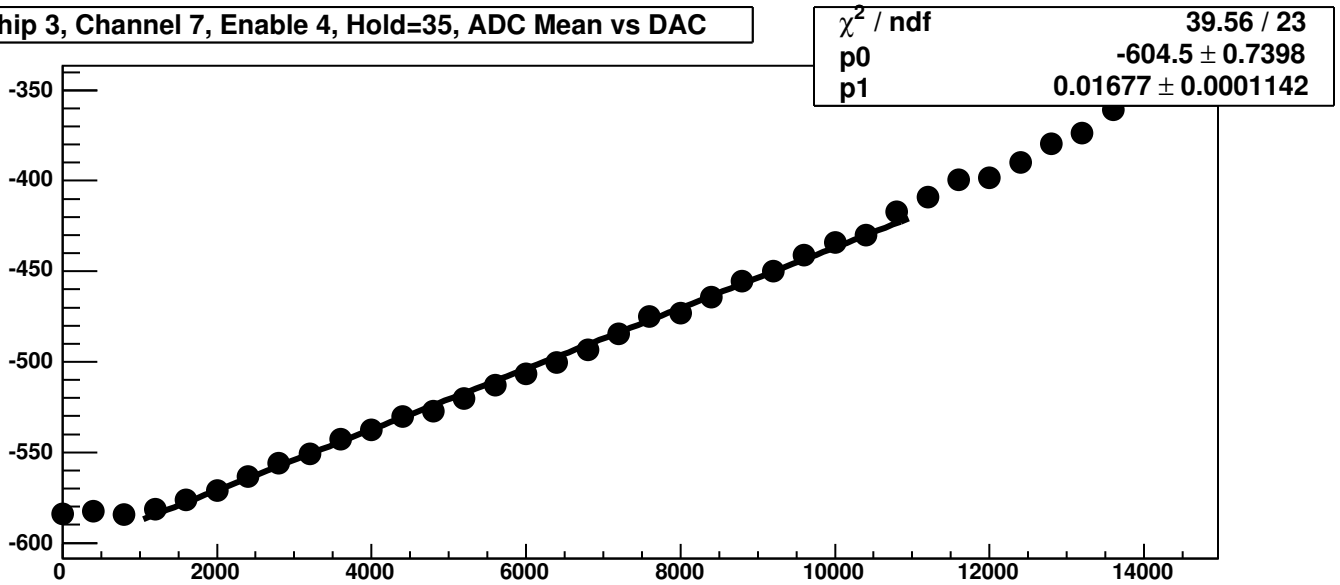

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.1 / 23
p0 -605.6 ± 0.8354
p1 0.0137 ± 0.0001262
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

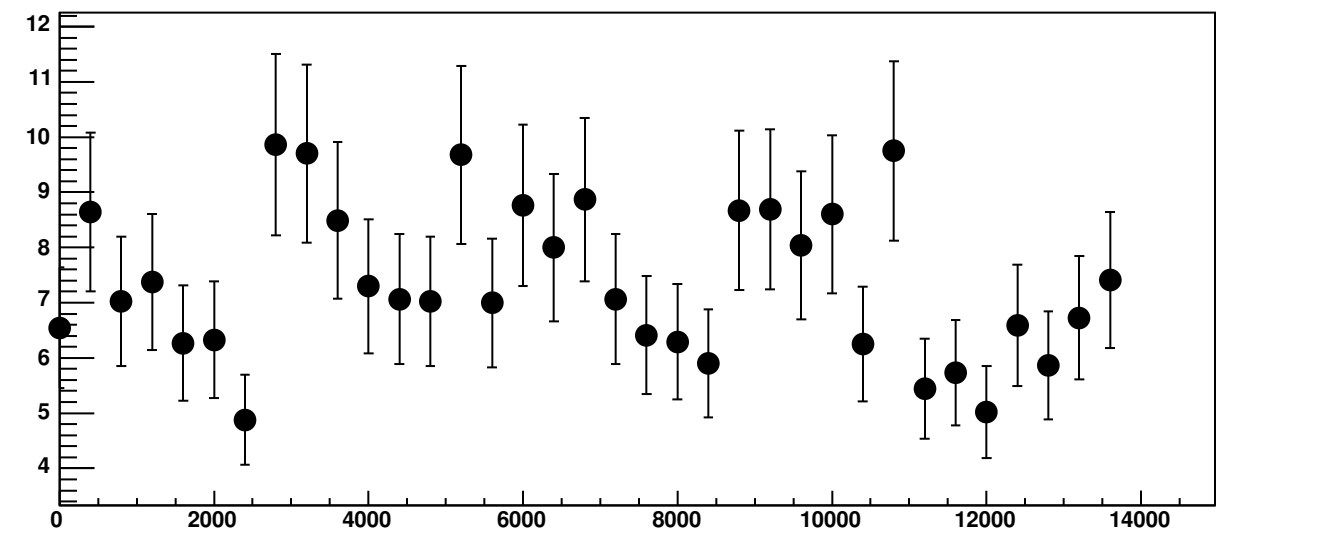

Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

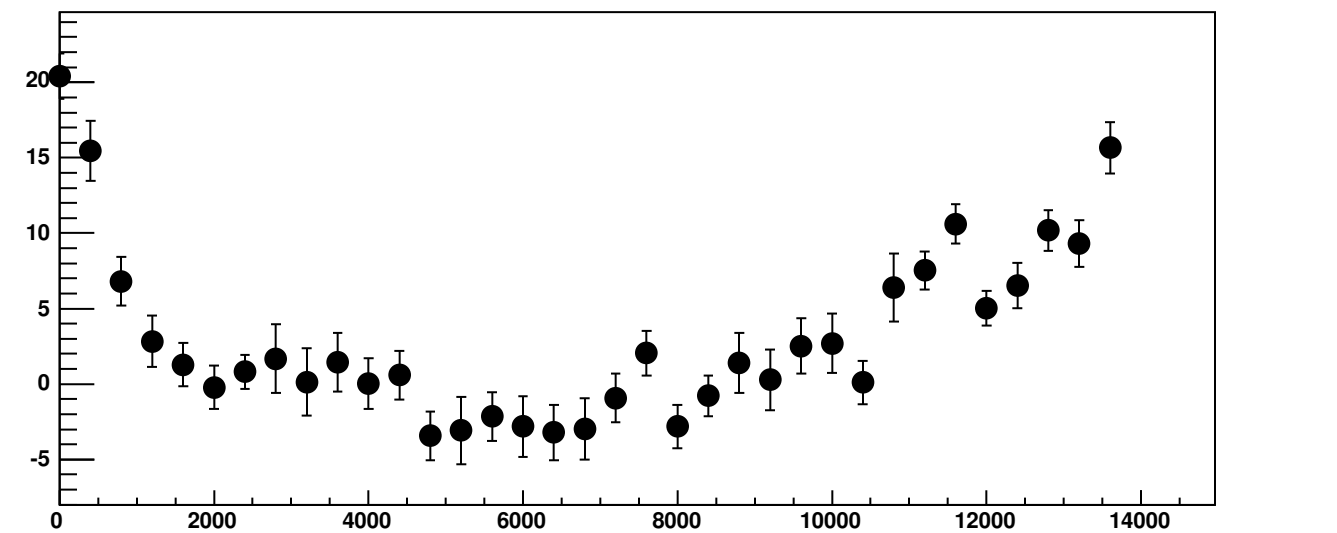
Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



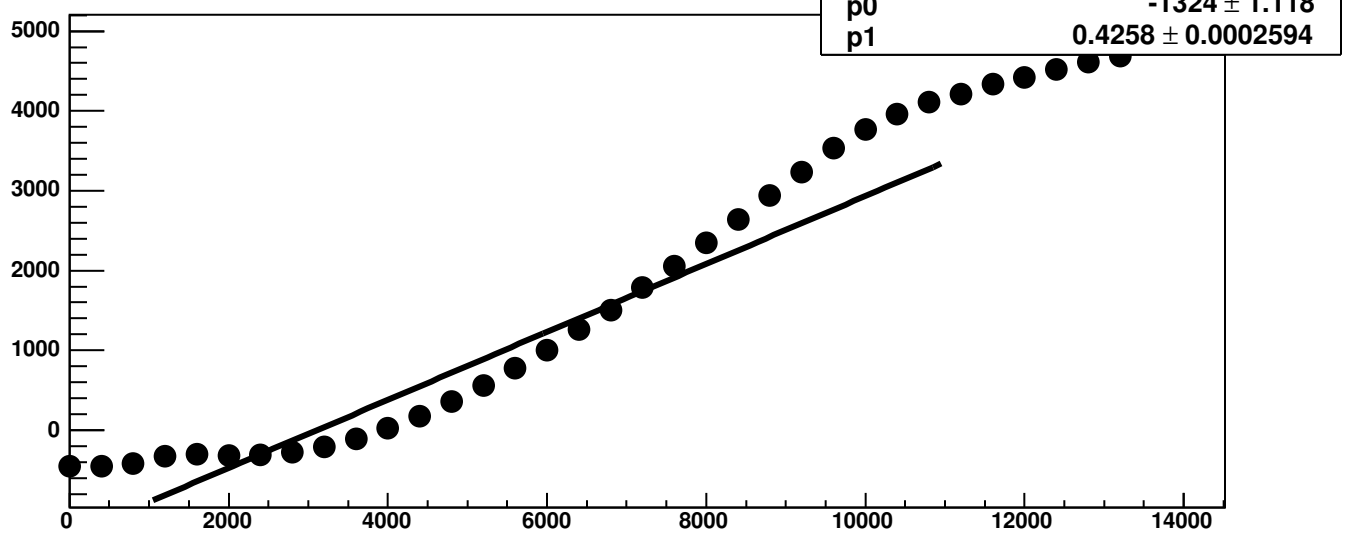
Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



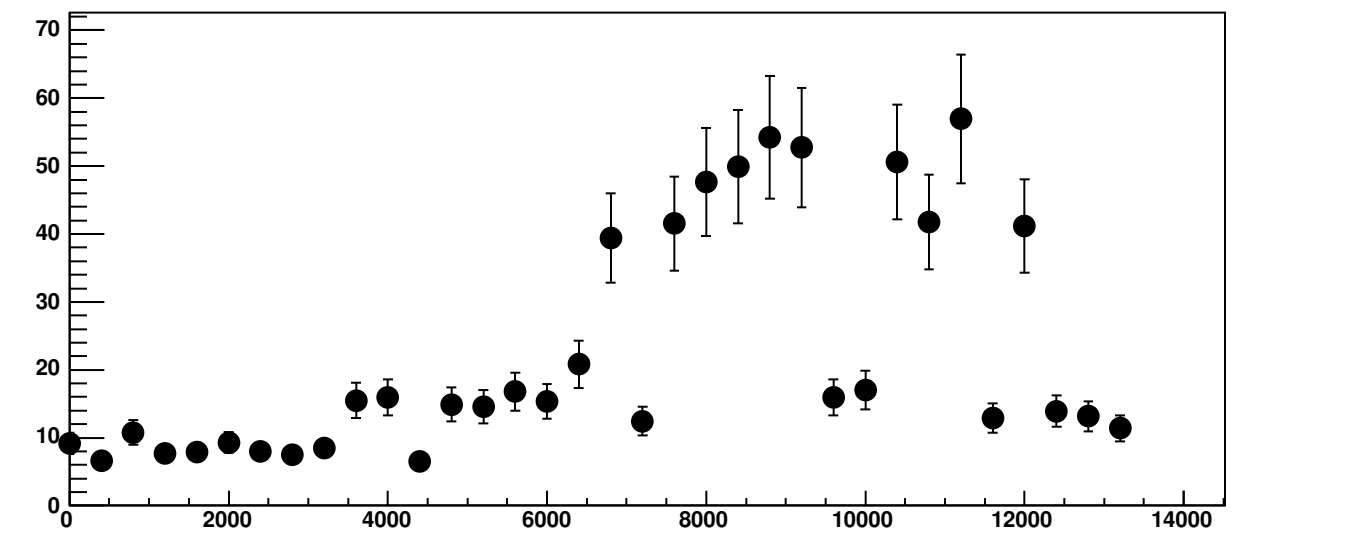
Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



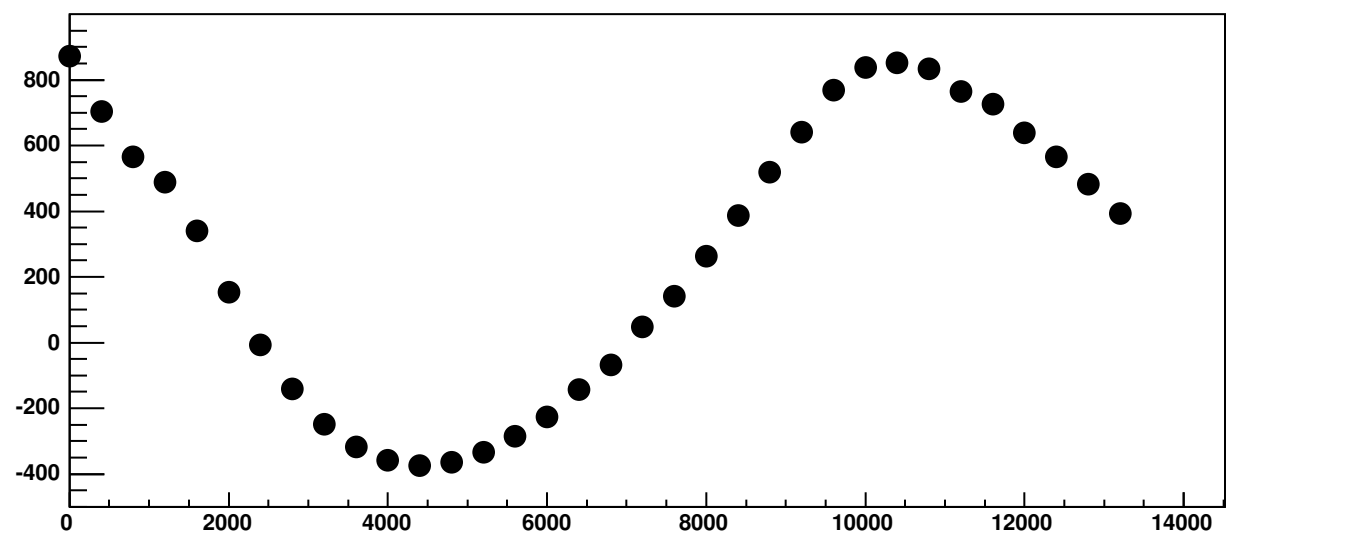
Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

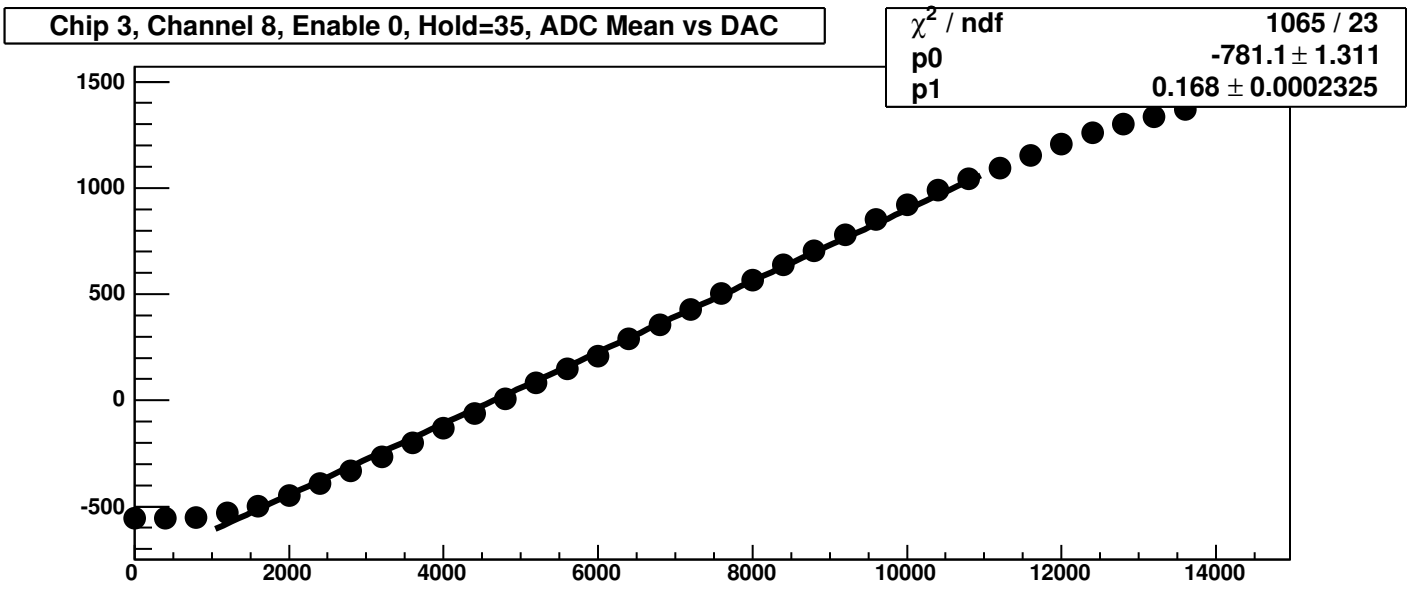


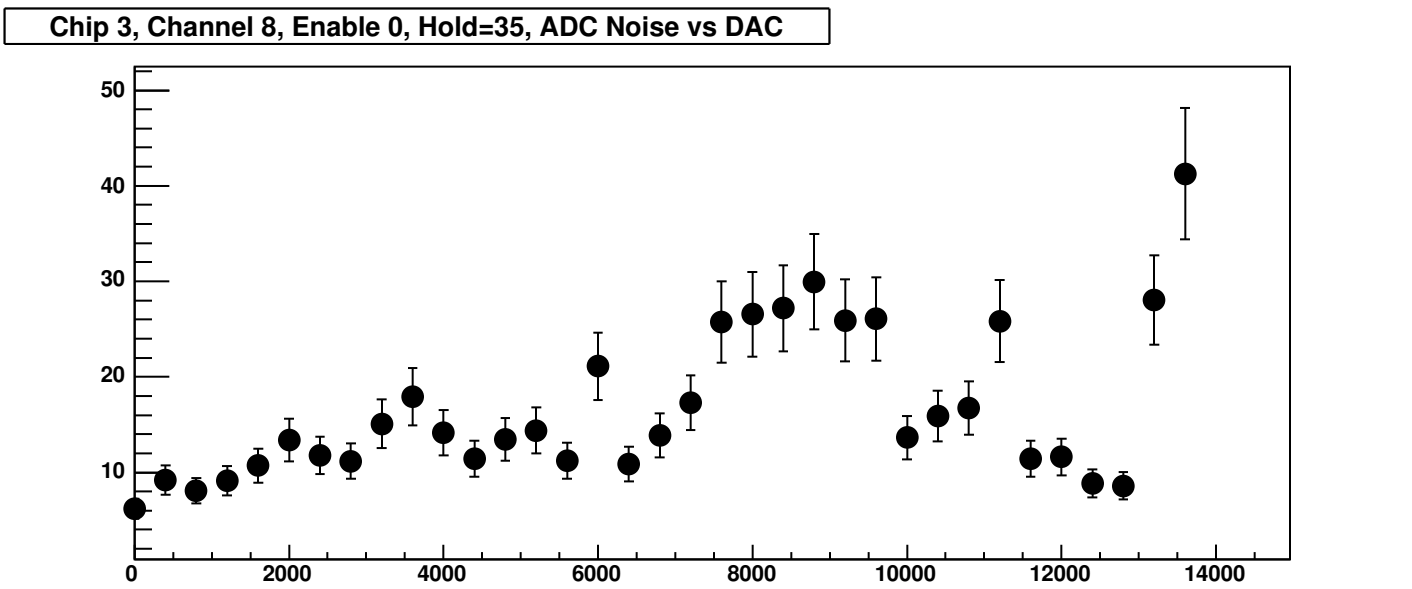
Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC

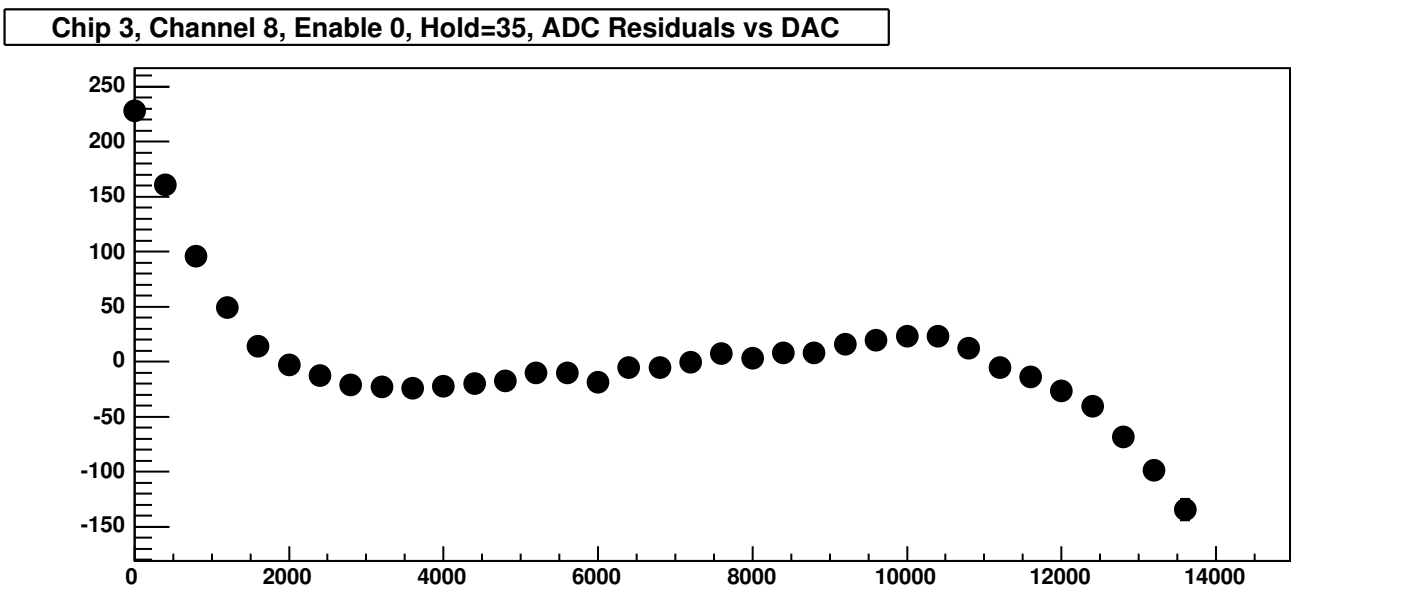


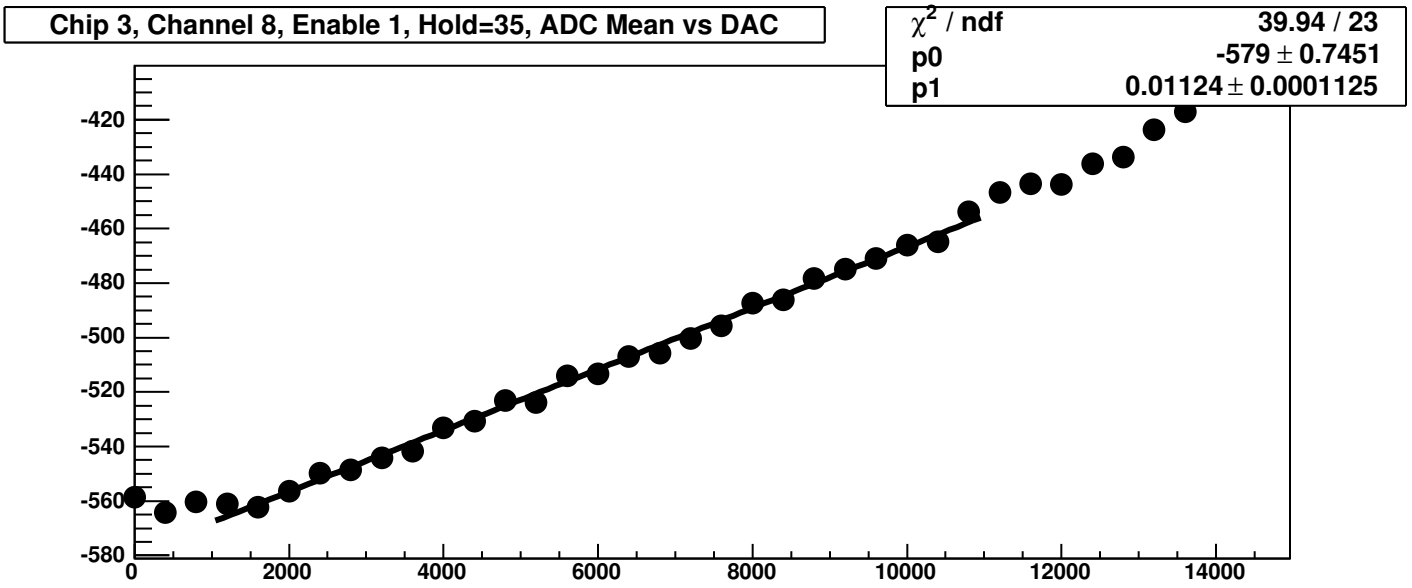
Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

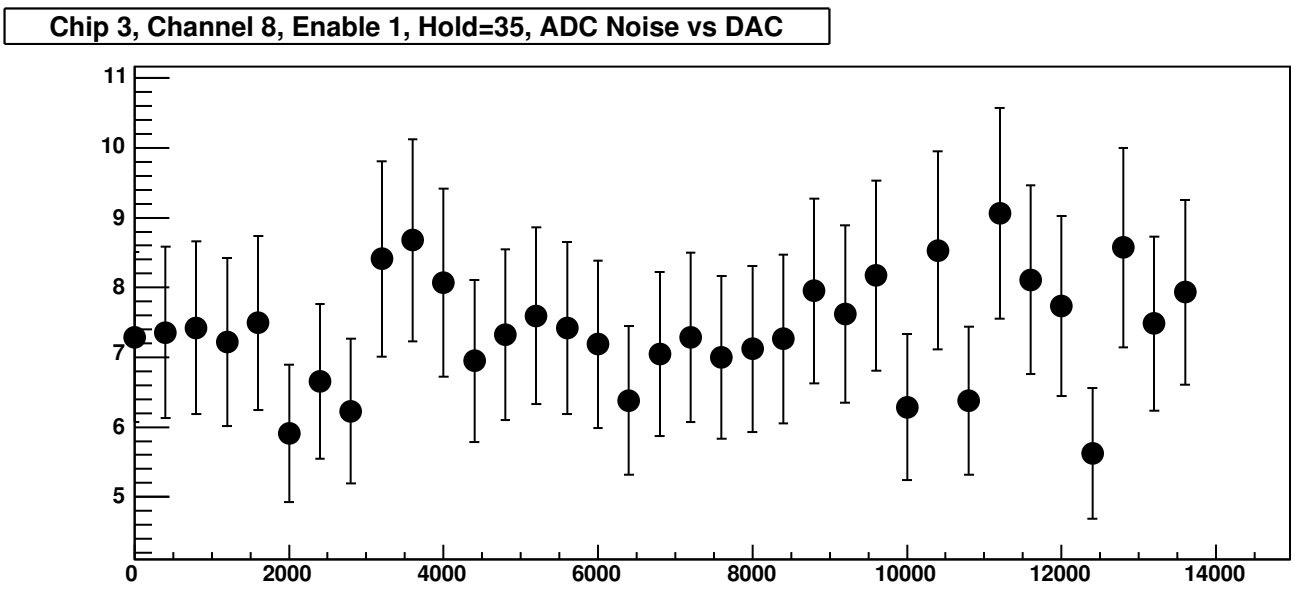


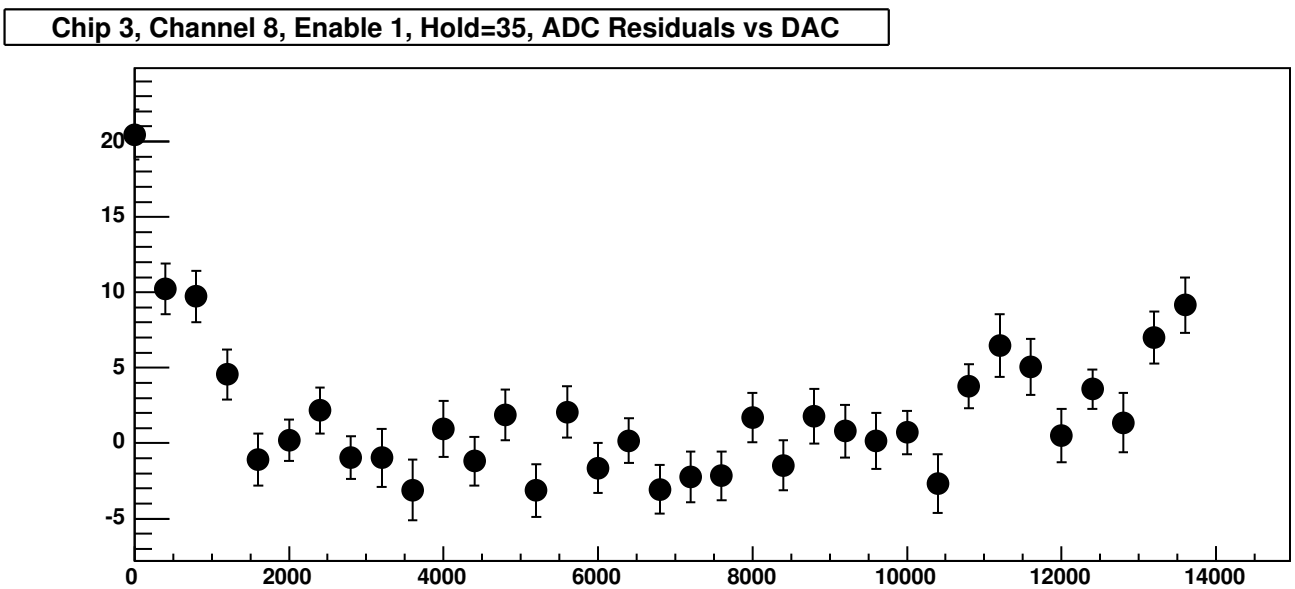

Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1065 / 23
p0 -781.1 ± 1.311
p1 0.168 ± 0.0002325
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

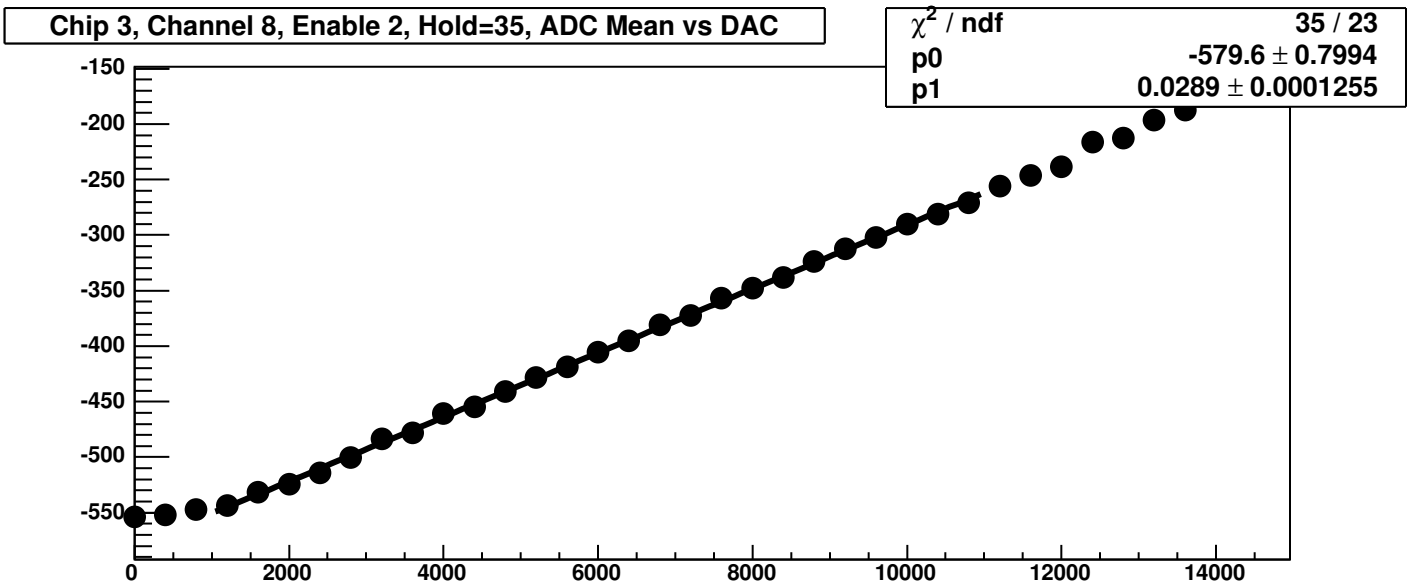

Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

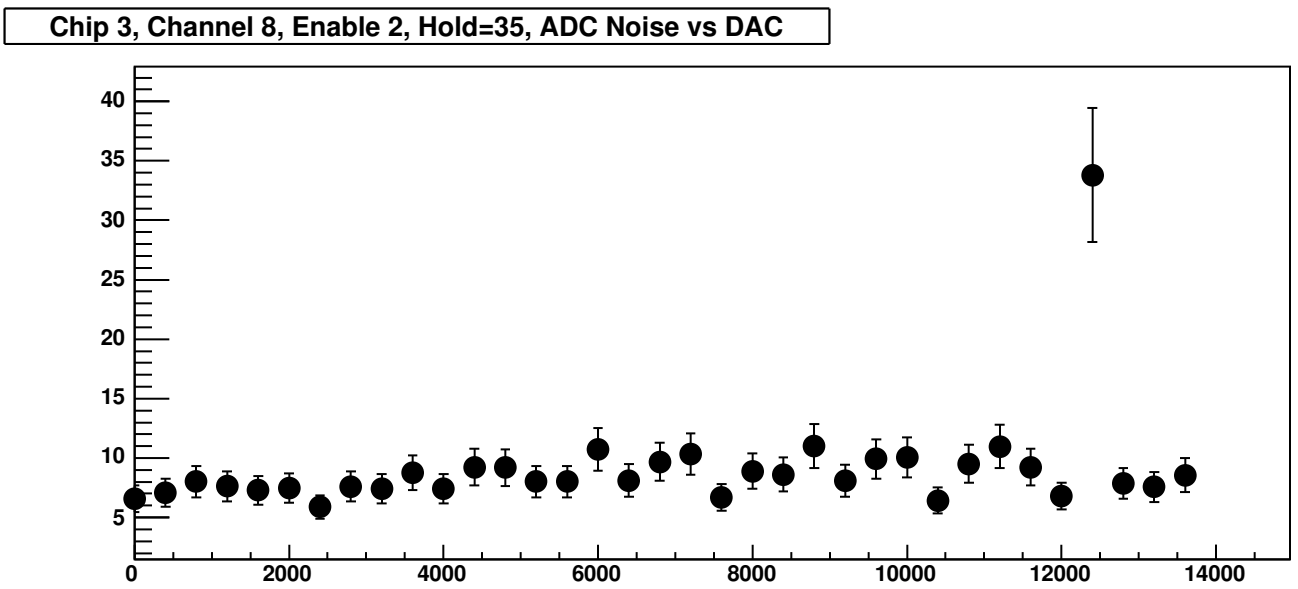

Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

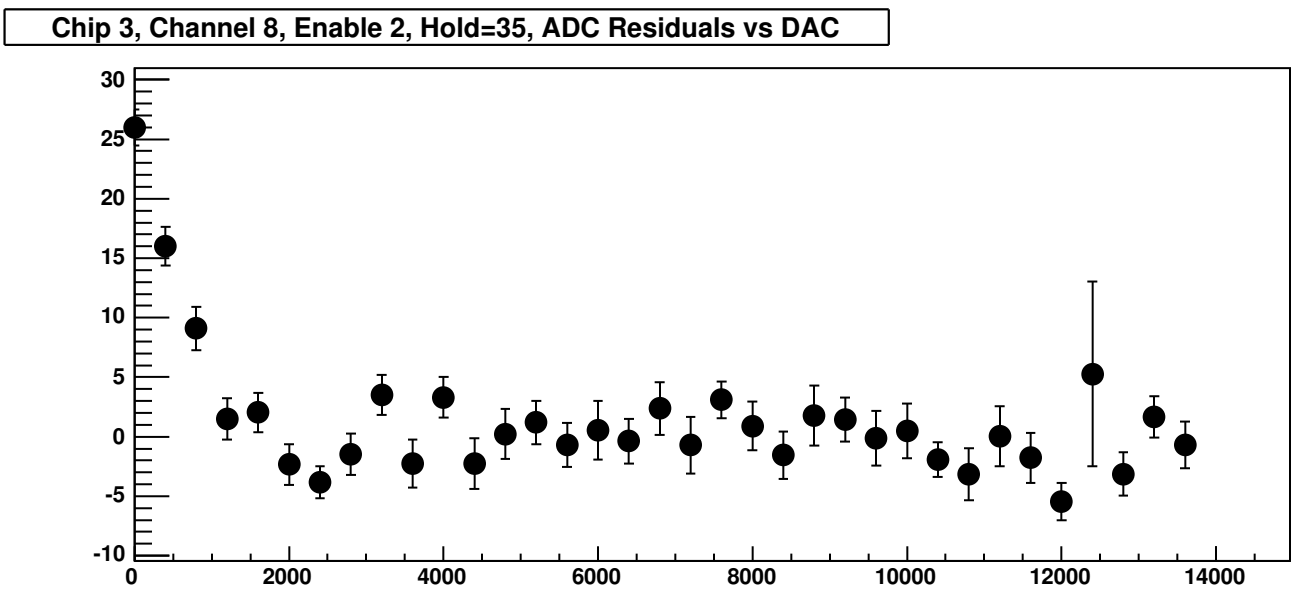

Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.94 / 23
p0 -579 ± 0.7451
p1 0.01124 ± 0.0001125
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

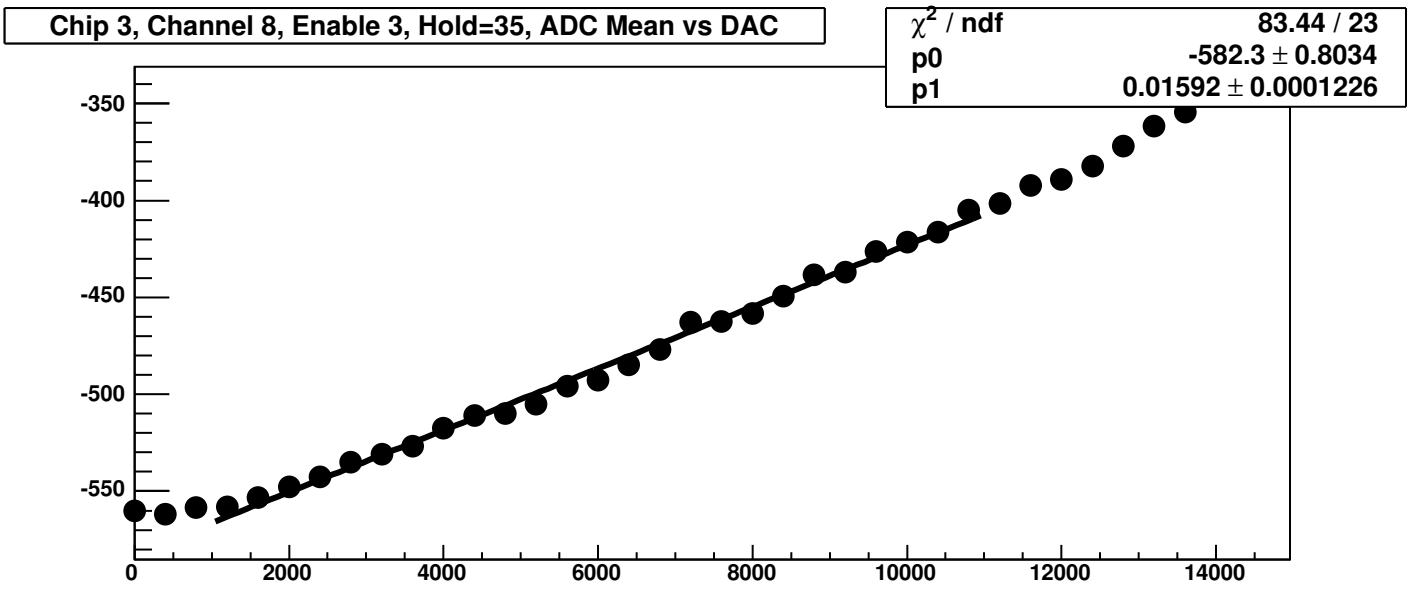

Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

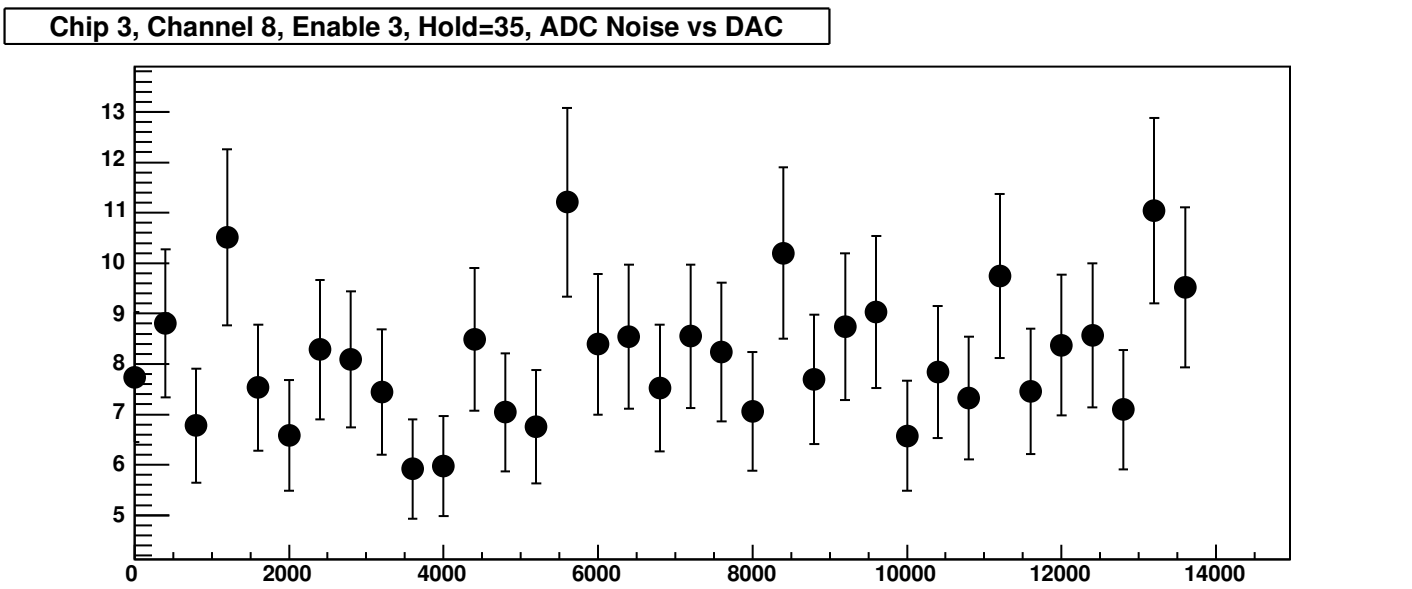

Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

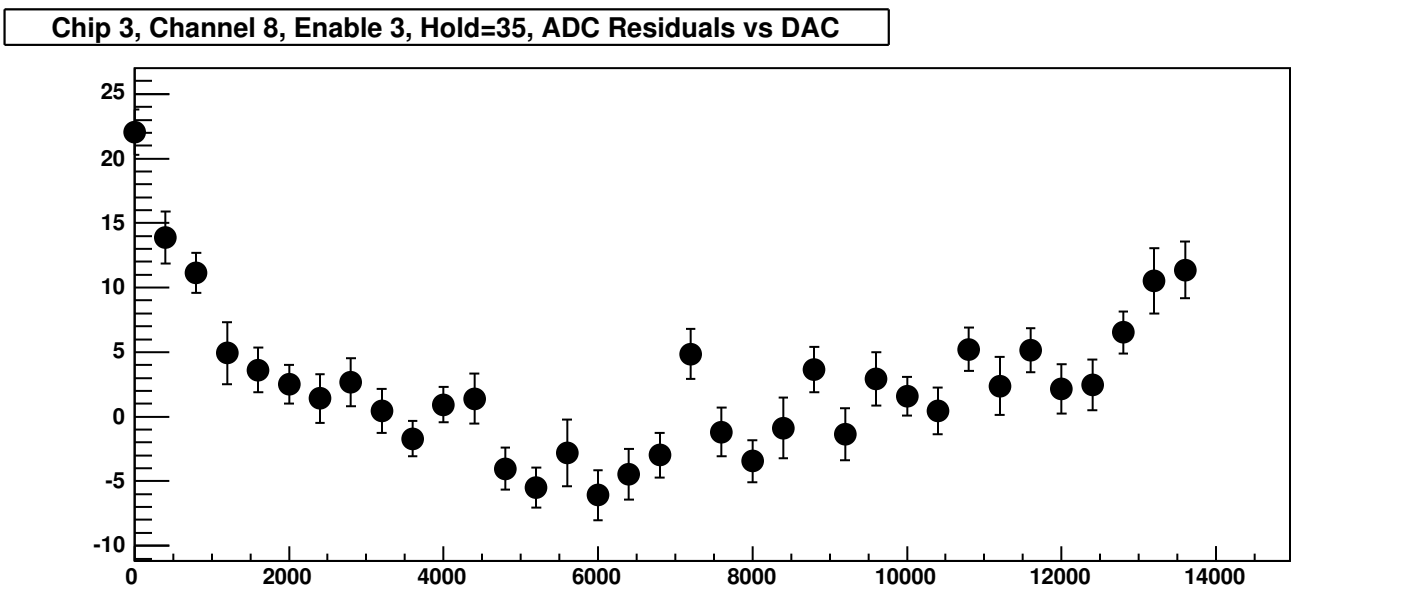

Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35 / 23
p0 -579.6 ± 0.7994
p1 0.0289 ± 0.0001255
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

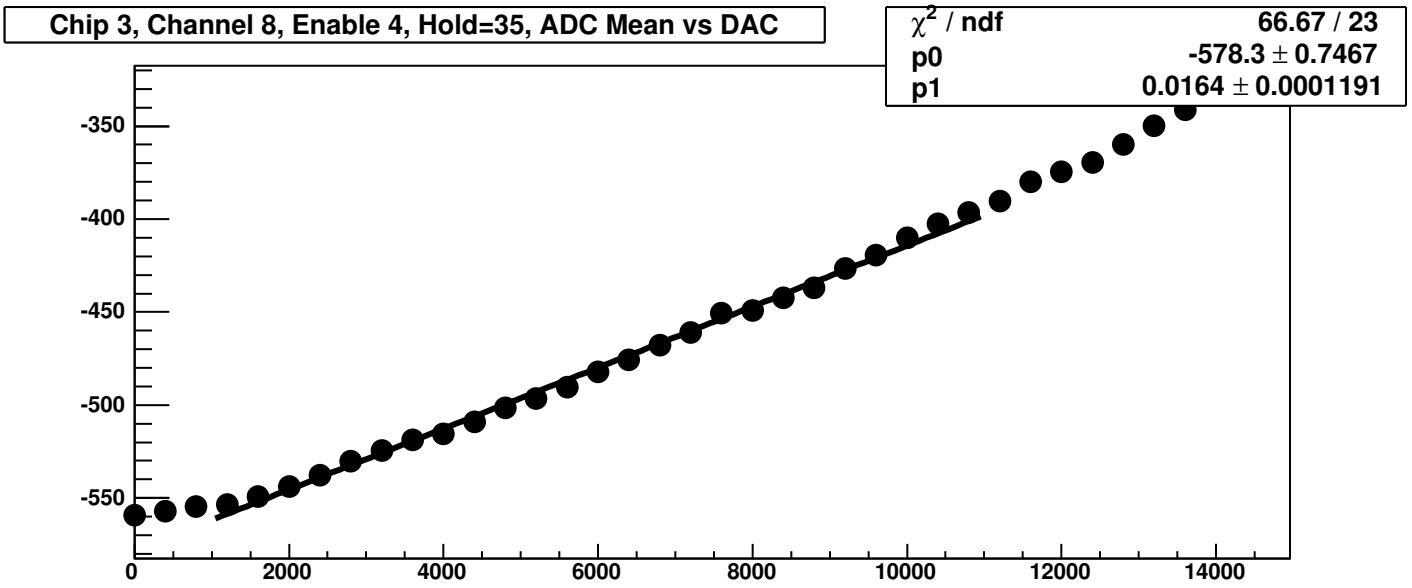

Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

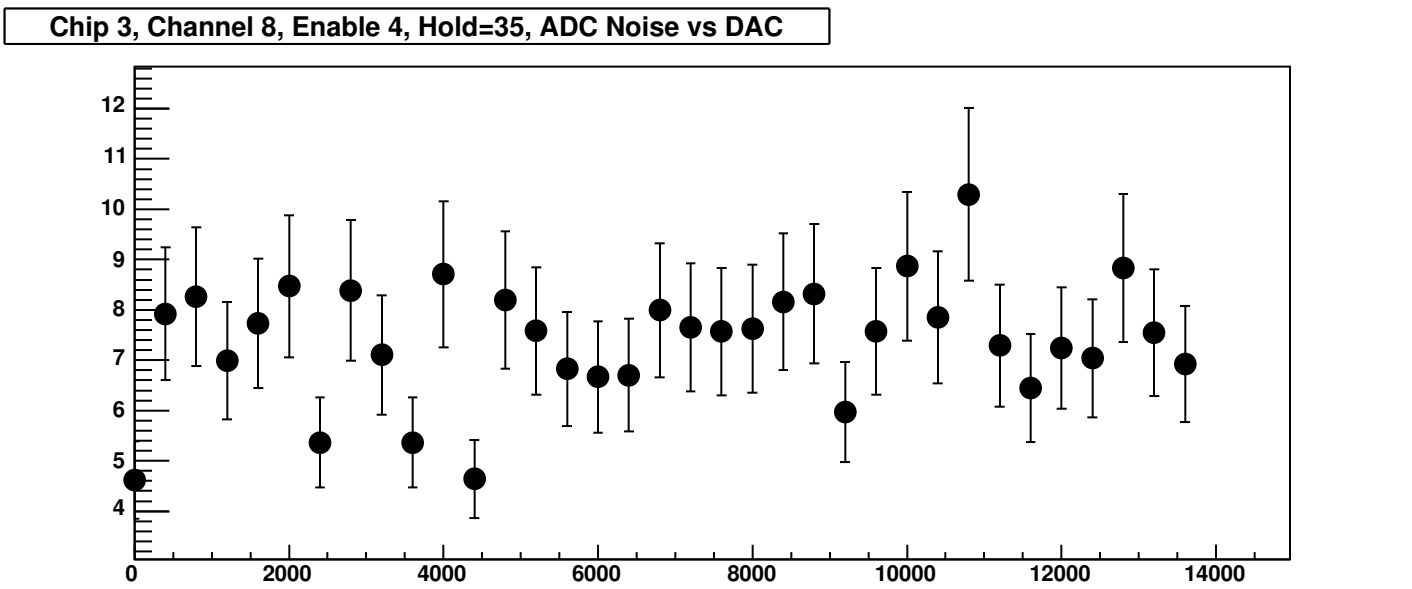

Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

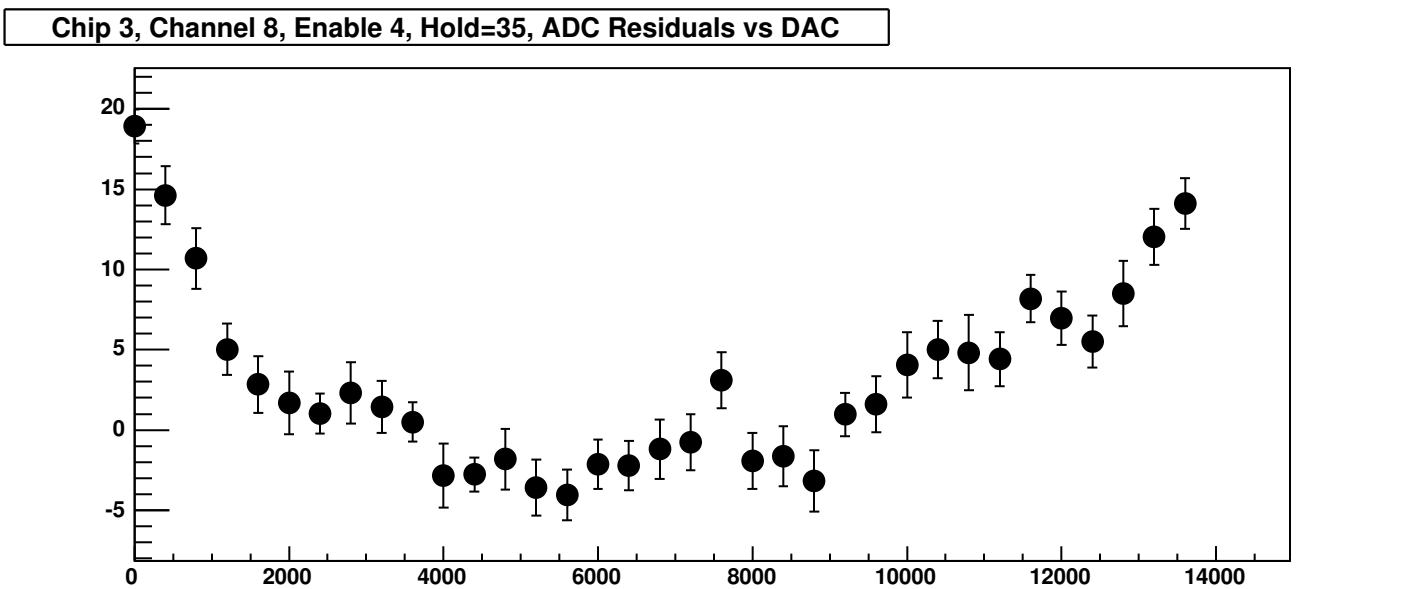

Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 83.44 / 23
p0 -582.3 ± 0.8034
p1 0.01592 ± 0.0001226
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

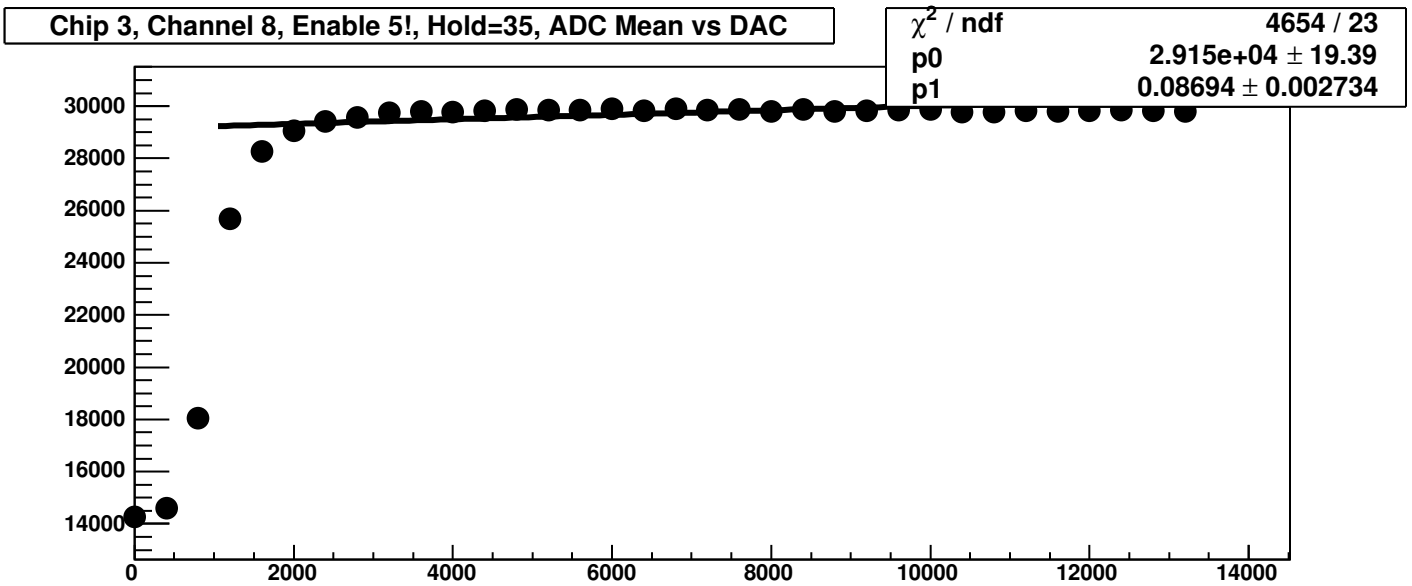

Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

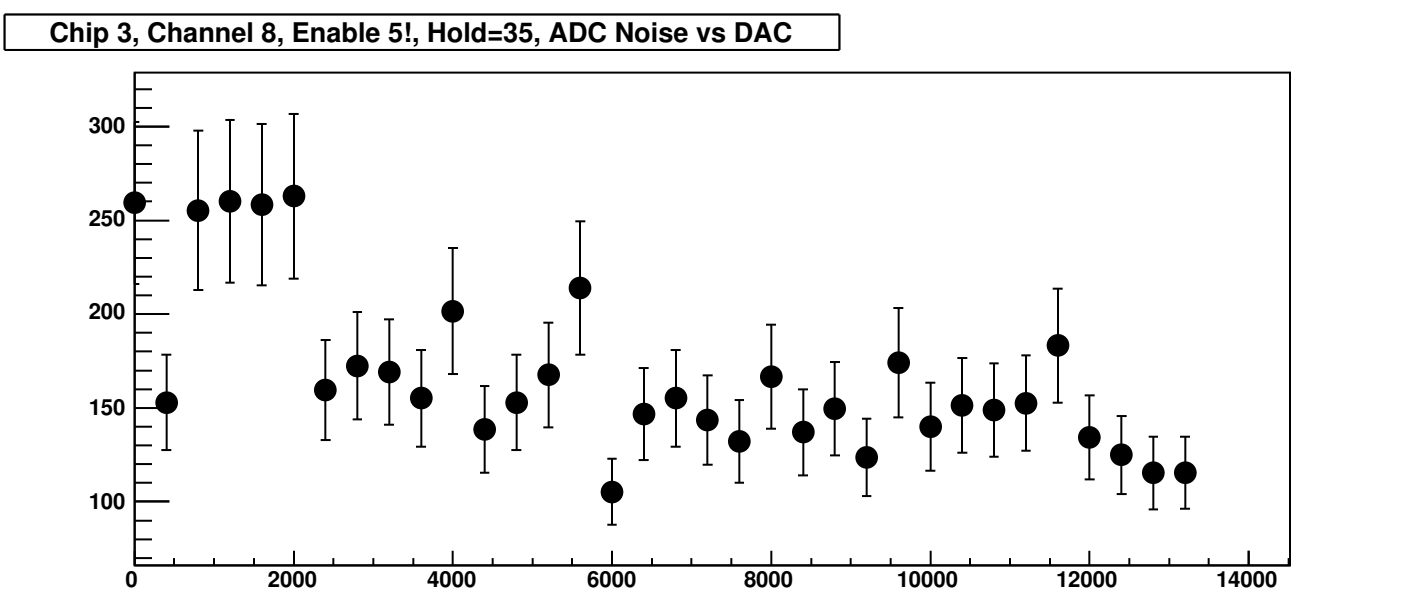

Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

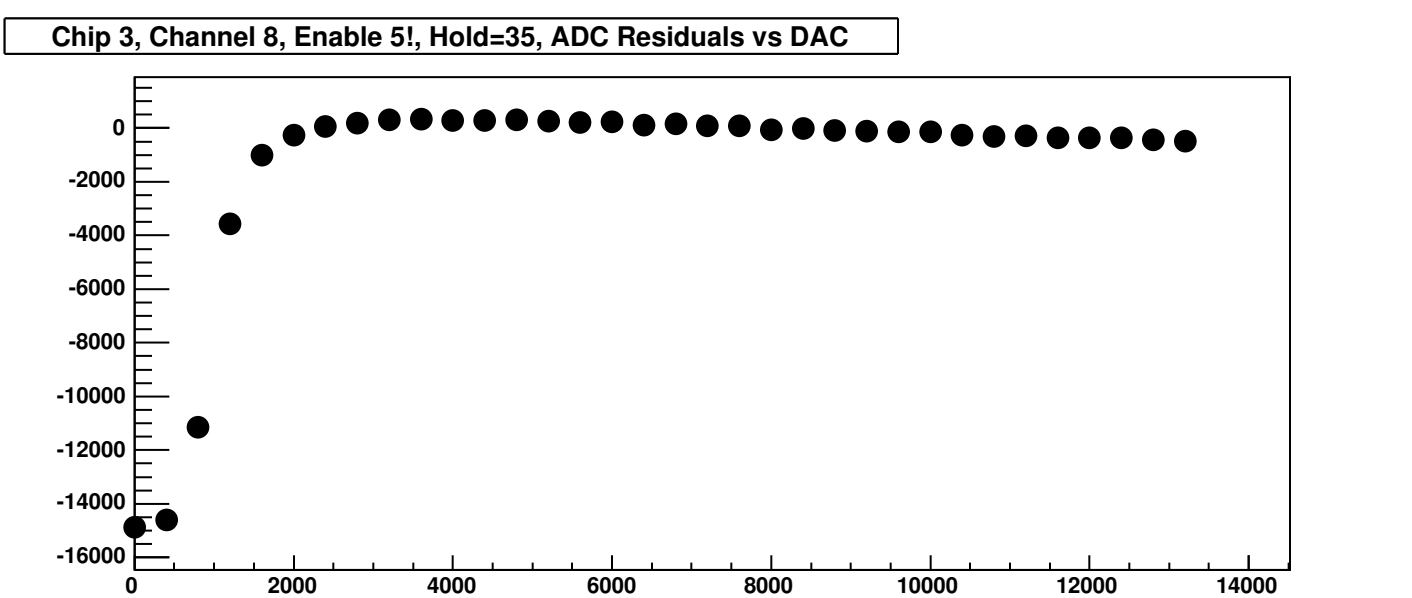

Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 66.67 / 23
p0 -578.3 ± 0.7467
p1 0.0164 ± 0.0001191
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

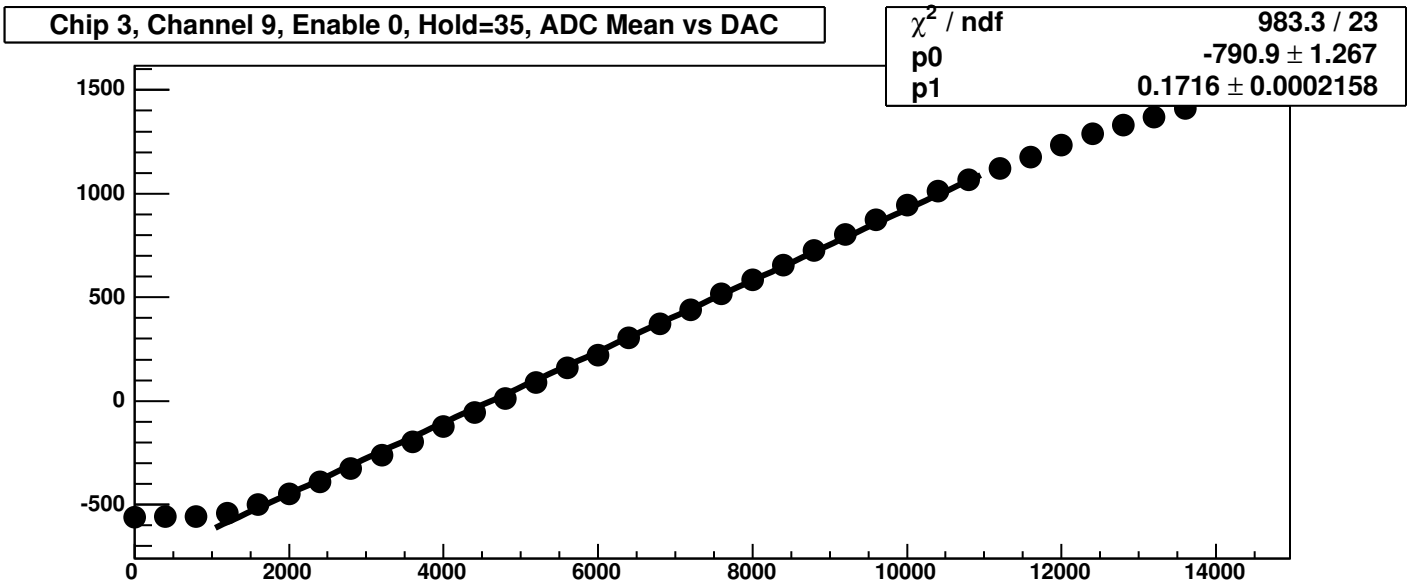

Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

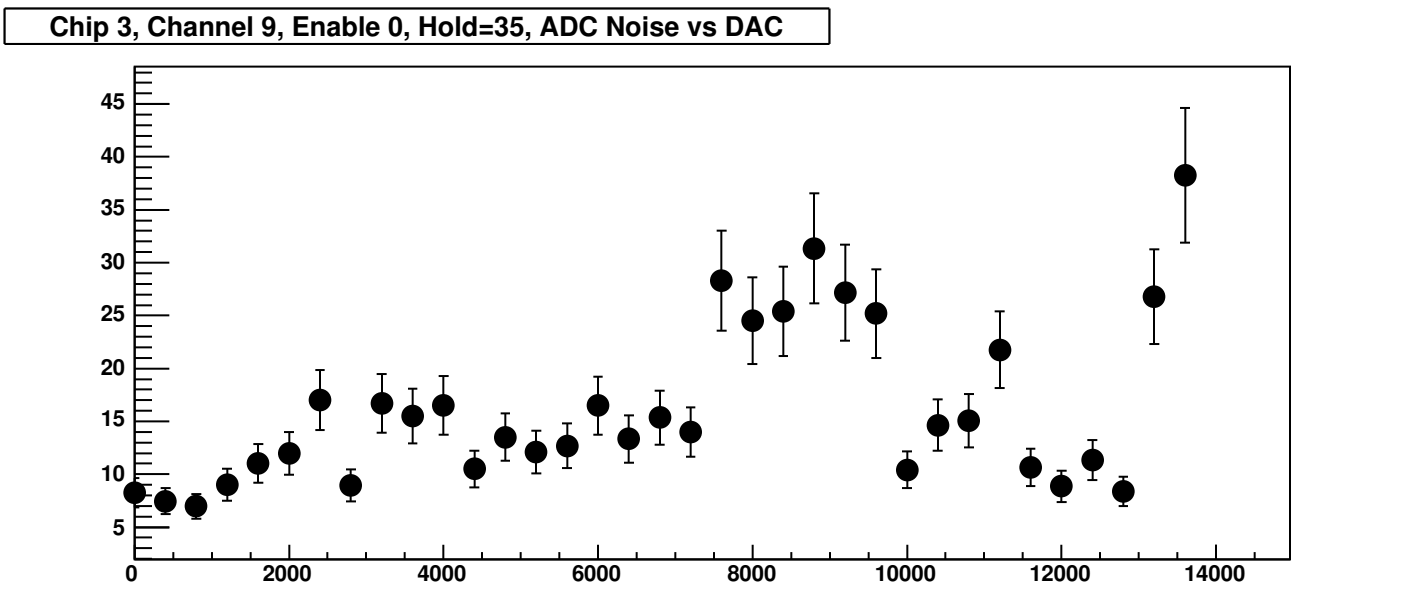

Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

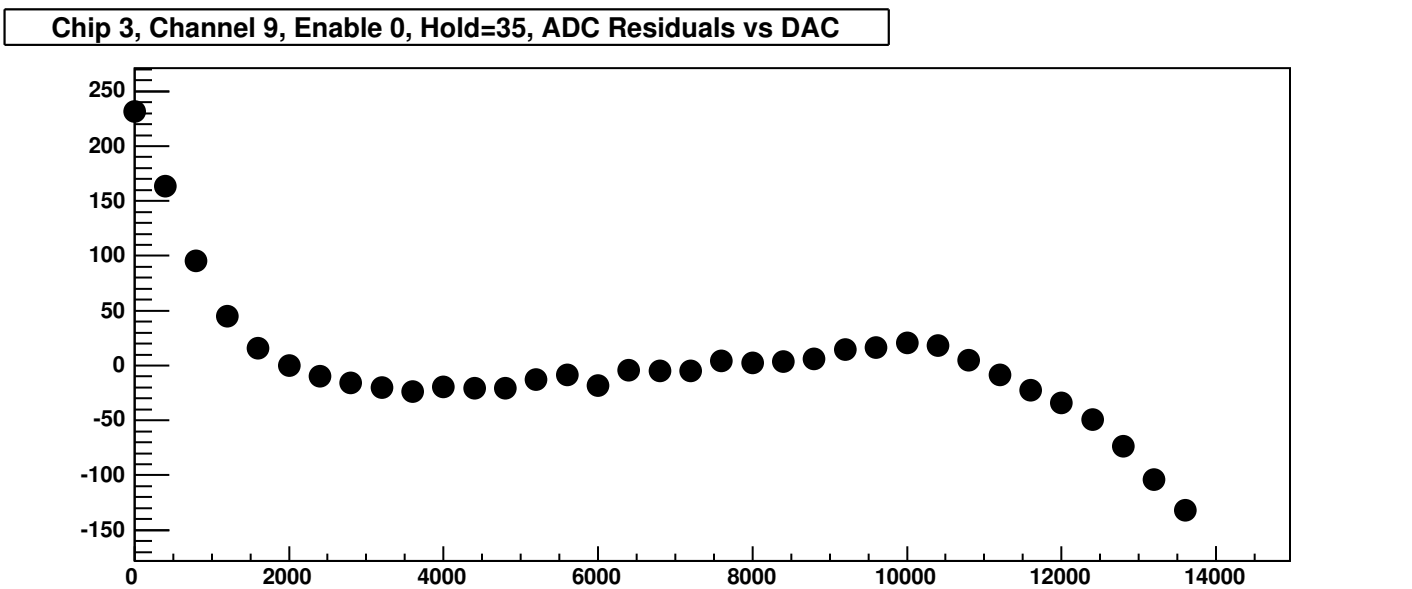

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4654 / 23
p0 2.915e+04 ± 19.39
p1 0.08694 ± 0.002734
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

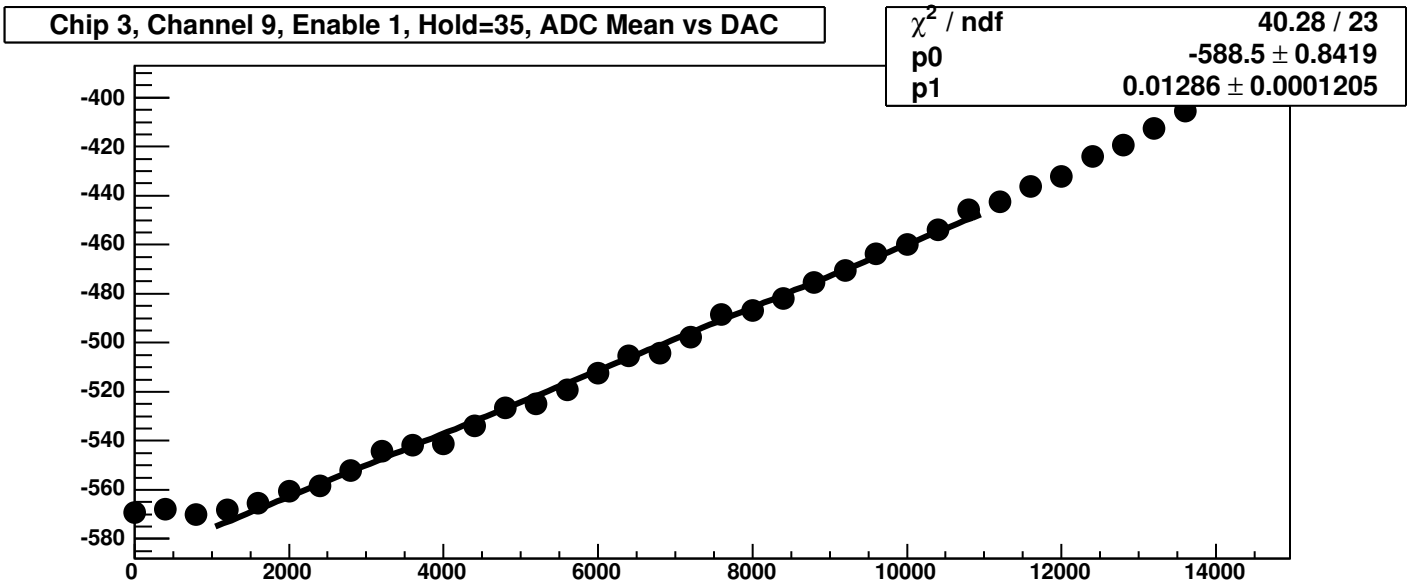

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

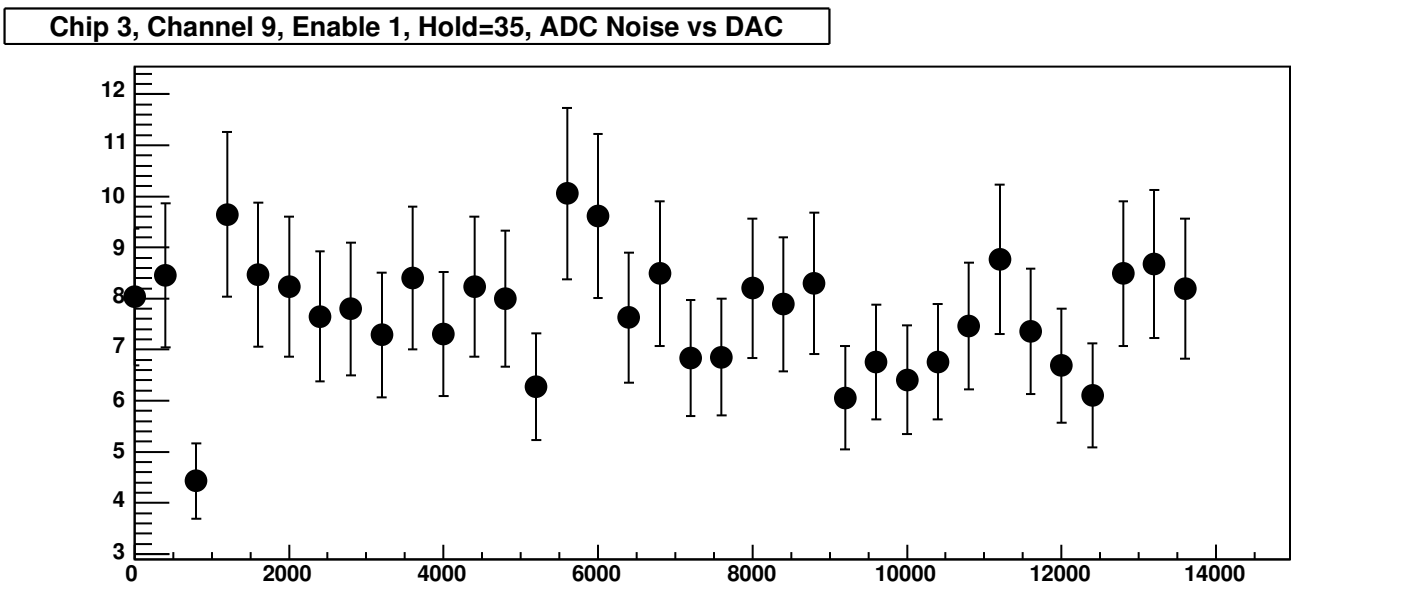

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

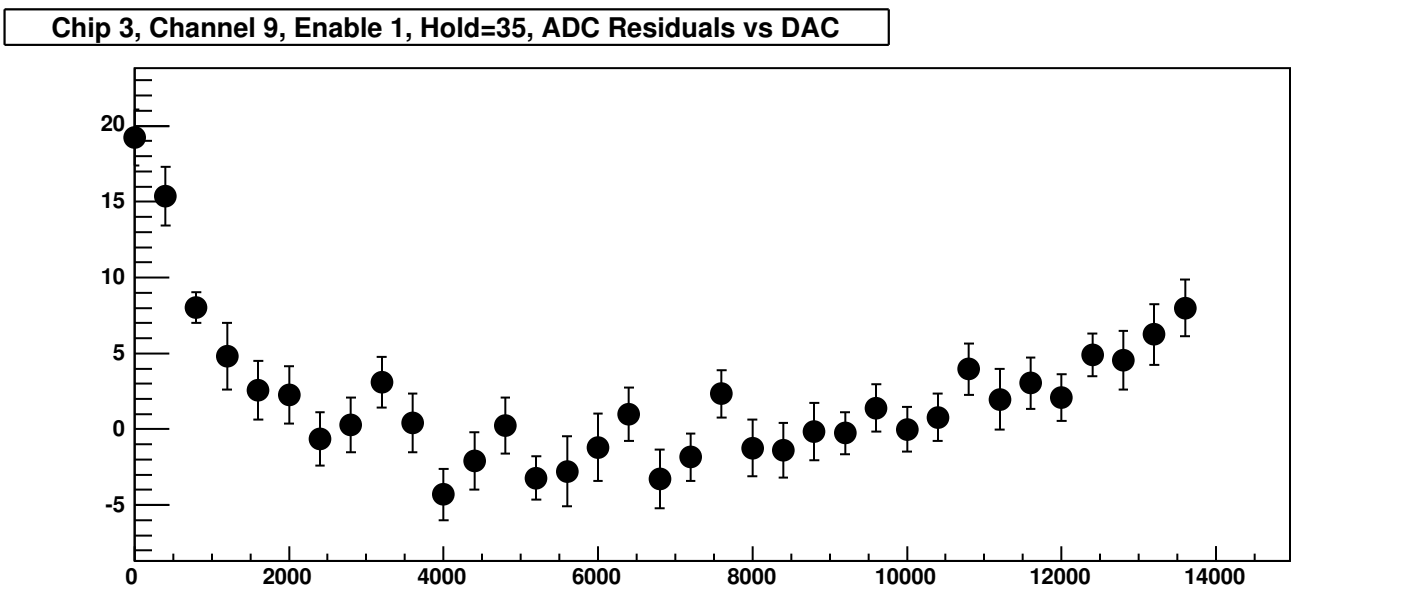

Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 983.3 / 23
p0 -790.9 ± 1.267
p1 0.1716 ± 0.0002158
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

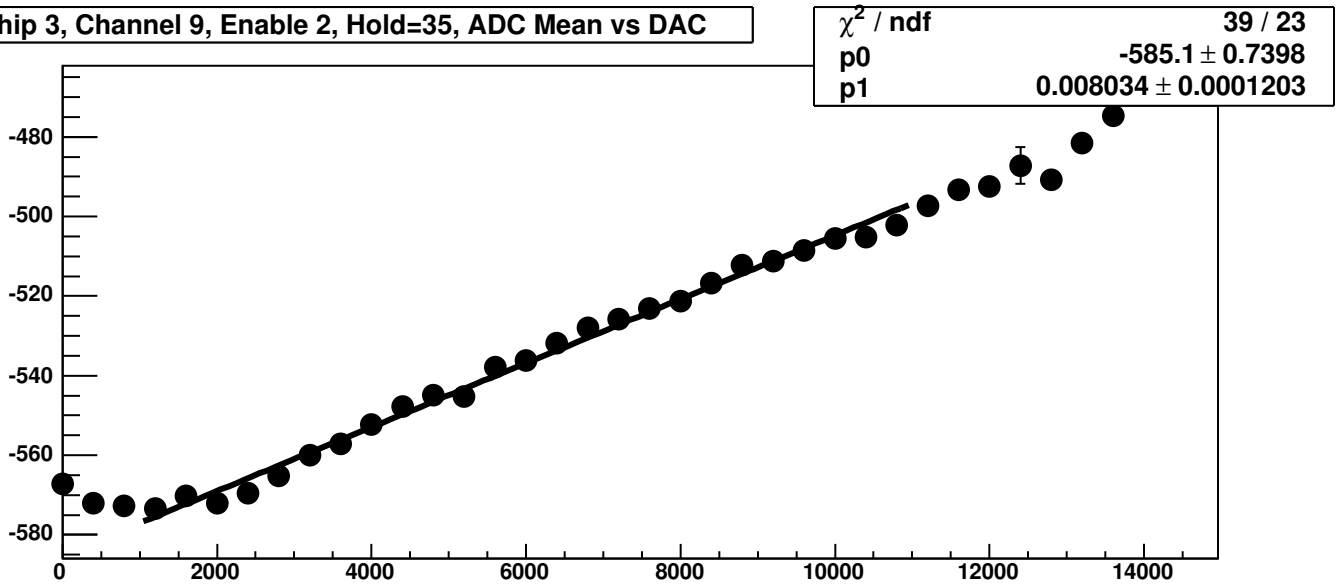

Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.28 / 23
p0 -588.5 ± 0.8419
p1 0.01286 ± 0.0001205
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

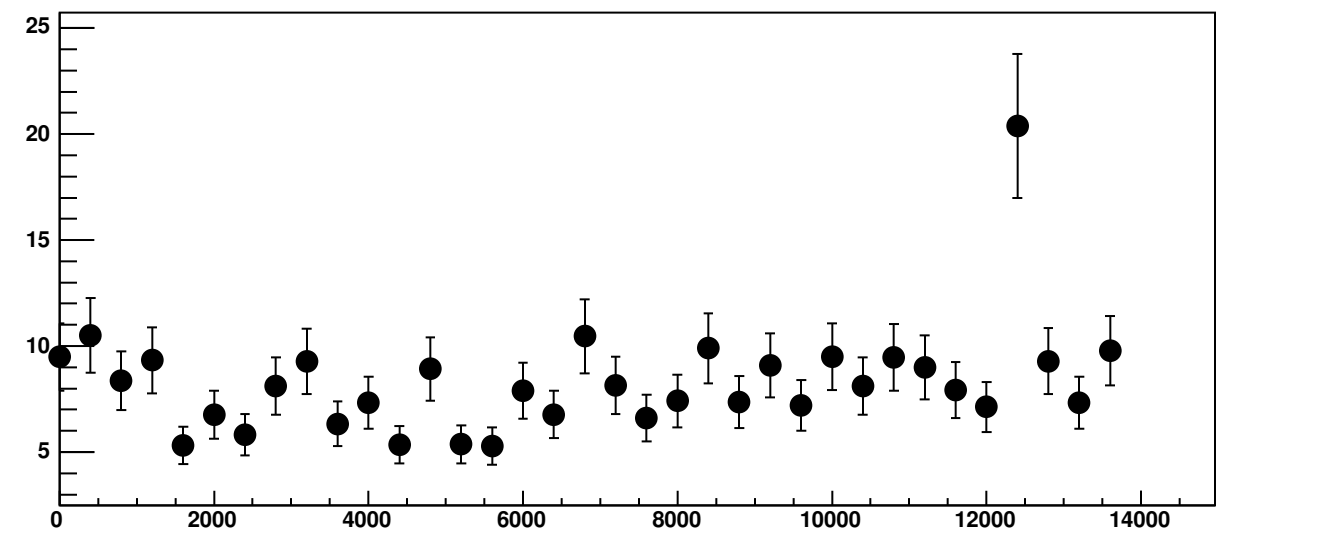

Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

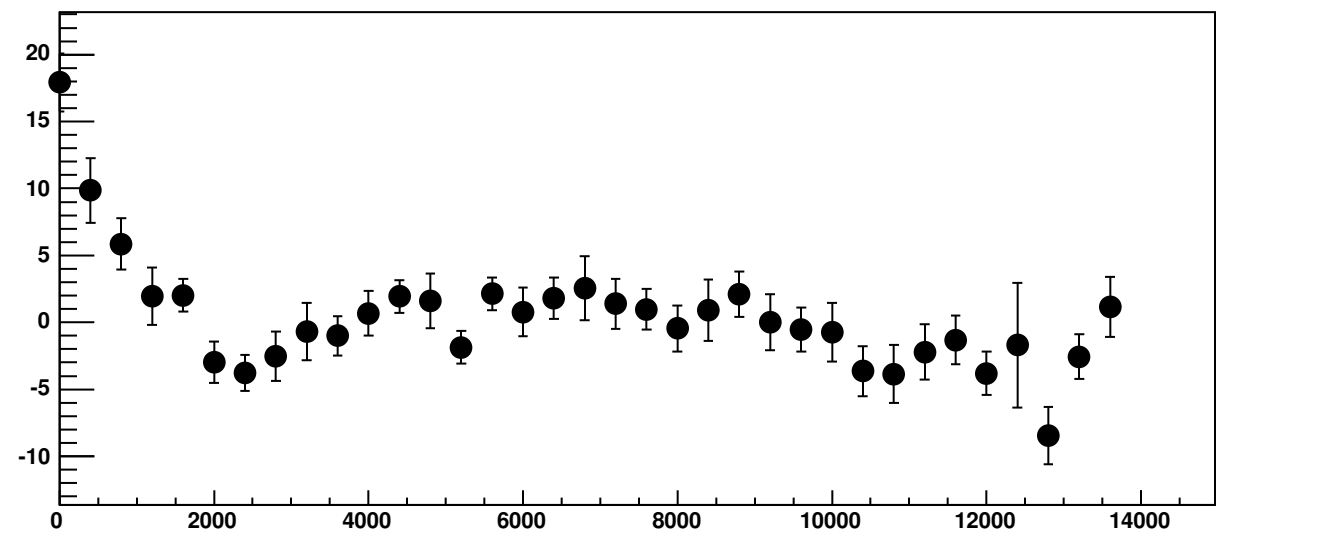
Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

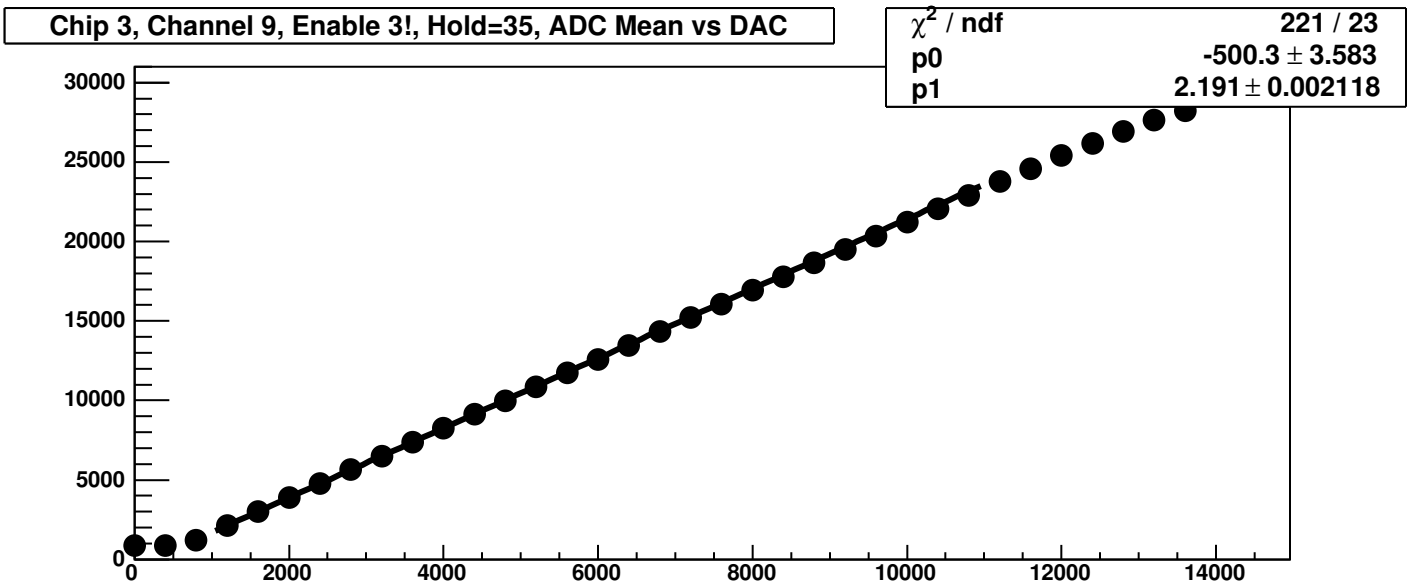


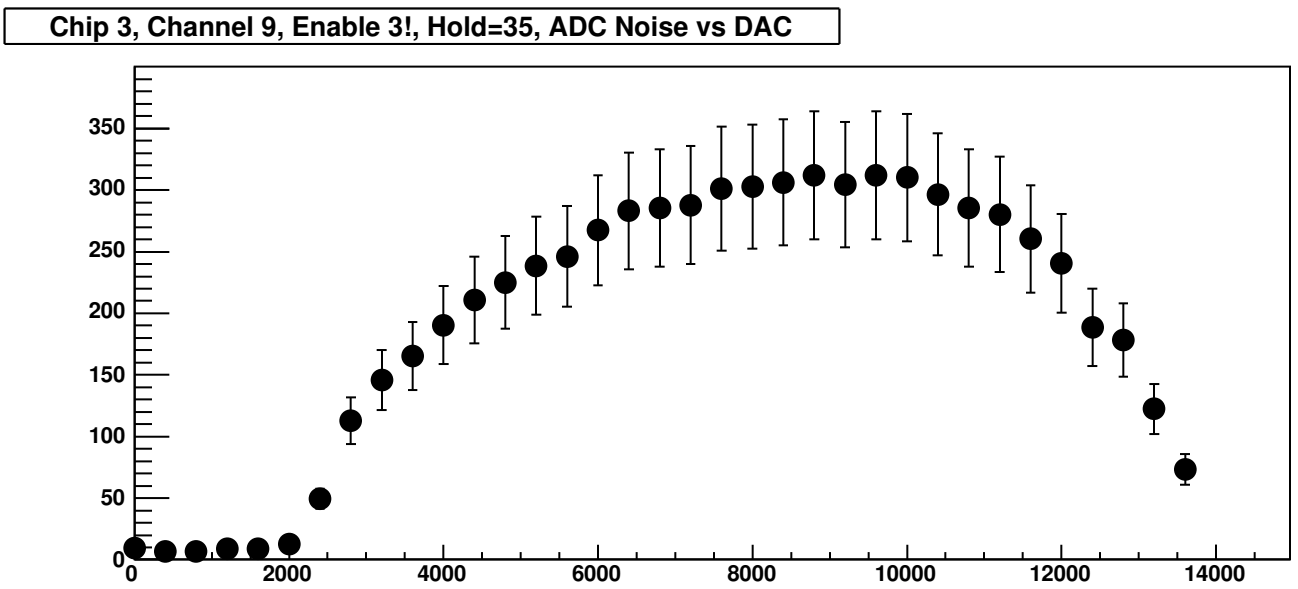
Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

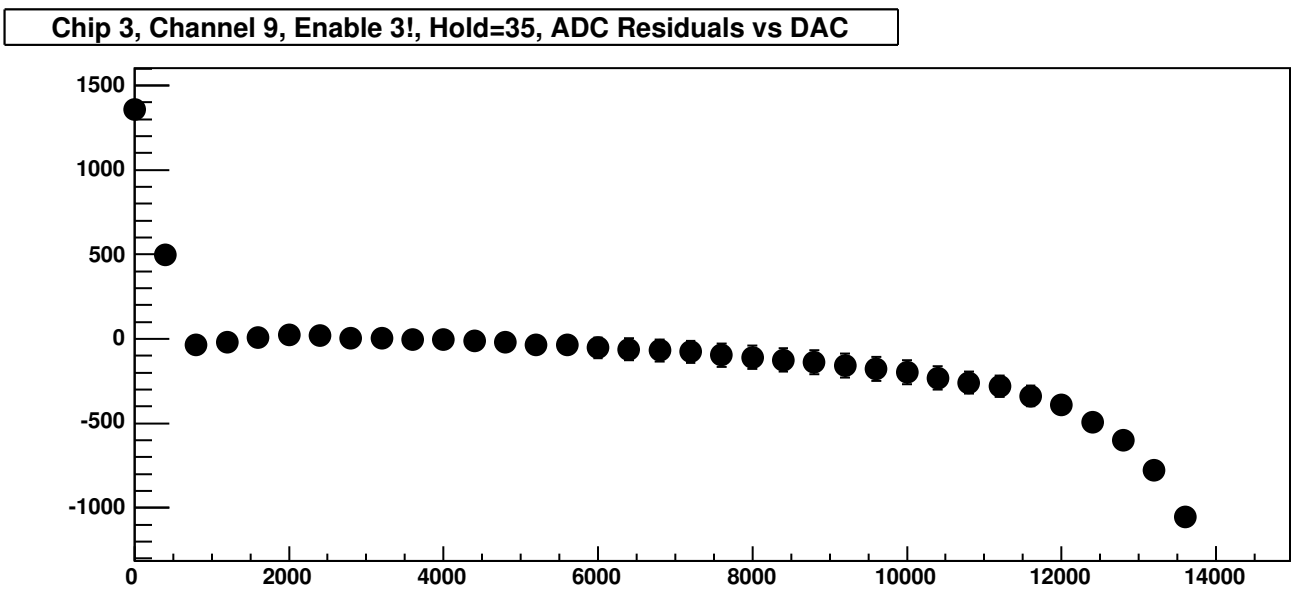


Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

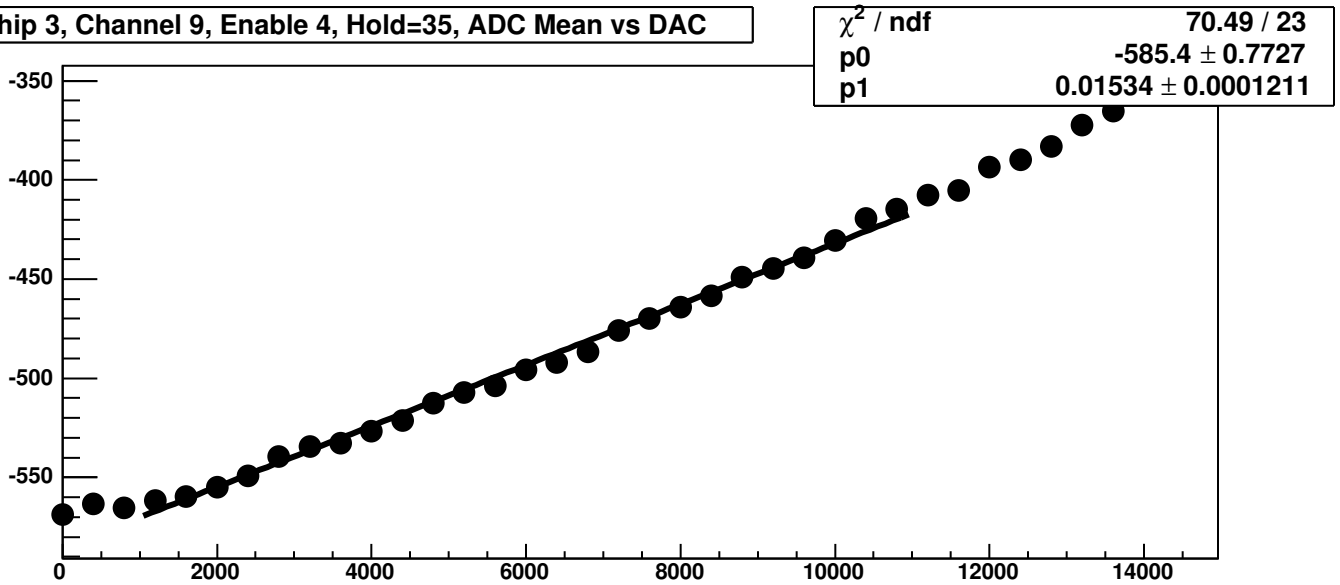



Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 221 / 23
p0 -500.3 ± 3.583
p1 2.191 ± 0.002118
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

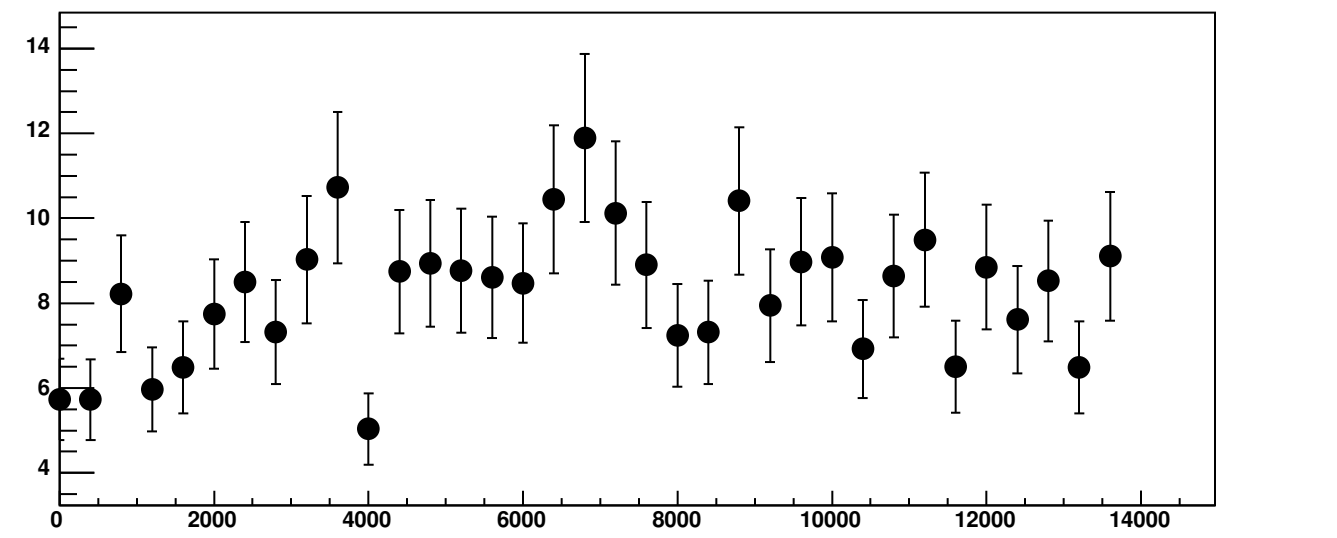

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

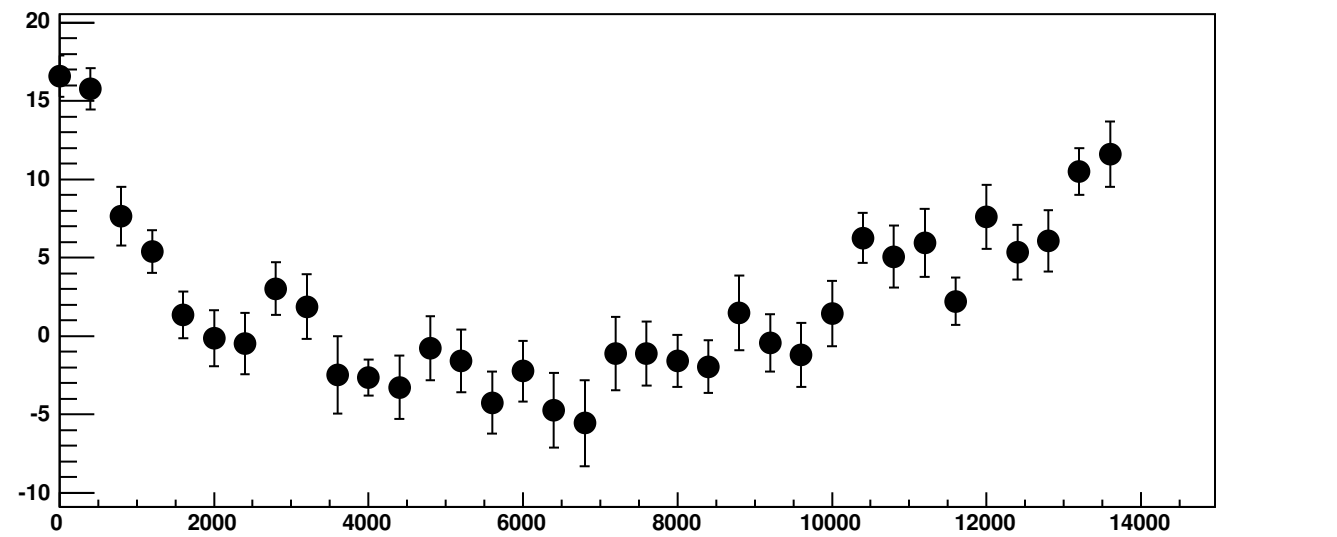
Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

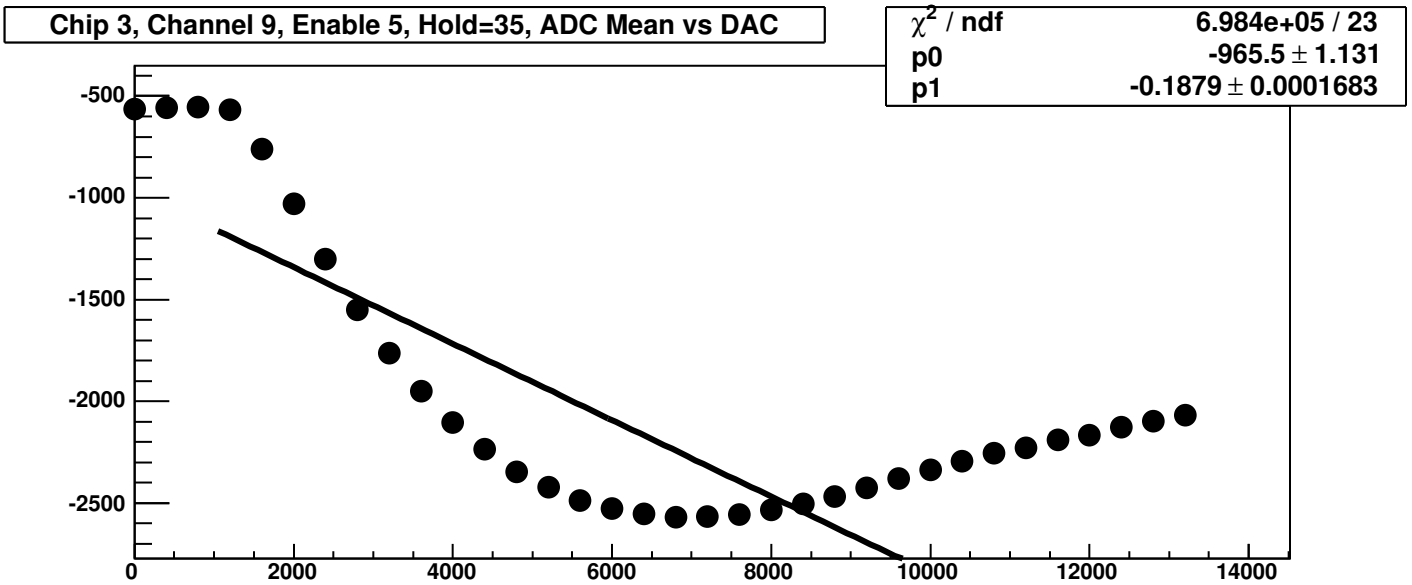


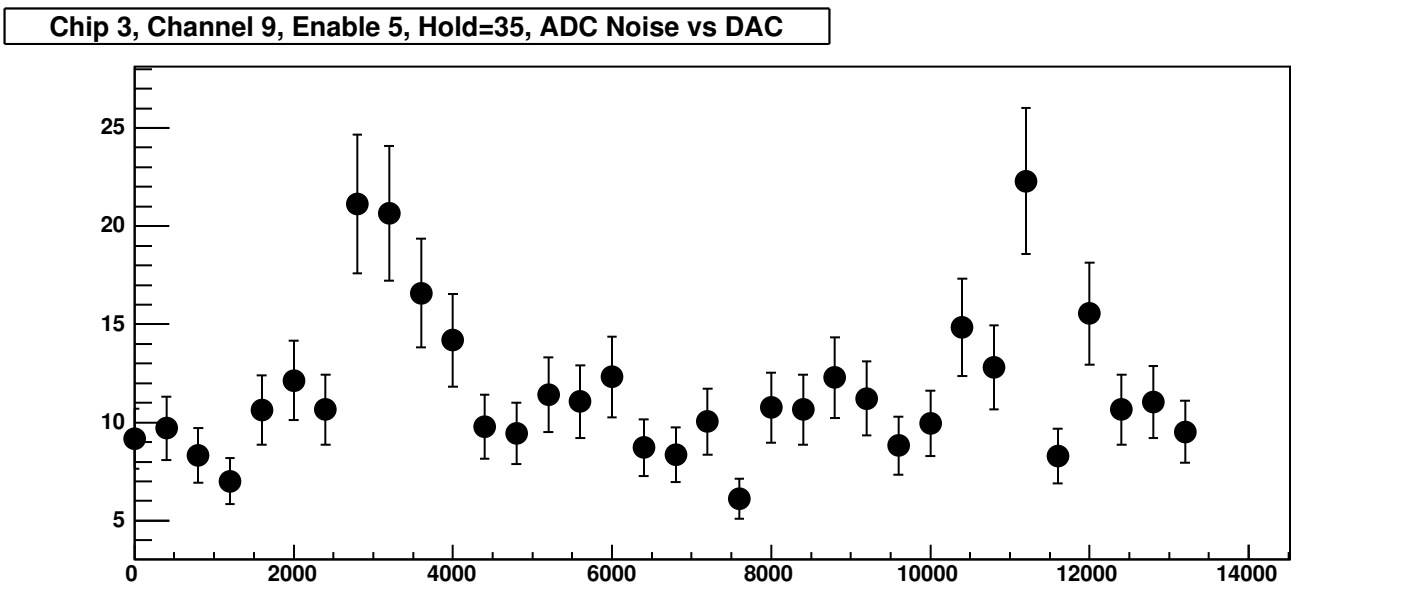
Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

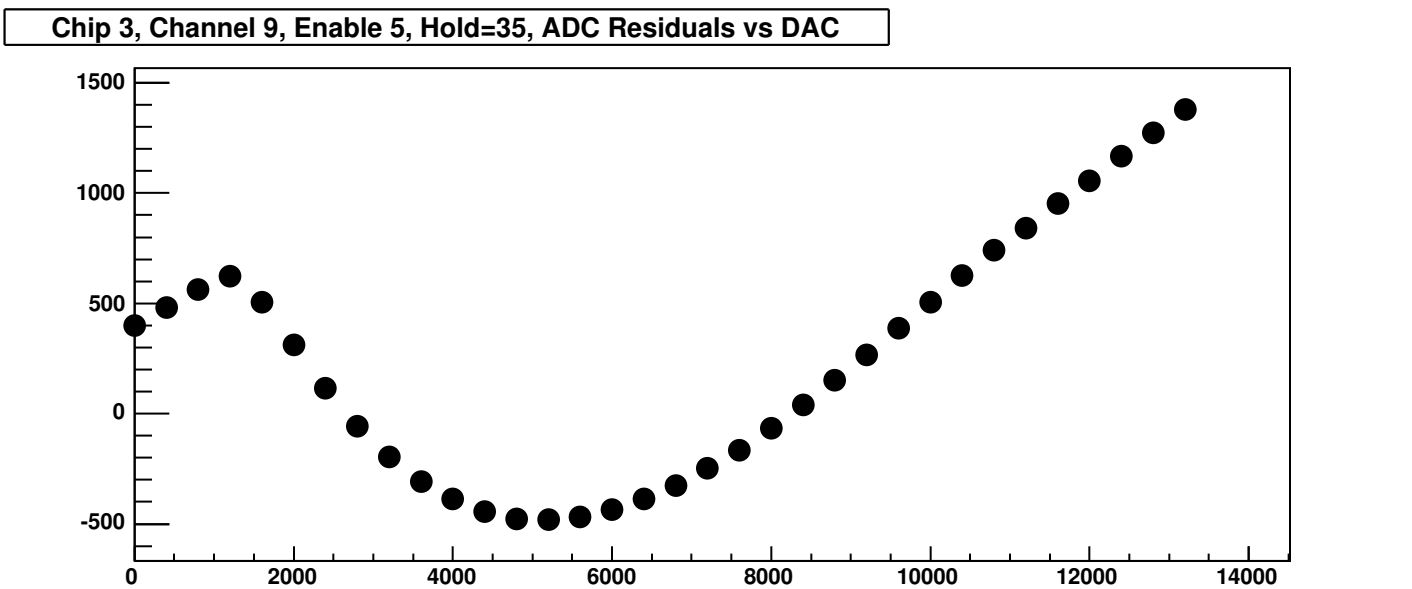


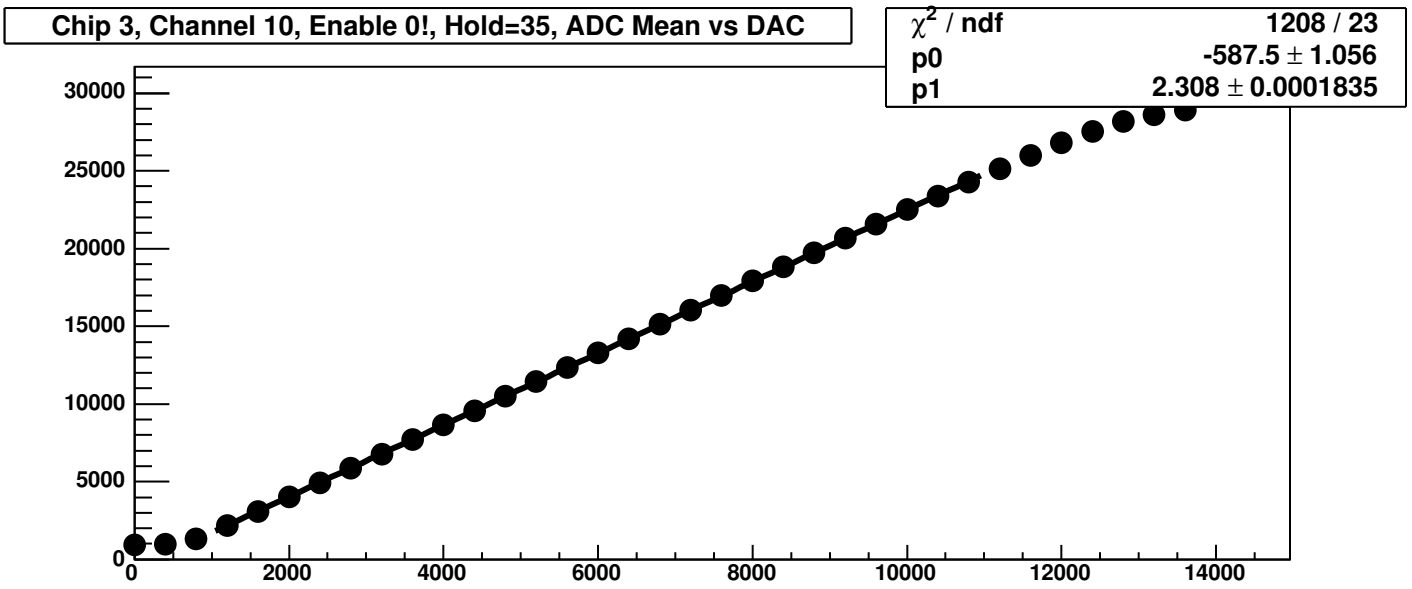
Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

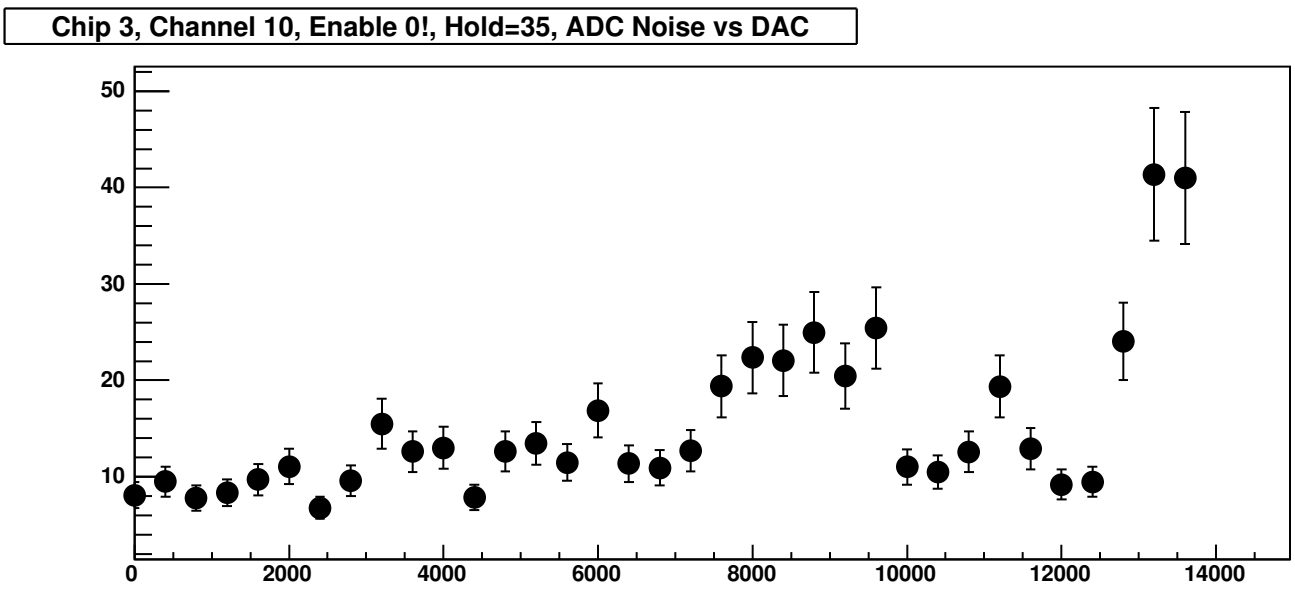


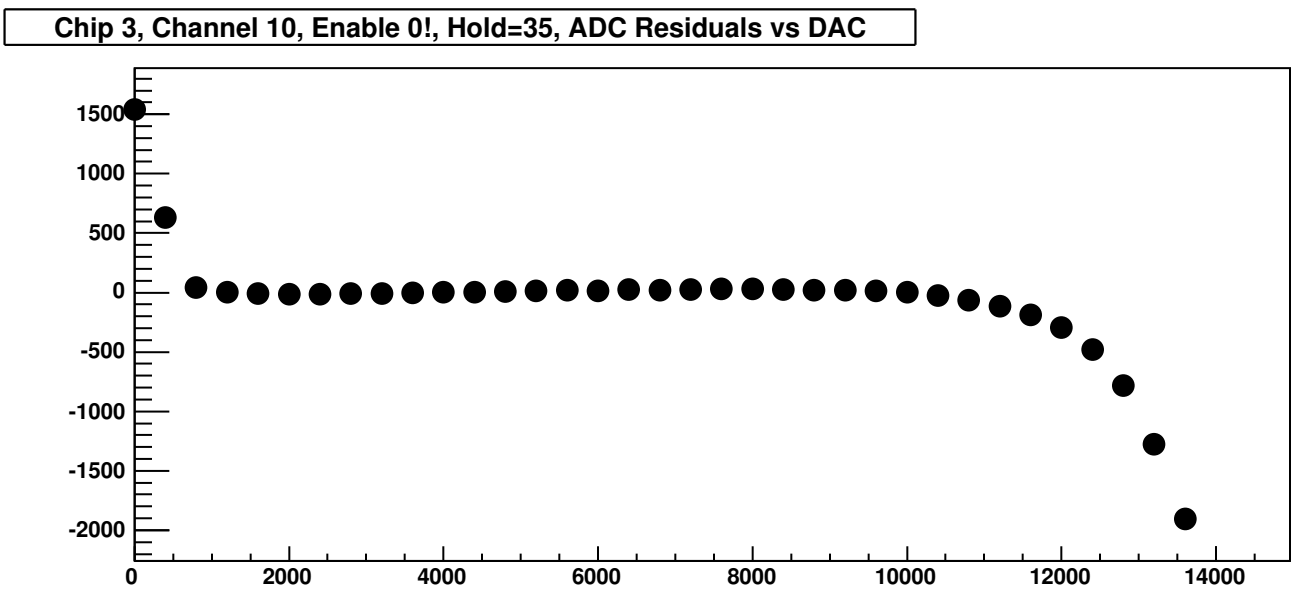

Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.984e+05 / 23
p0 -965.5 ± 1.131
p1 -0.1879 ± 0.0001683
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

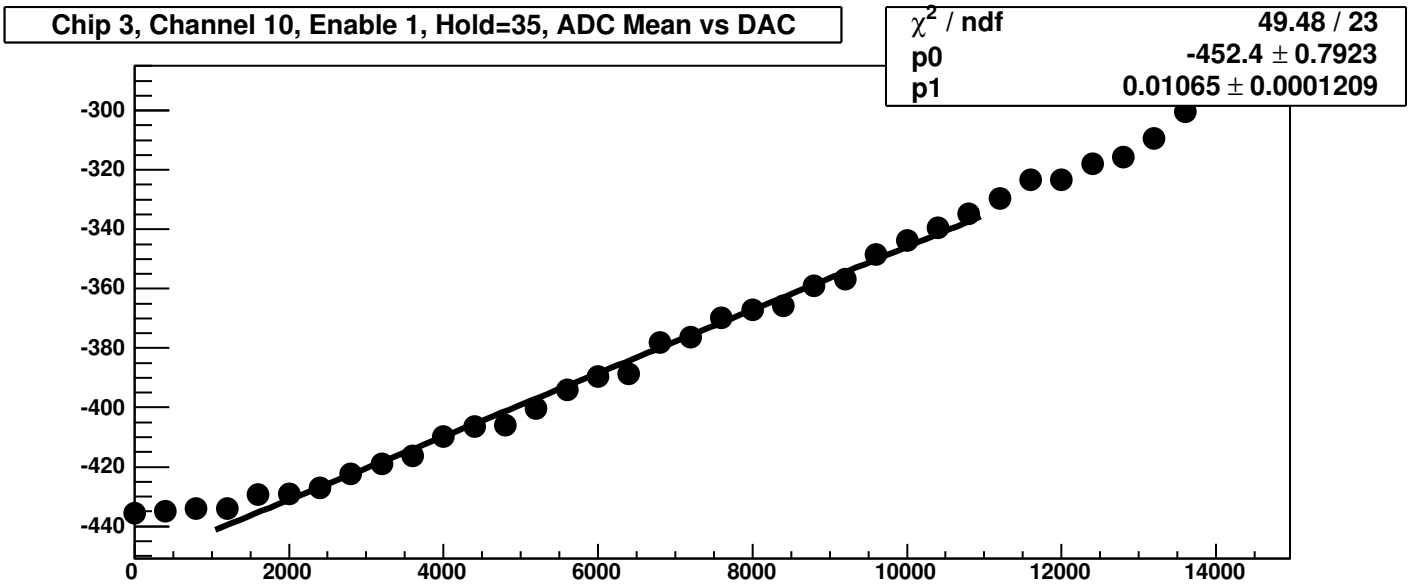

Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

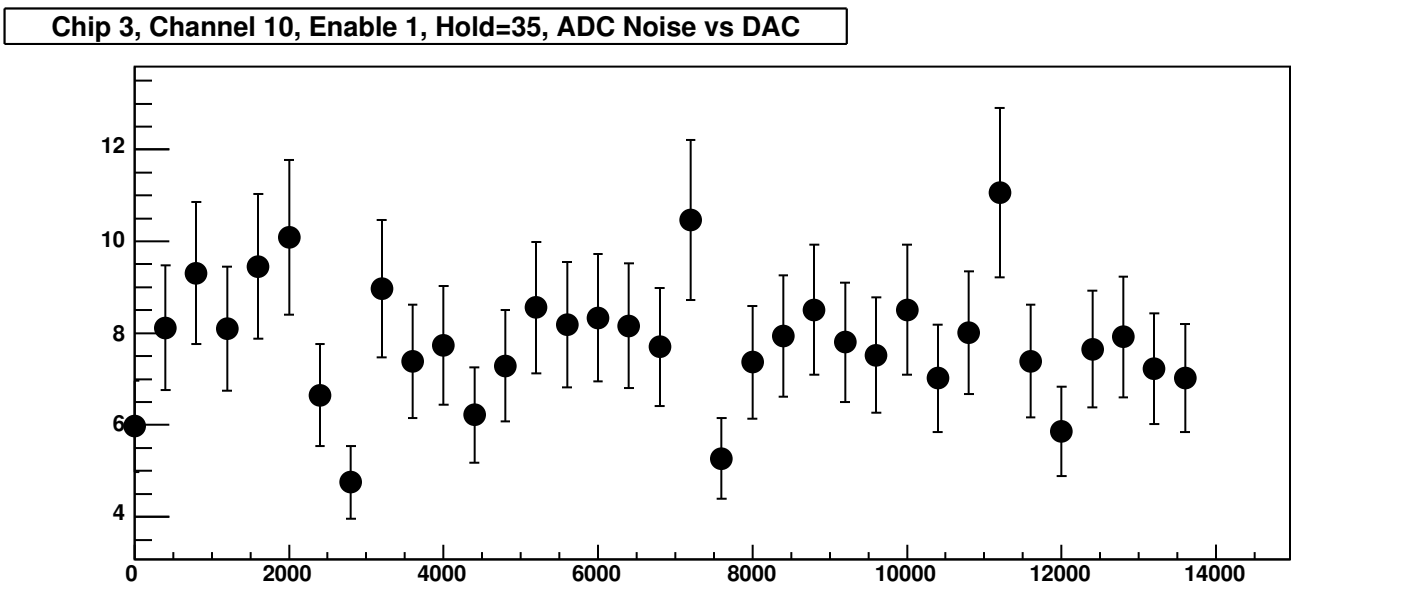

Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

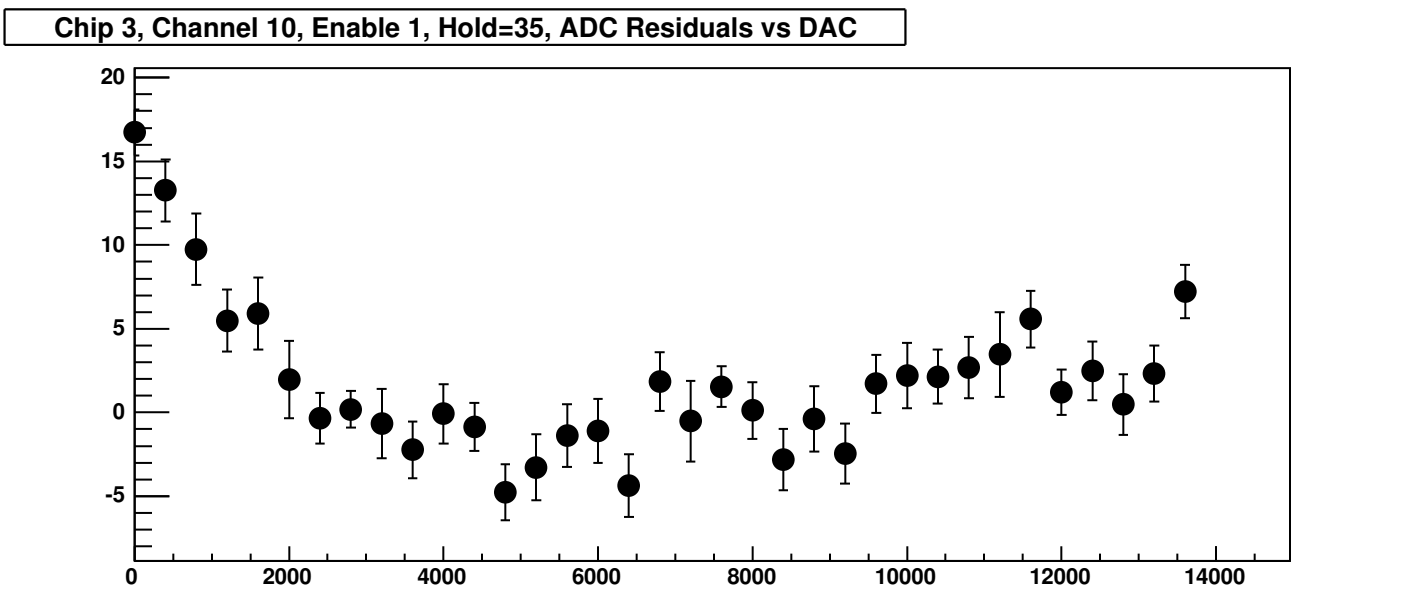

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1208 / 23
p0 -587.5 ± 1.056
p1 2.308 ± 0.0001835
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

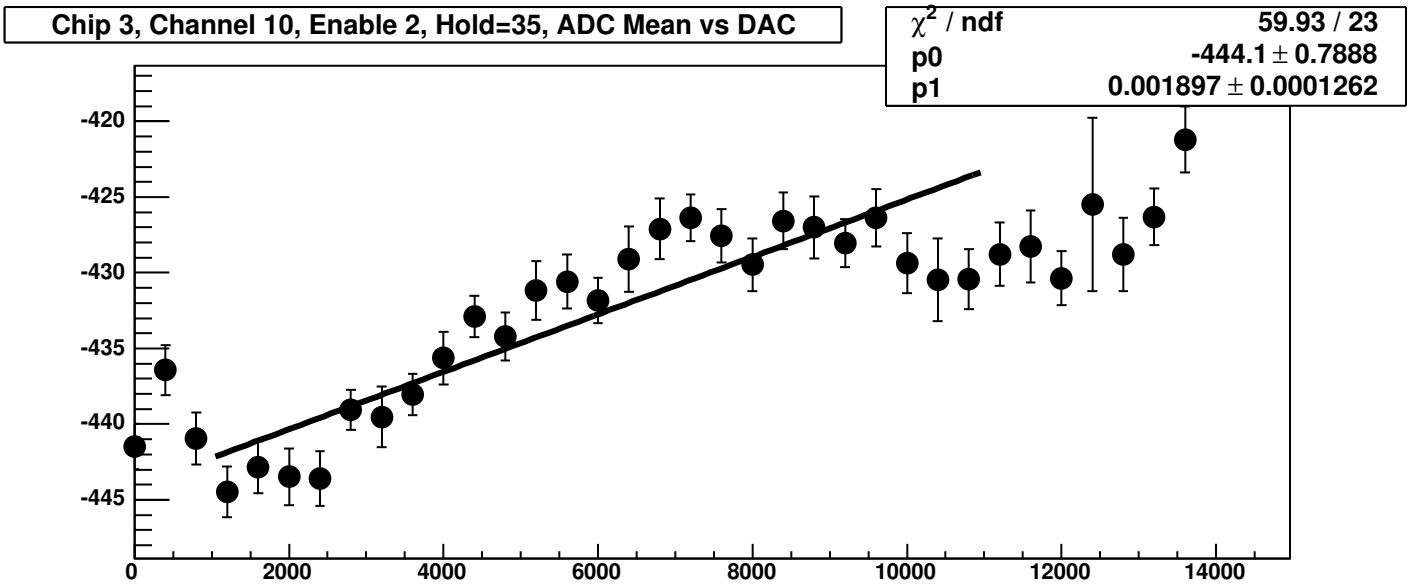

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

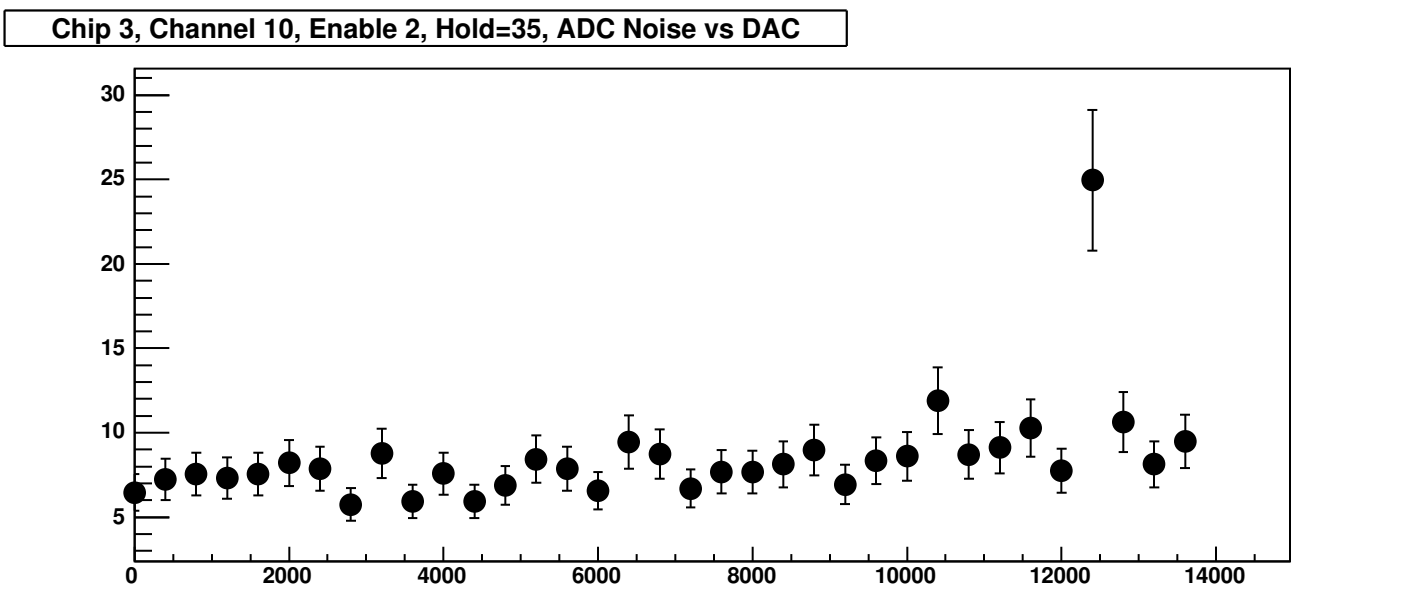

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

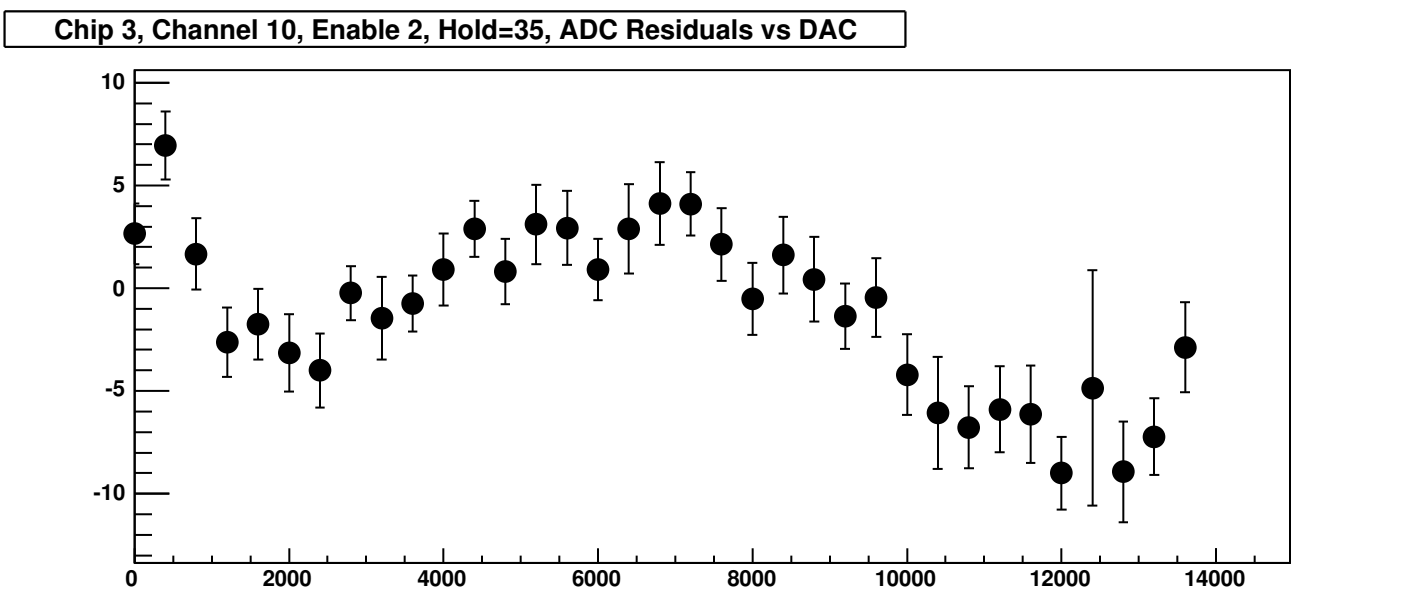

Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.48 / 23
p0 -452.4 ± 0.7923
p1 0.01065 ± 0.0001209
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

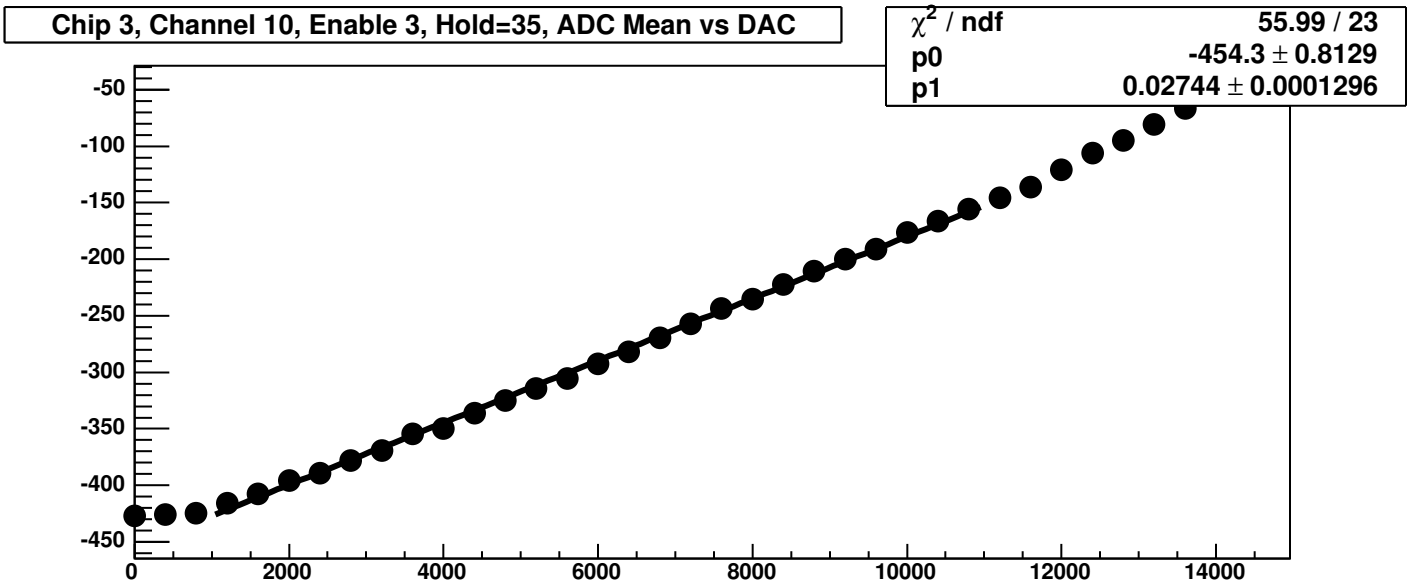

Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

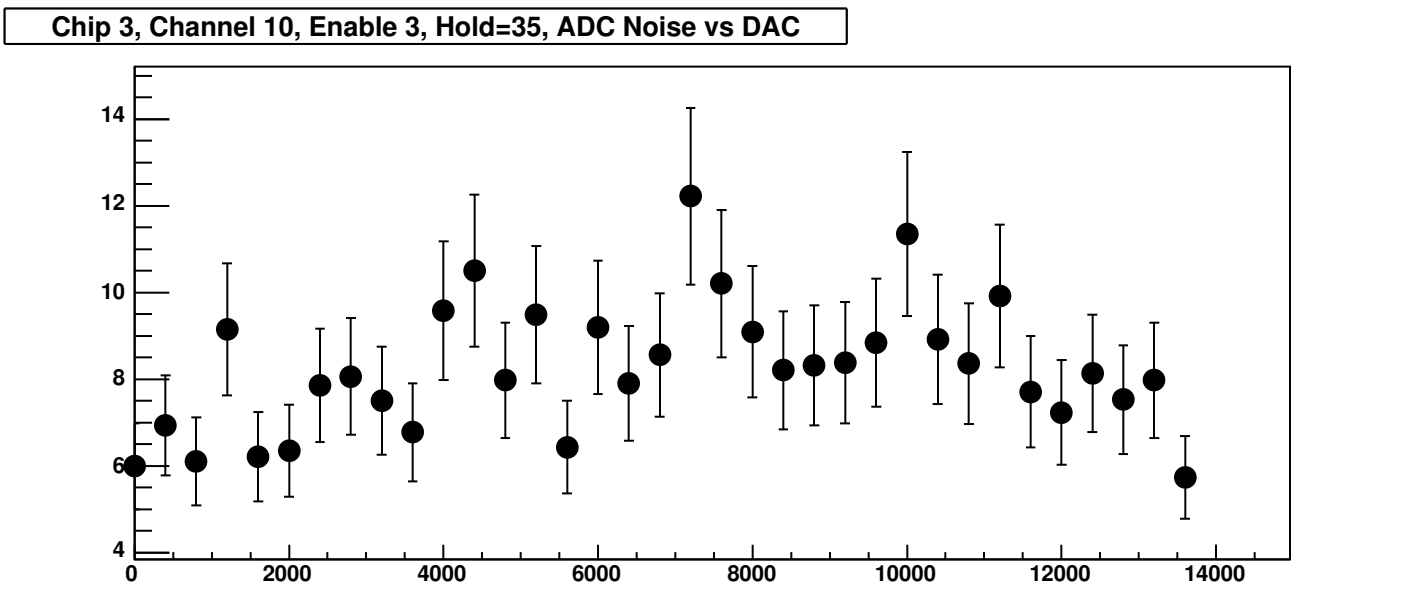

Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

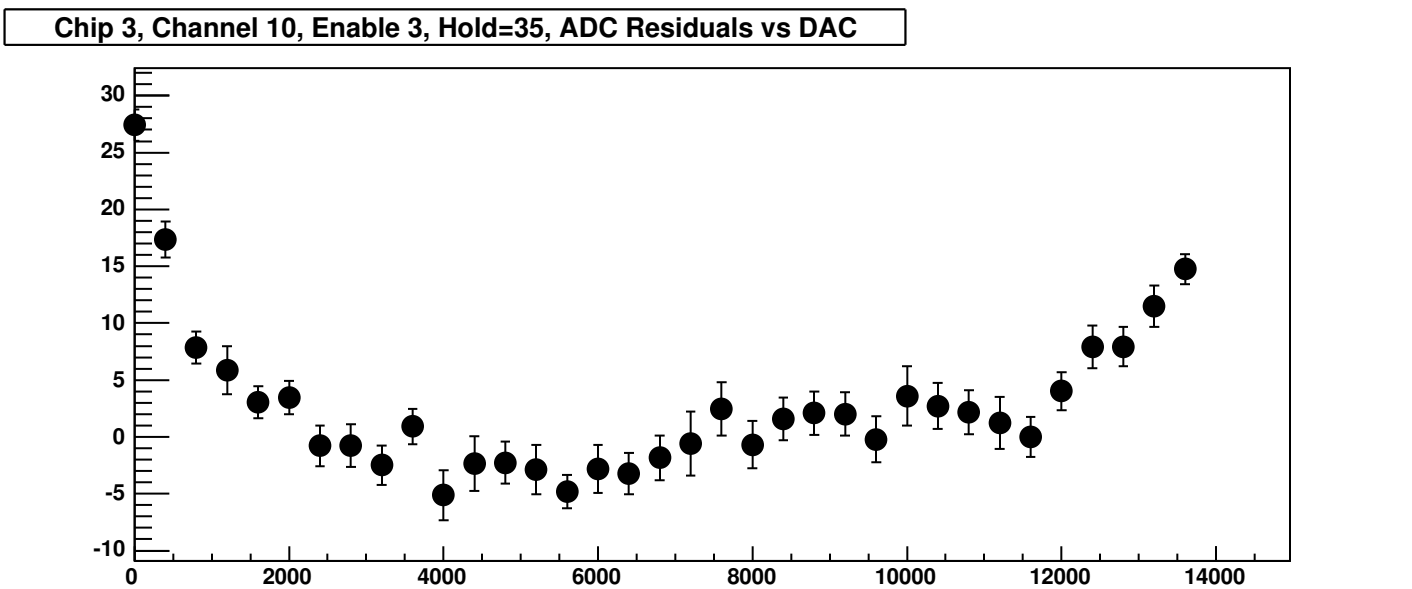

Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 59.93 / 23
p0 -444.1 ± 0.7888
p1 0.001897 ± 0.0001262
-420
-425
-430
-435
-440
-445
0
2000
4000
6000
8000
10000
12000
14000

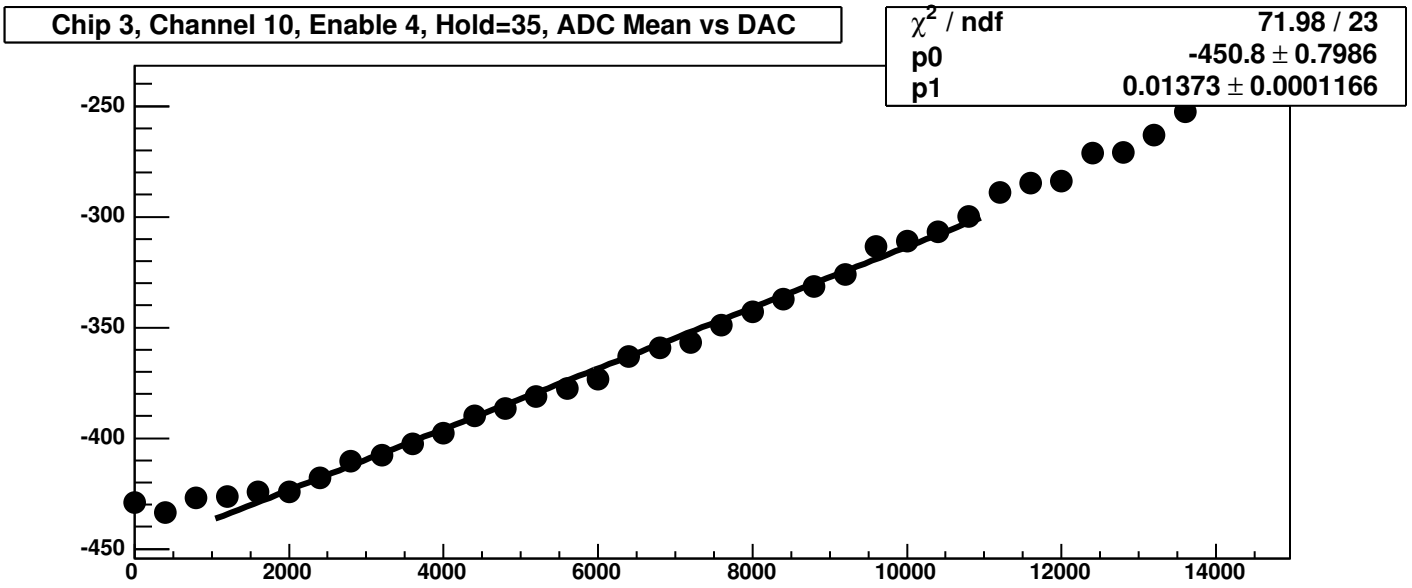

Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

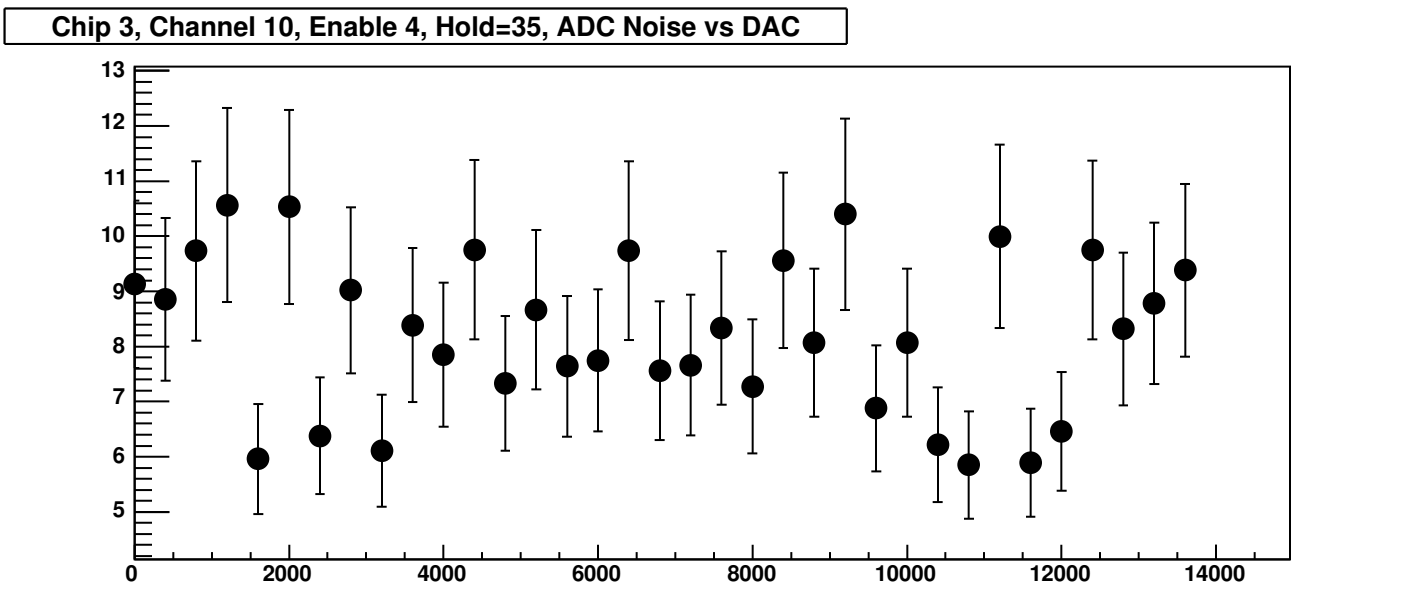

Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

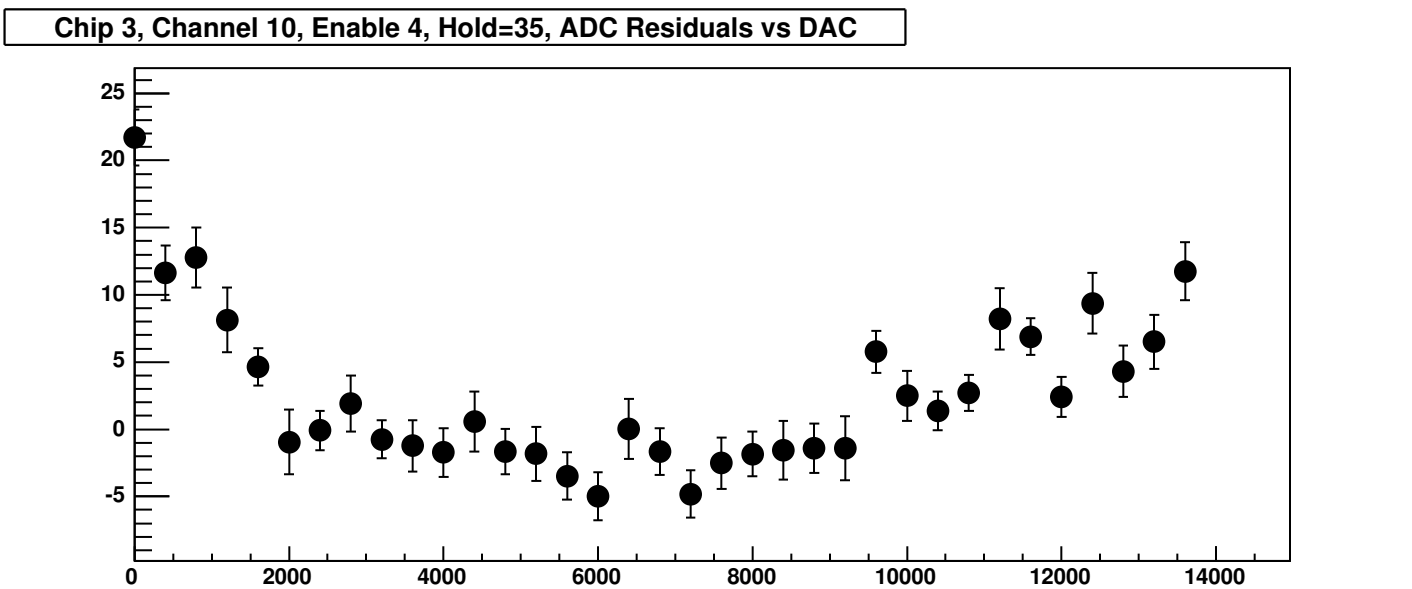

Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 55.99 / 23
p0 -454.3 ± 0.8129
p1 0.02744 ± 0.0001296
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

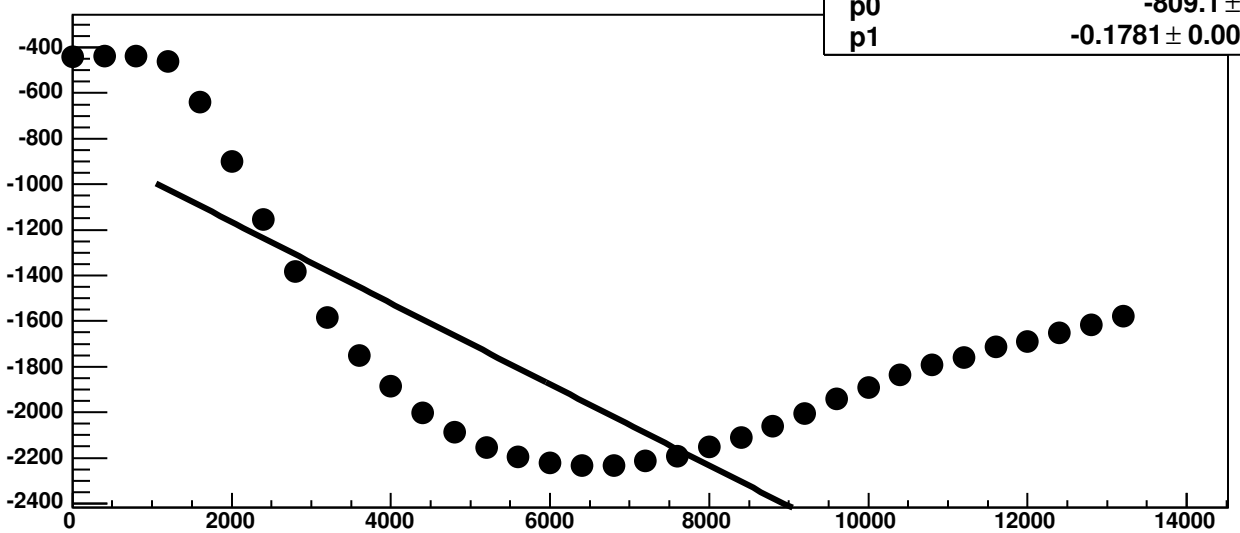

Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 71.98 / 23
p0 -450.8 ± 0.7986
p1 0.01373 ± 0.0001166
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

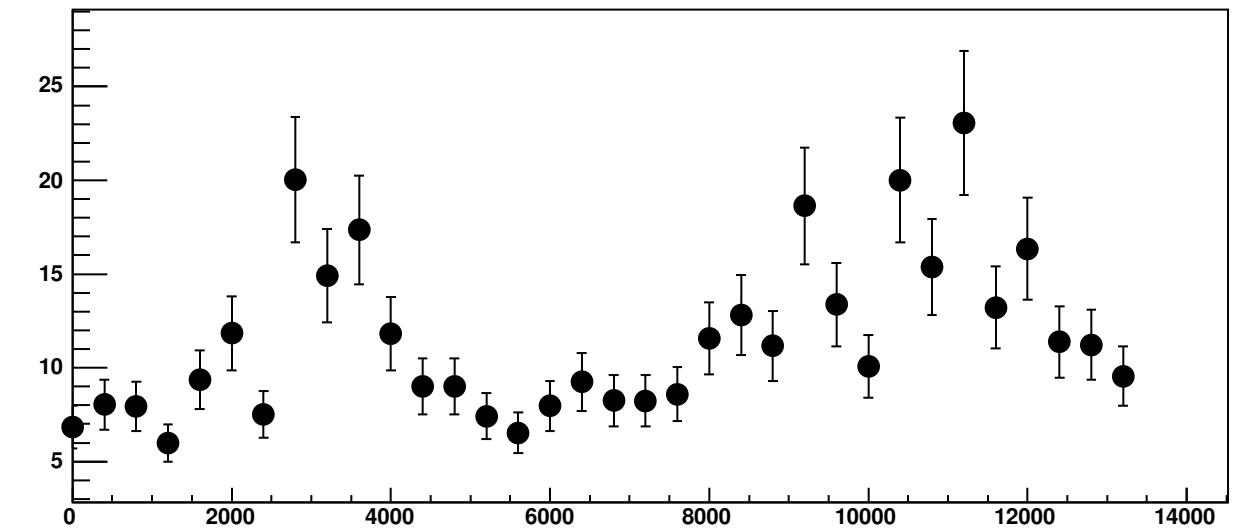

Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

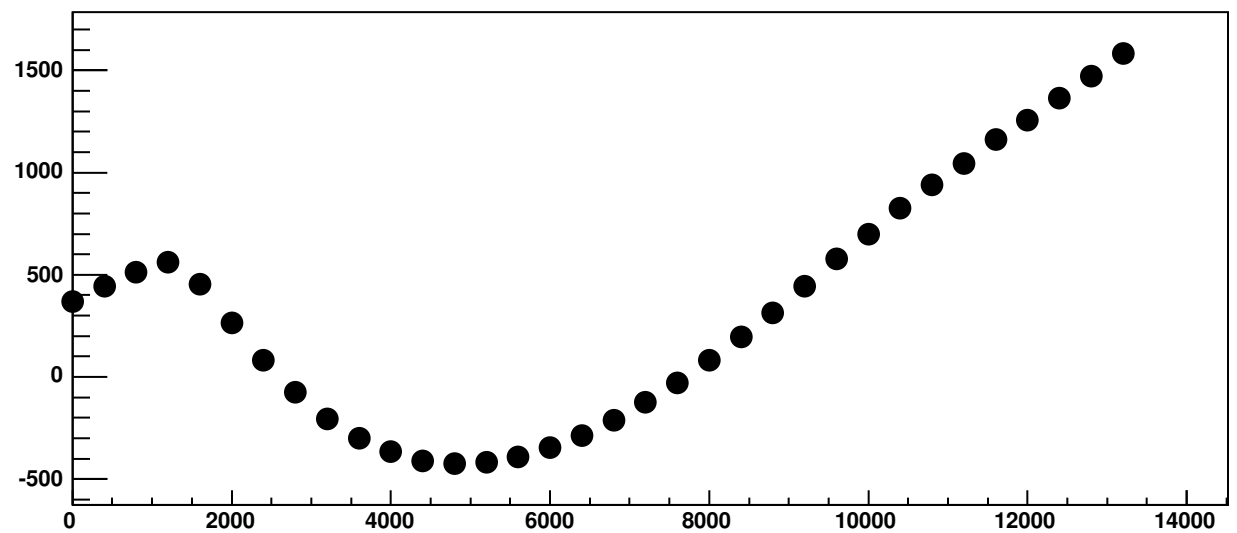
χ^2 / ndf	7.929e+05 / 23
p0	-809.1 ± 1.003
p1	-0.1781 ± 0.0001677

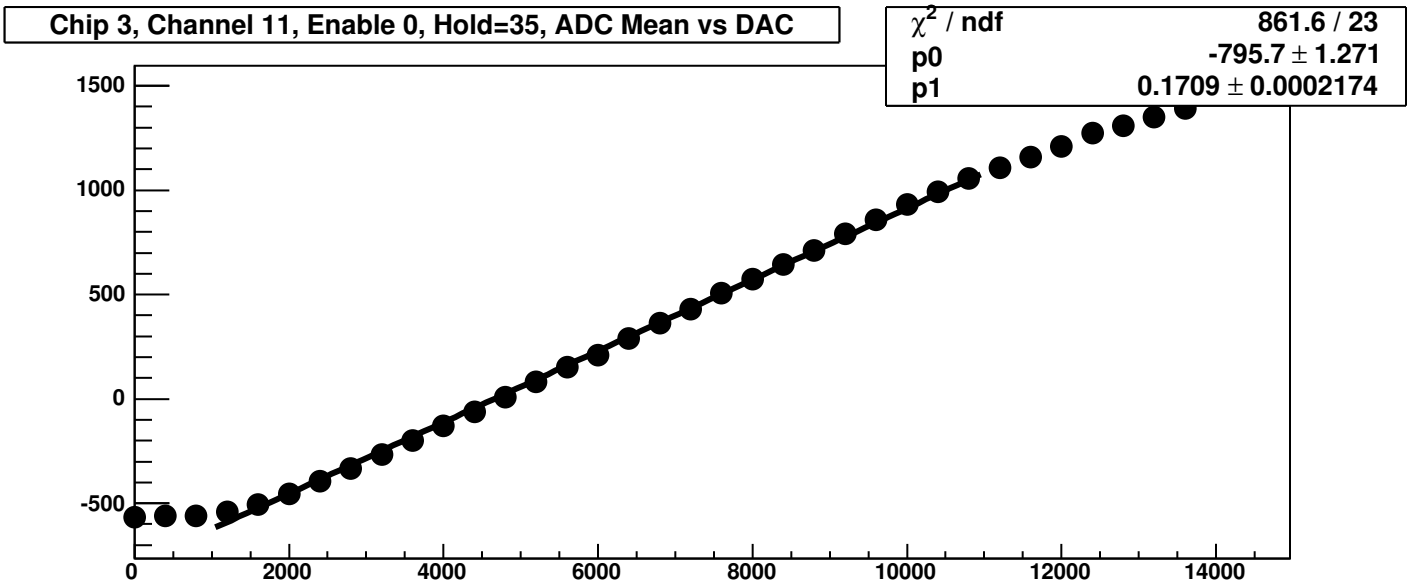


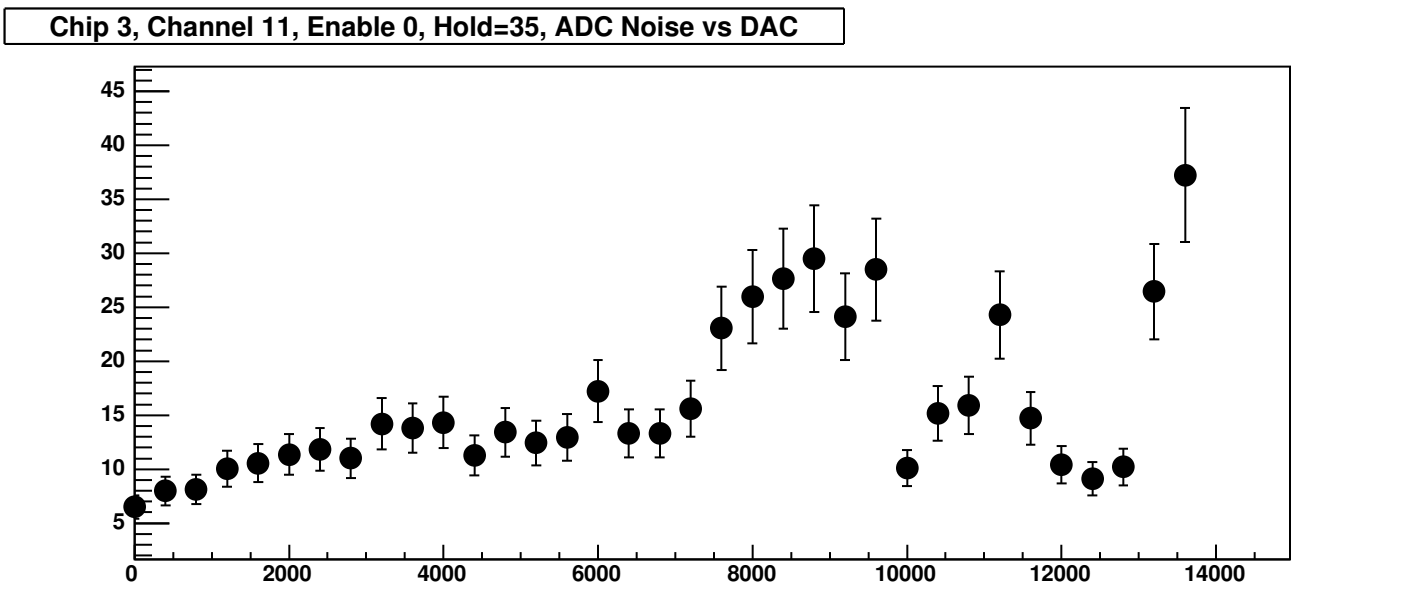
Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

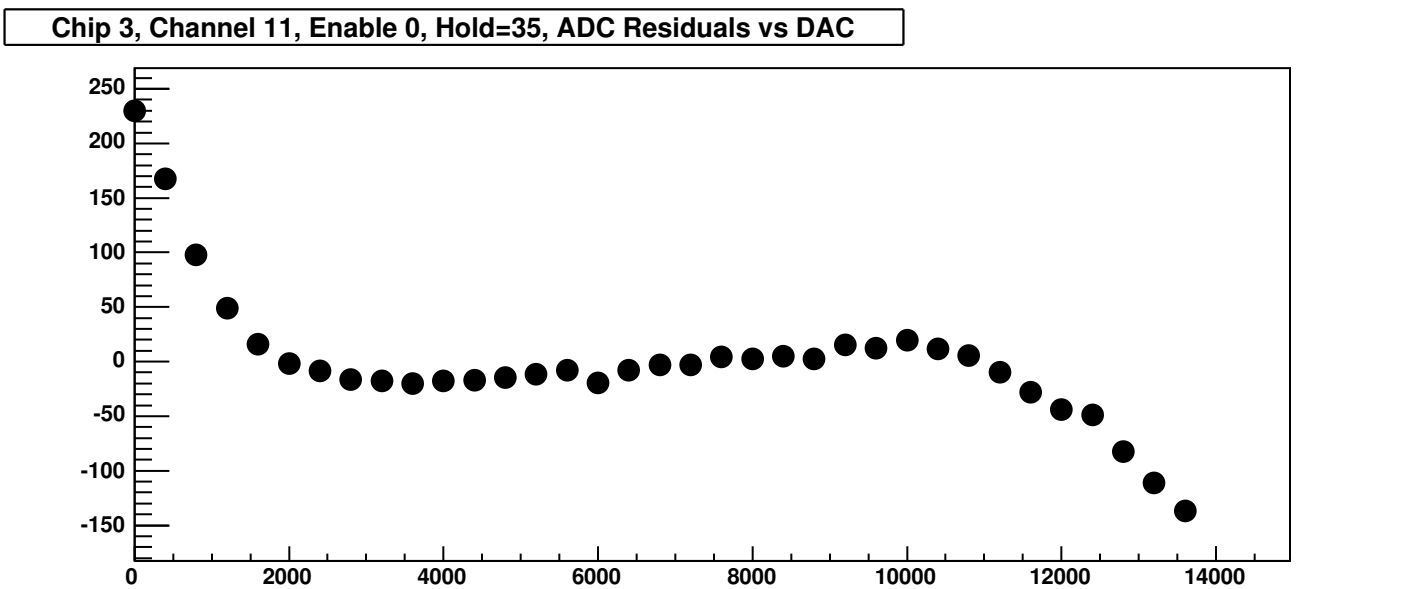


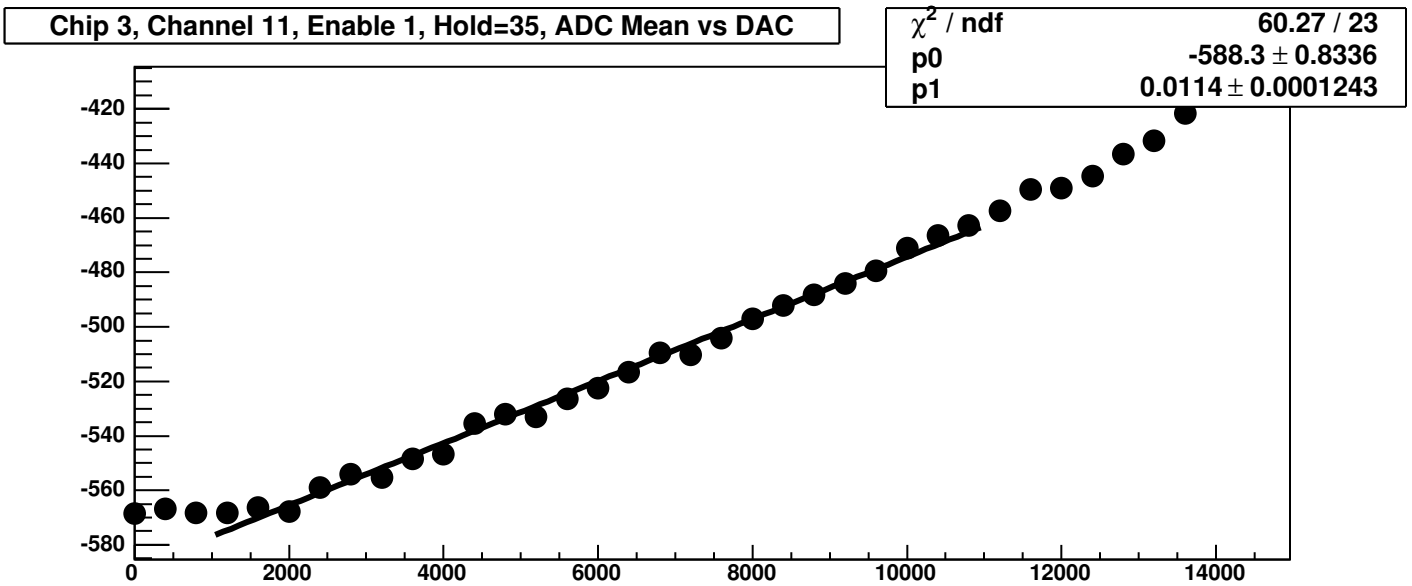
Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

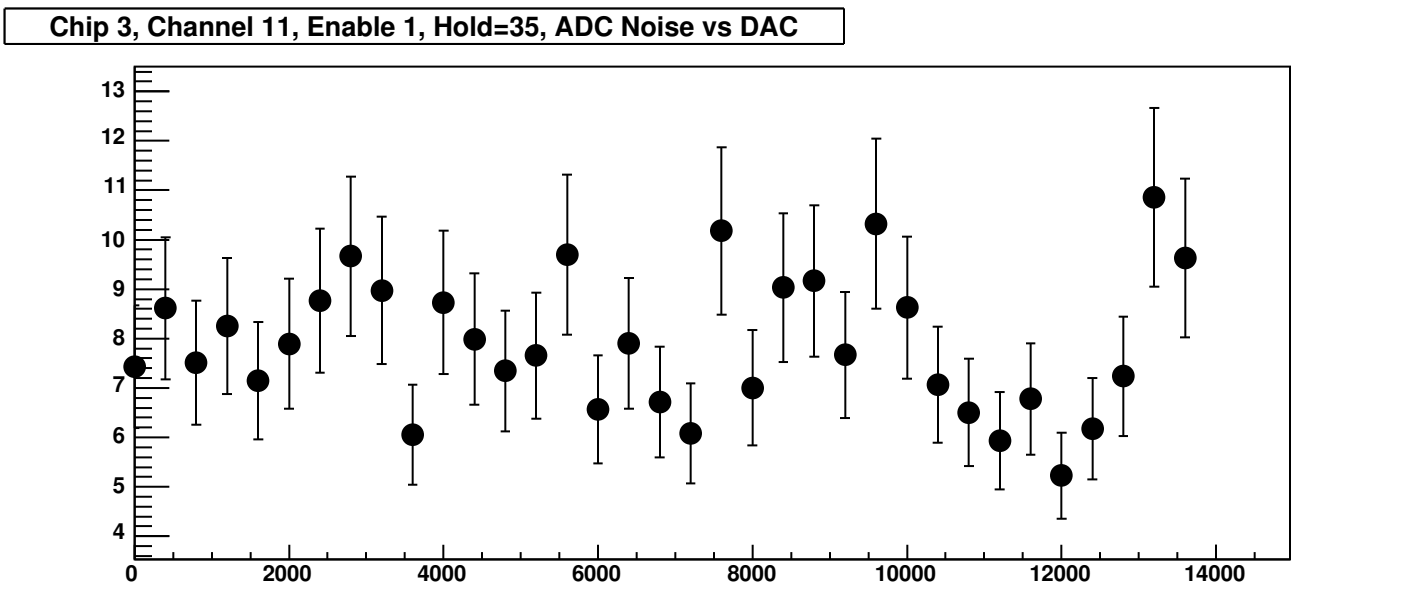


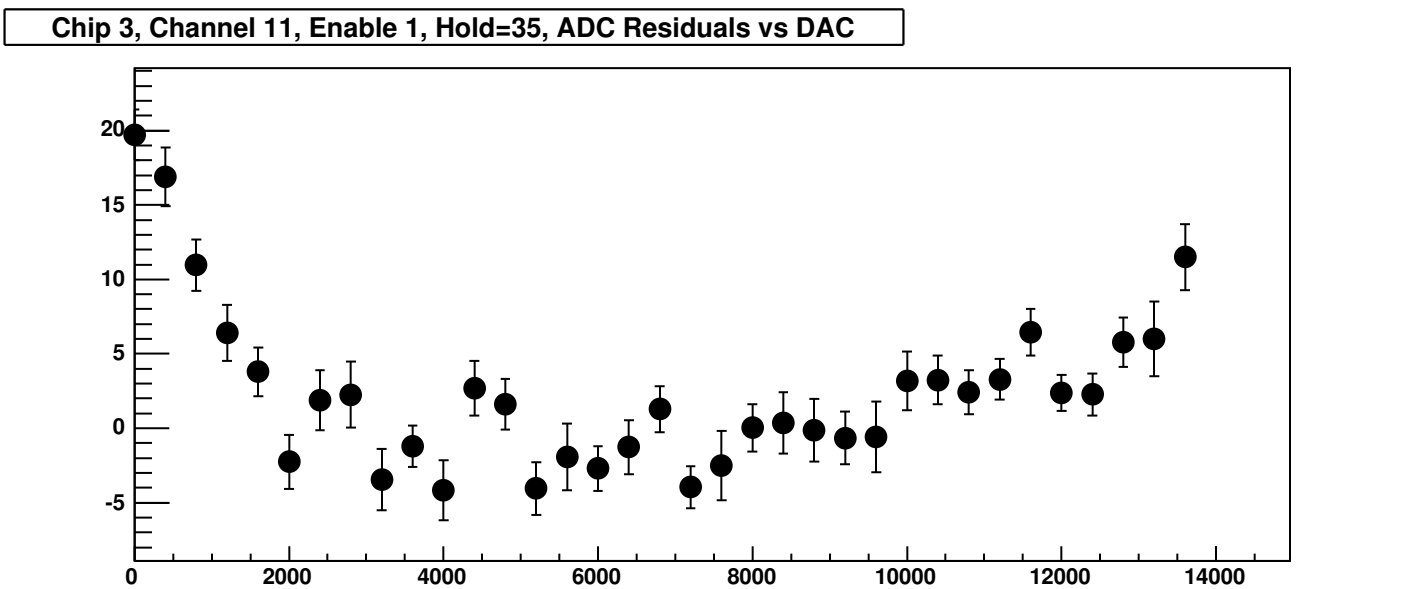

Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 861.6 / 23
p0 -795.7 ± 1.271
p1 0.1709 ± 0.0002174
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

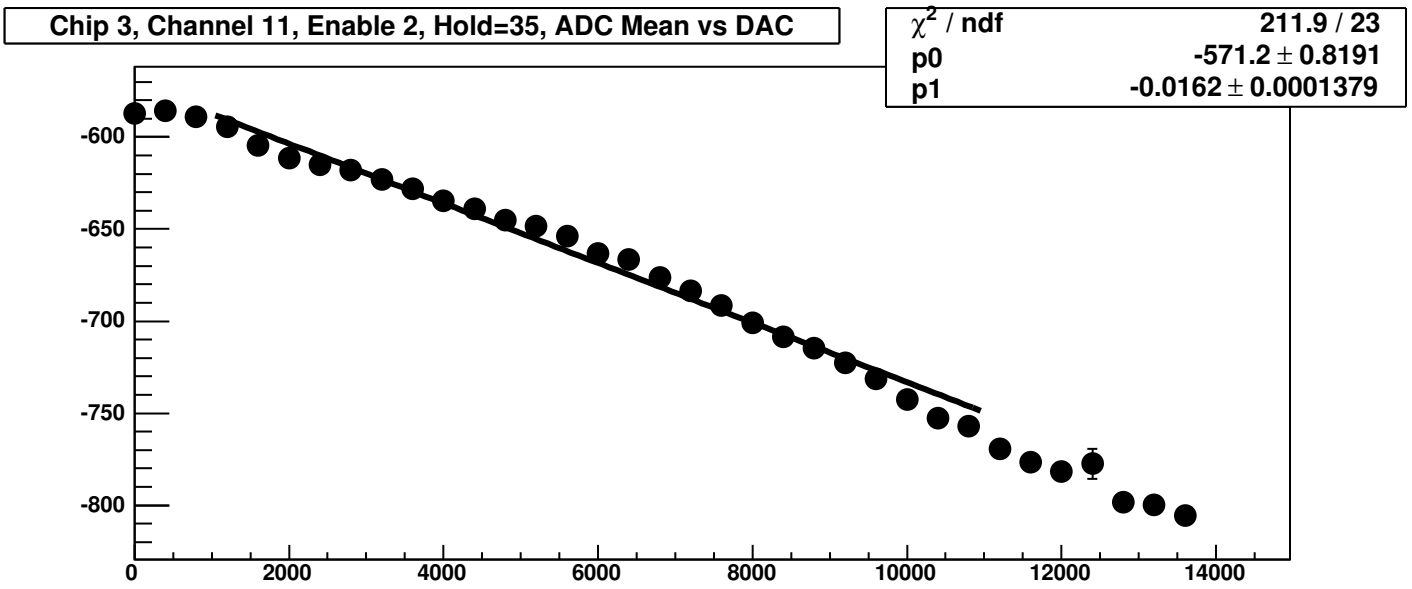

Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

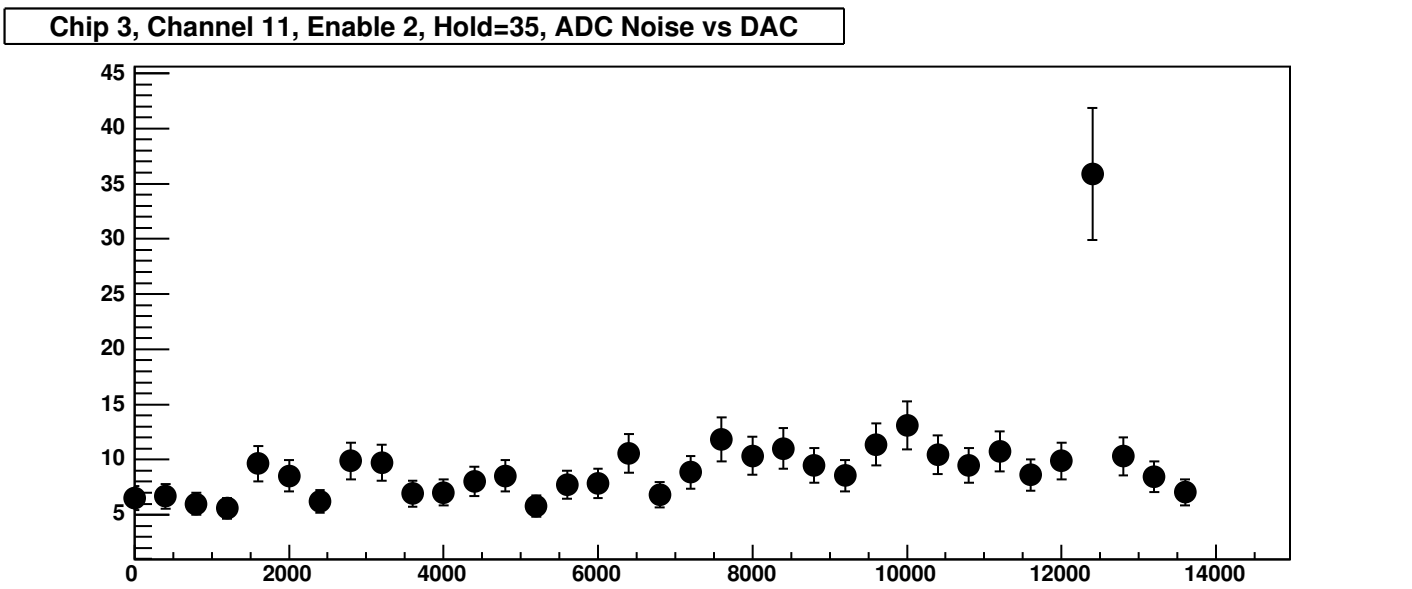

Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

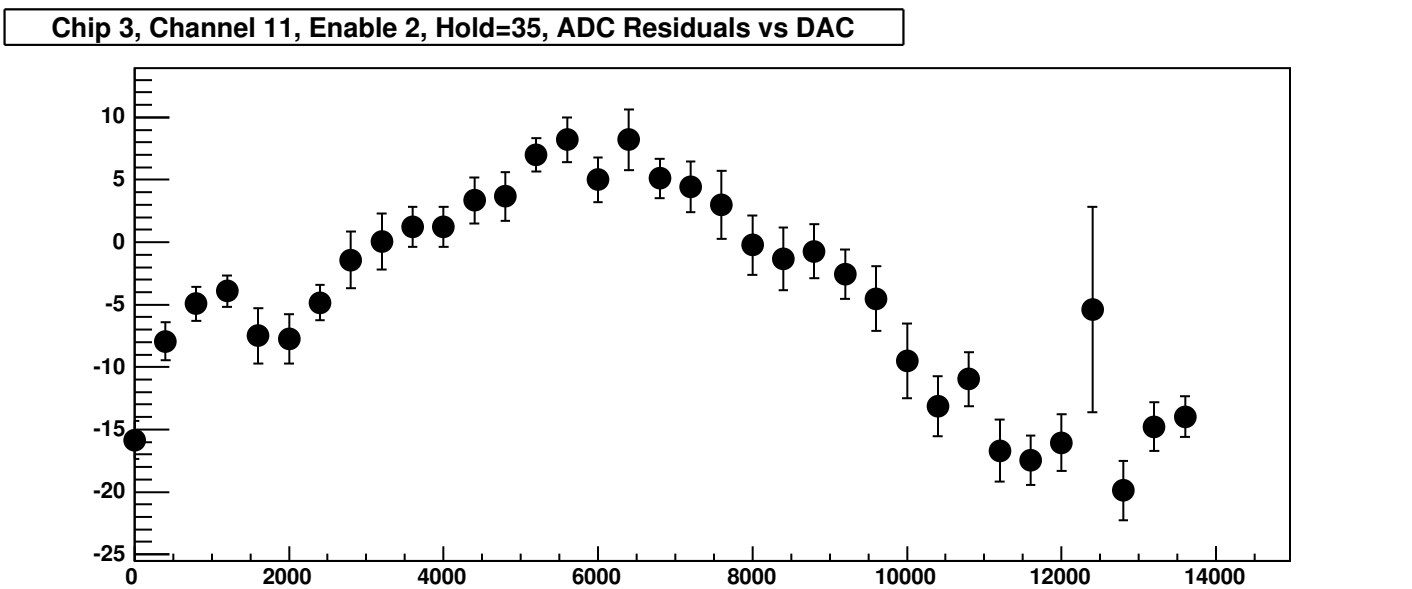

Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.27 / 23
p0 -588.3 ± 0.8336
p1 0.0114 ± 0.0001243
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

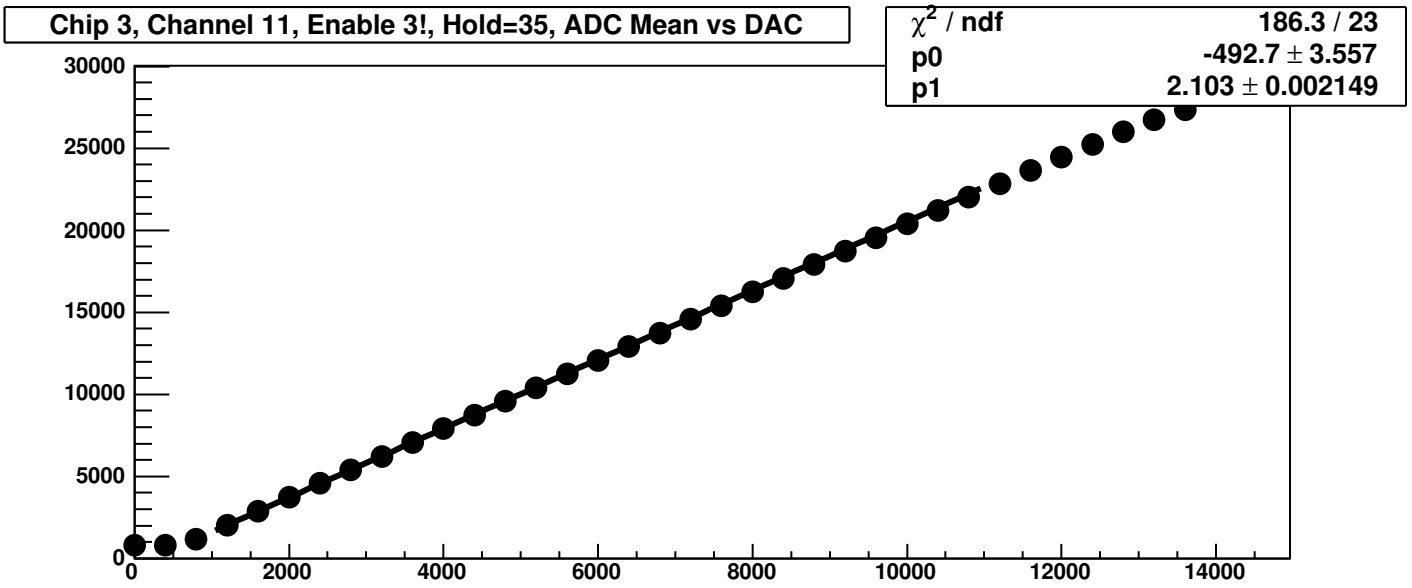

Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

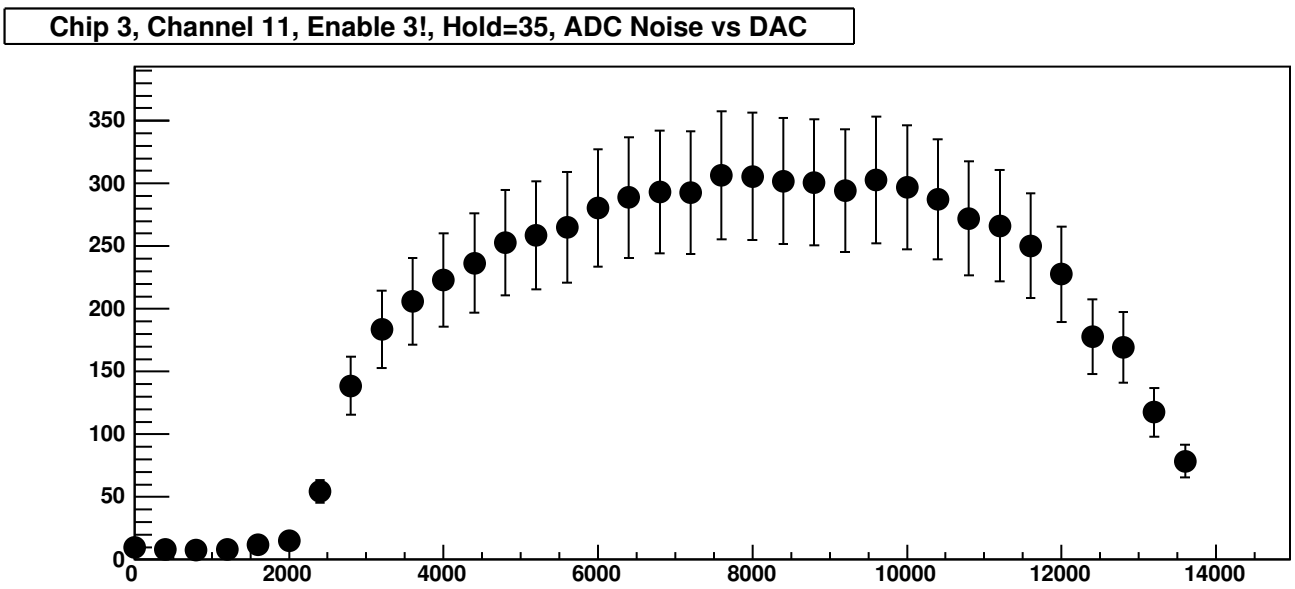

Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

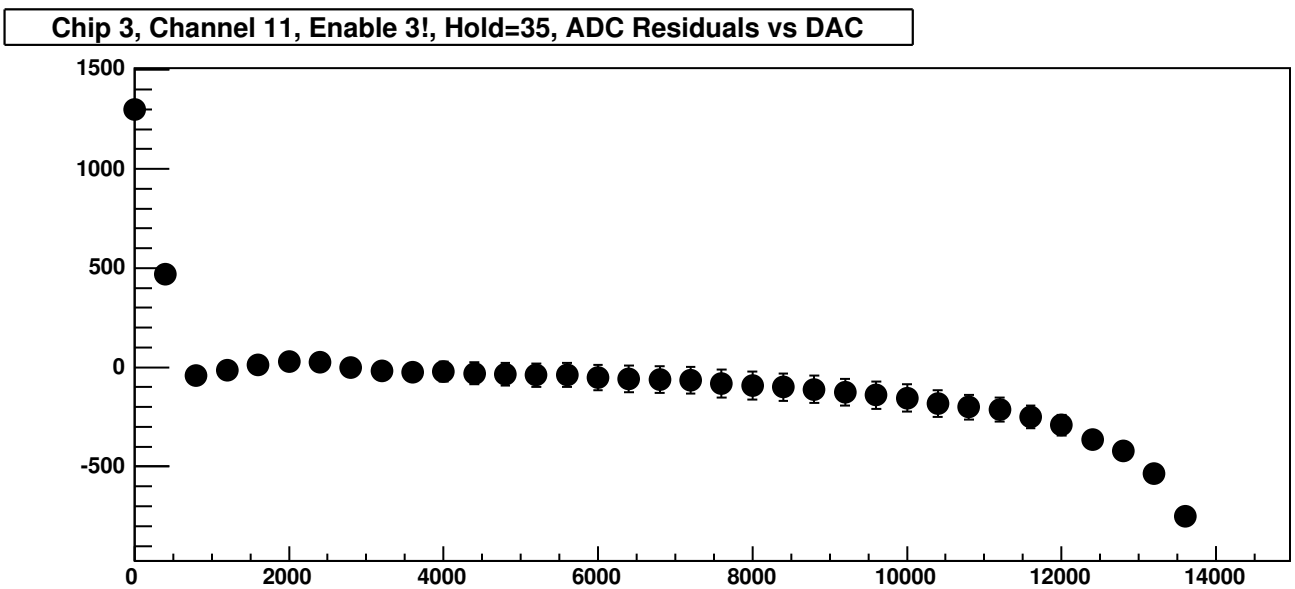

Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 211.9 / 23
p0 -571.2 ± 0.8191
p1 -0.0162 ± 0.0001379
-600
-650
-700
-750
-800
0
2000
4000
6000
8000
10000
12000
14000

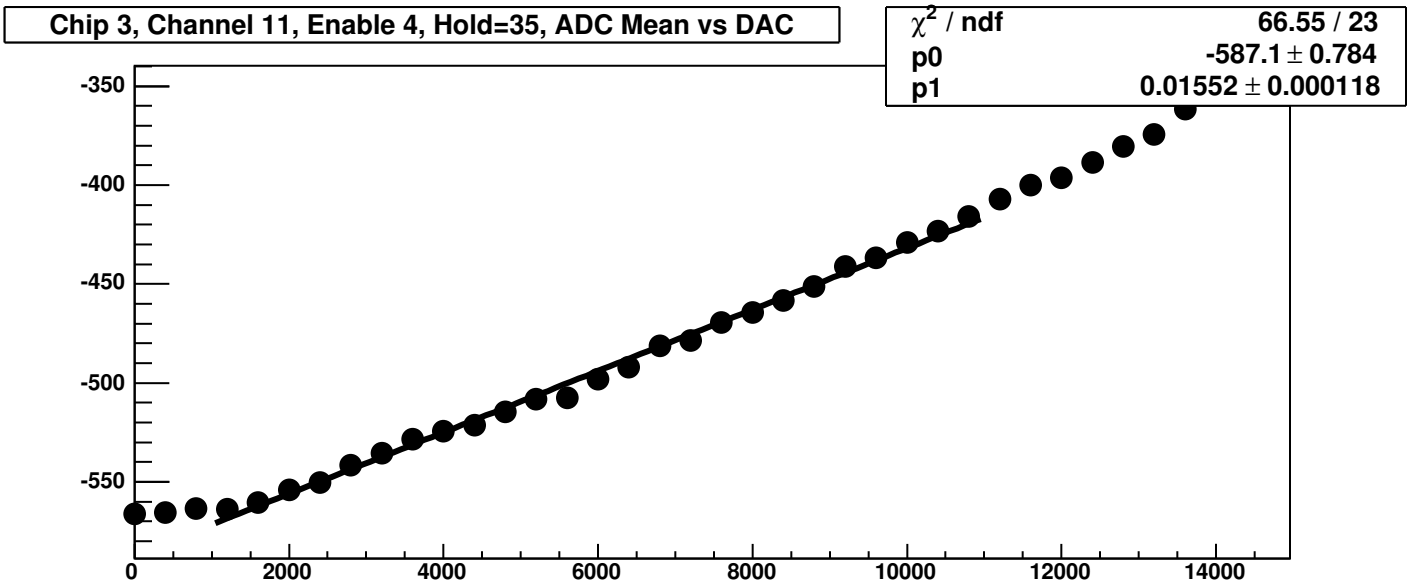

Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

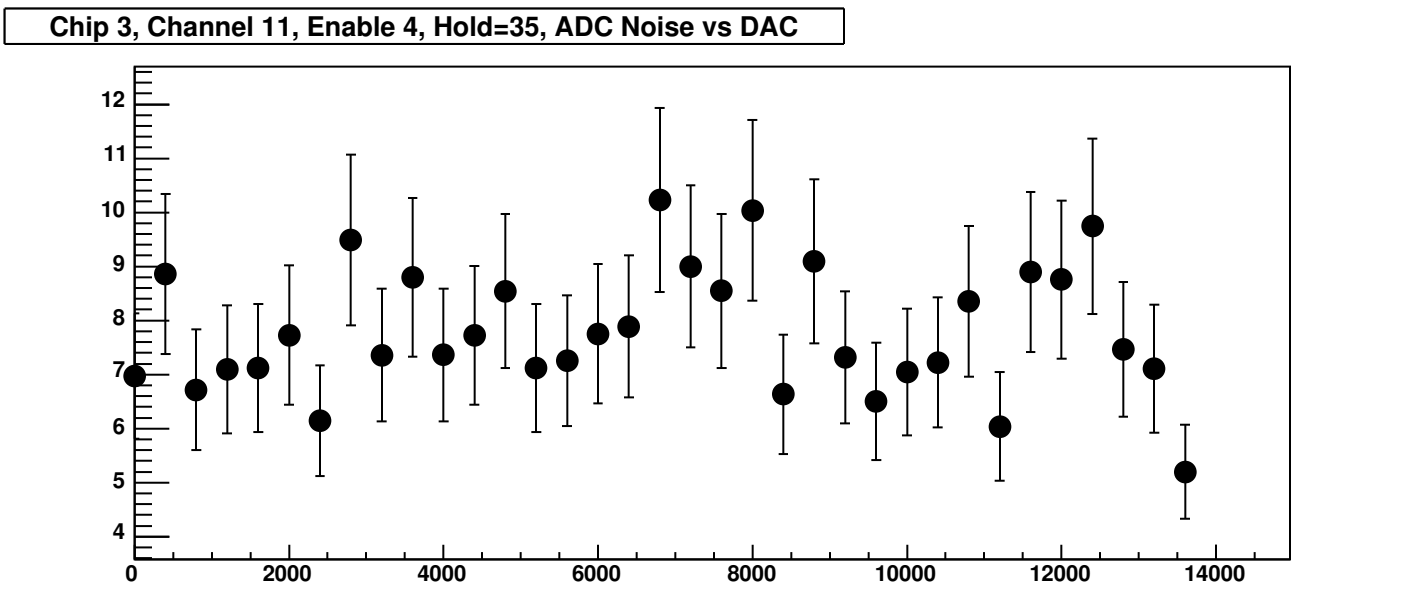

Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

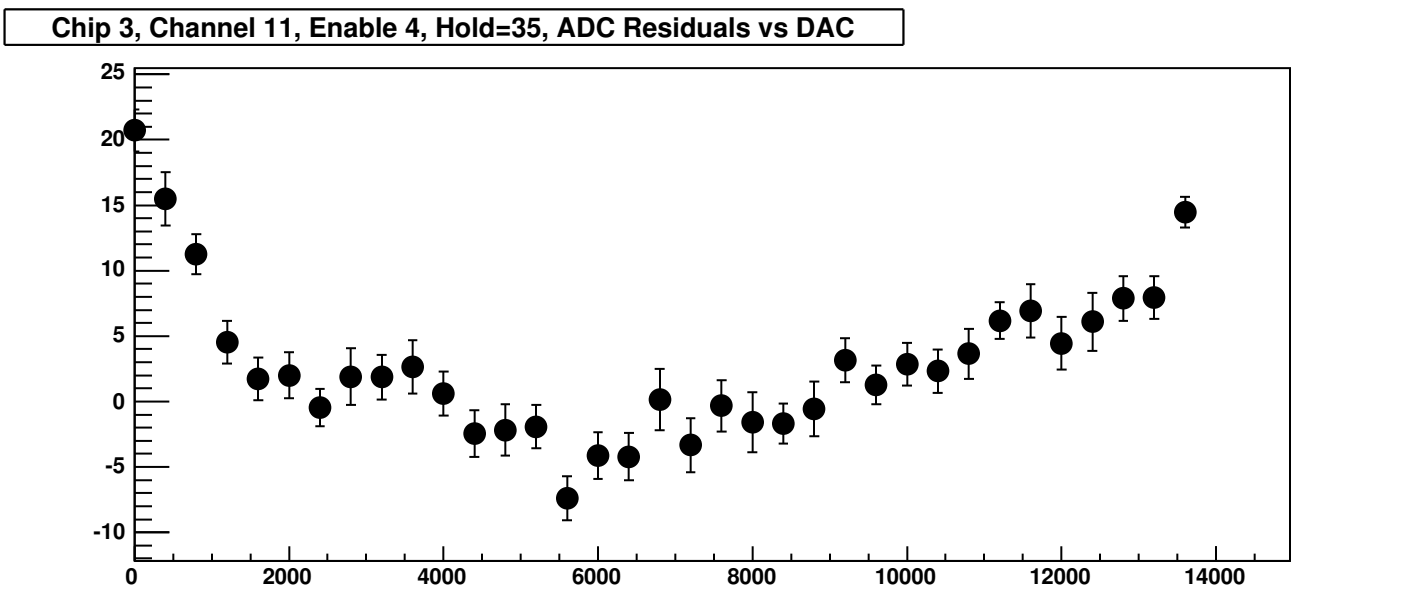

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 186.3 / 23
p0 -492.7 ± 3.557
p1 2.103 ± 0.002149
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

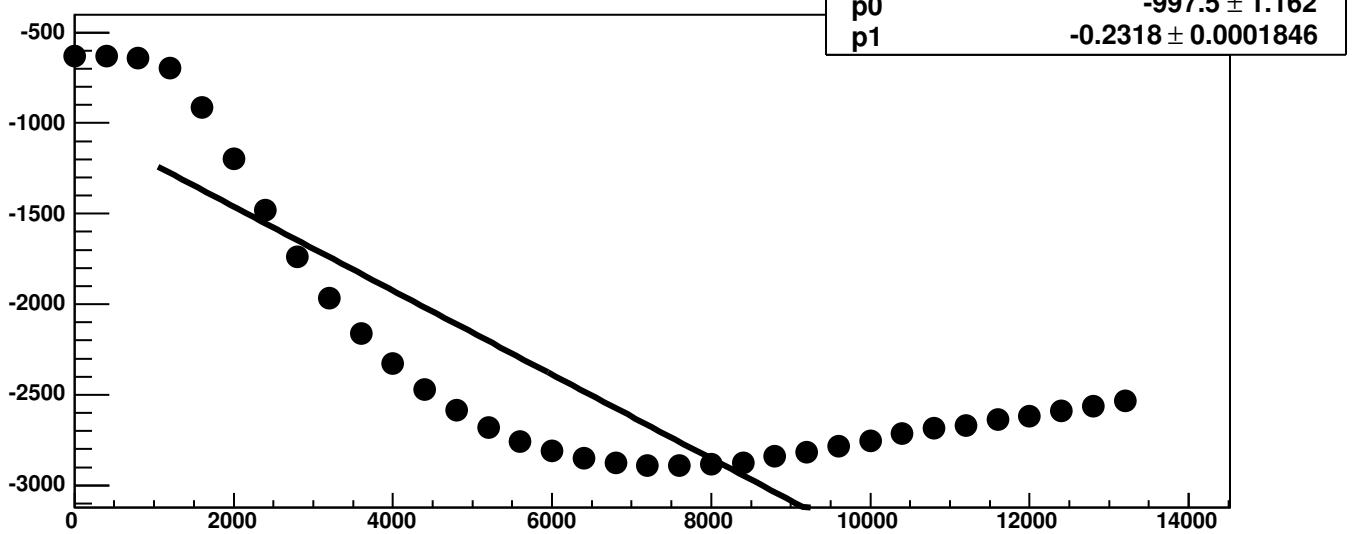

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 66.55 / 23
p0 -587.1 ± 0.784
p1 0.01552 ± 0.000118
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

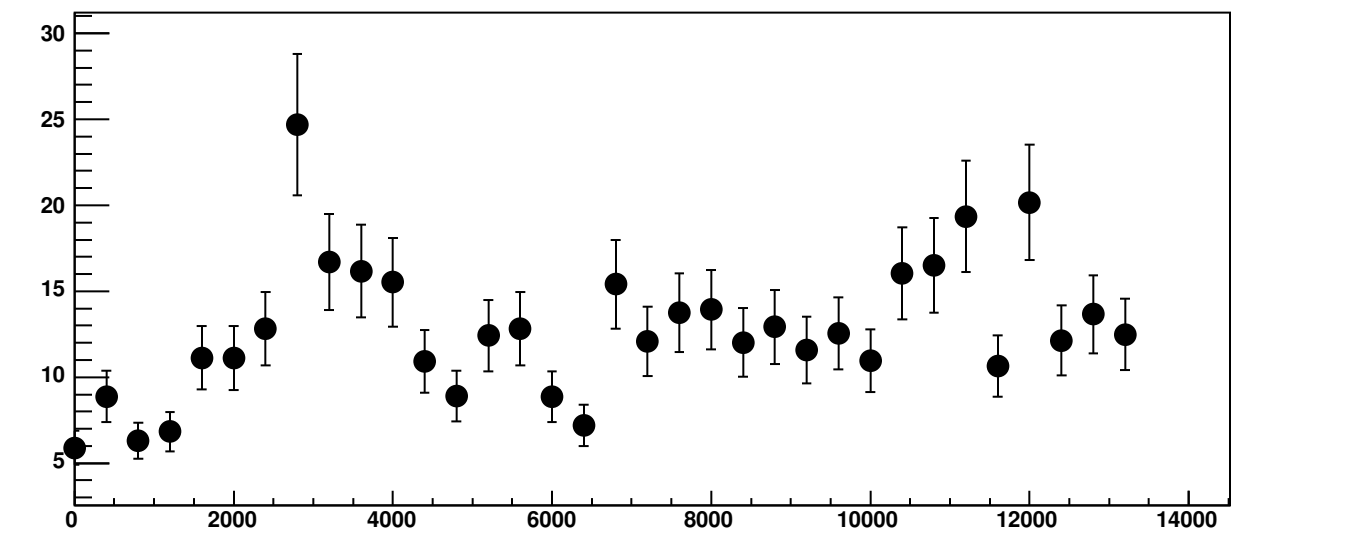

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

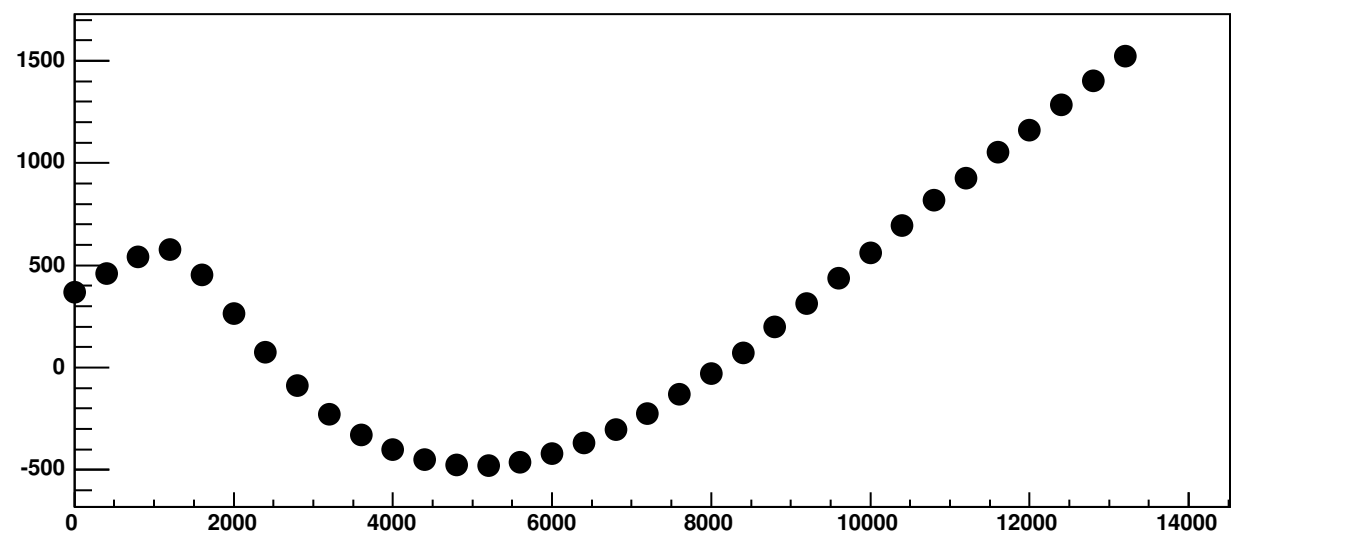
Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

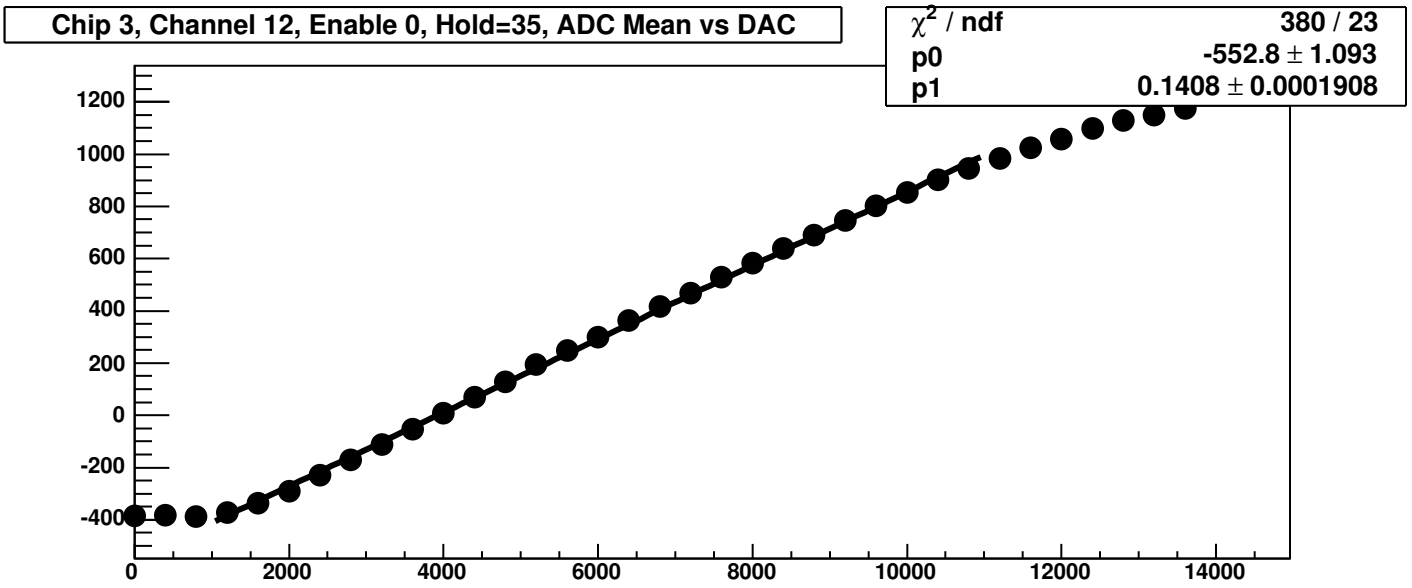


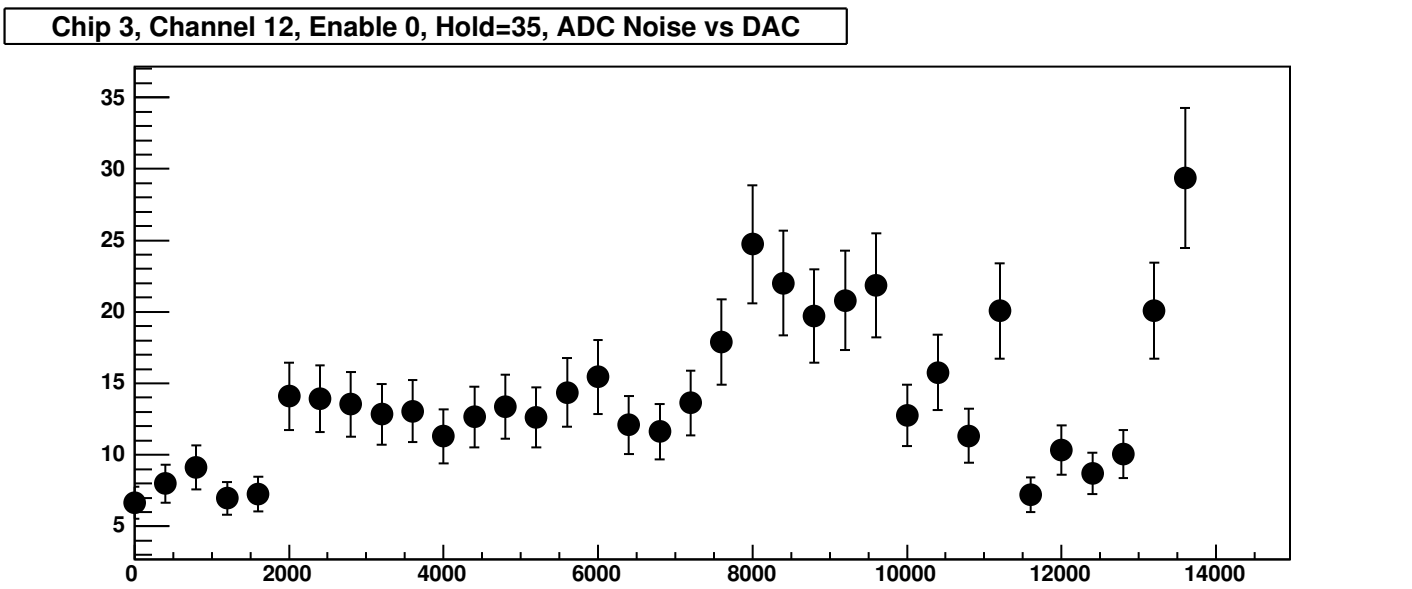
Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

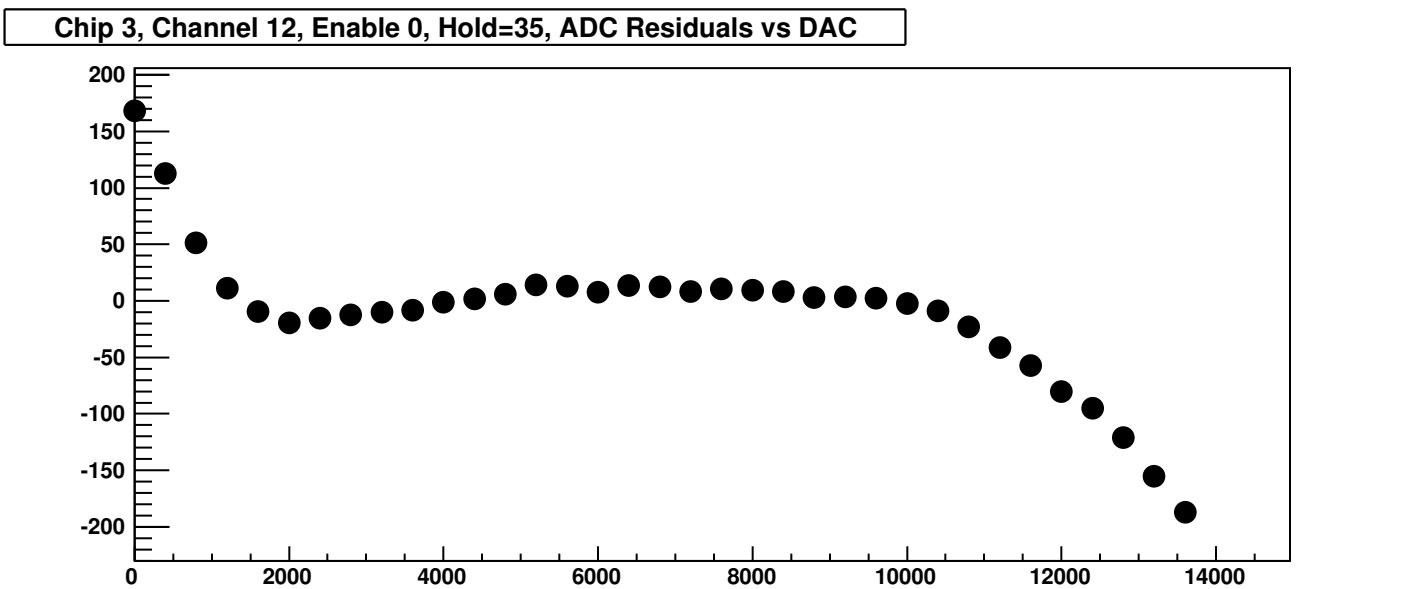


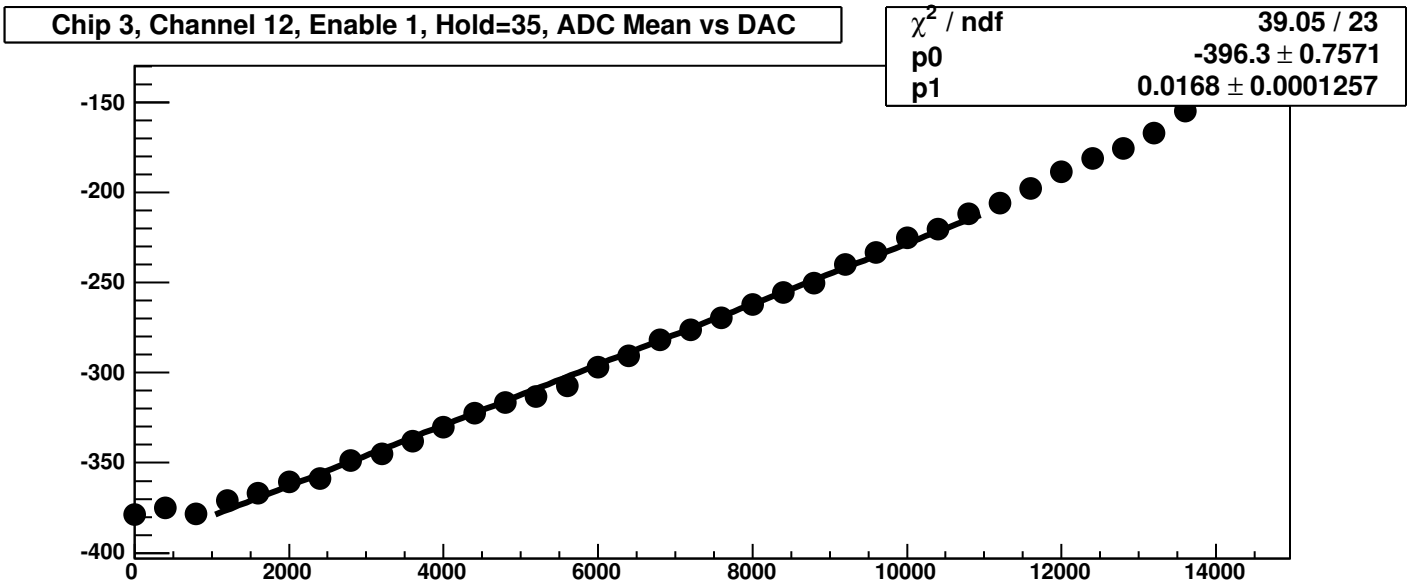
Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

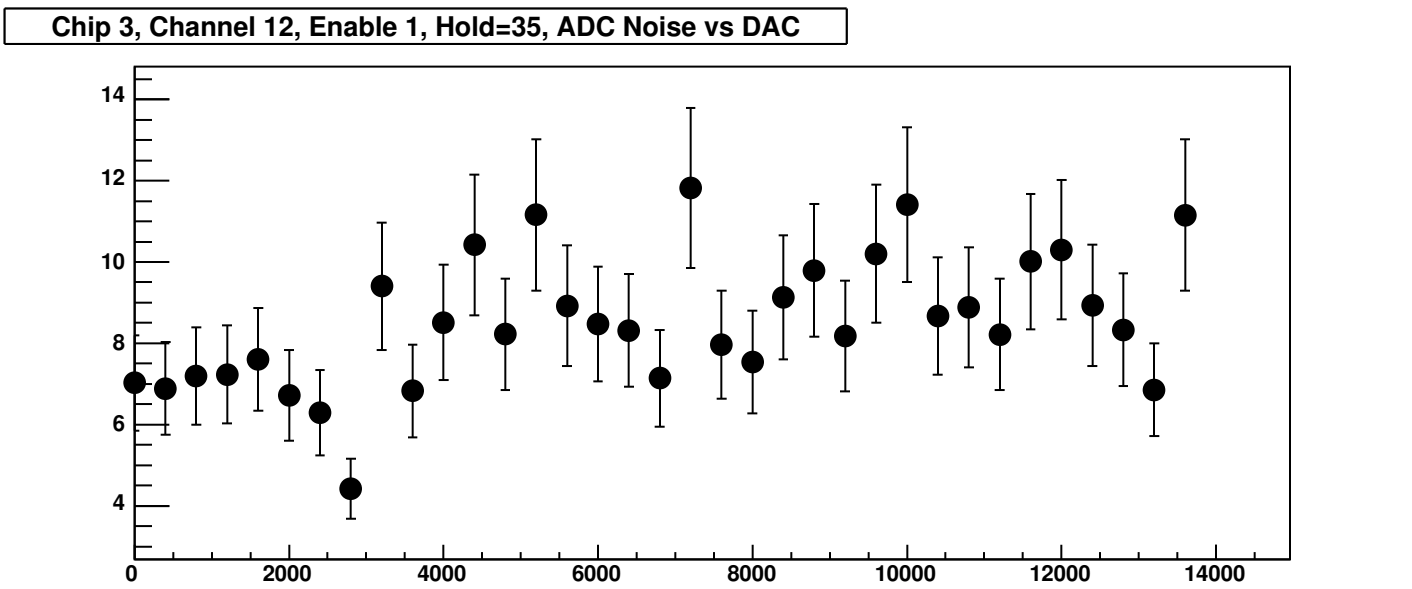


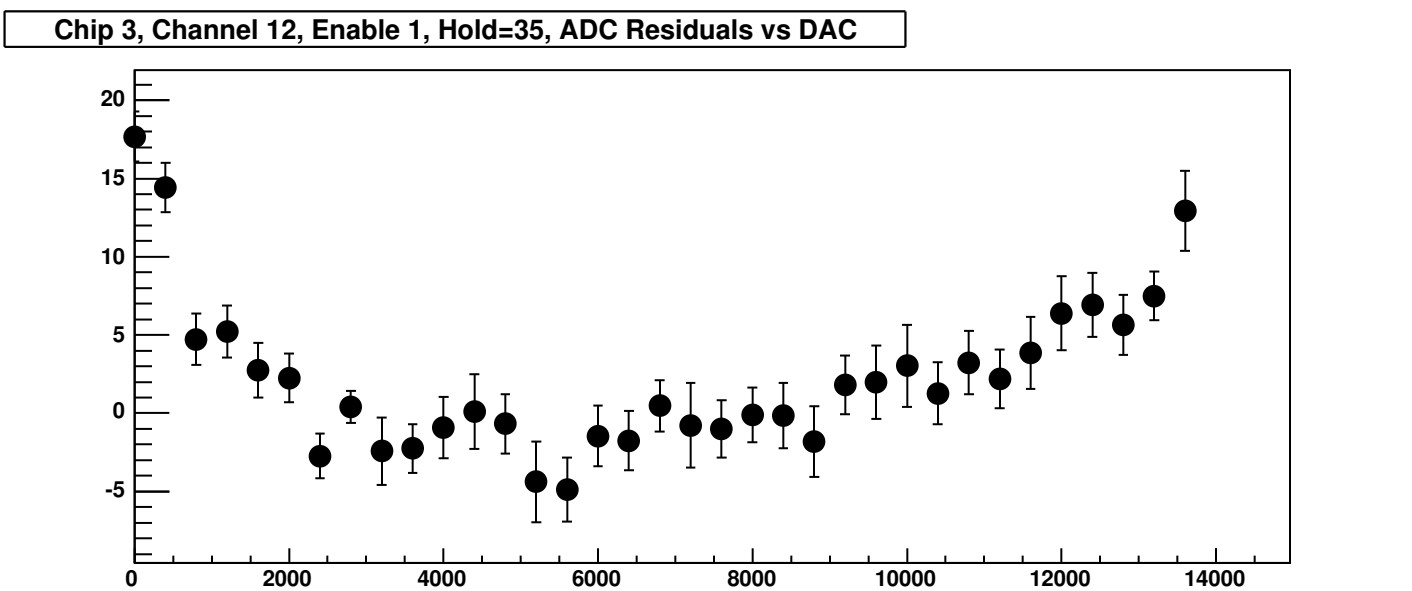

Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 380 / 23
p0 -552.8 ± 1.093
p1 0.1408 ± 0.0001908
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

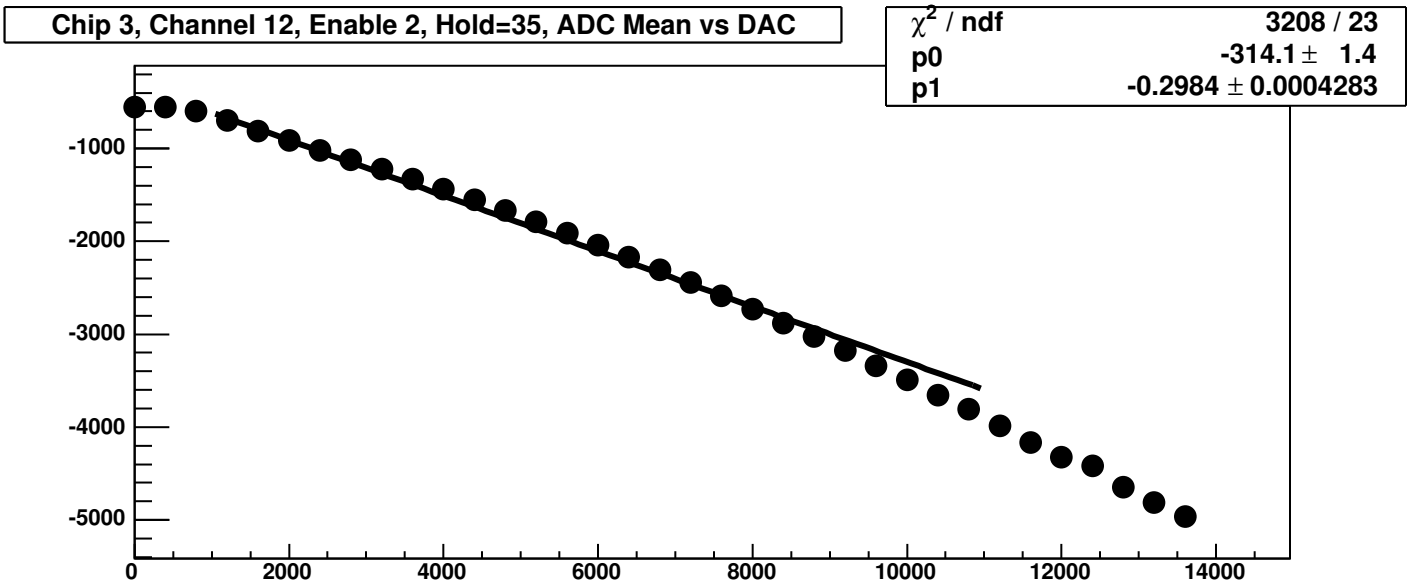

Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

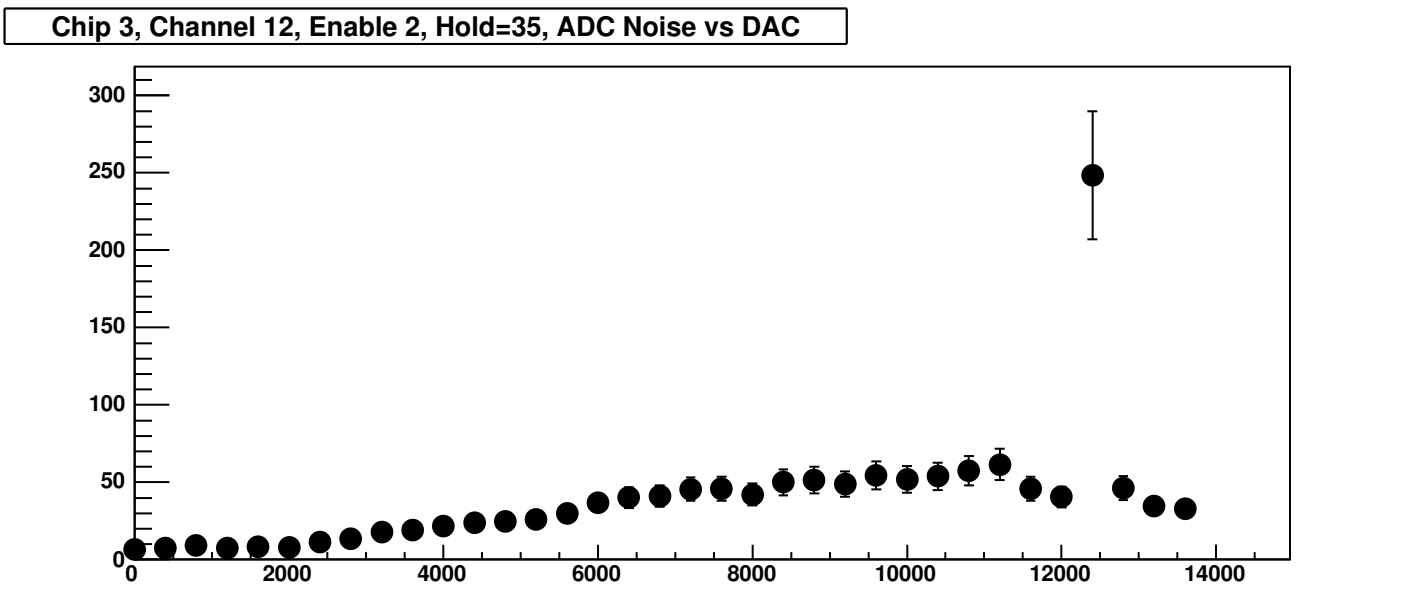

Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

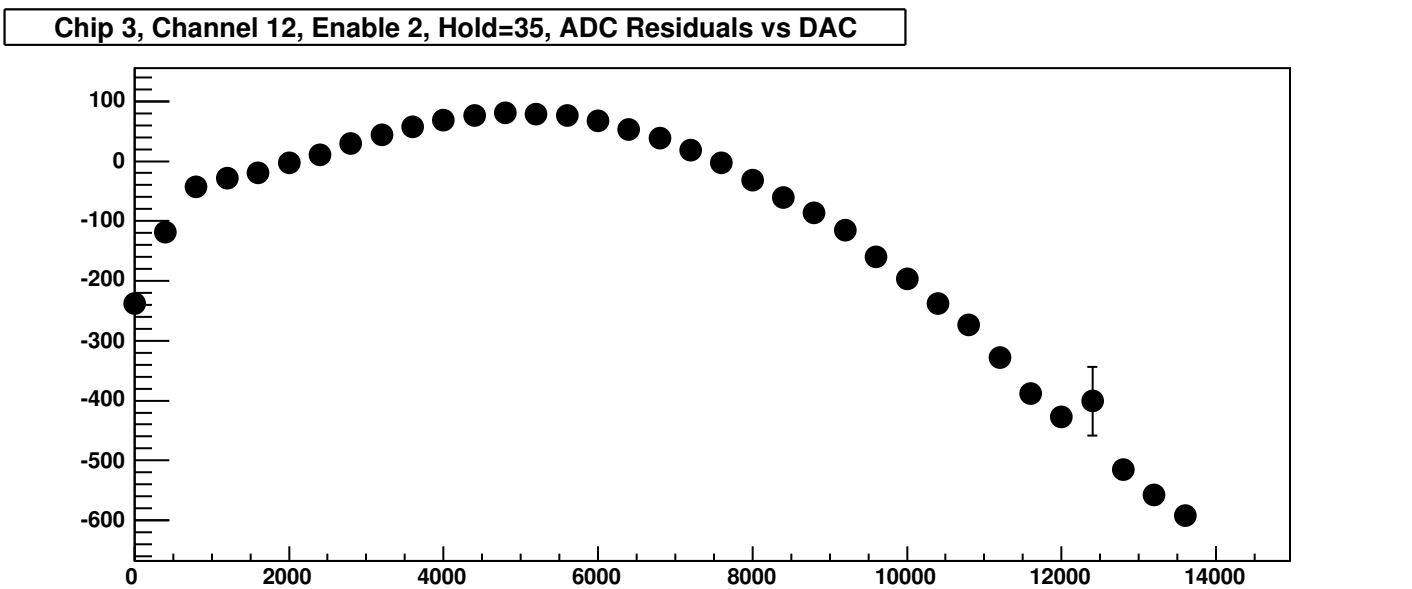

Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.05 / 23
p0 -396.3 ± 0.7571
p1 0.0168 ± 0.0001257
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

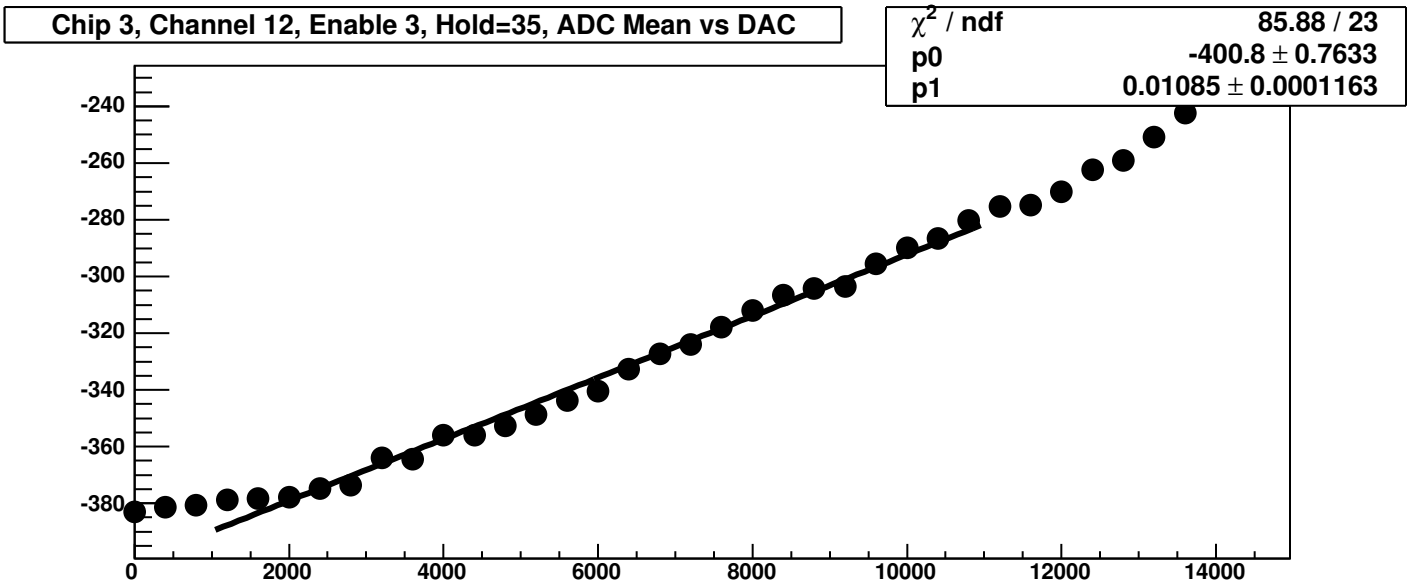

Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

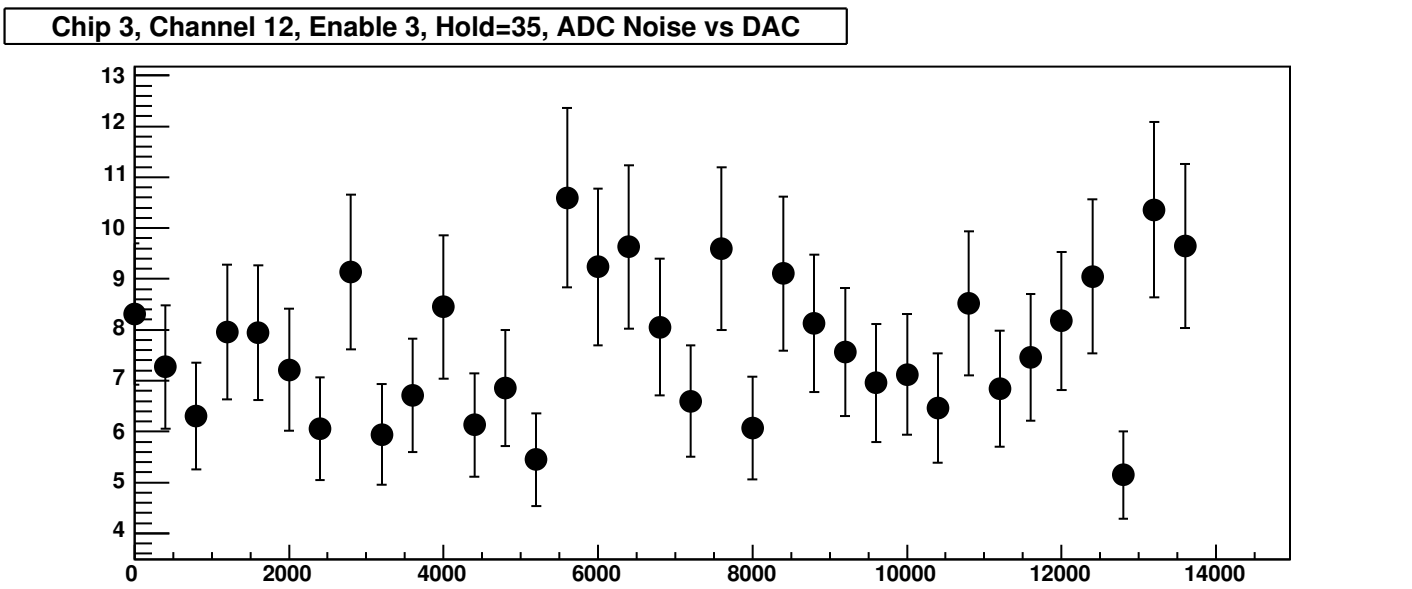

Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

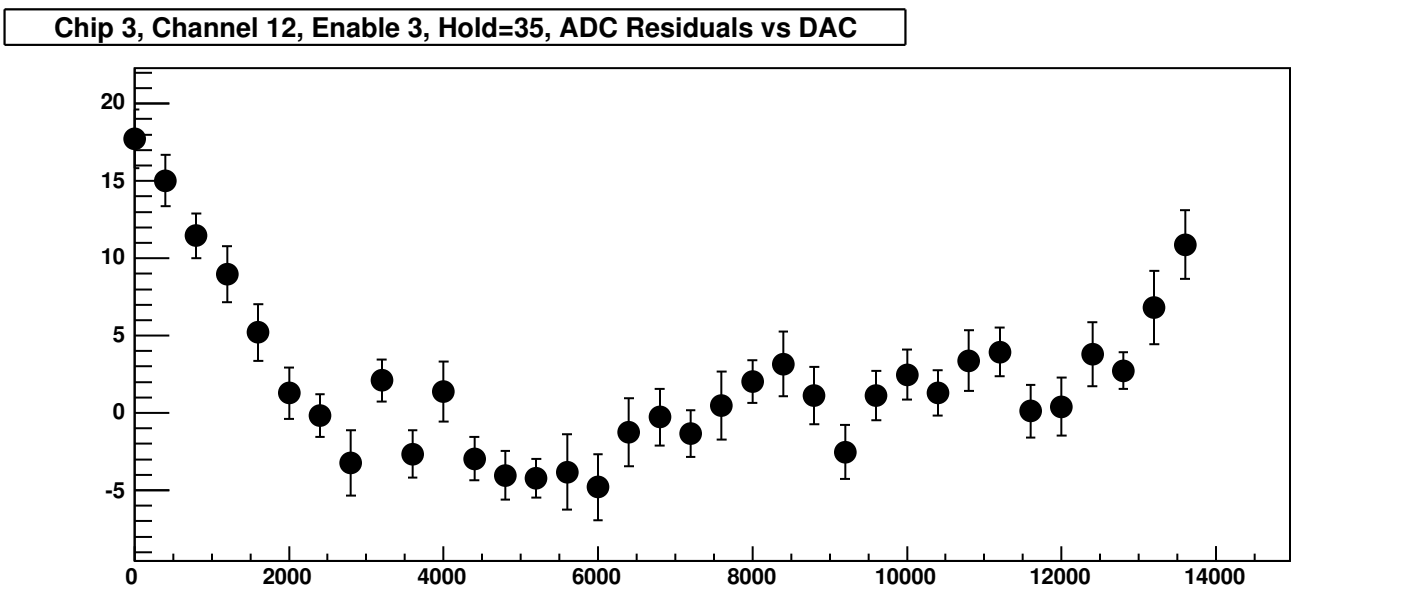

Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3208 / 23
p0 -314.1 ± 1.4
p1 -0.2984 ± 0.0004283
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

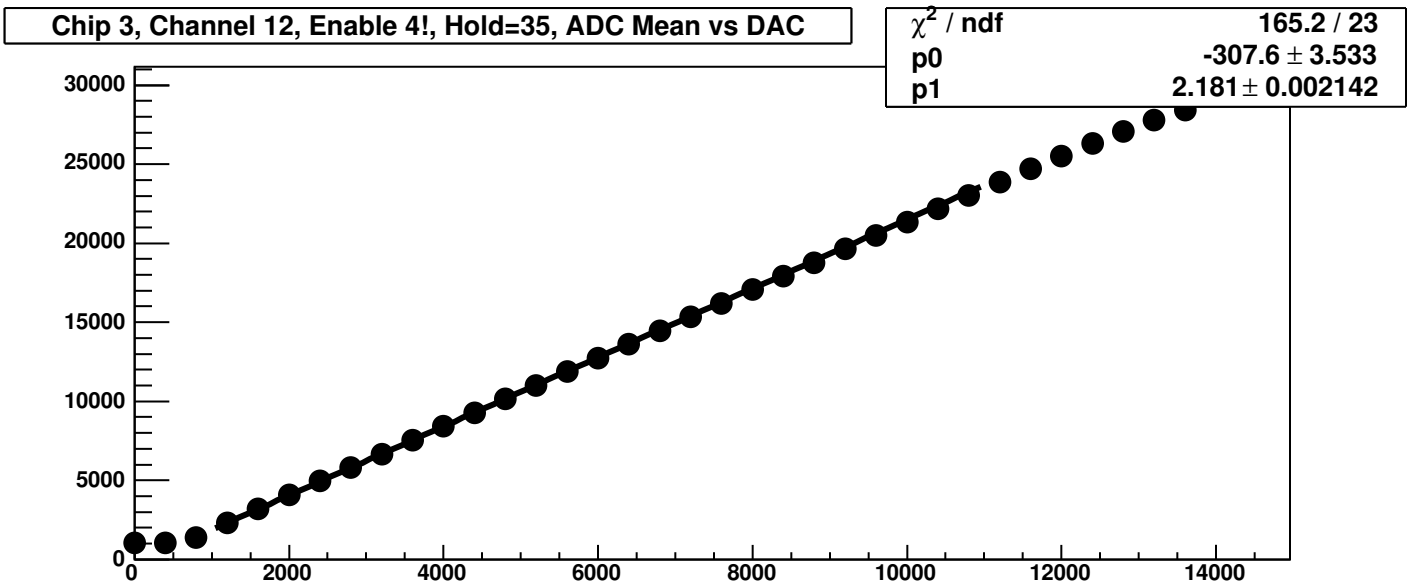

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

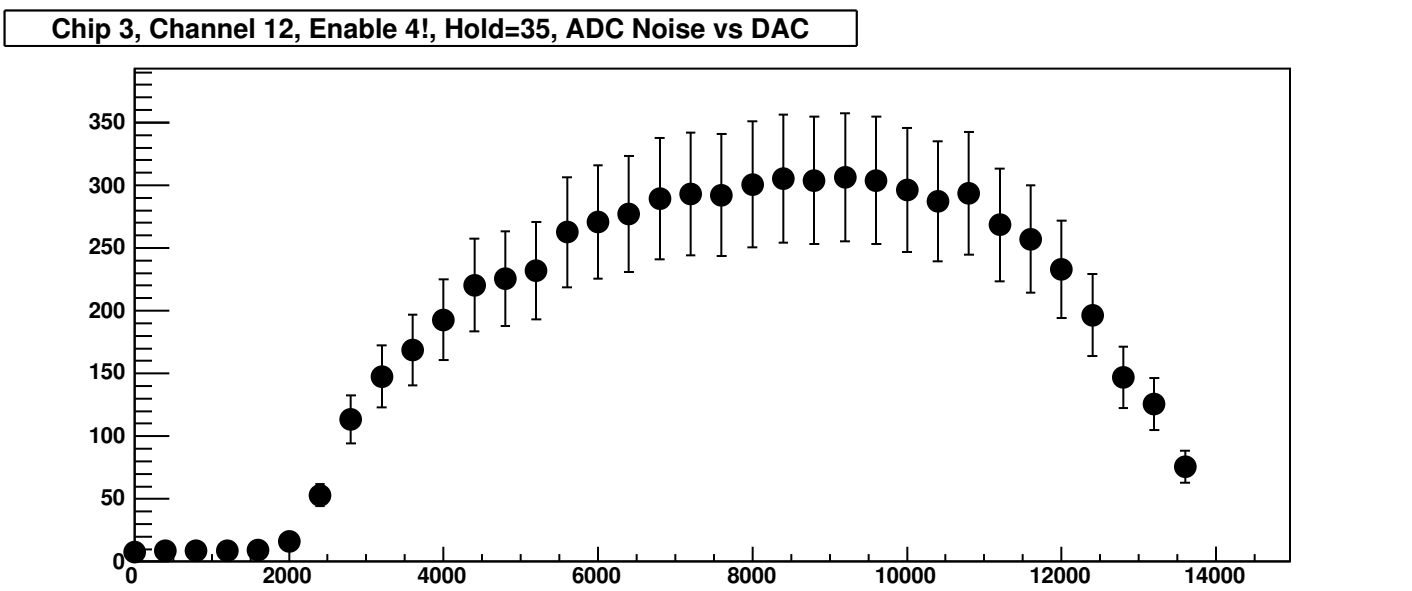

Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

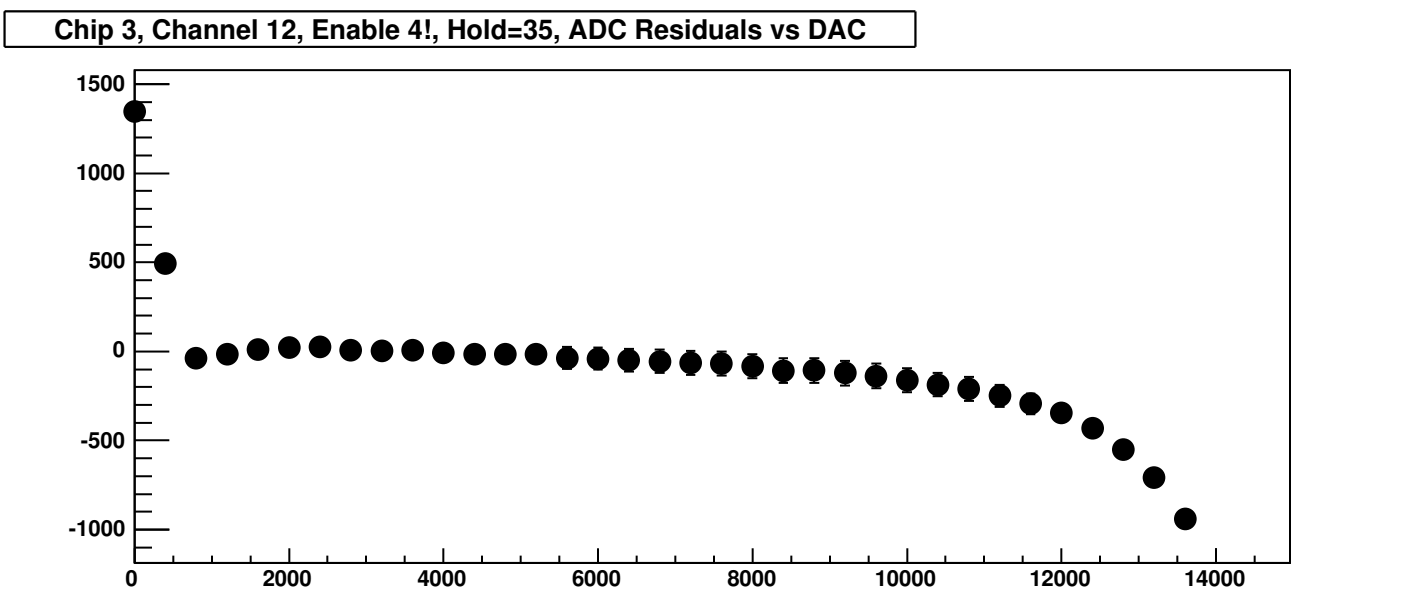

Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 85.88 / 23
p0 -400.8 ± 0.7633
p1 0.01085 ± 0.0001163
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

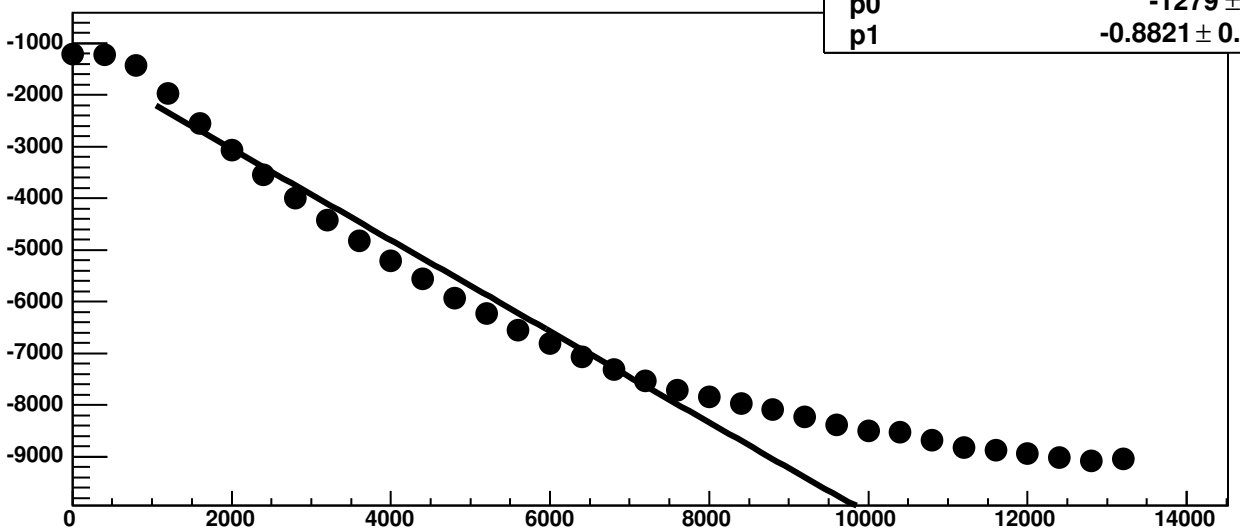

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 165.2 / 23
p0 -307.6 ± 3.533
p1 2.181 ± 0.002142
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

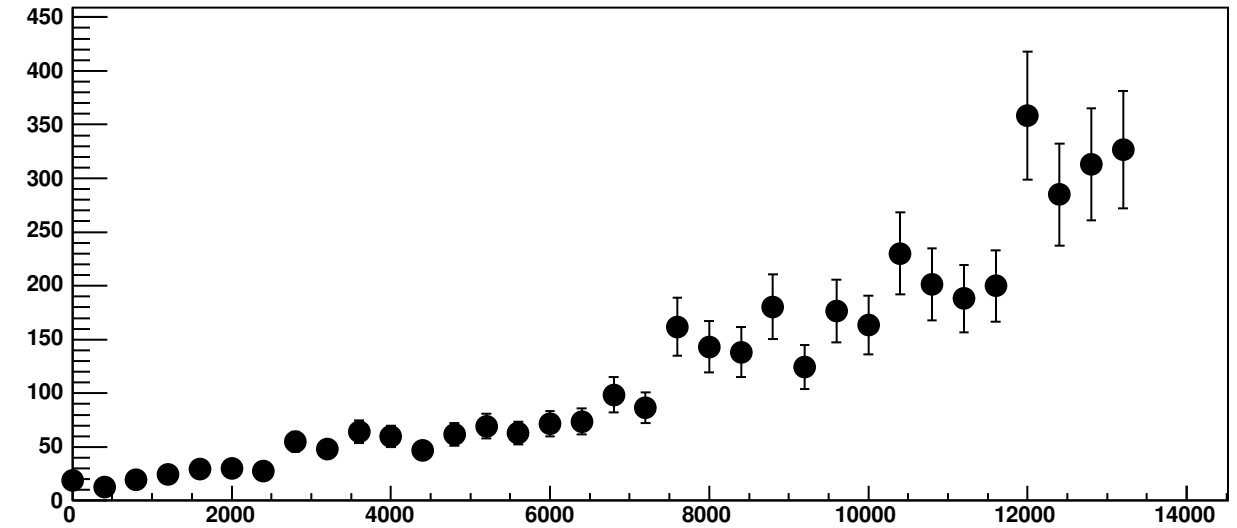

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

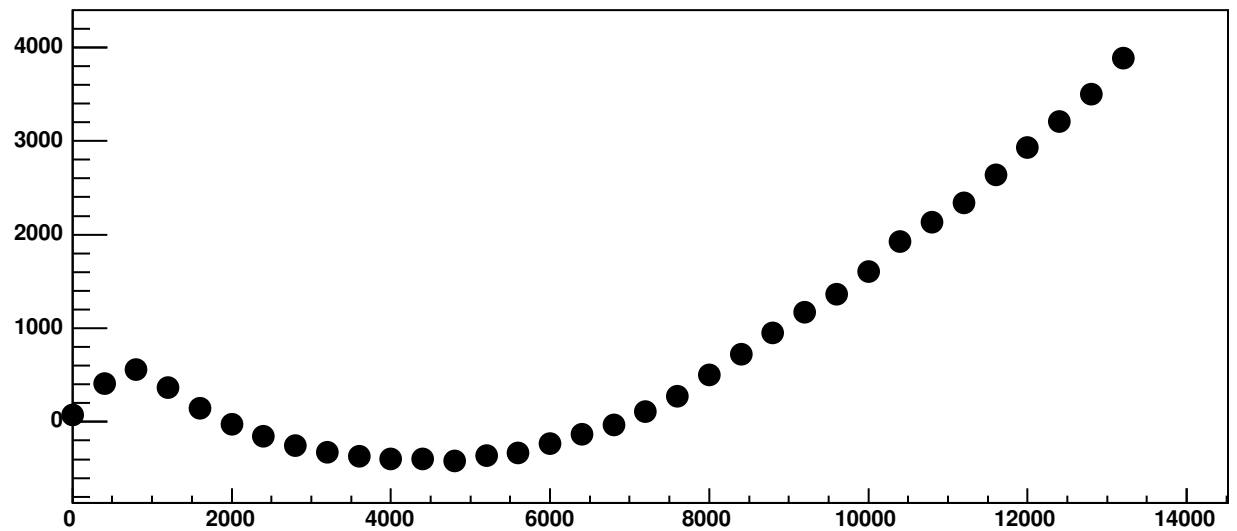
χ^2 / ndf	2.115e+04 / 23
p0	-1279 ± 4.468
p1	-0.8821 ± 0.00123

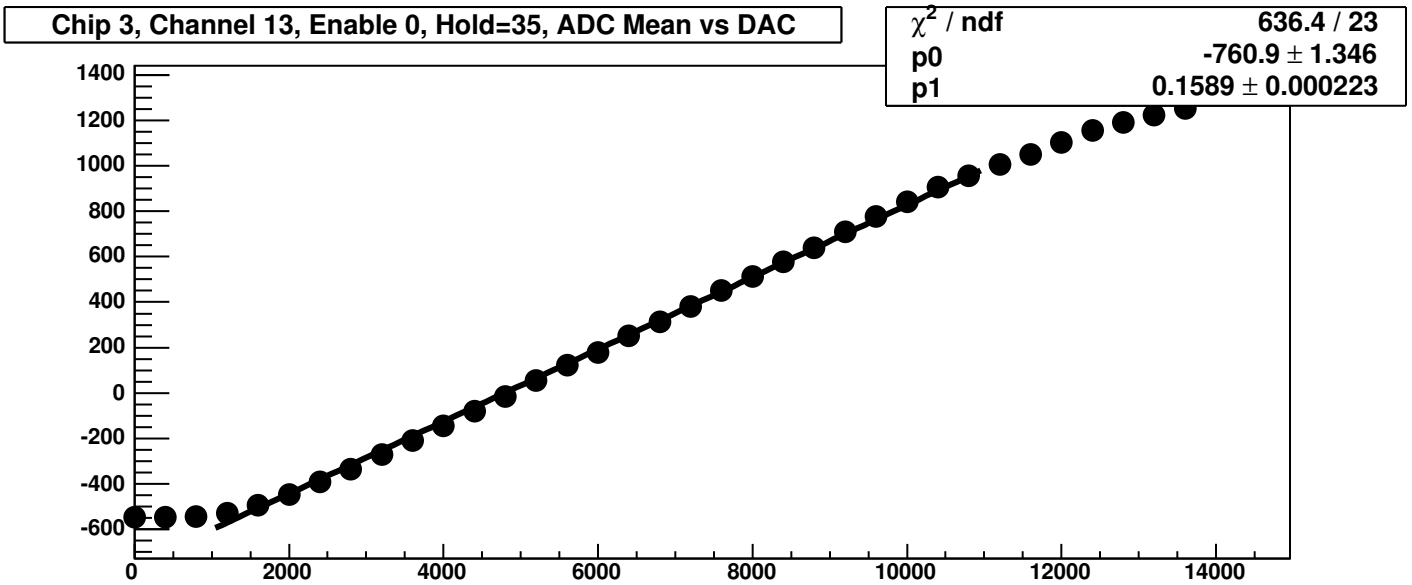


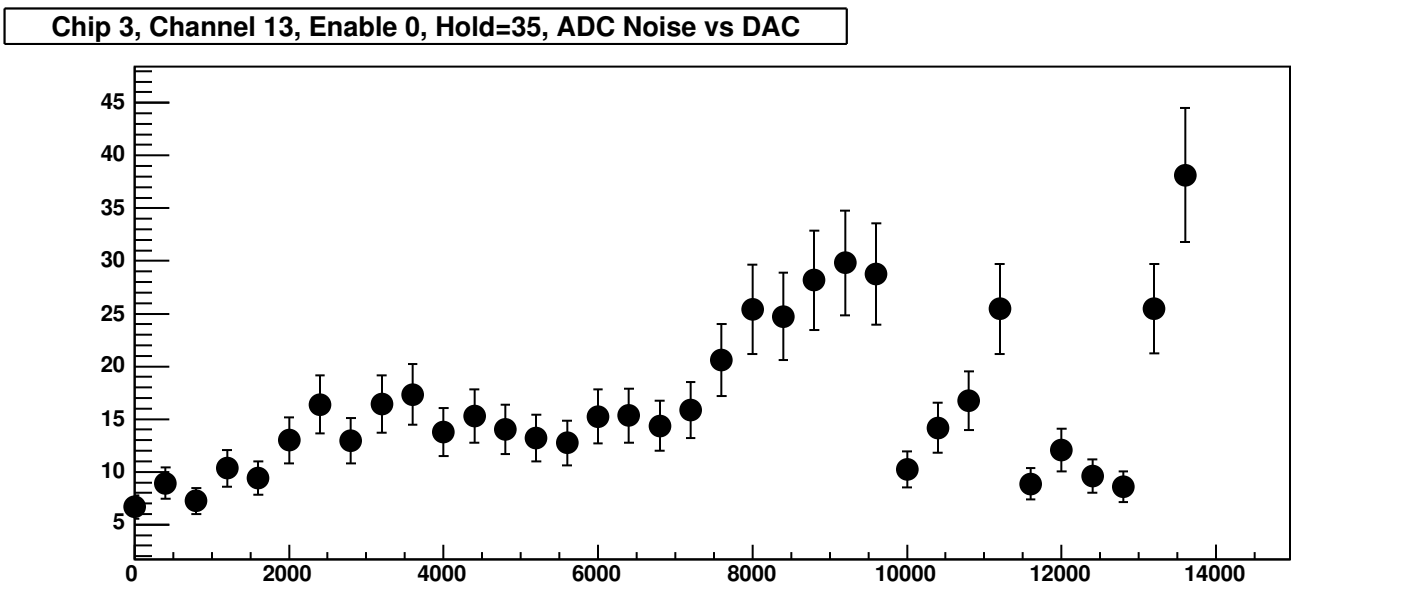
Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

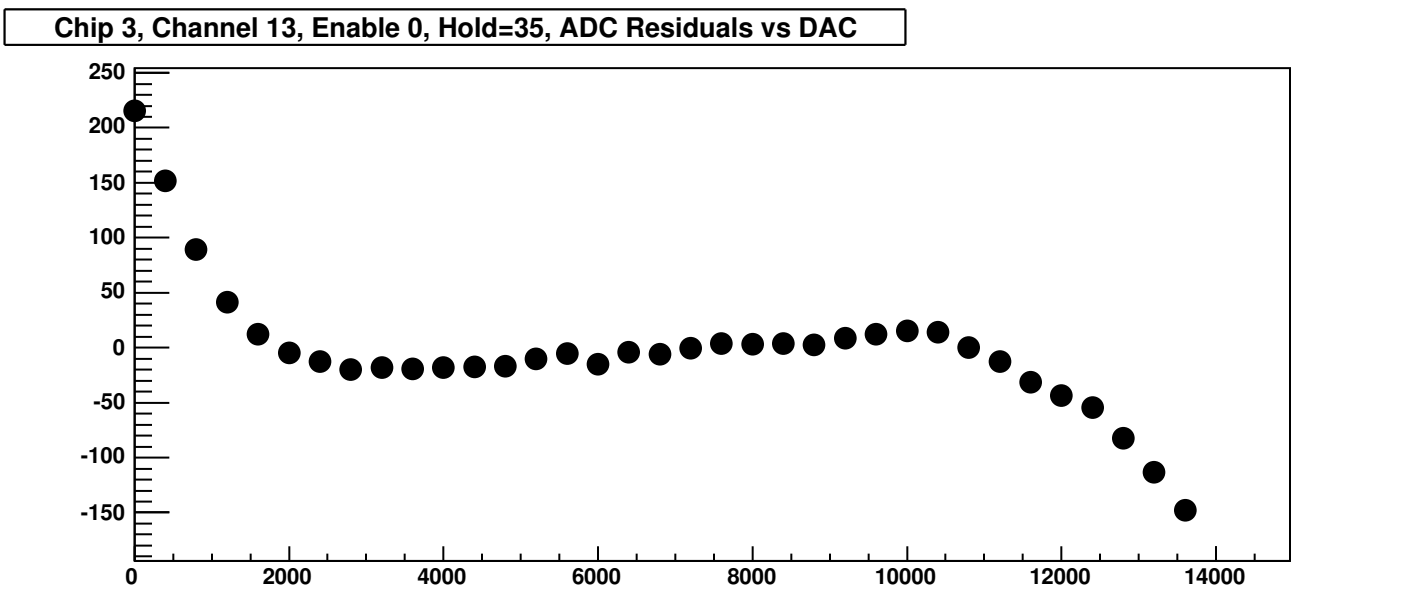


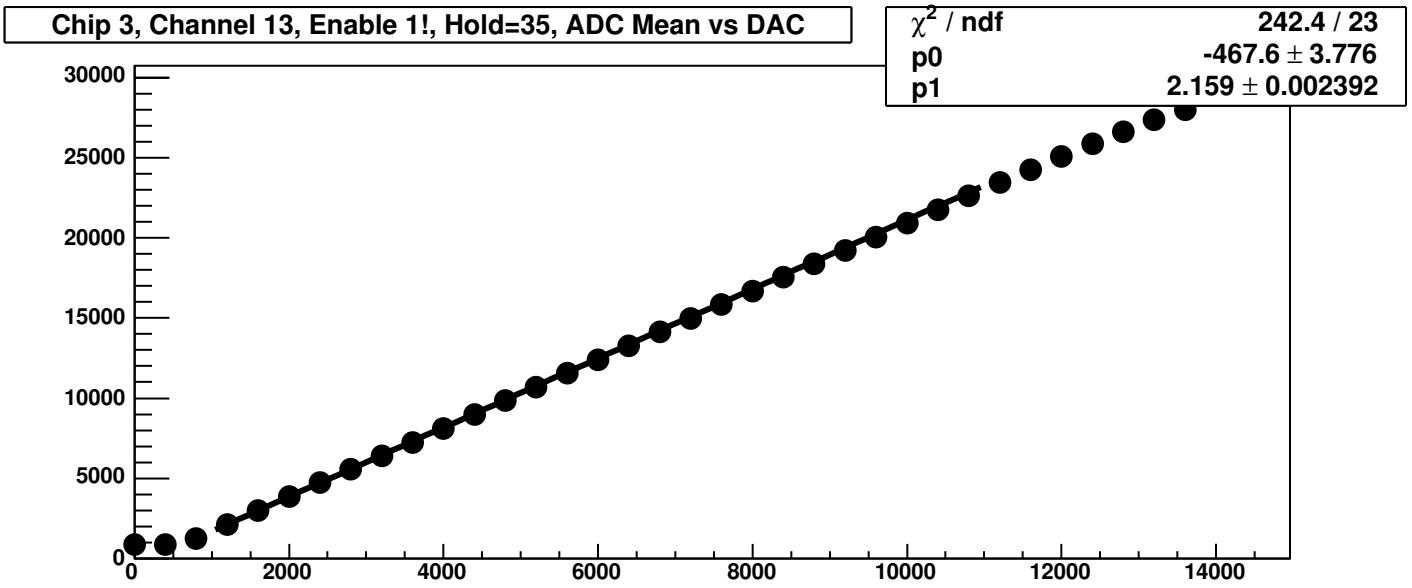
Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

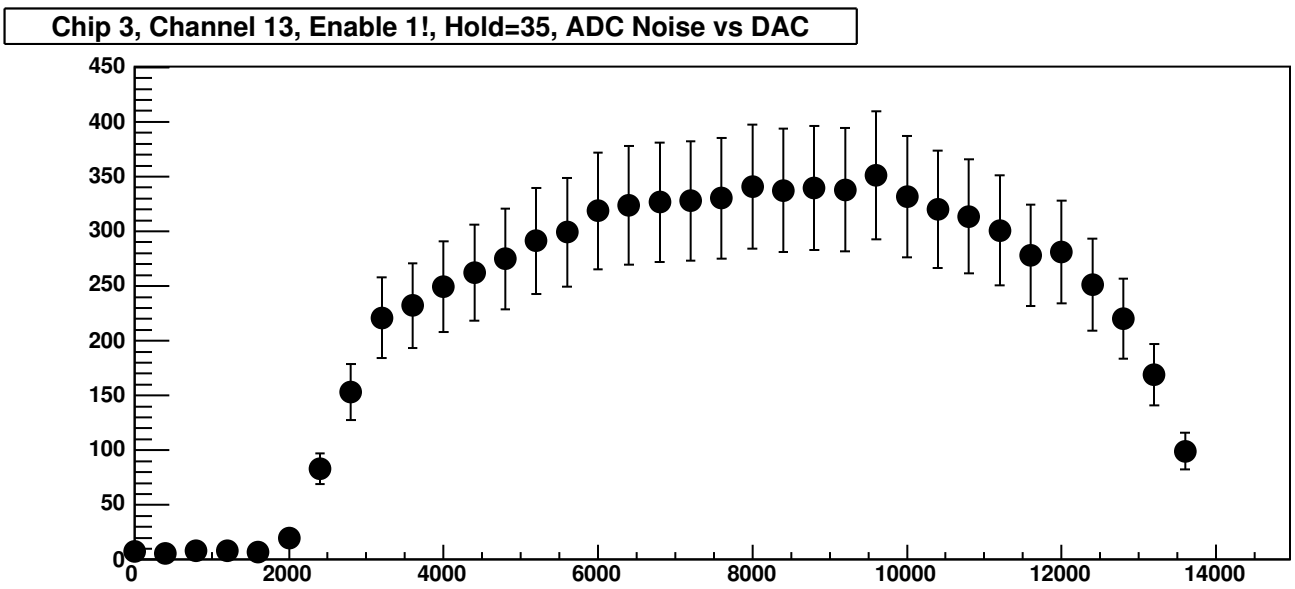


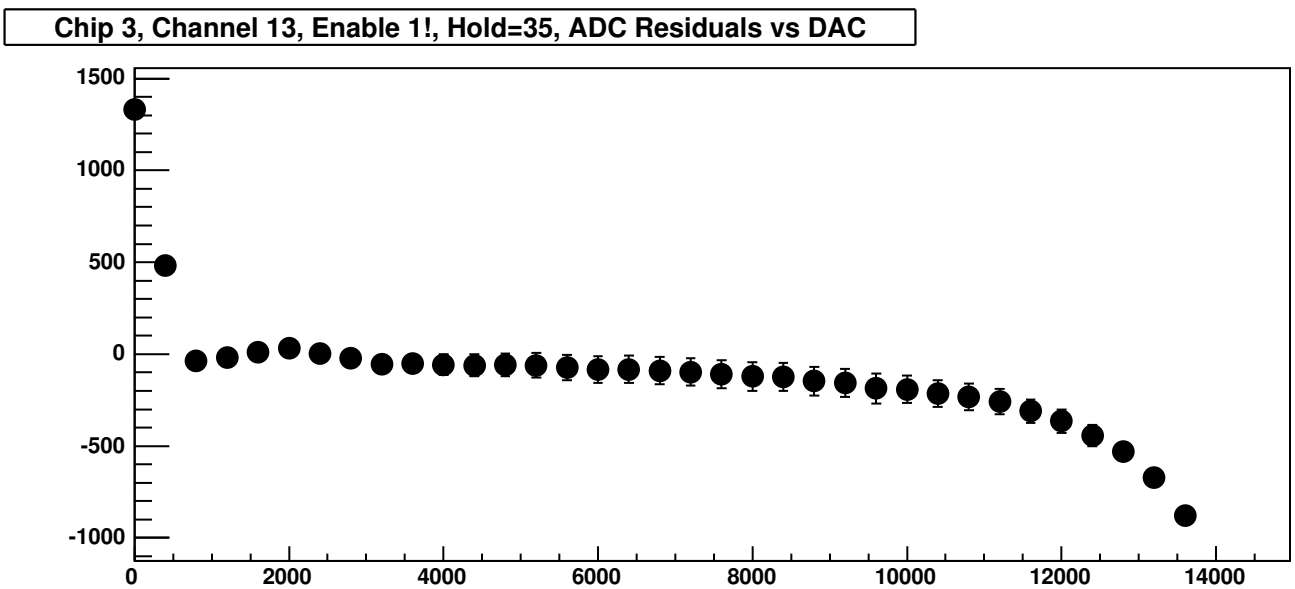

Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 636.4 / 23
p0 -760.9 ± 1.346
p1 0.1589 ± 0.000223
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

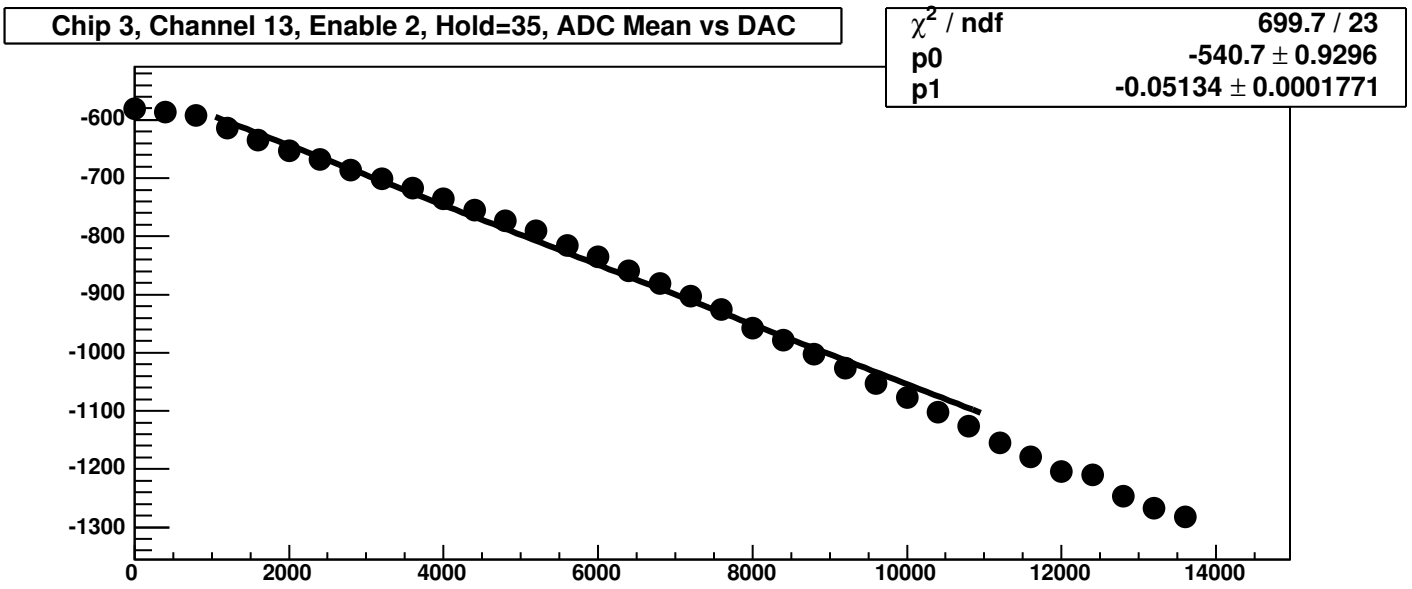

Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

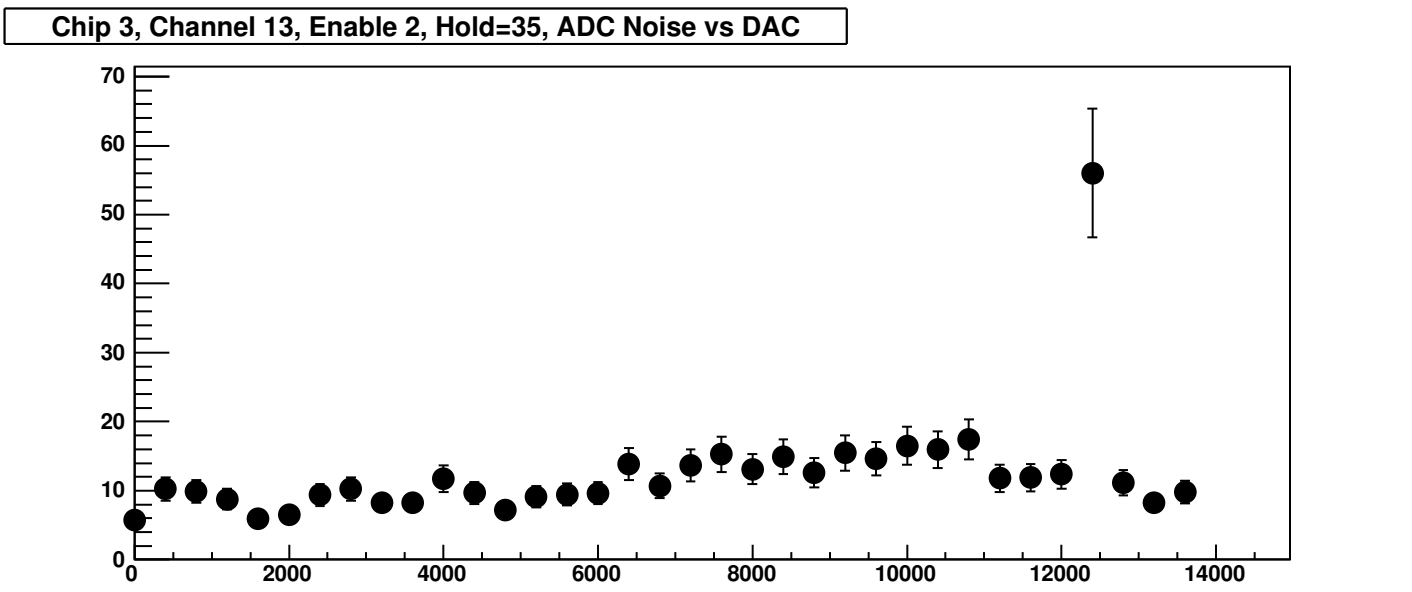

Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

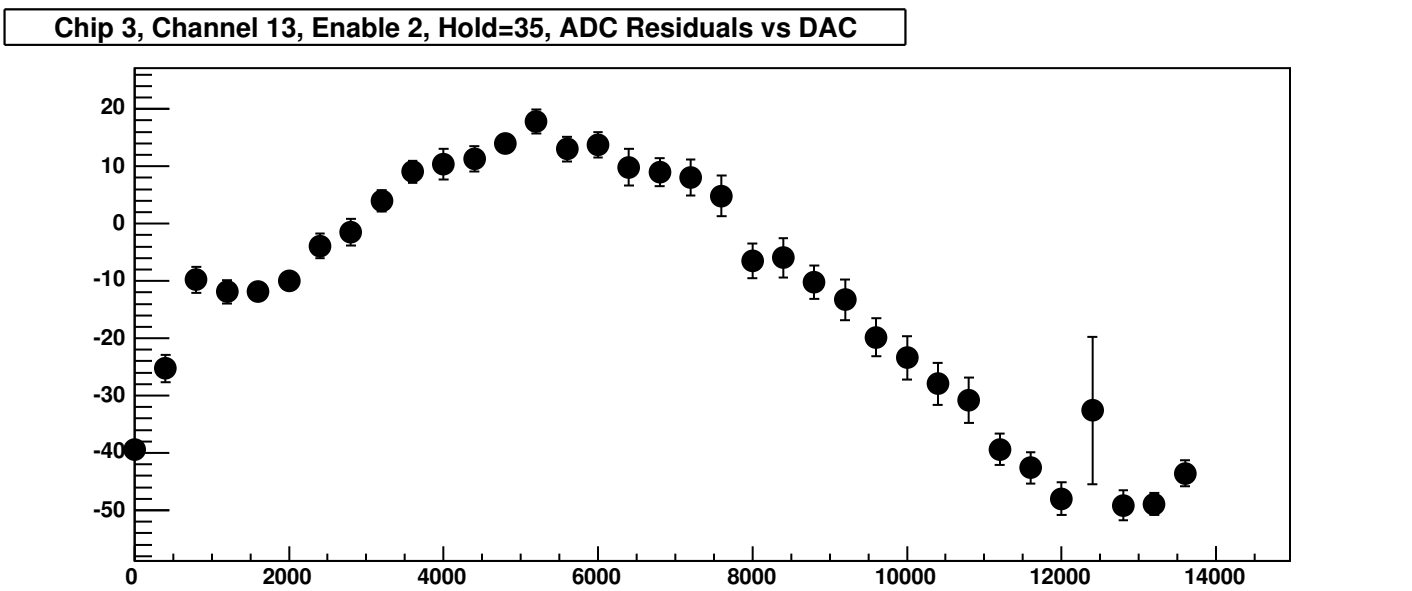

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 242.4 / 23
p0 -467.6 ± 3.776
p1 2.159 ± 0.002392
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

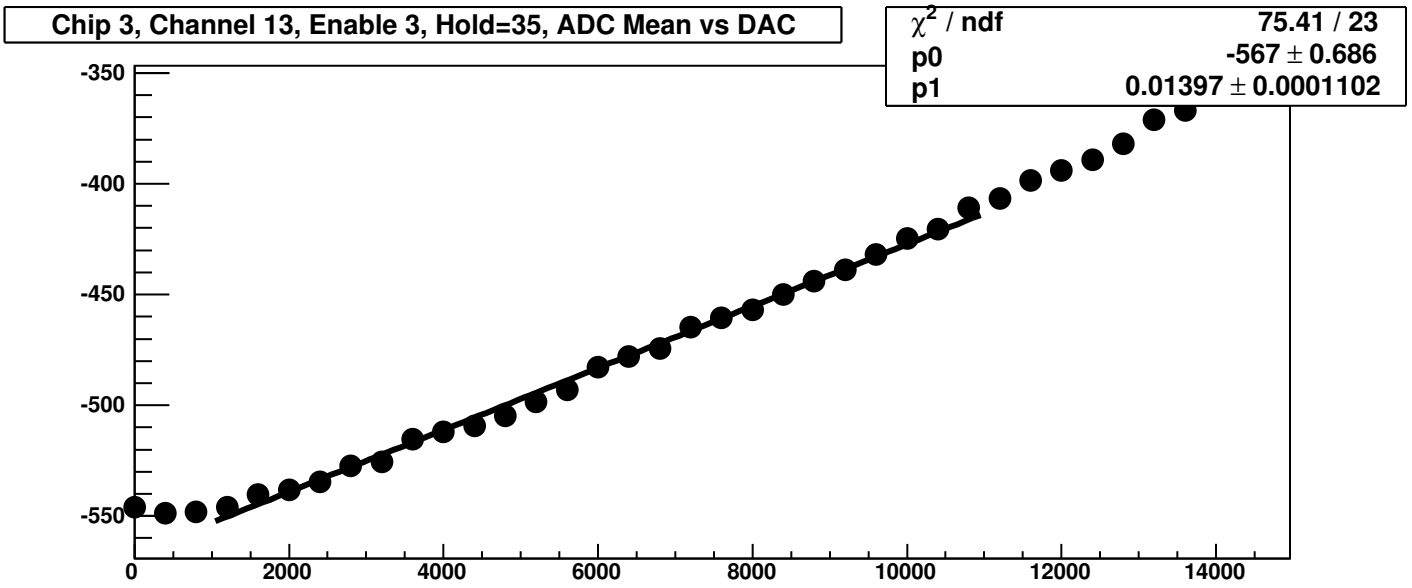

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

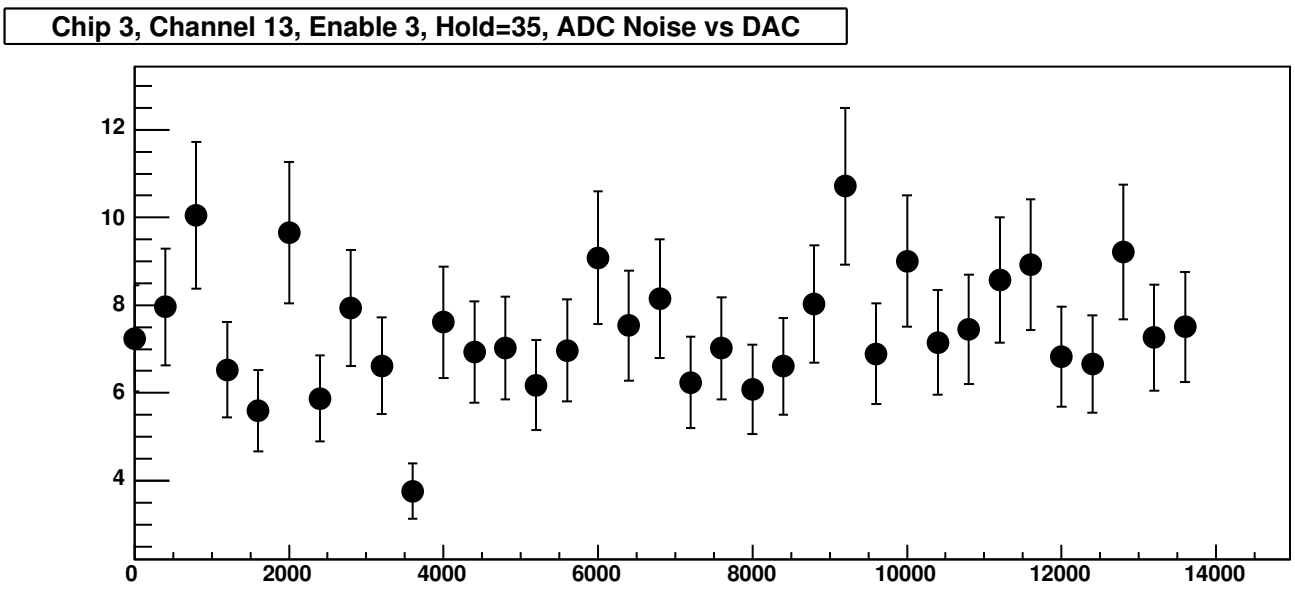

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

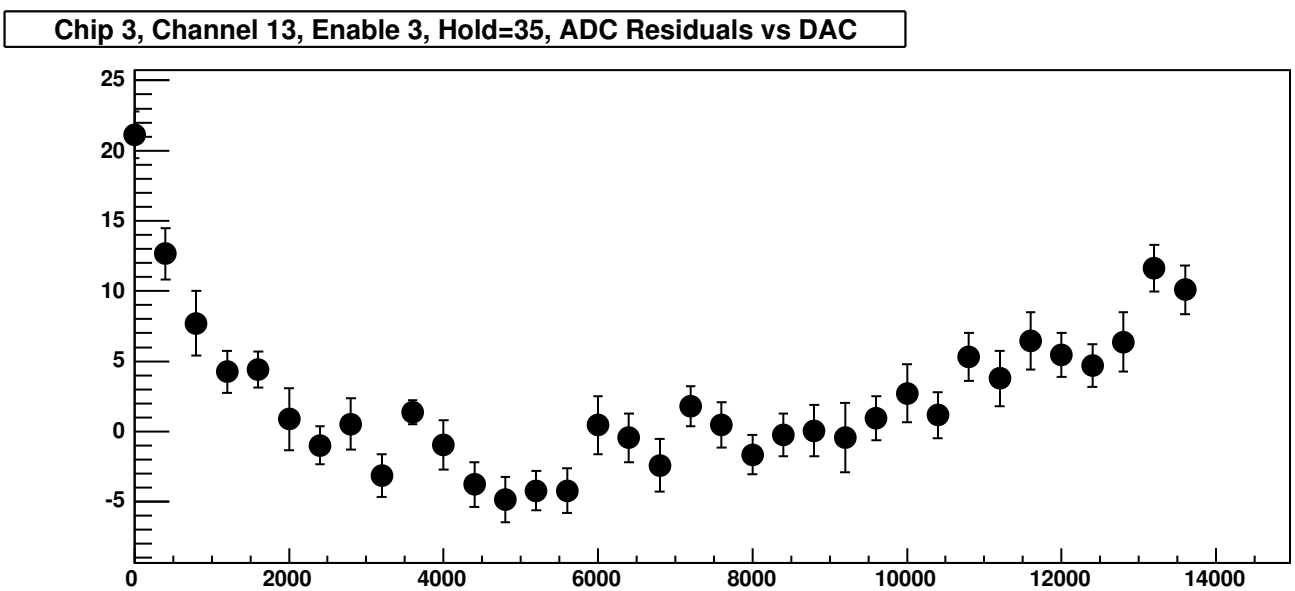

Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 699.7 / 23
p0 -540.7 ± 0.9296
p1 -0.05134 ± 0.0001771
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

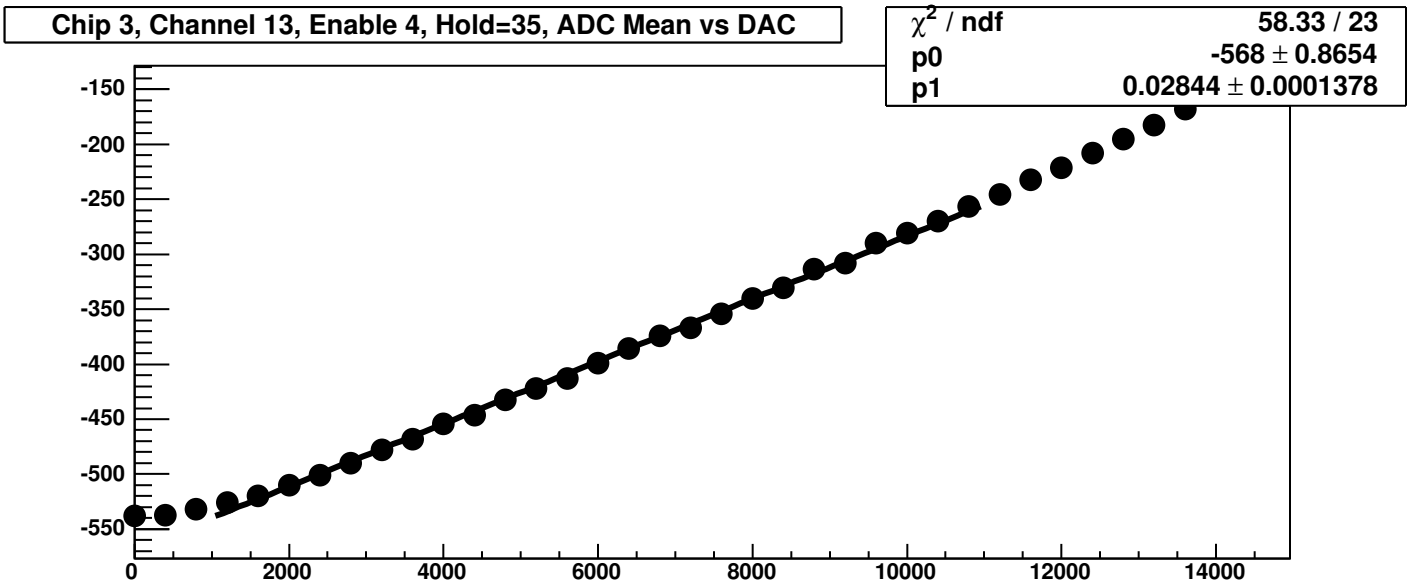

Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

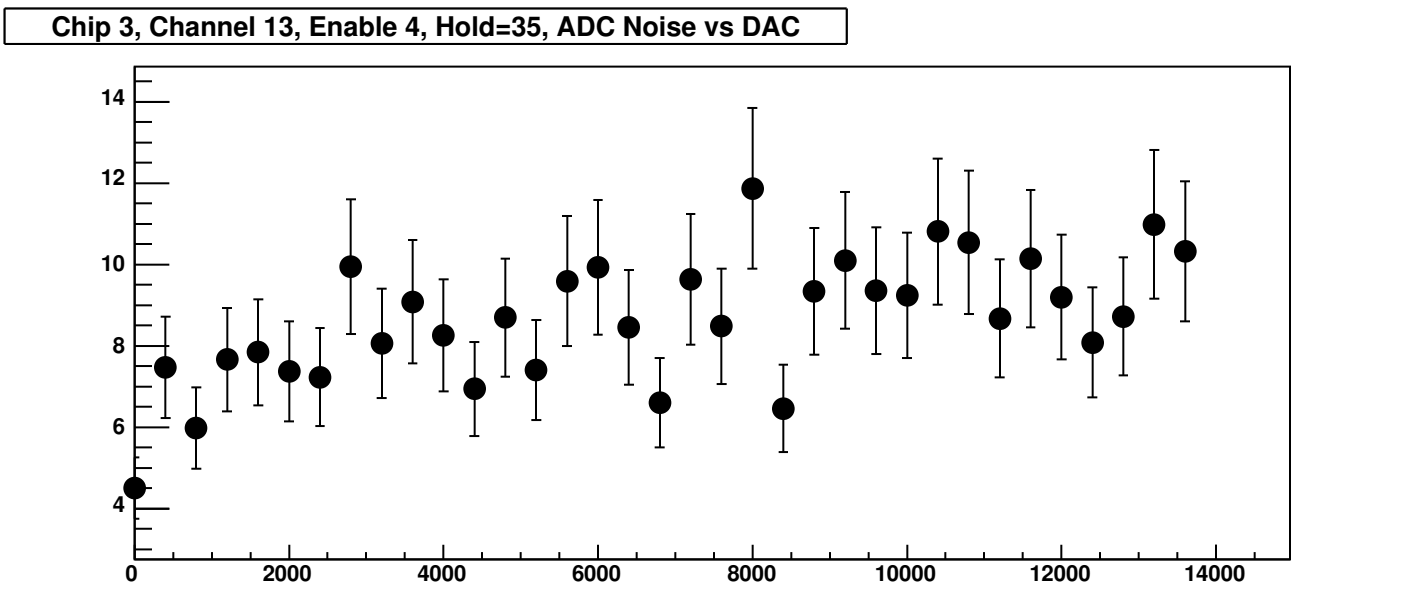

Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

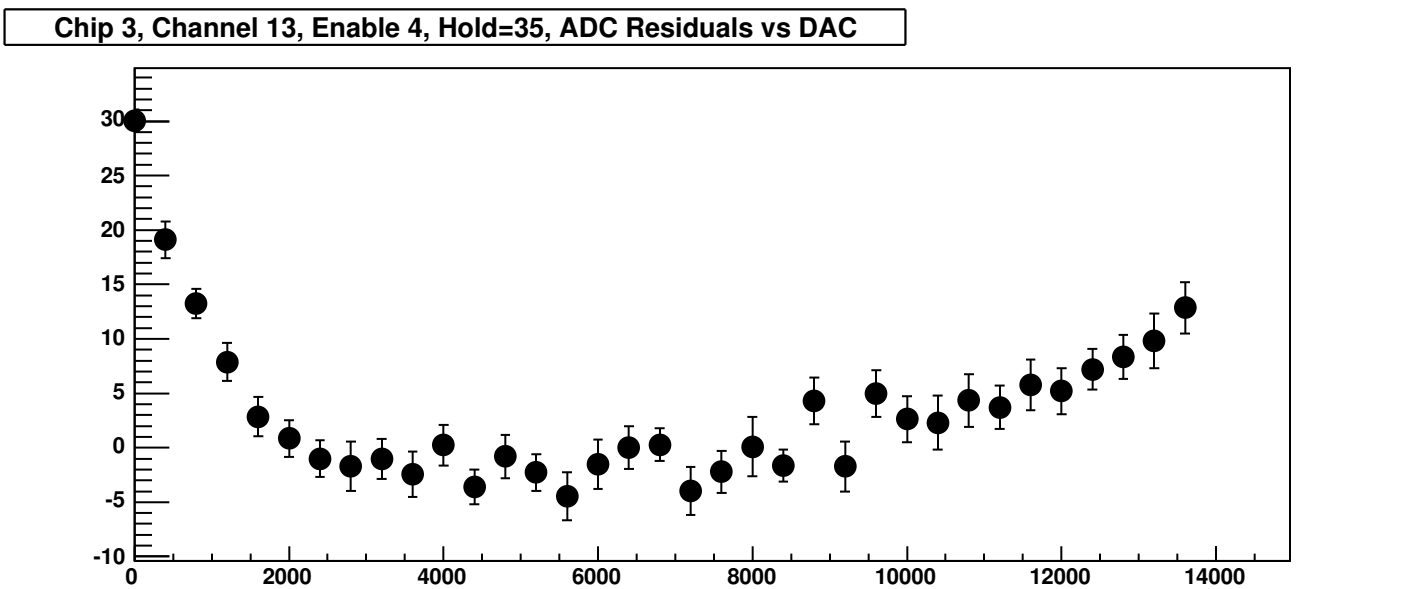

Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 75.41 / 23
p0 -567 ± 0.686
p1 0.01397 ± 0.0001102
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

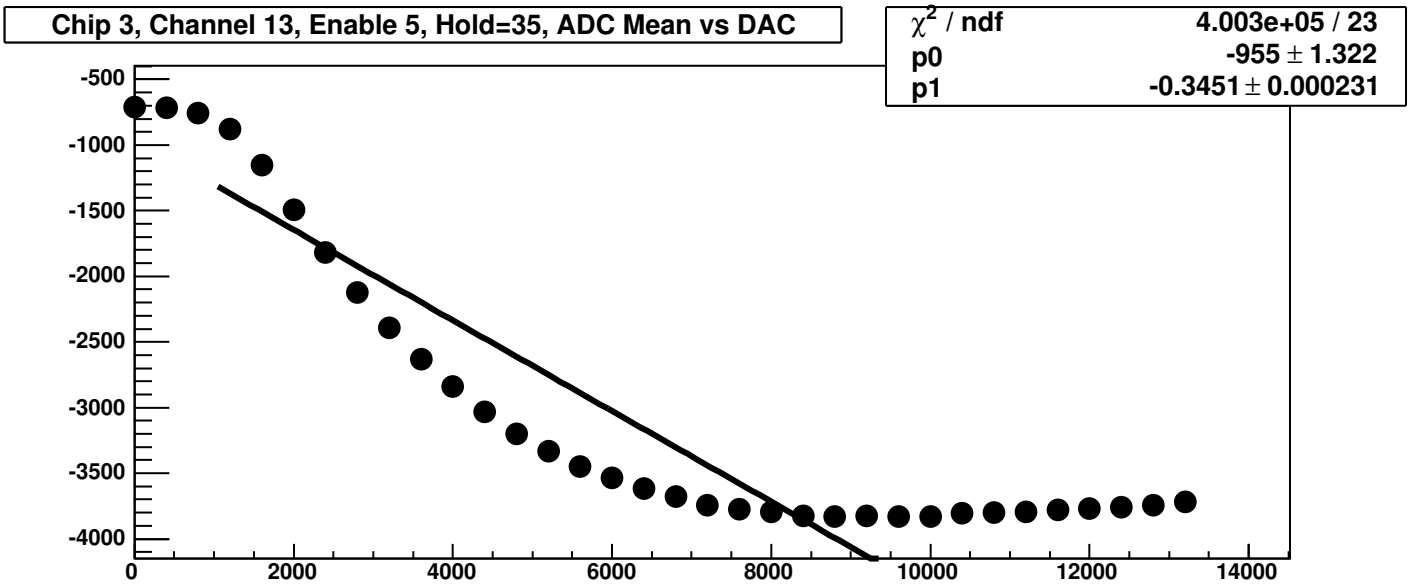

Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

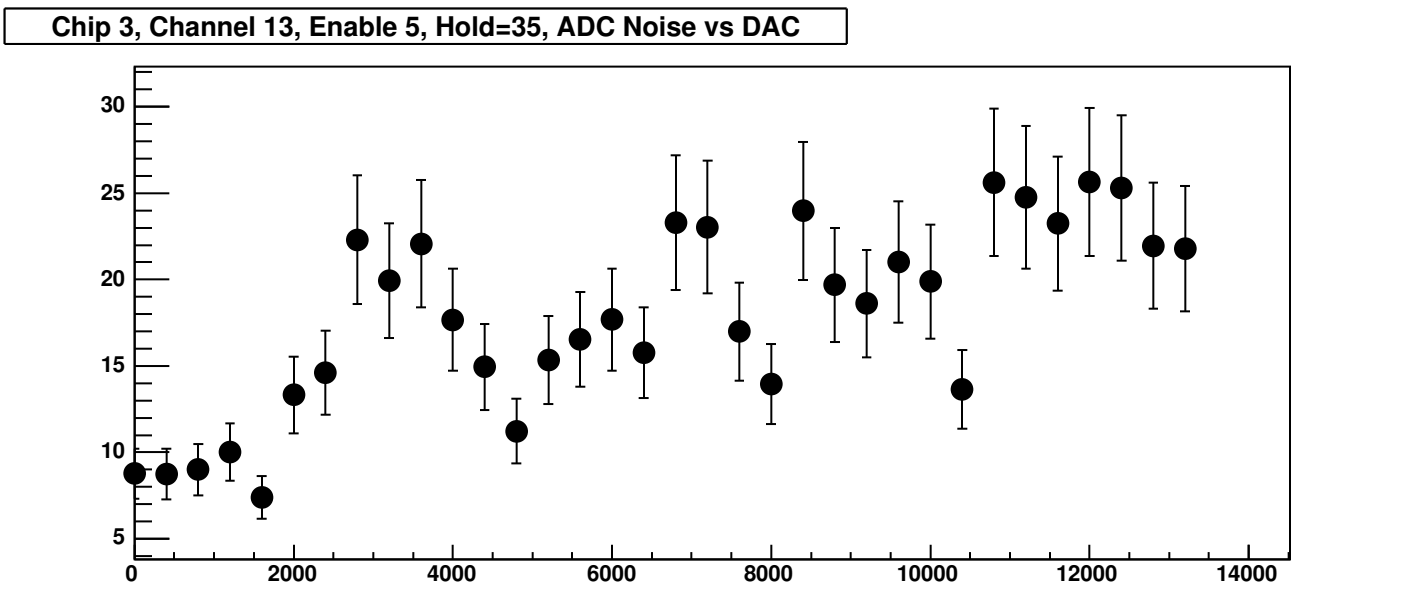

Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

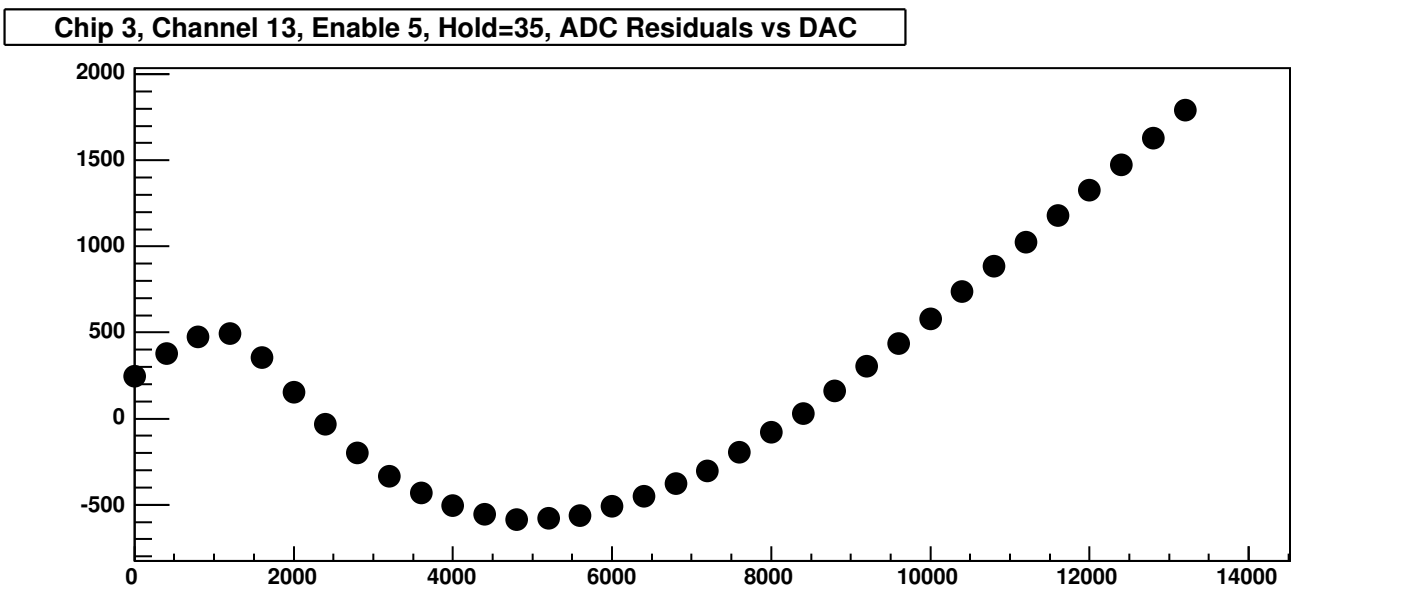

Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 58.33 / 23
p0 -568 ± 0.8654
p1 0.02844 ± 0.0001378
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

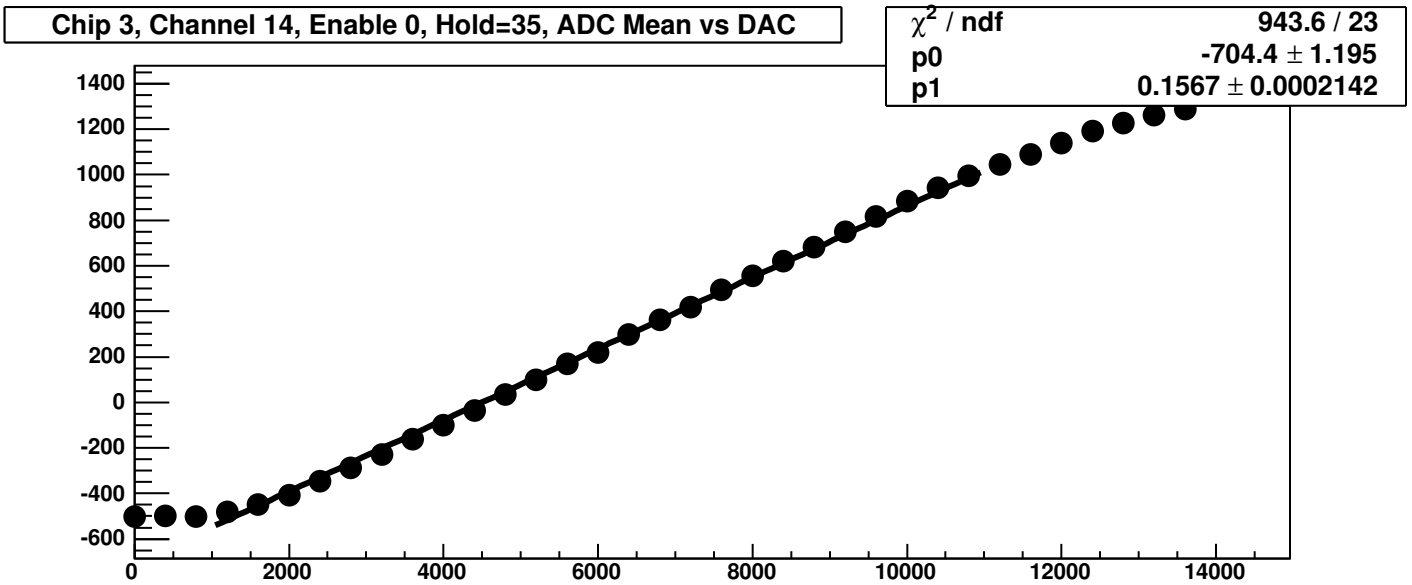

Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

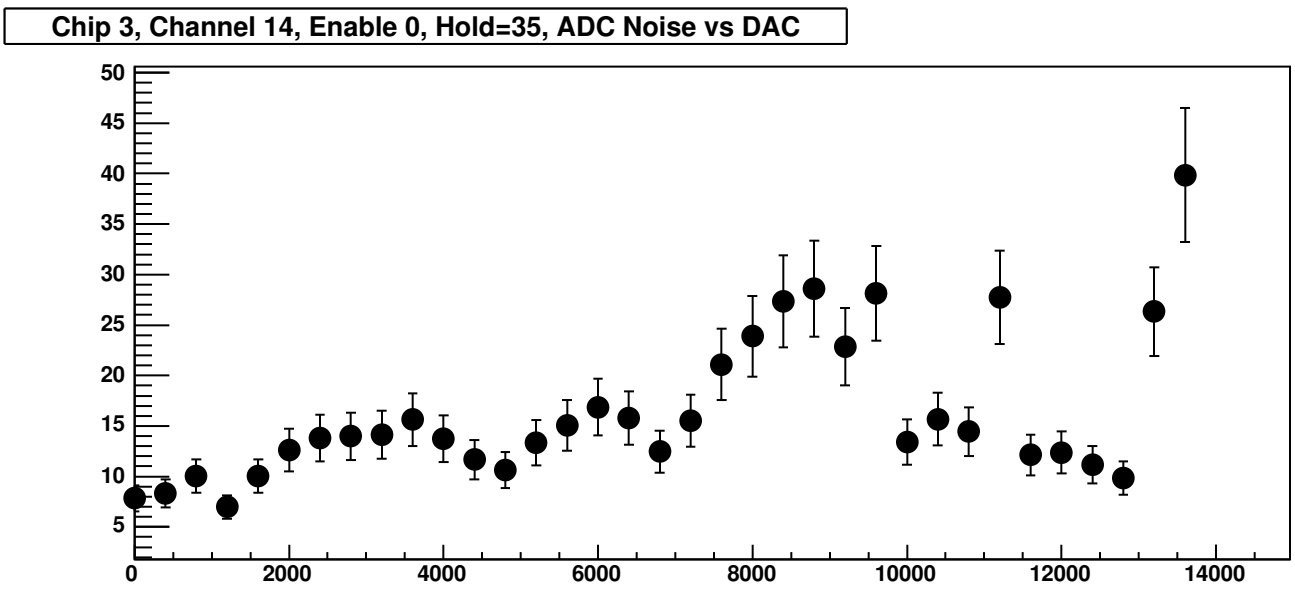

Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

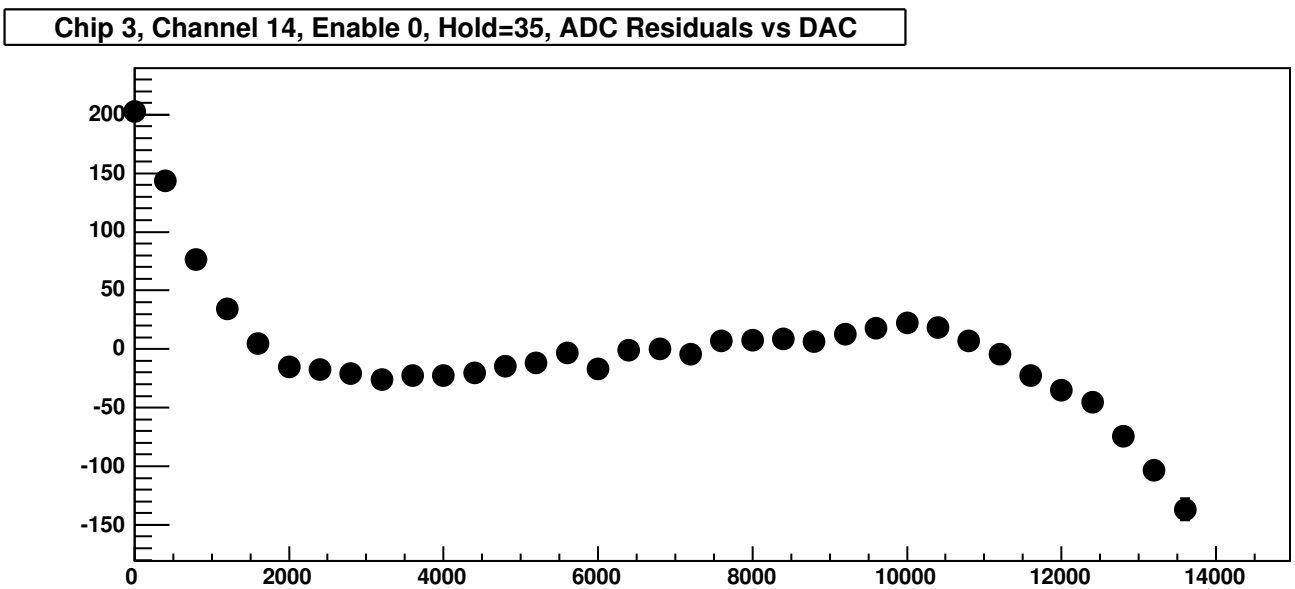

Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.003e+05 / 23
p0 -955 ± 1.322
p1 -0.3451 ± 0.000231
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
2000
4000
6000
8000
10000
12000
14000

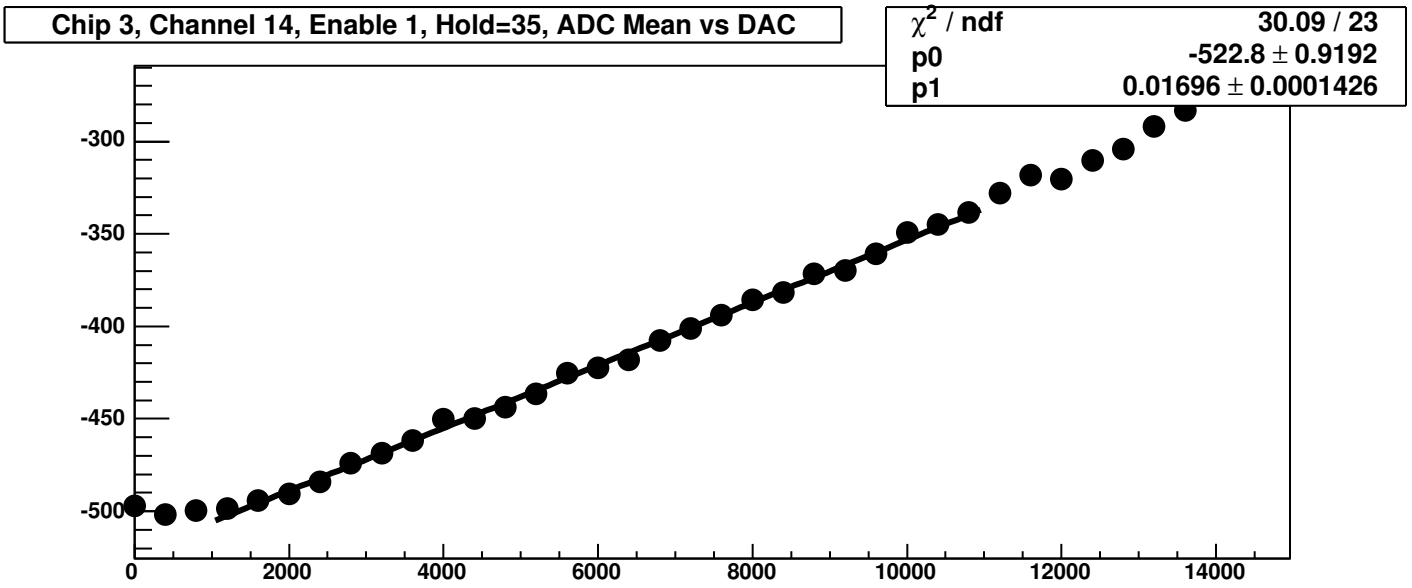

Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

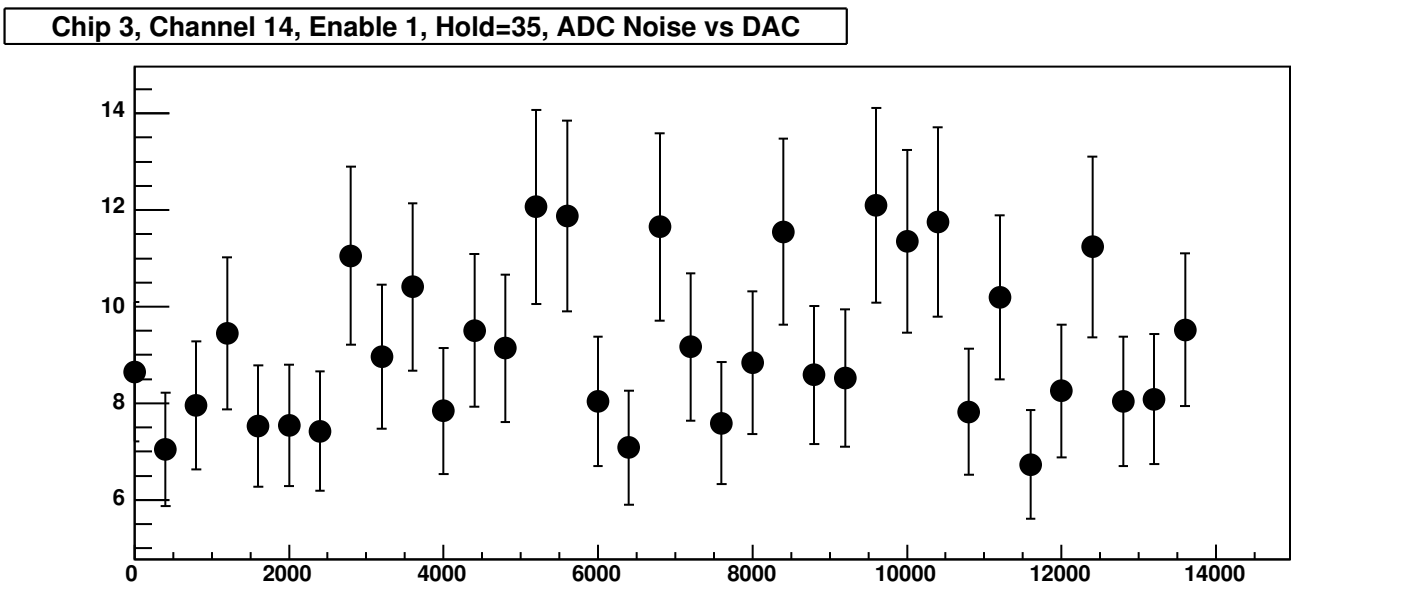

Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

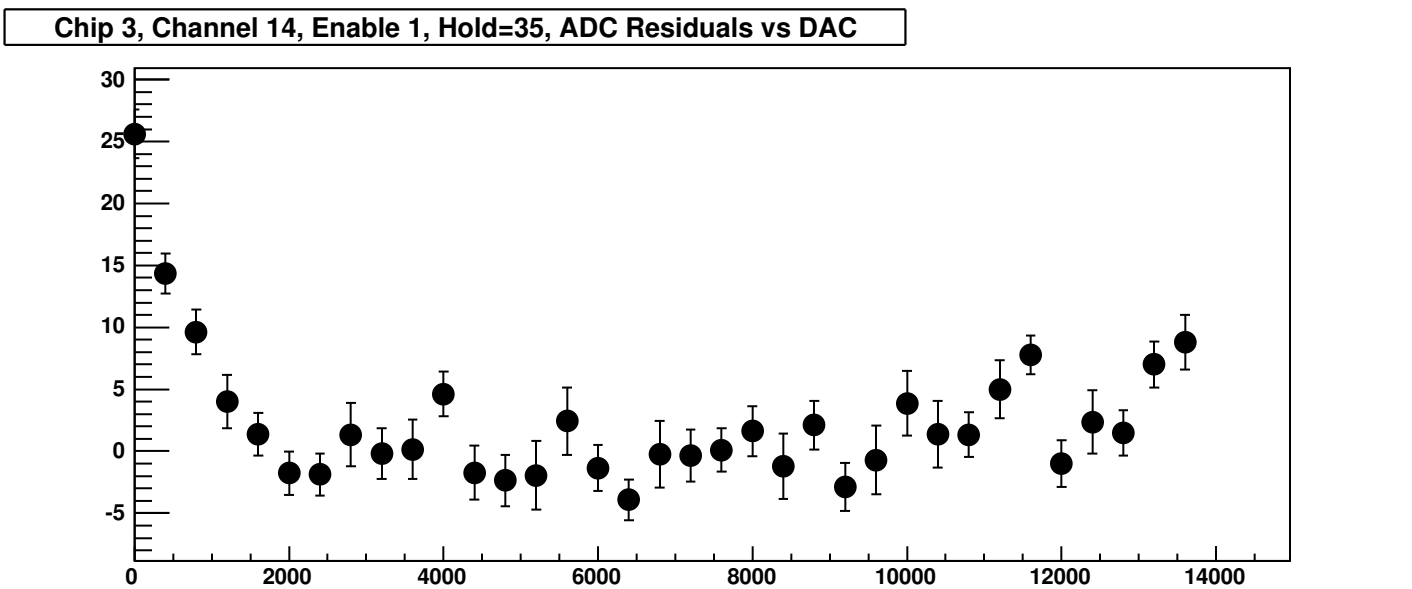

Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 943.6 / 23
p0 -704.4 ± 1.195
p1 0.1567 ± 0.0002142
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

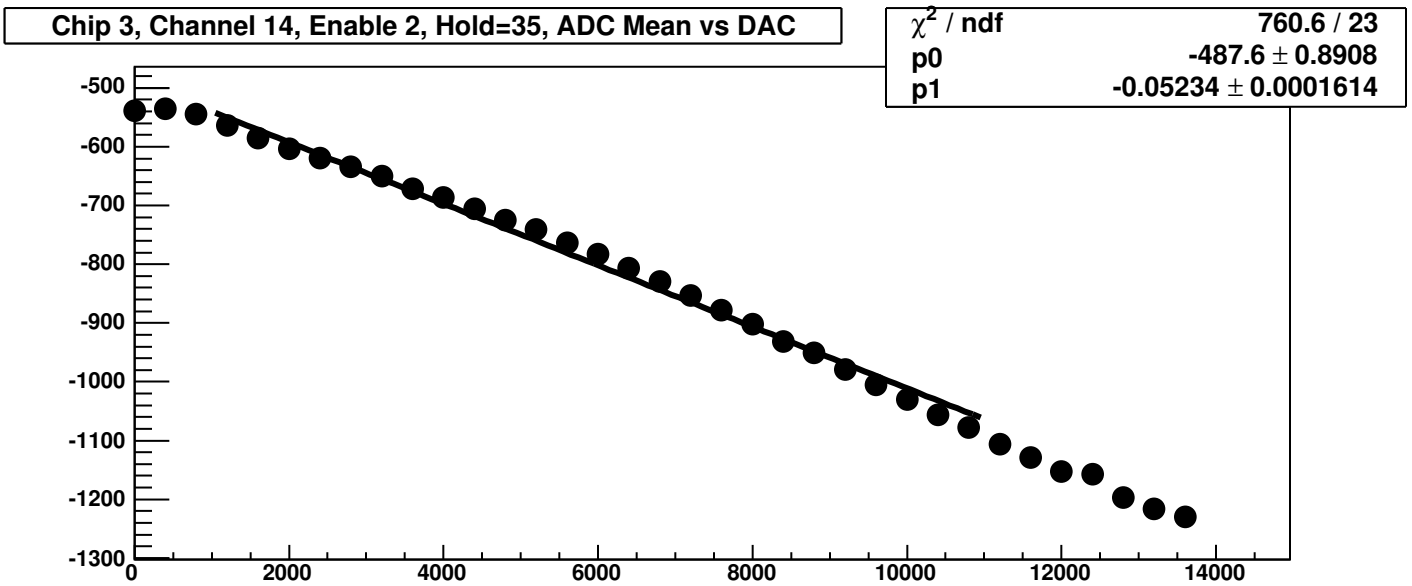

Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

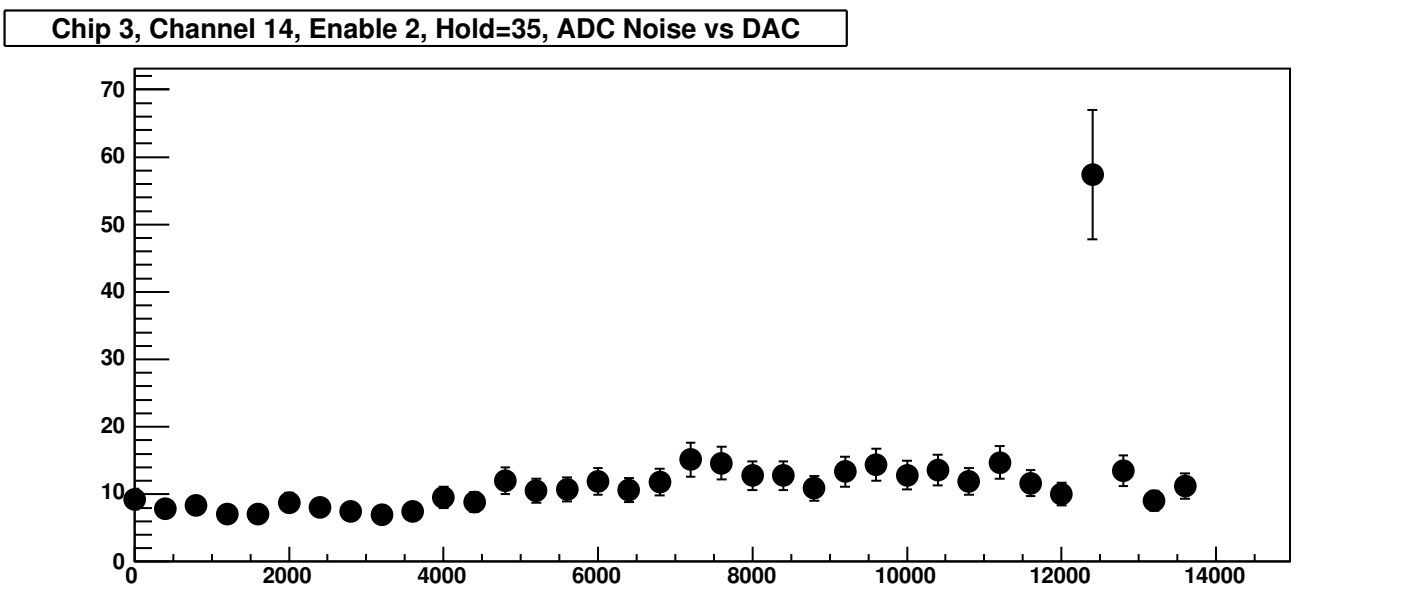

Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

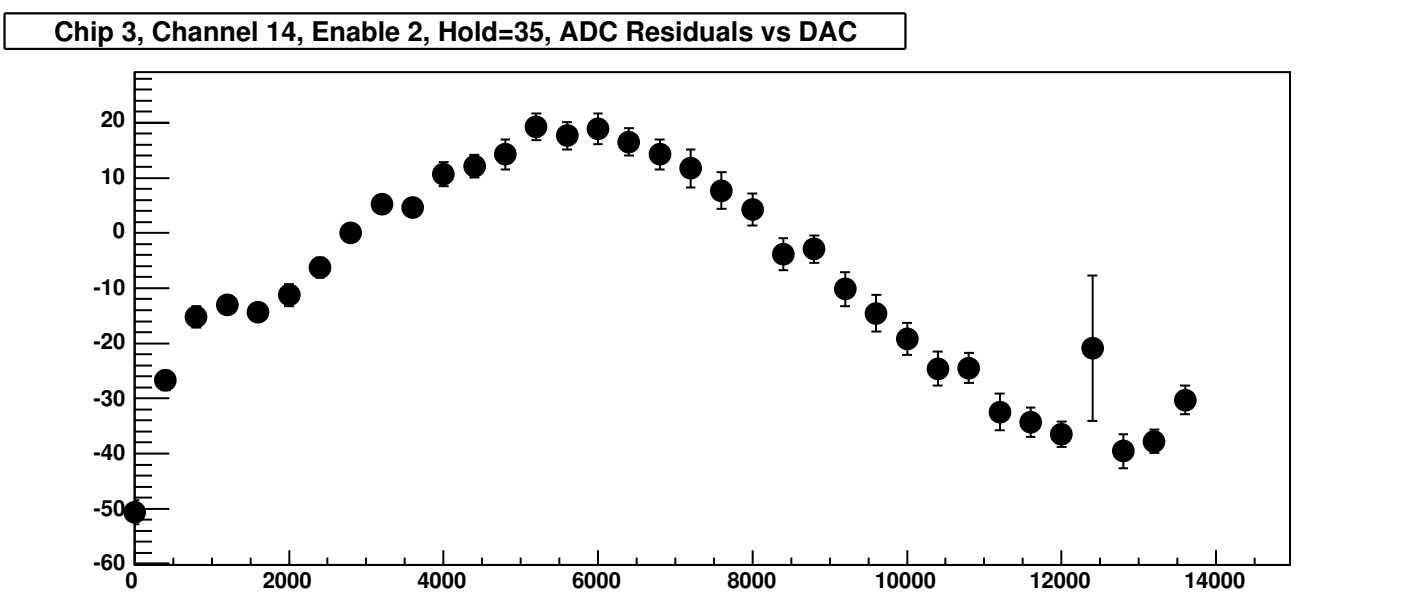

Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.09 / 23
p0 -522.8 ± 0.9192
p1 0.01696 ± 0.0001426
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

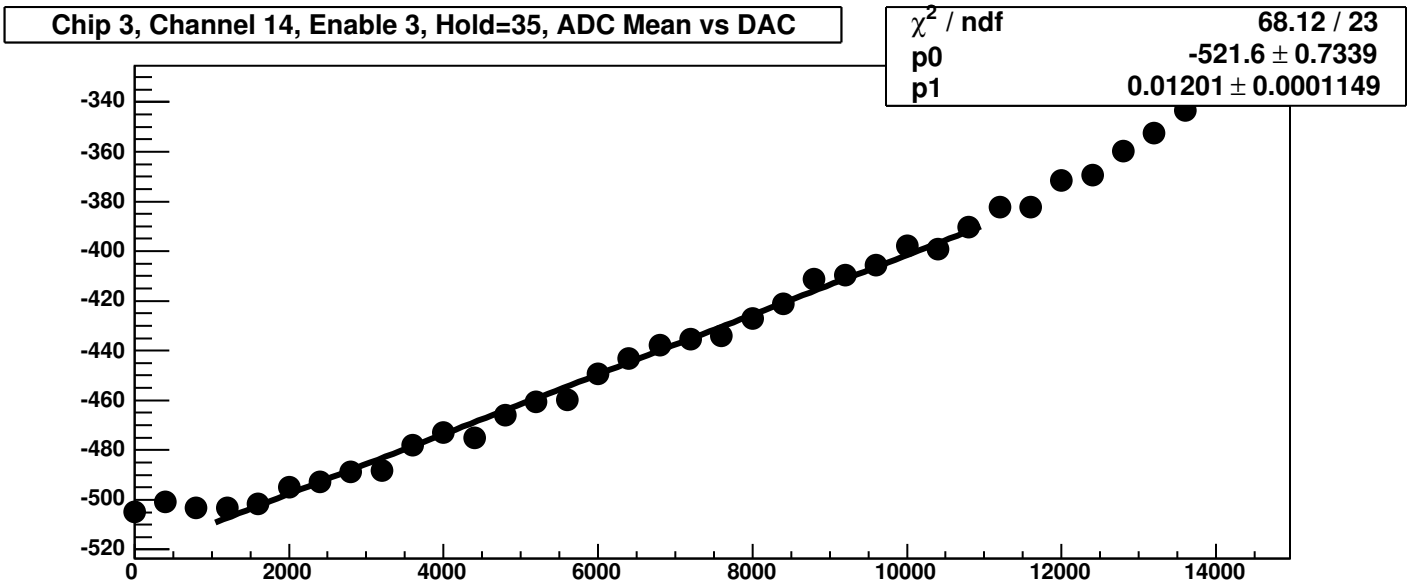

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

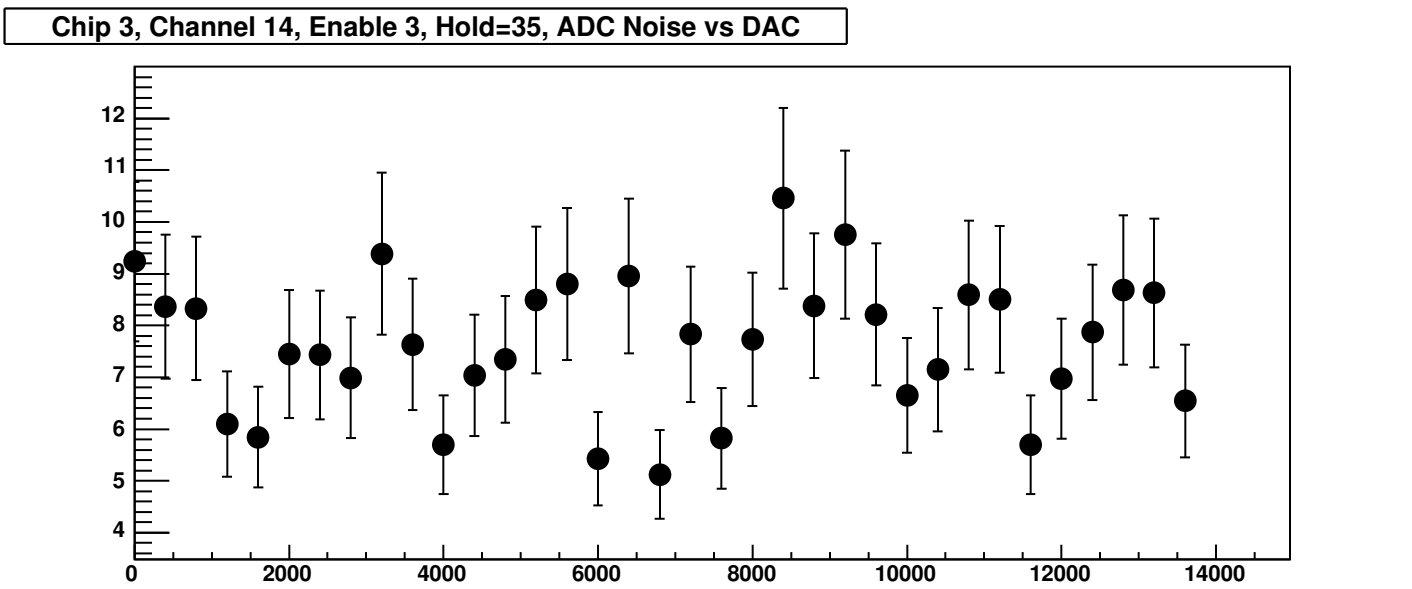

Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

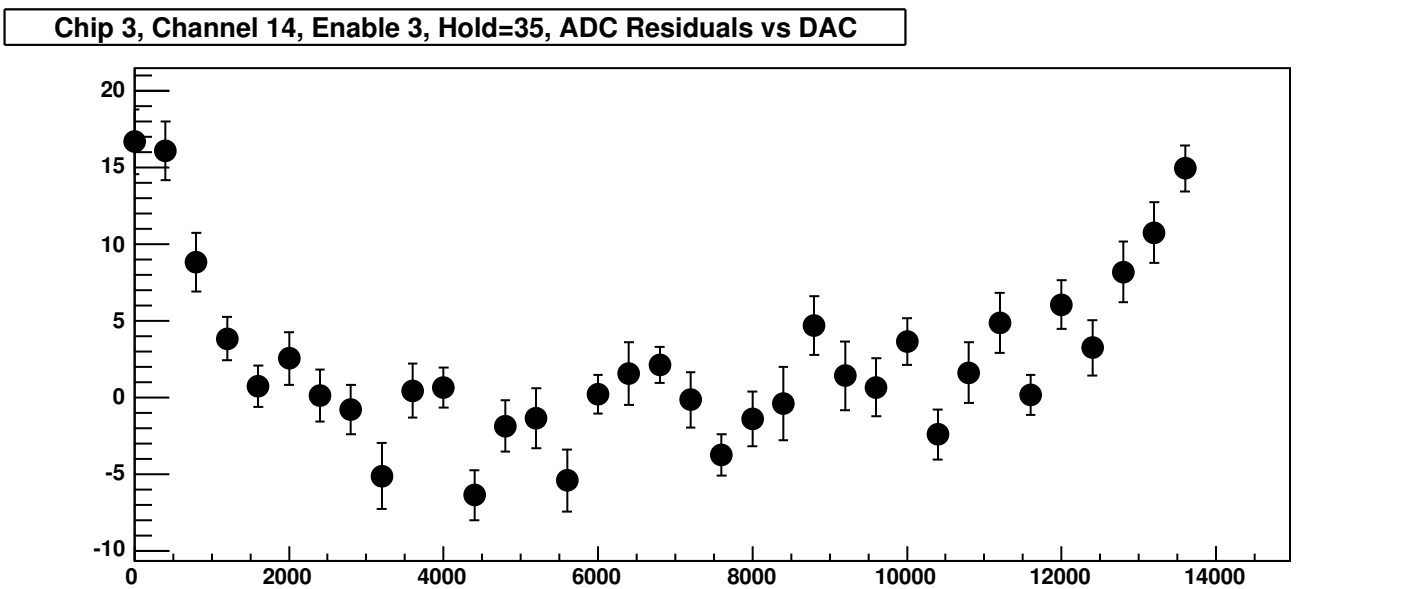

Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 760.6 / 23
p0 -487.6 ± 0.8908
p1 -0.05234 ± 0.0001614
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

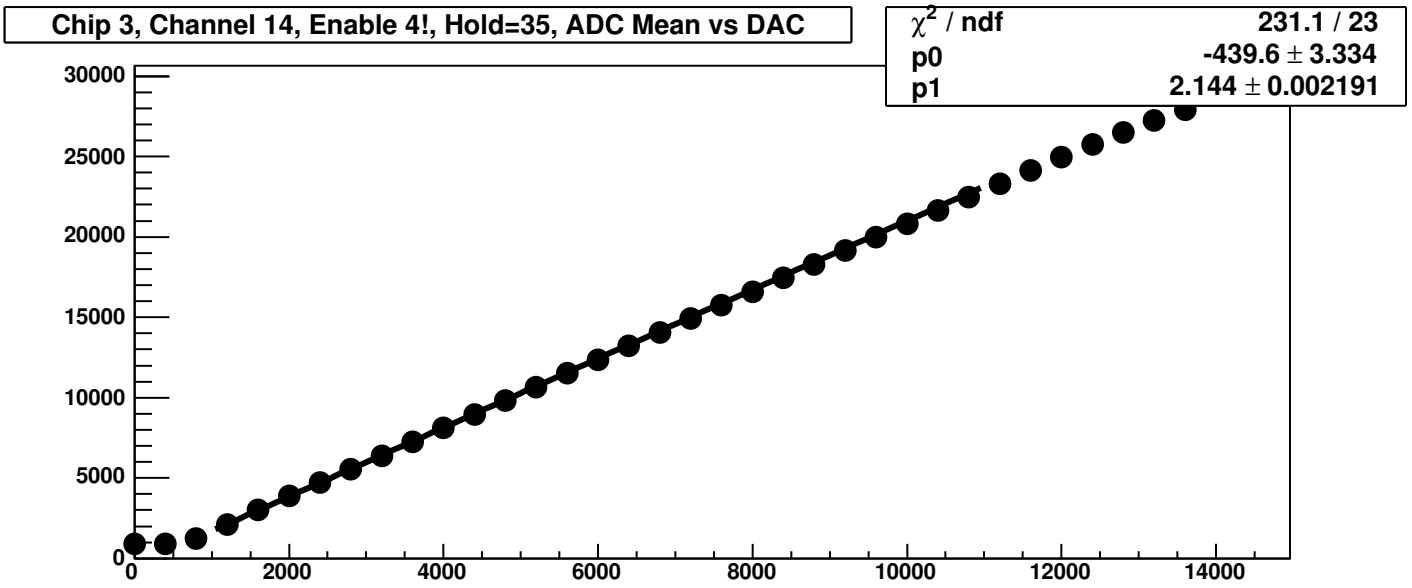

Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

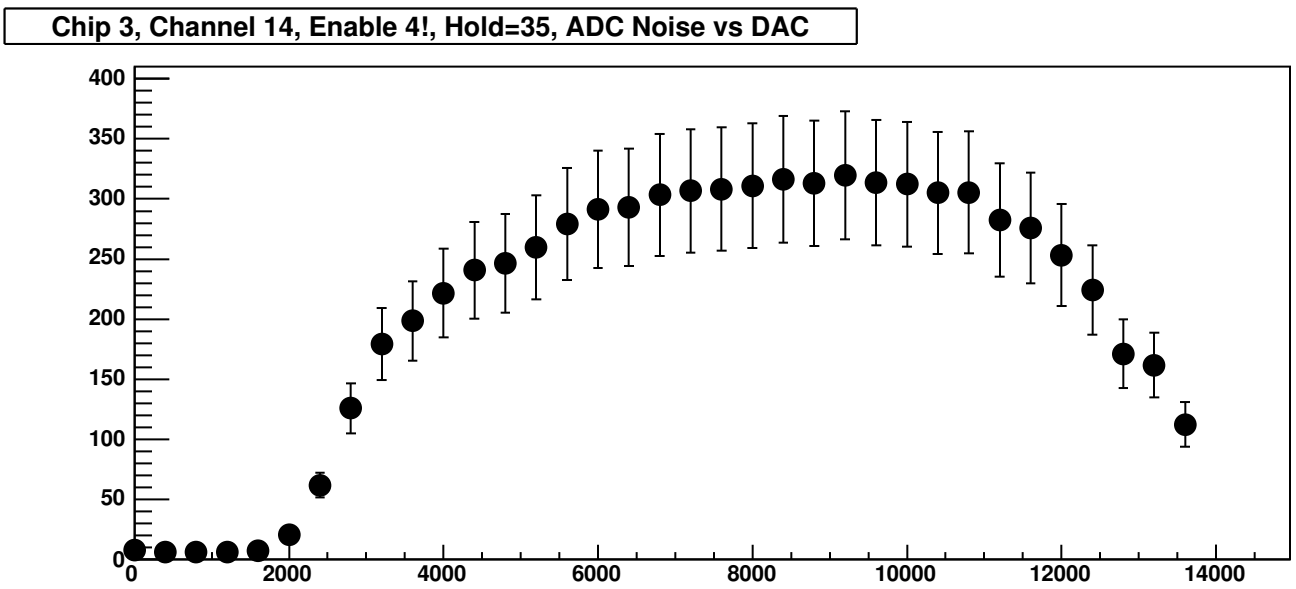

Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000

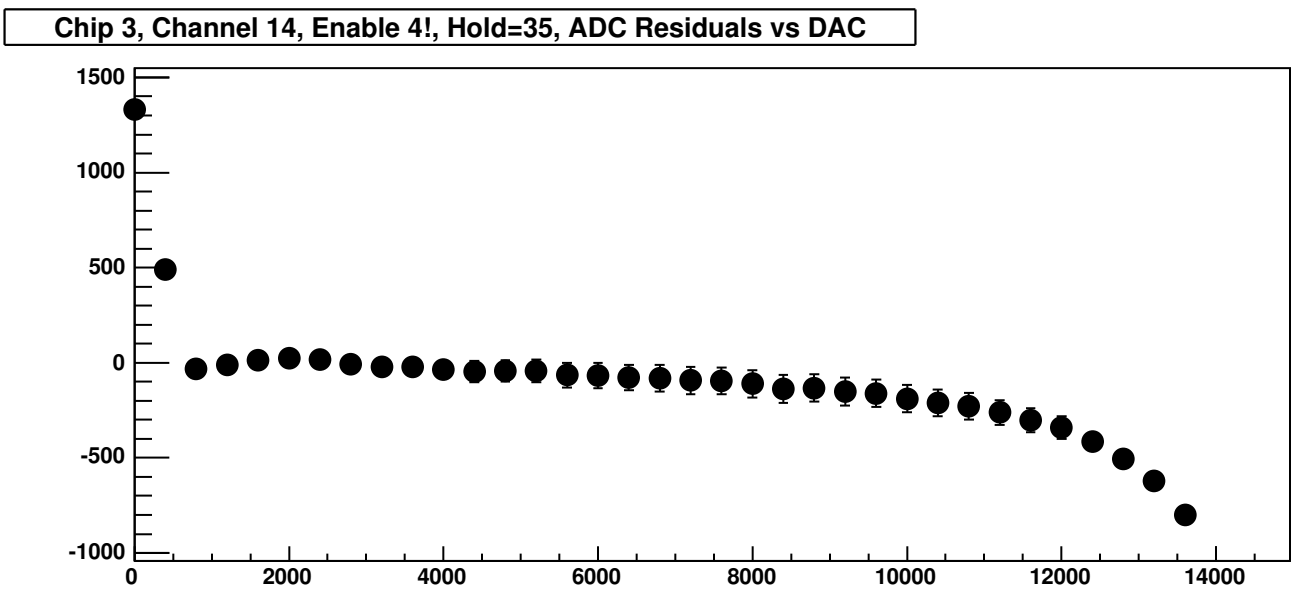

Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 68.12 / 23
p0 -521.6 ± 0.7339
p1 0.01201 ± 0.0001149
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

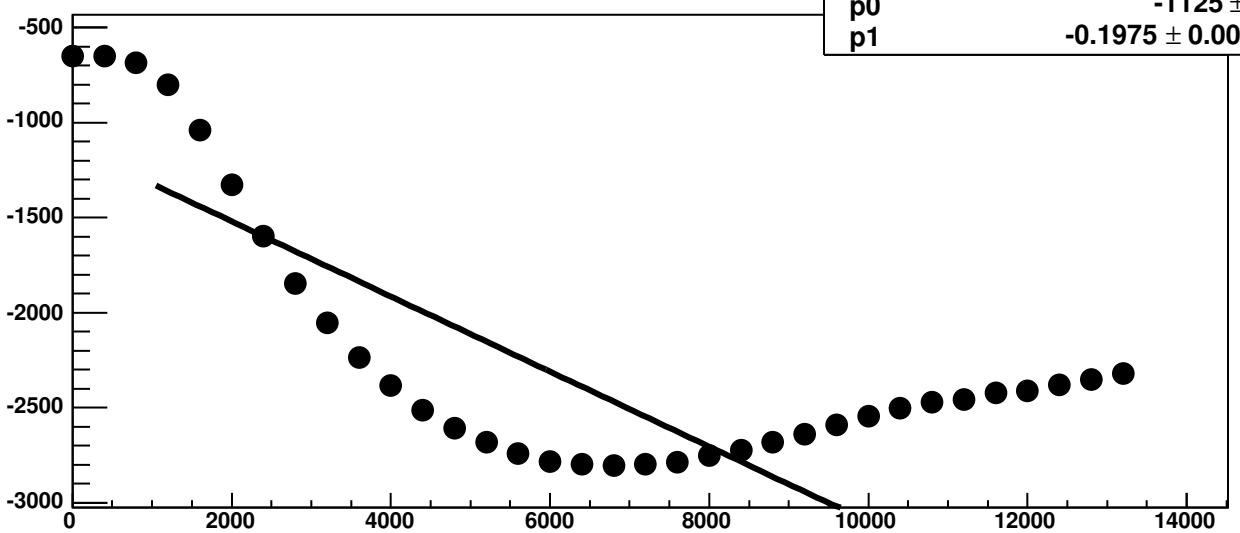

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 231.1 / 23
p0 -439.6 ± 3.334
p1 2.144 ± 0.002191
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

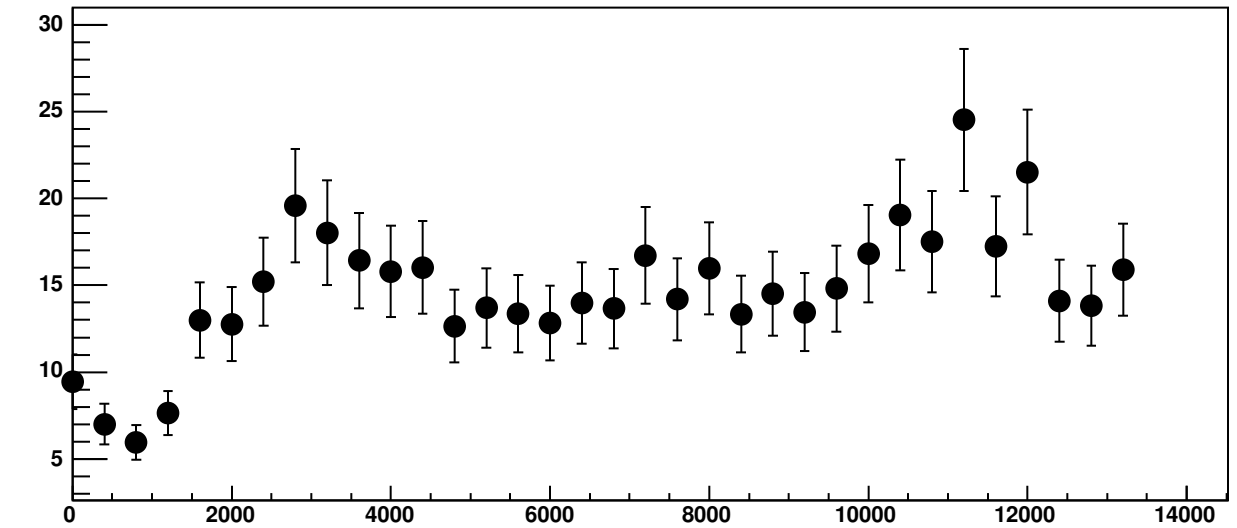

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

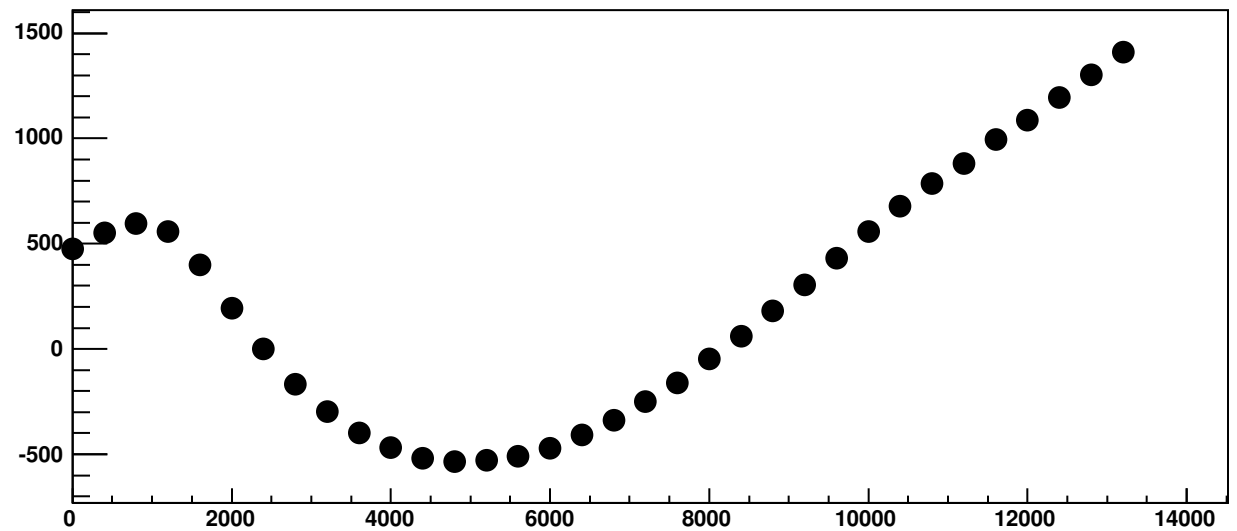
χ^2 / ndf	4.409e+05 / 23
p0	-1125 ± 1.331
p1	-0.1975 ± 0.0002148

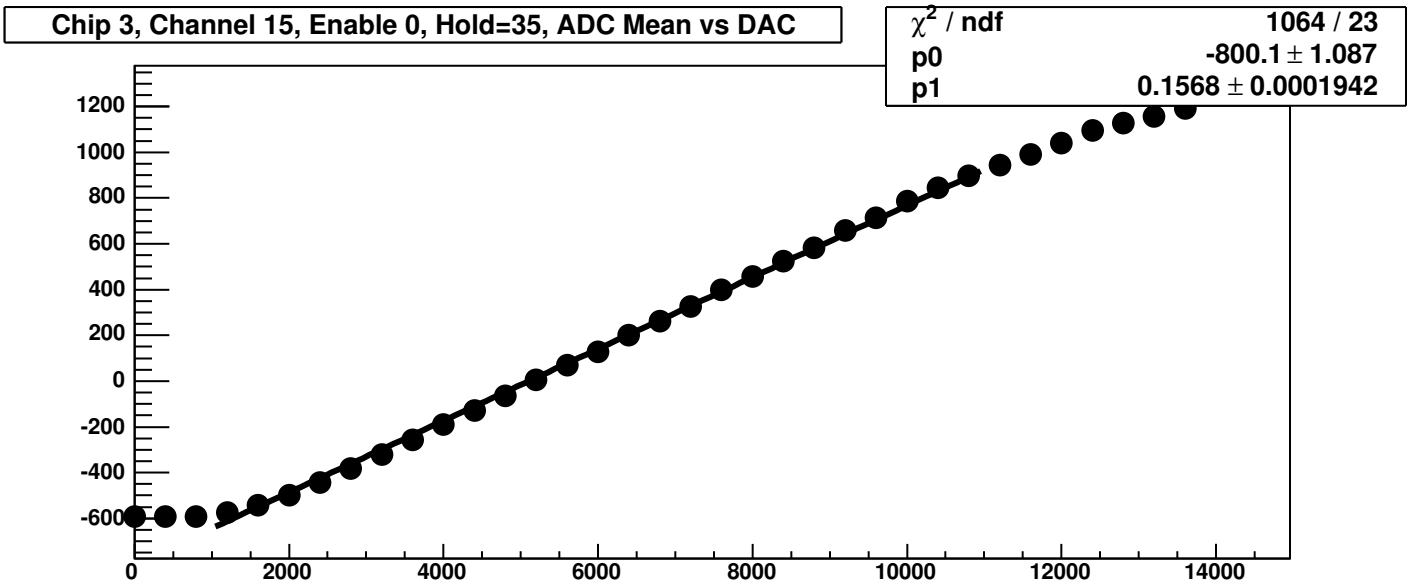


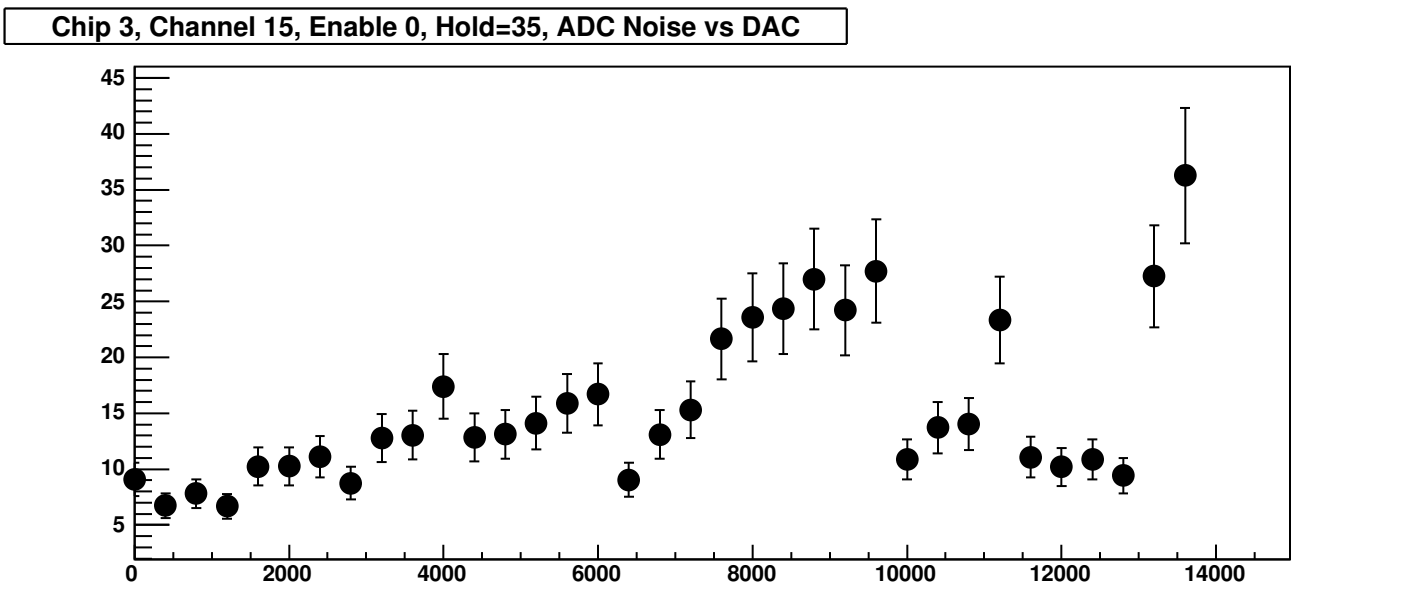
Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

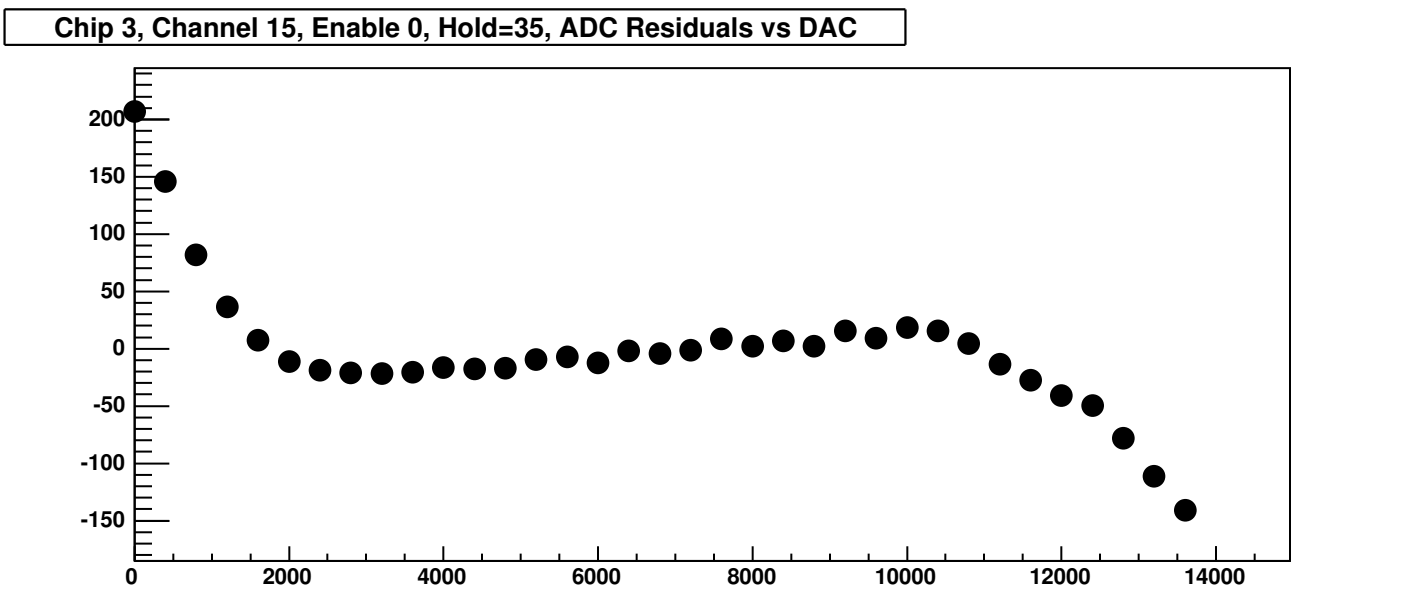


Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

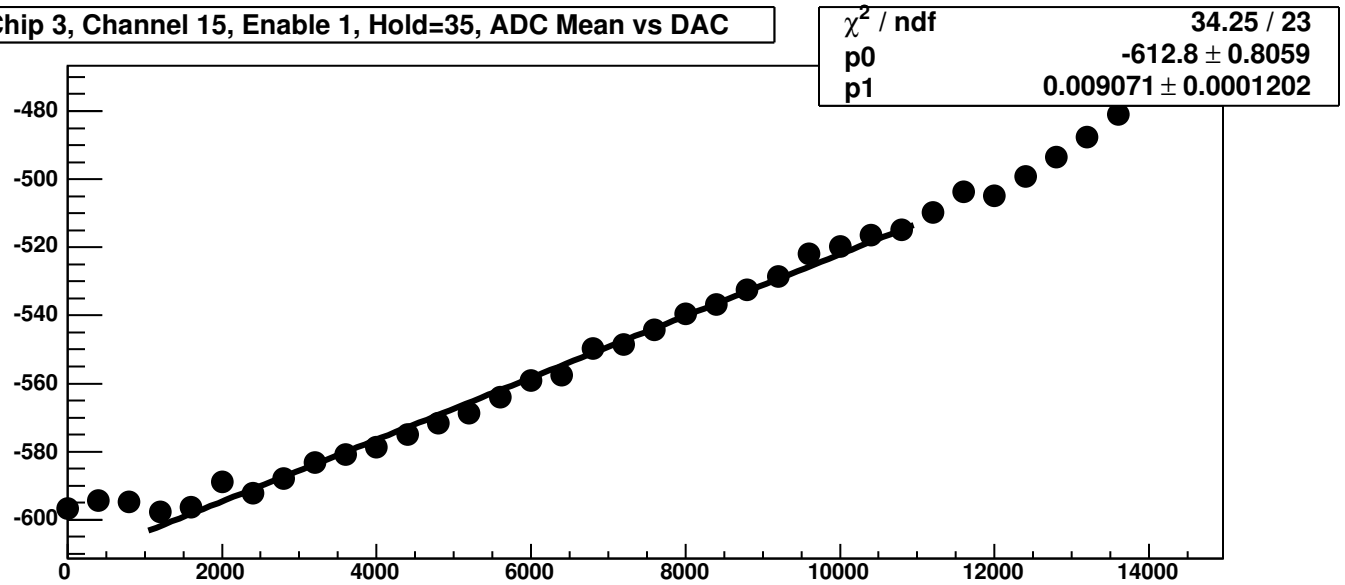



Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1064 / 23
p0 -800.1 ± 1.087
p1 0.1568 ± 0.0001942
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

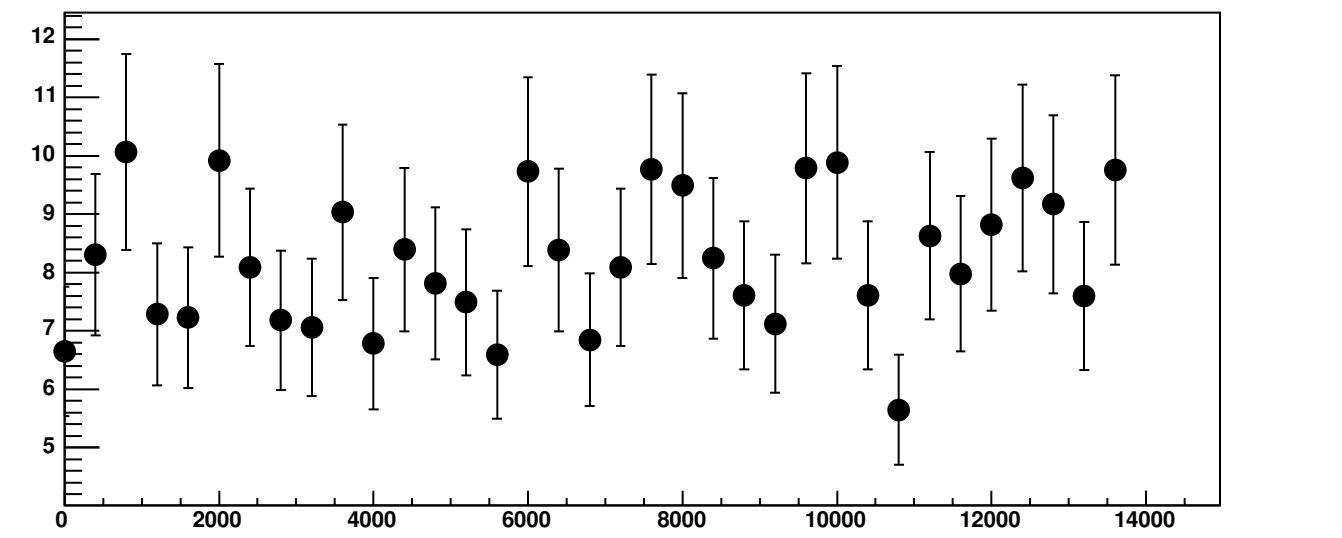

Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

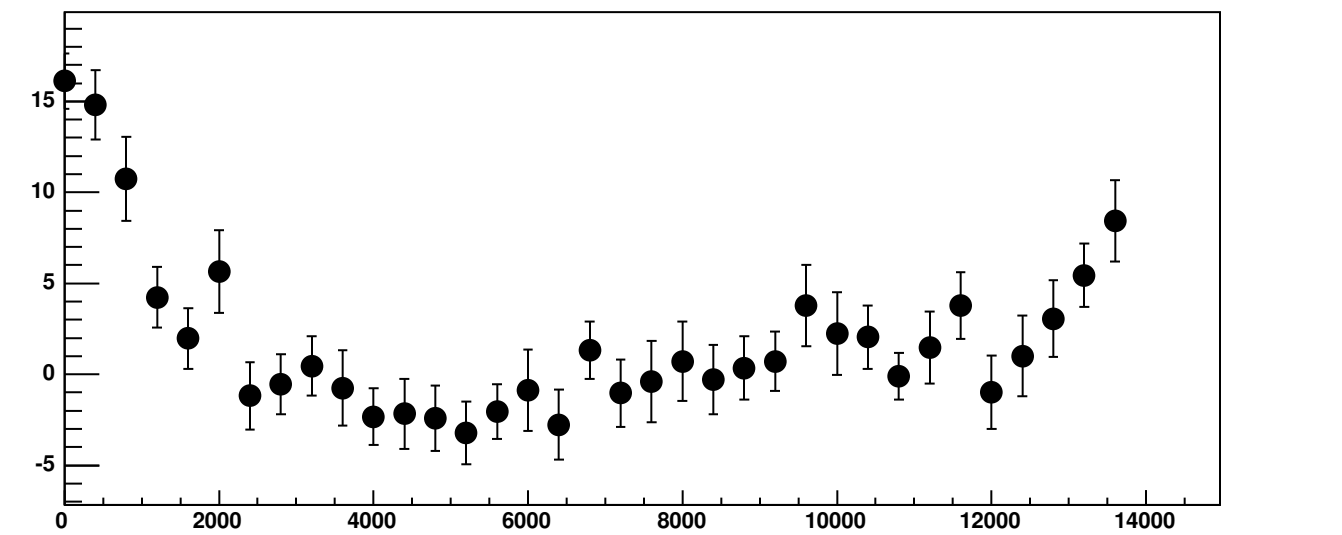
Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

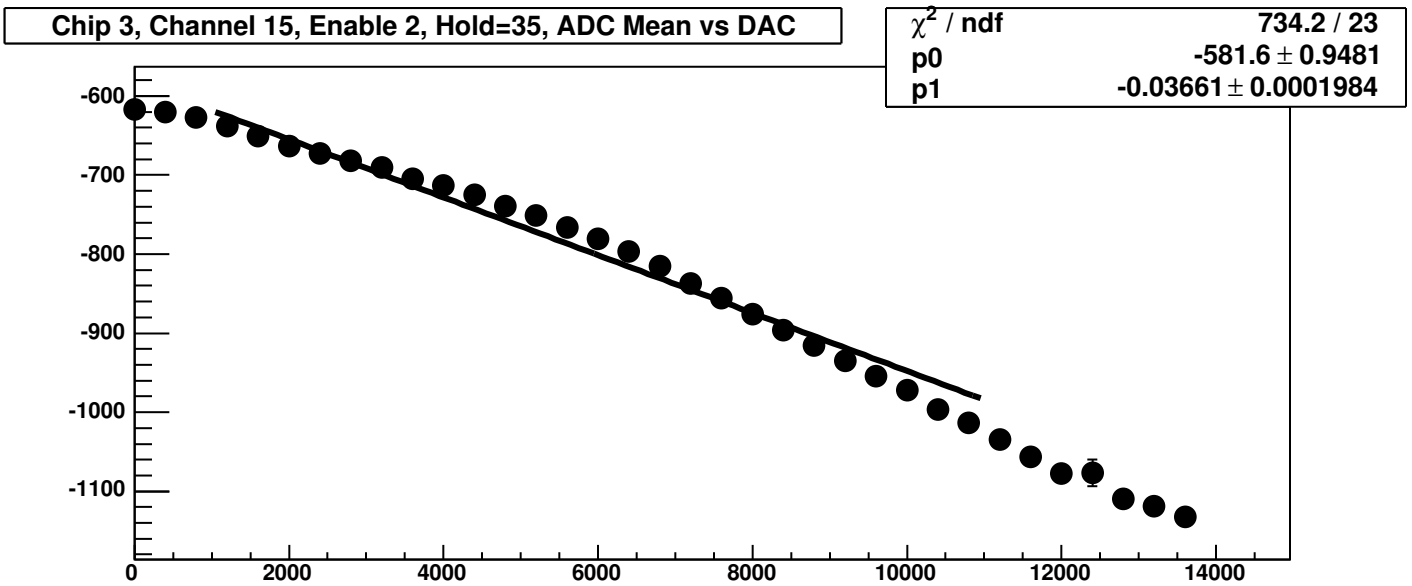


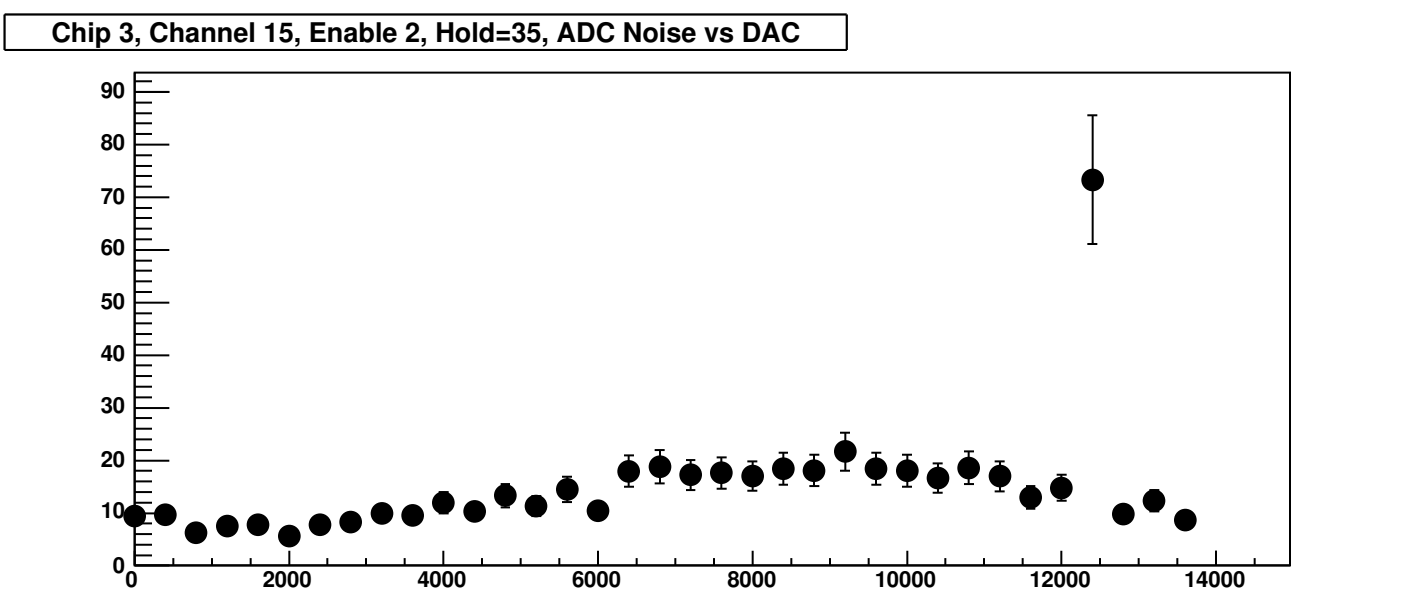
Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

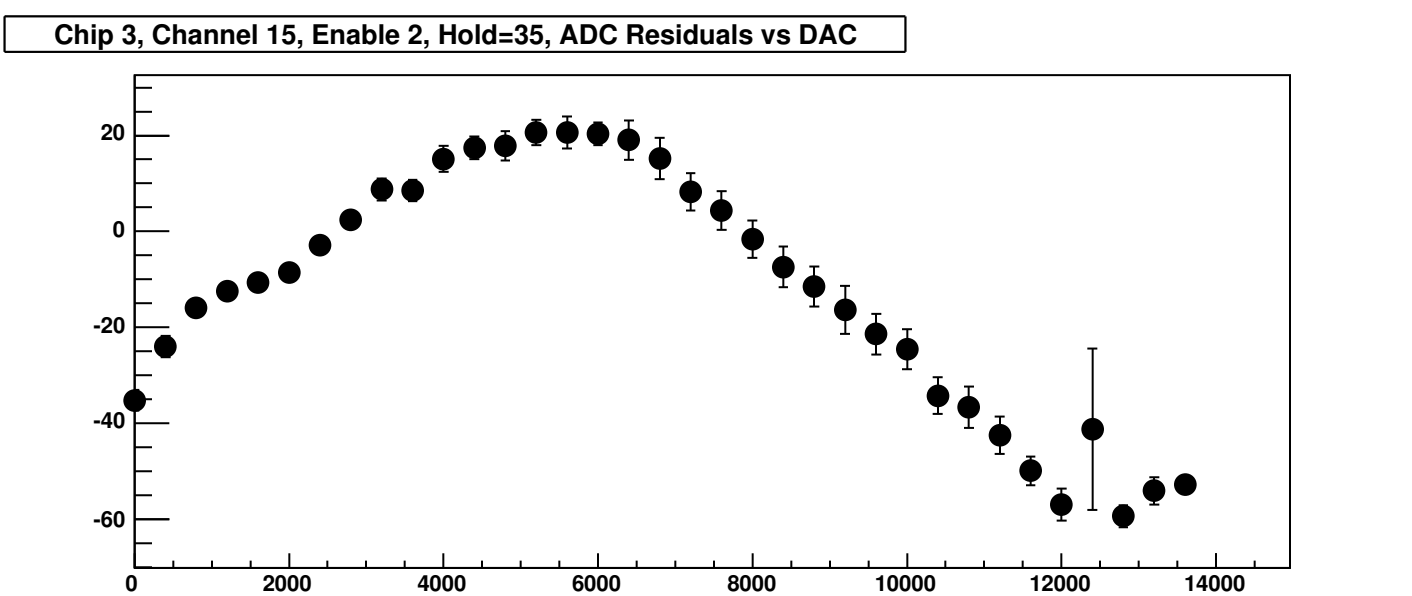


Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

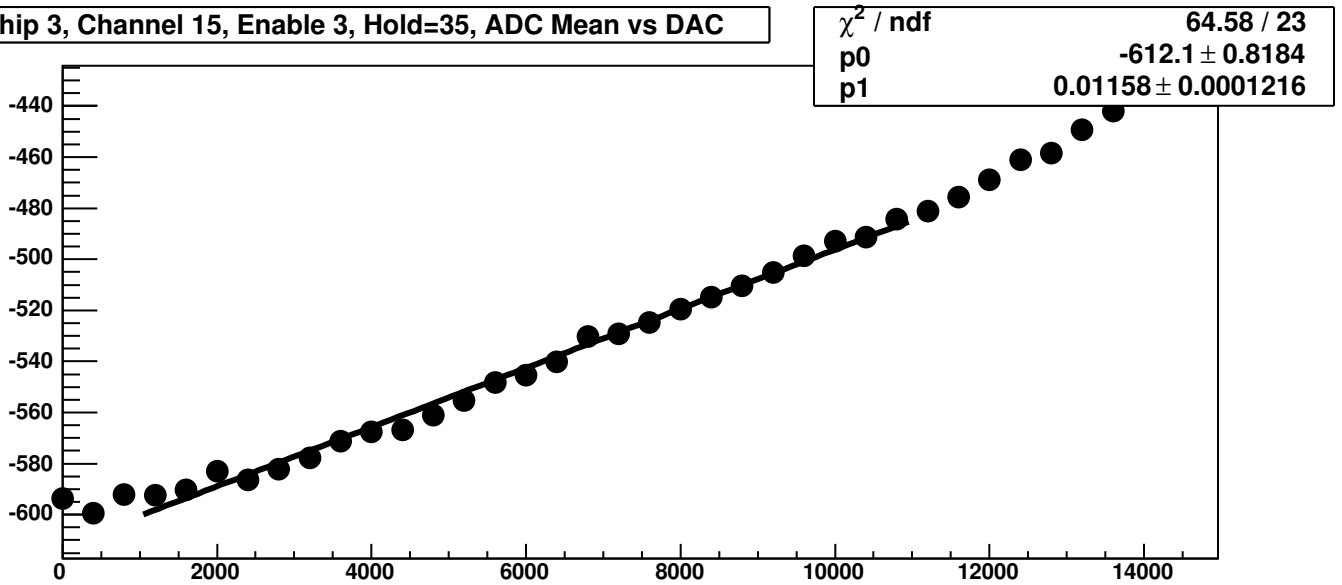



Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 734.2 / 23
p0 -581.6 ± 0.9481
p1 -0.03661 ± 0.0001984
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

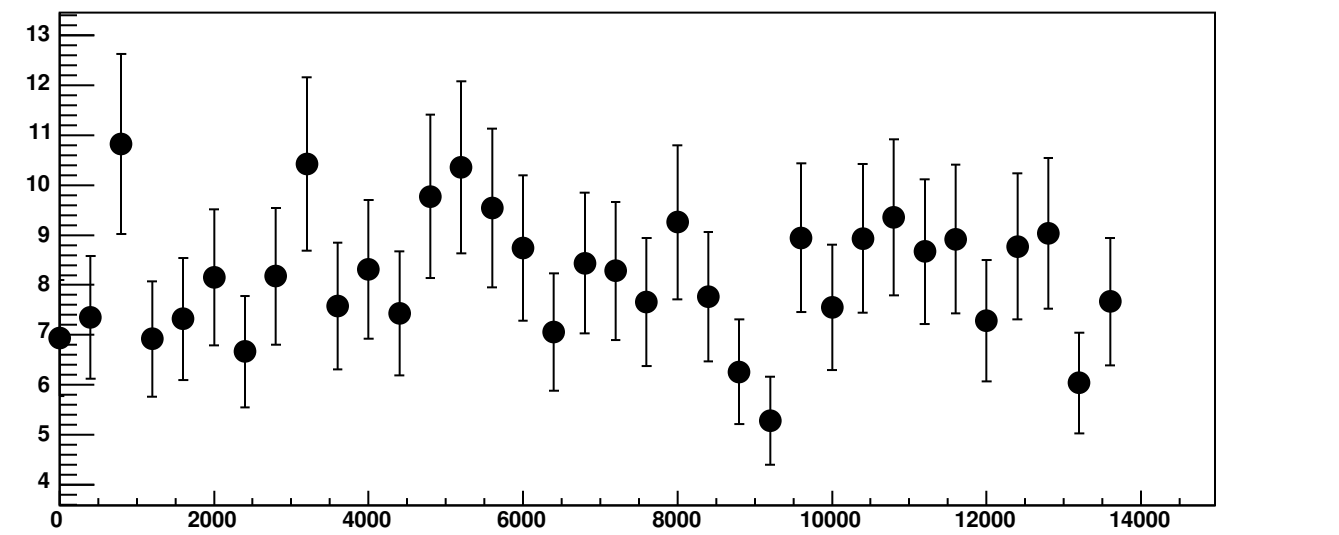

Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

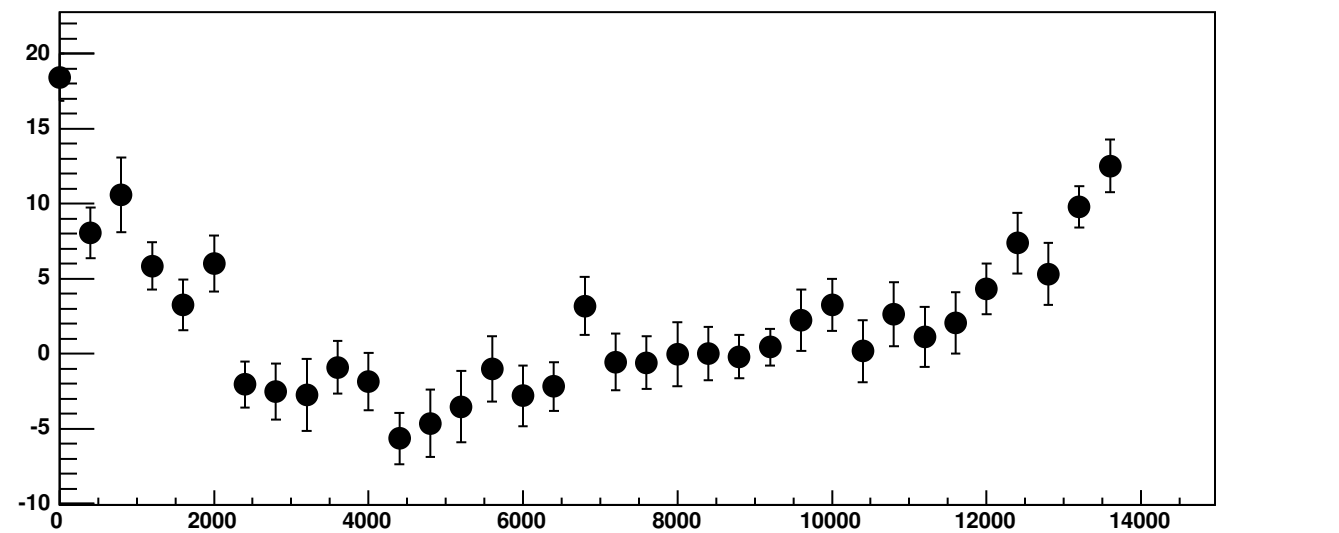
Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

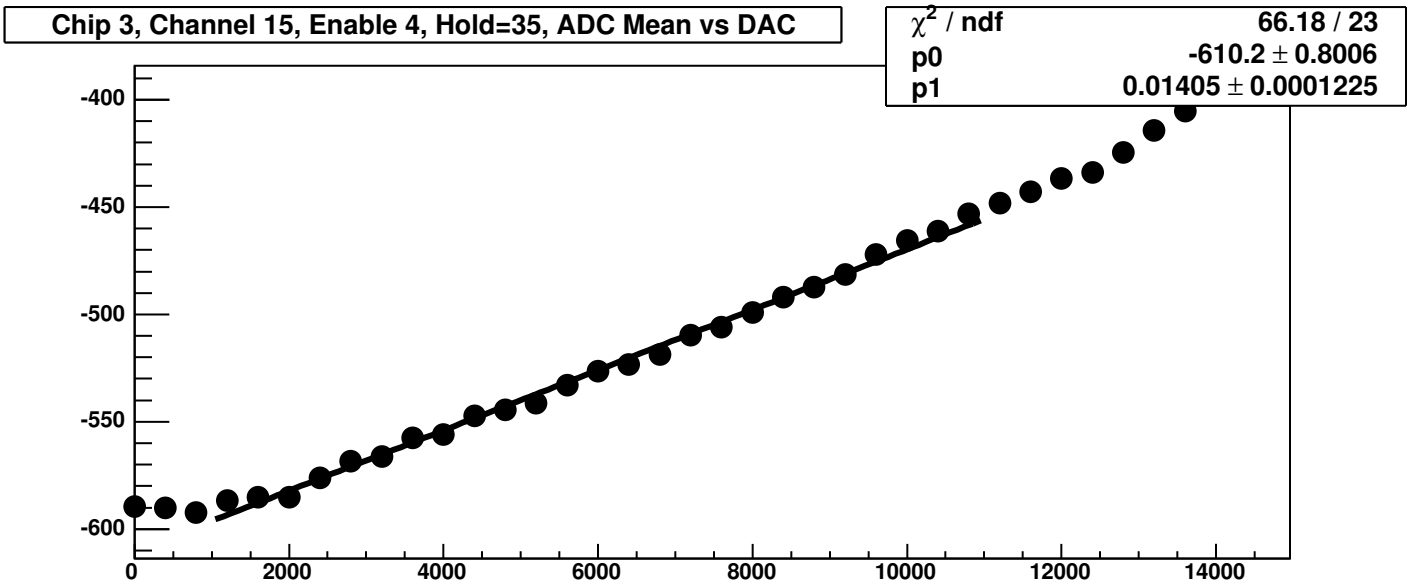


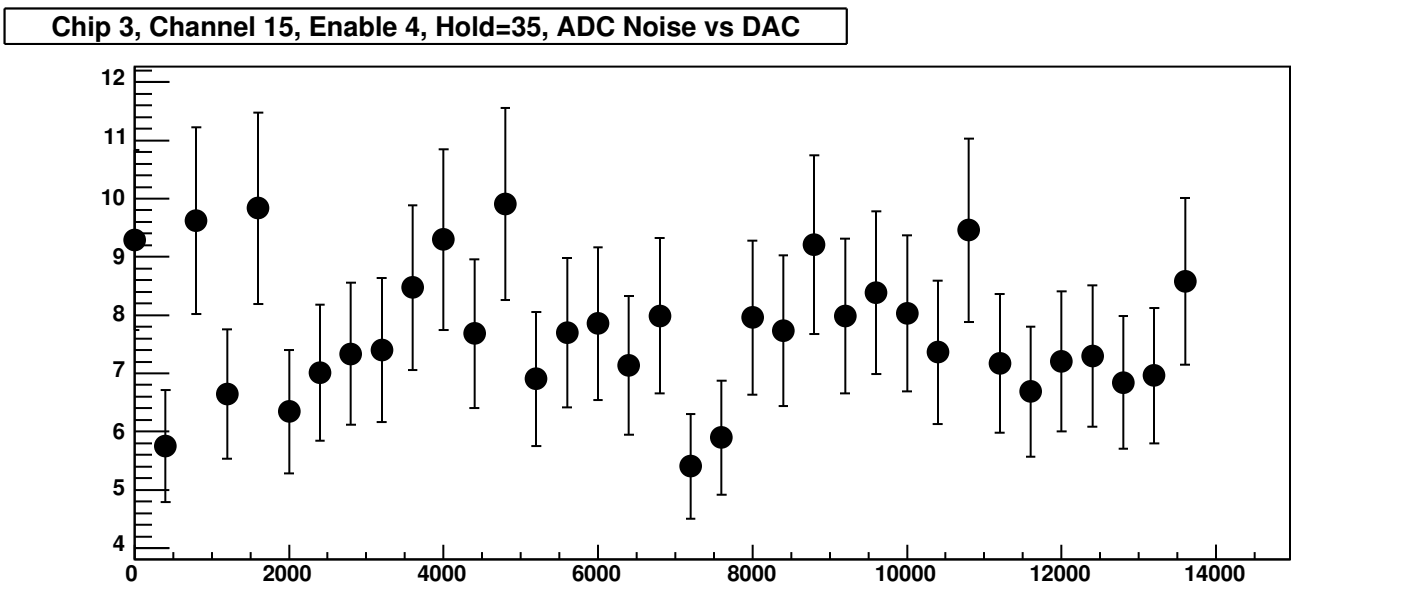
Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC

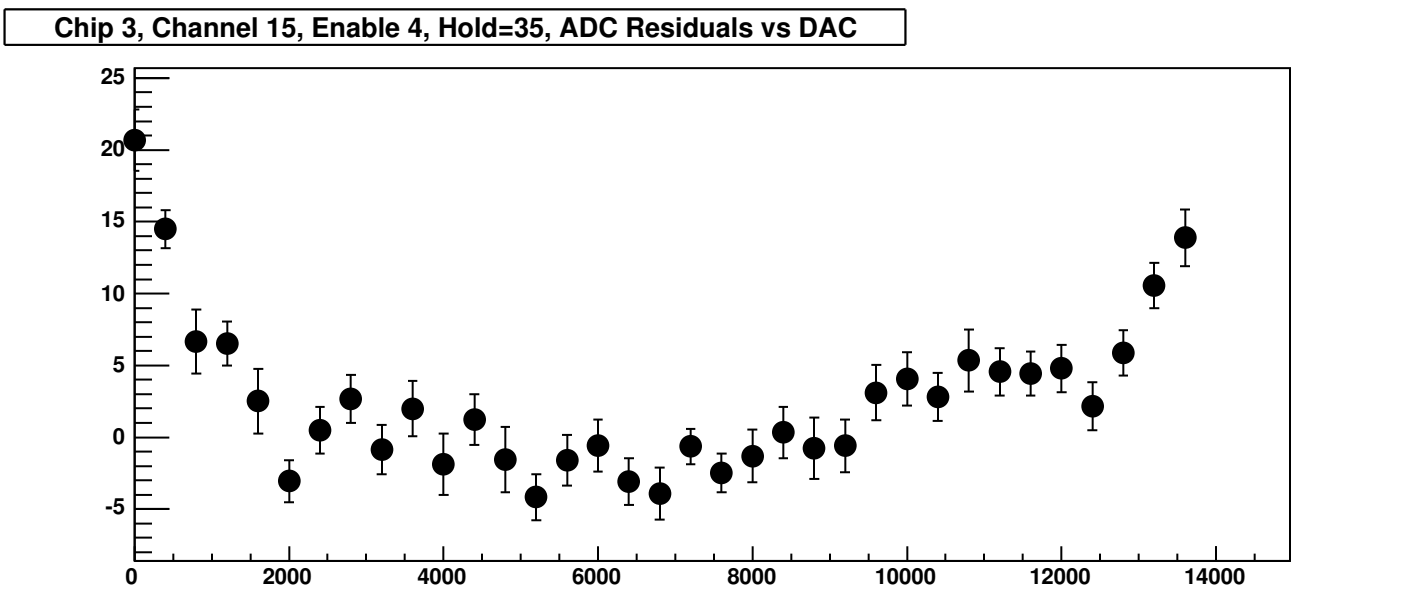


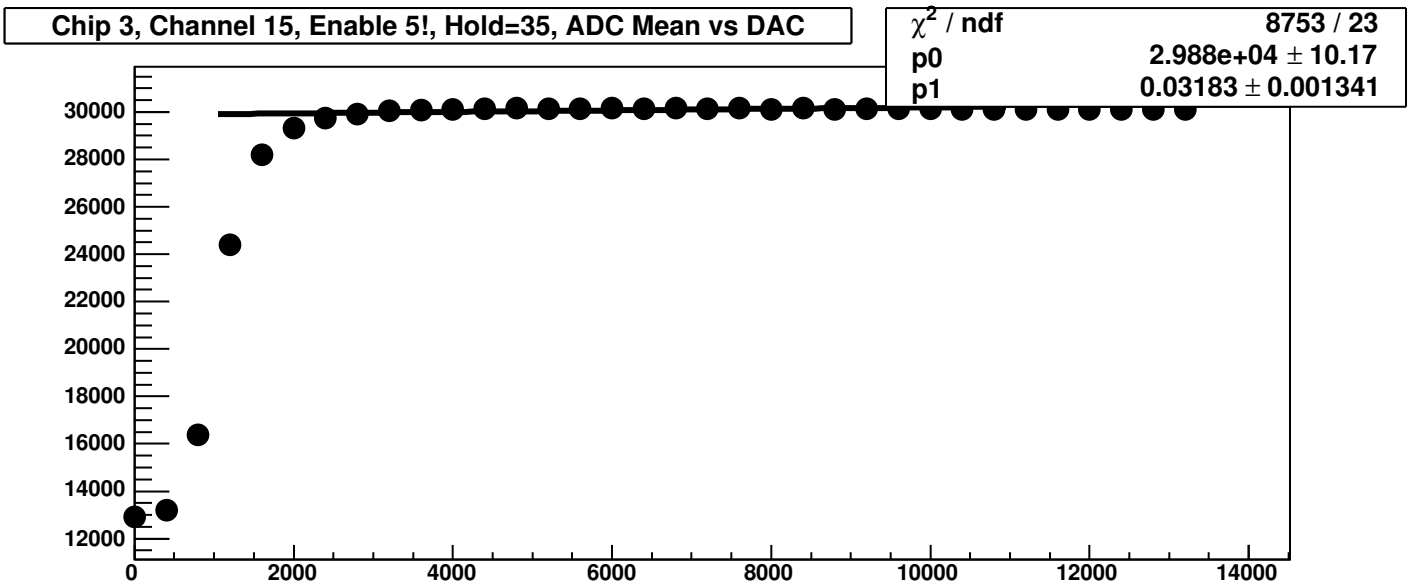
Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

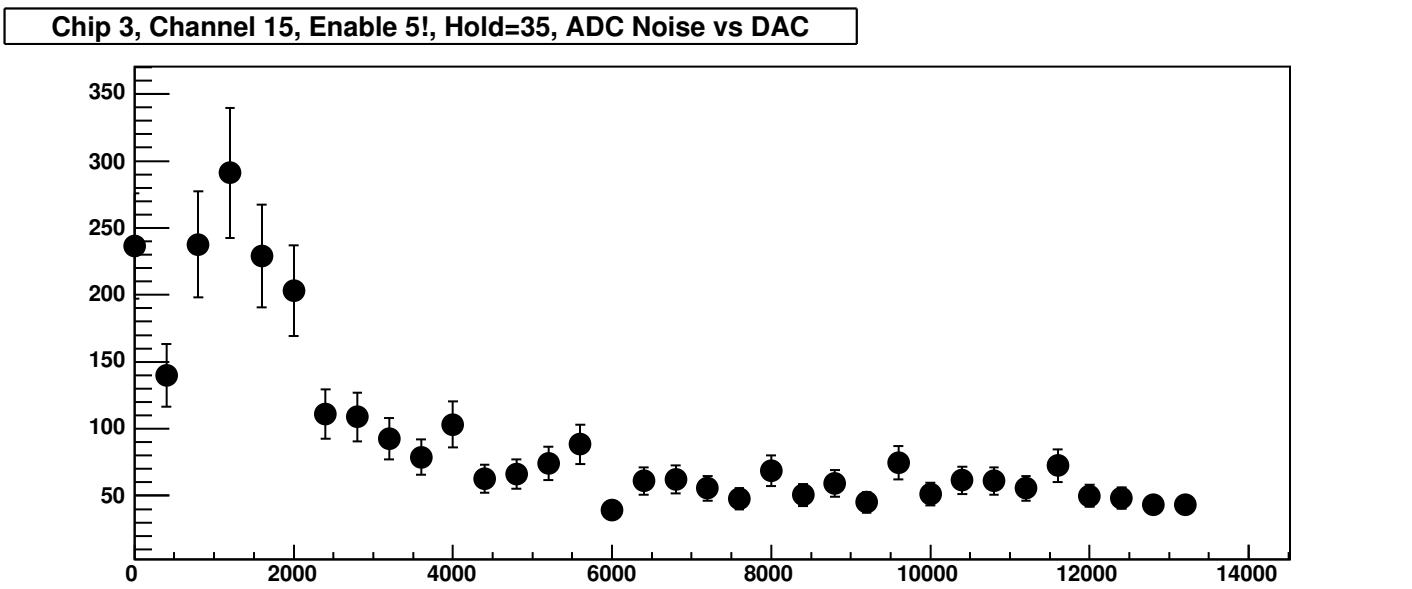


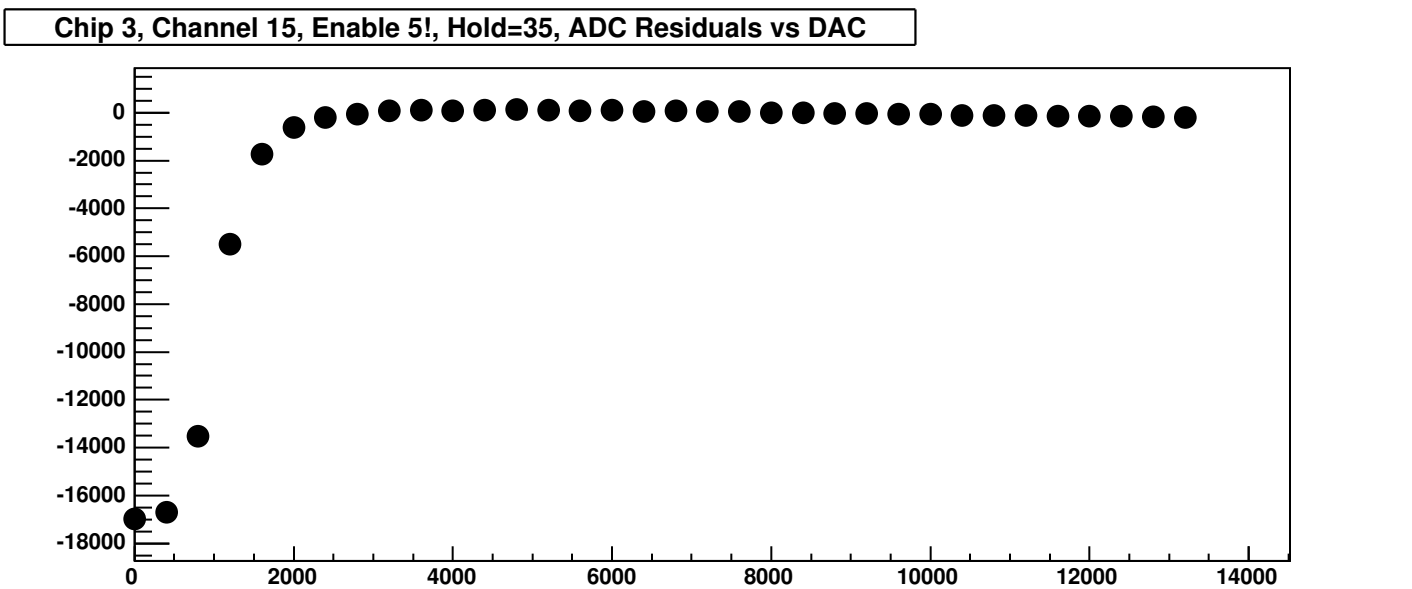

Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 66.18 / 23
p0 -610.2 ± 0.8006
p1 0.01405 ± 0.0001225
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

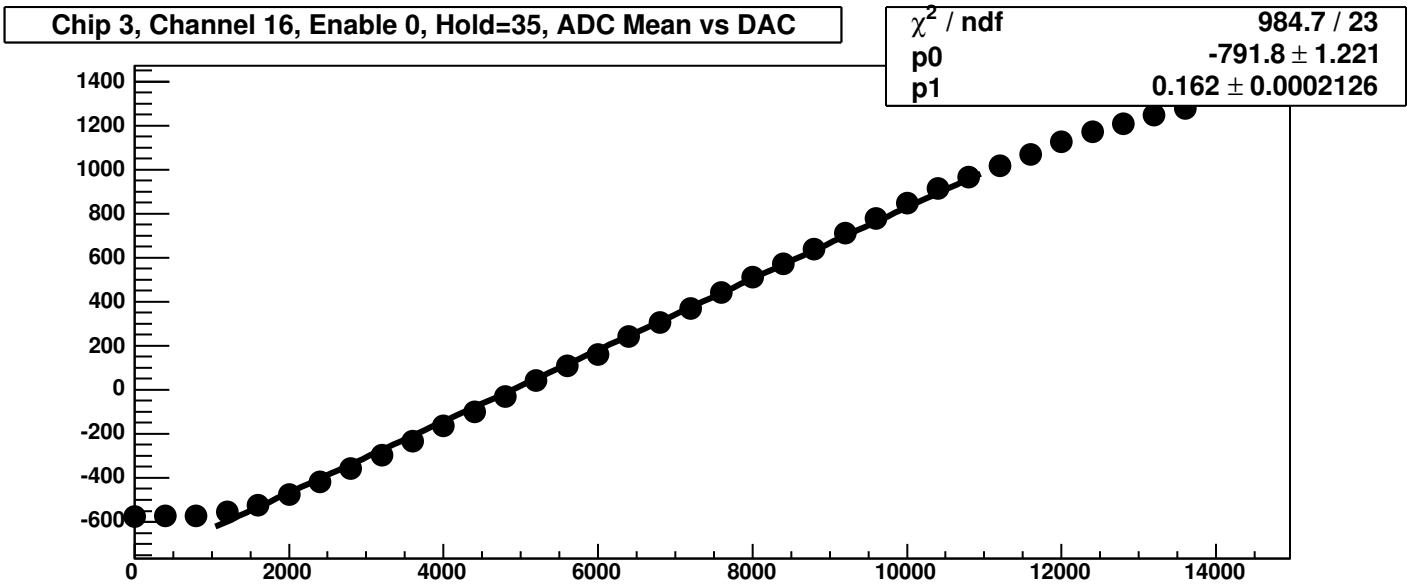

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

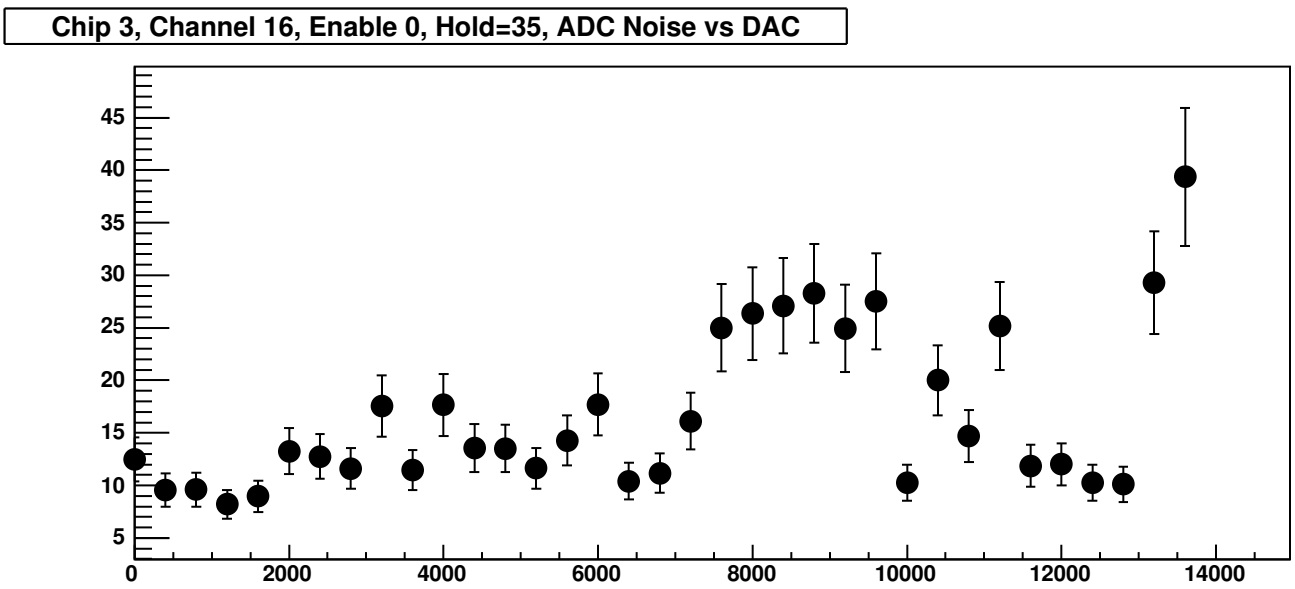

Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

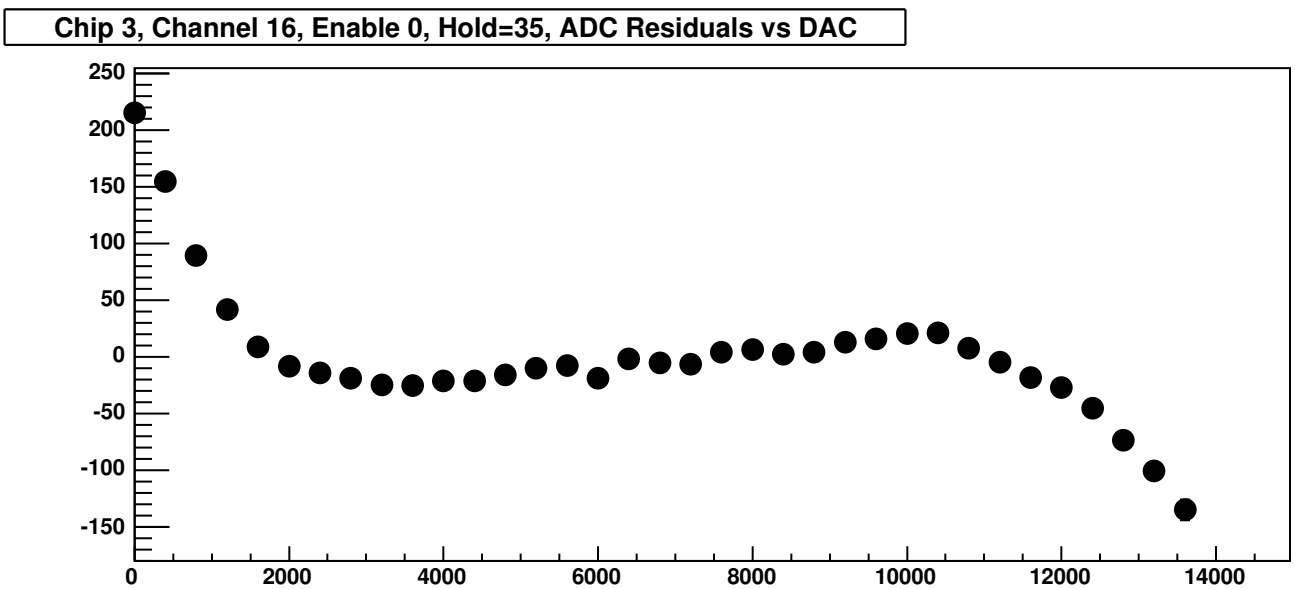

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 8753 / 23
p0 2.988e+04 ± 10.17
p1 0.03183 ± 0.001341
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
2000
4000
6000
8000
10000
12000
14000

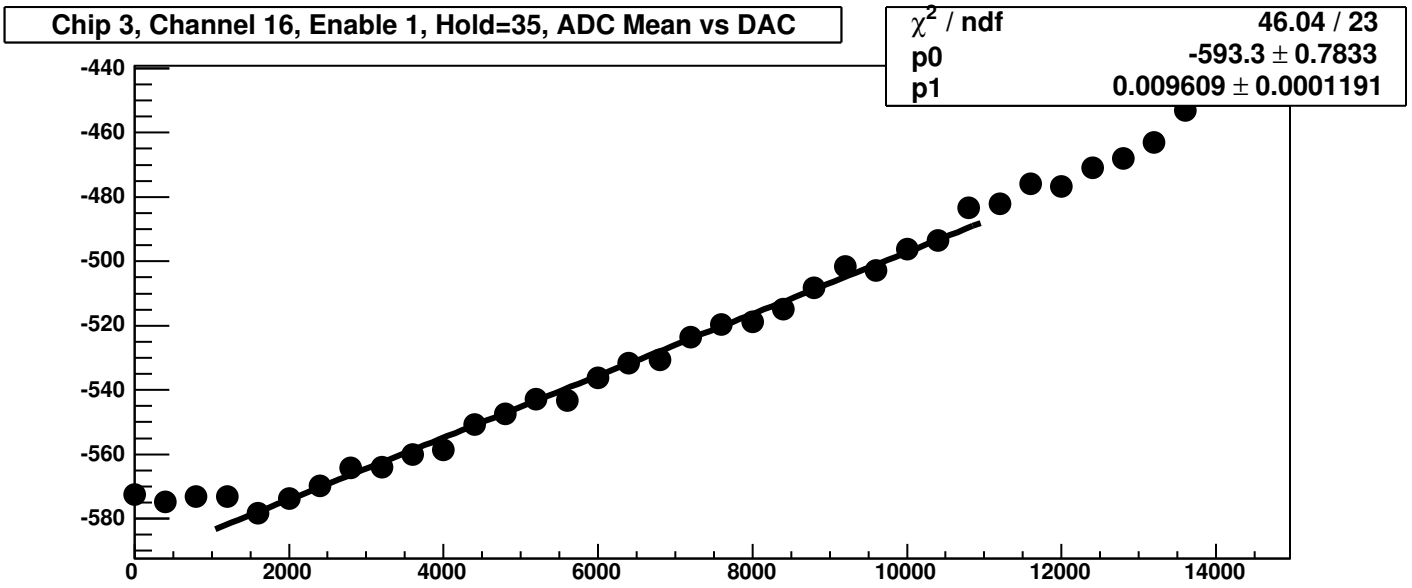

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

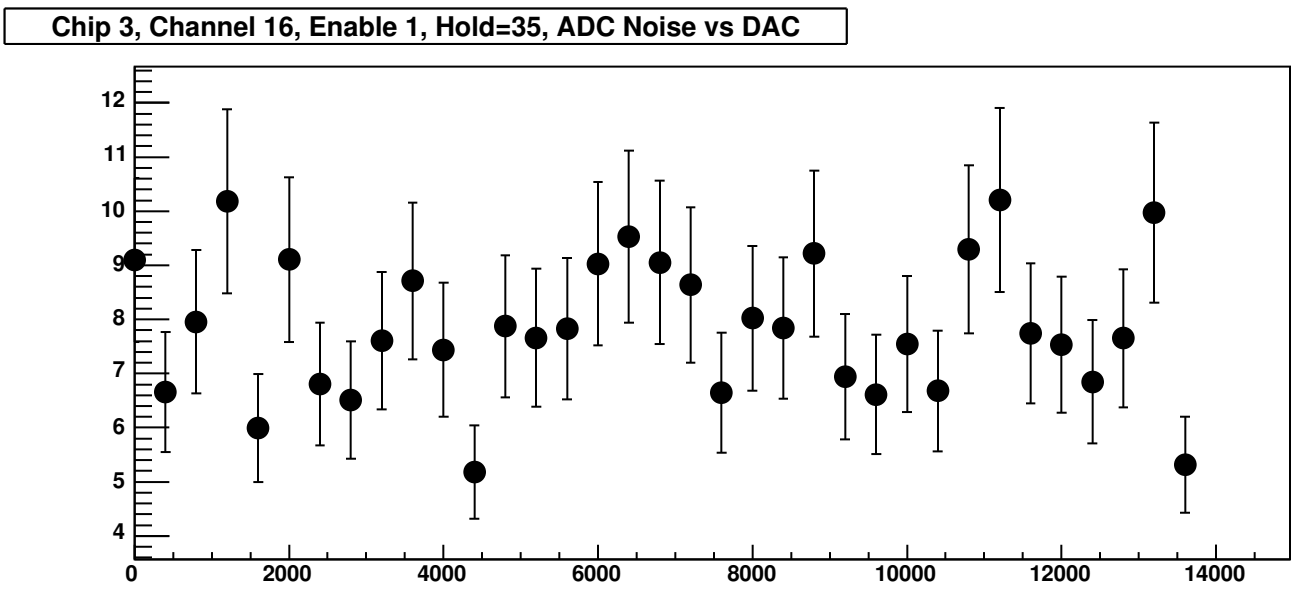

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
2000
4000
6000
8000
10000
12000
14000

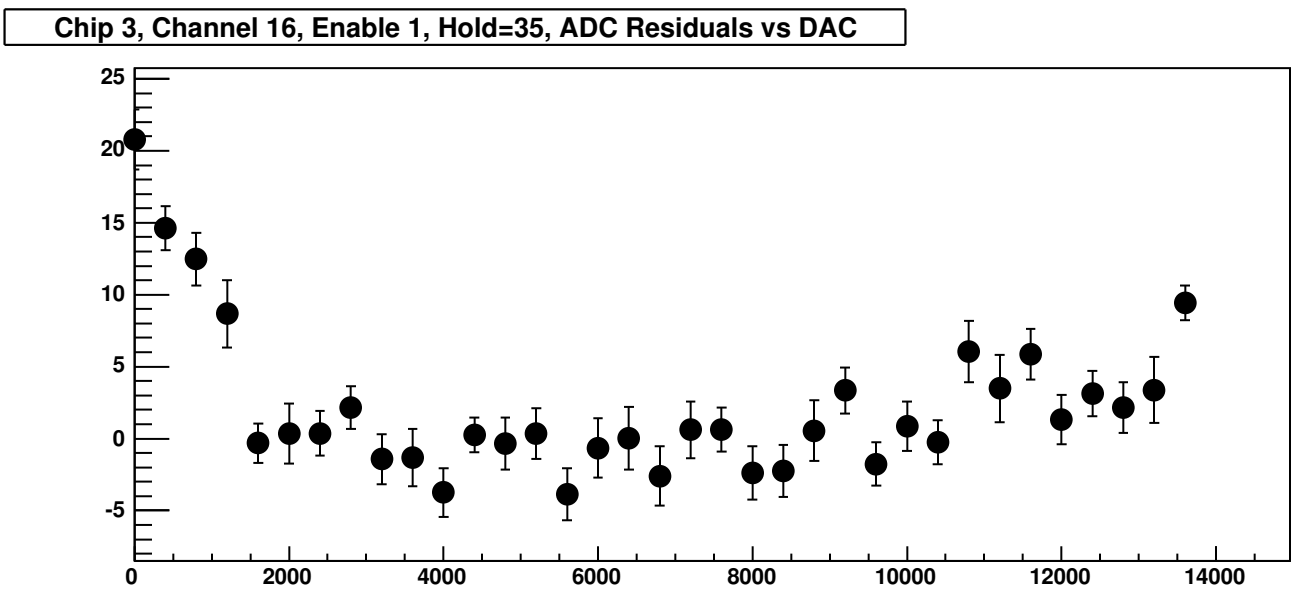

Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 984.7 / 23
p0 -791.8 ± 1.221
p1 0.162 ± 0.0002126
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

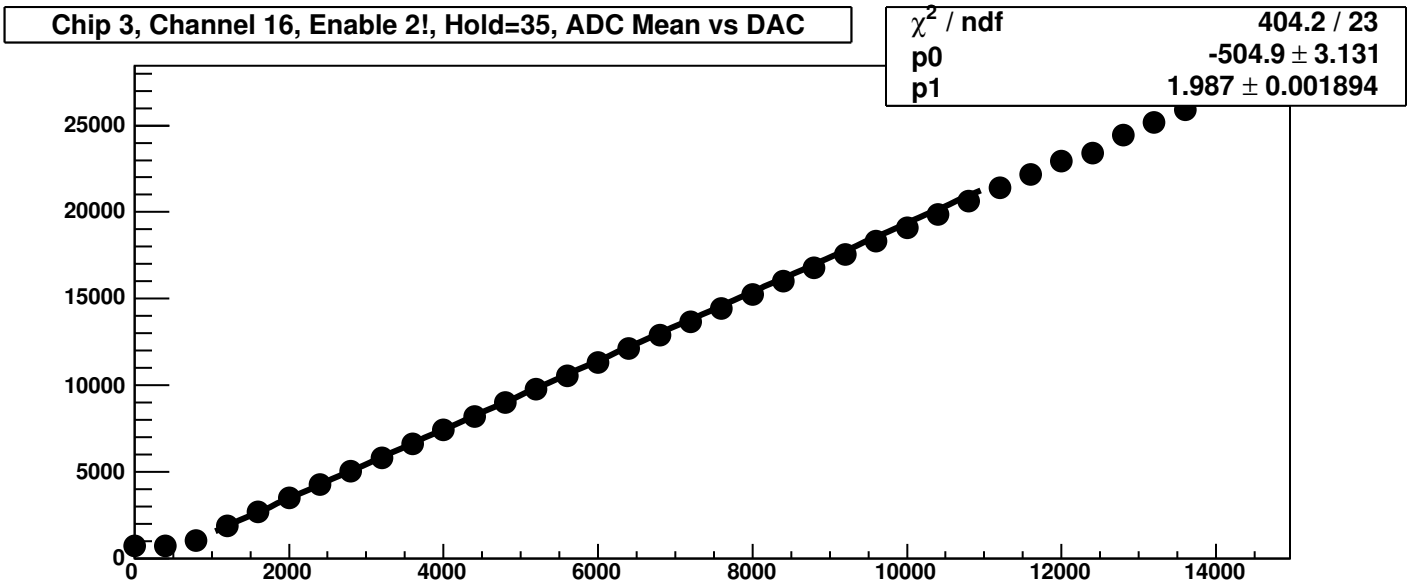

Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

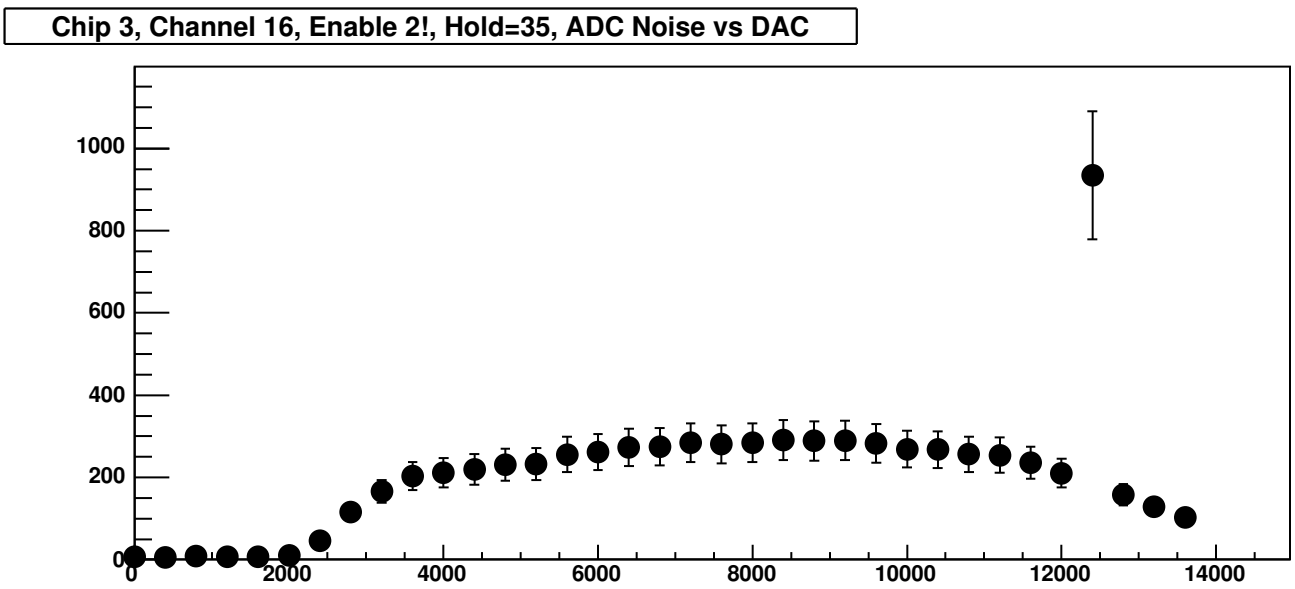

Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

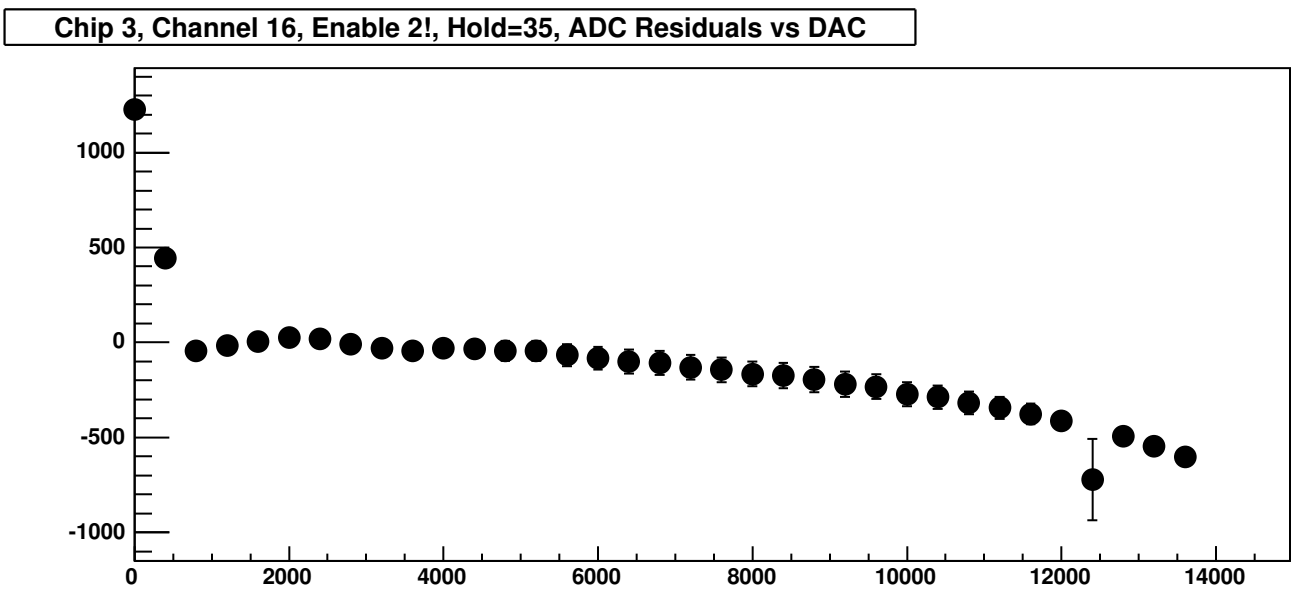

Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.04 / 23
p0 -593.3 ± 0.7833
p1 0.009609 ± 0.0001191
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

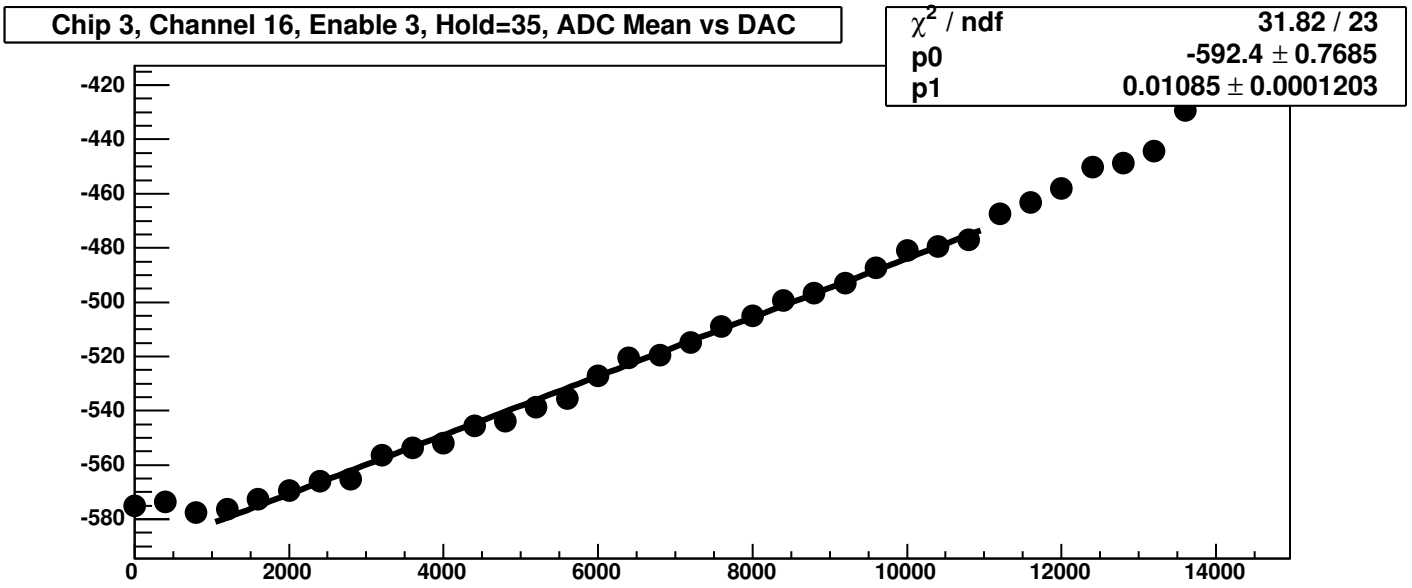

Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

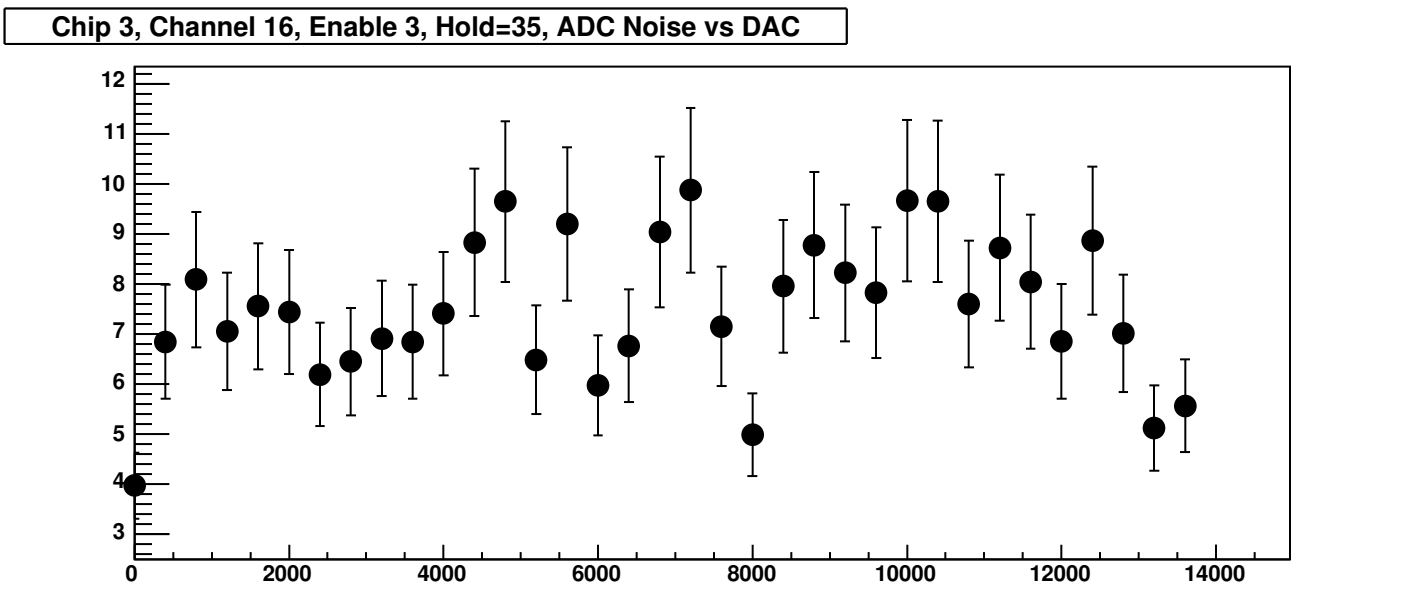

Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

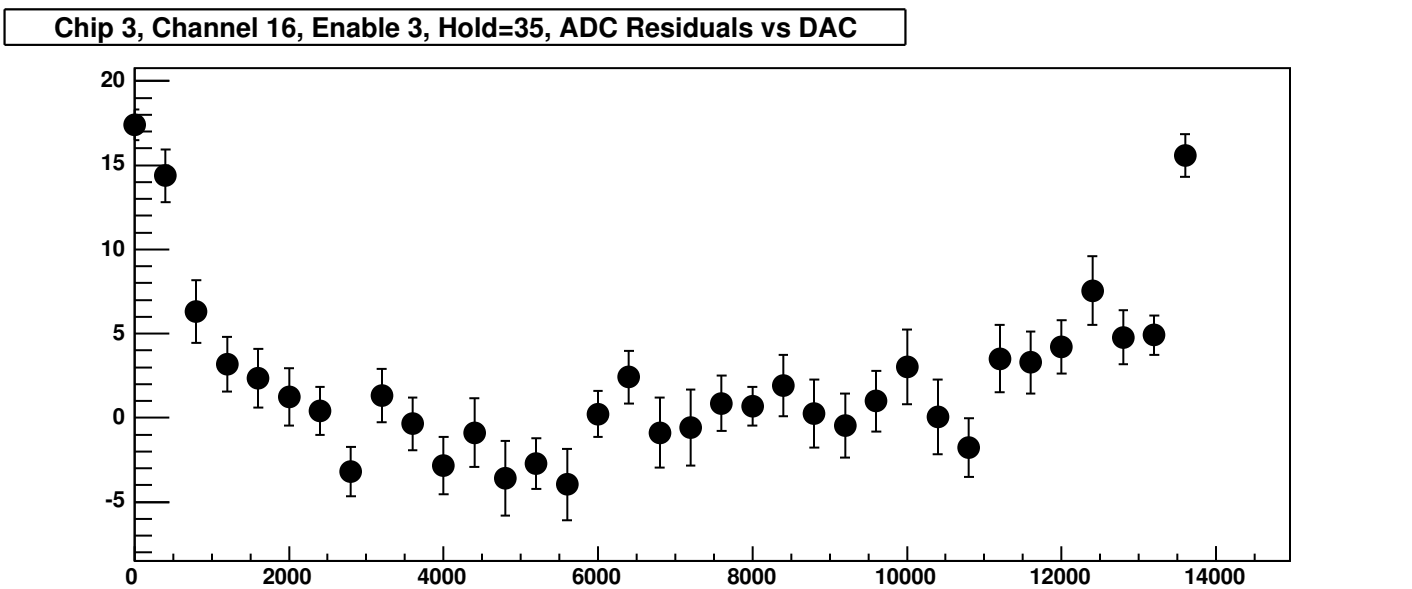

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 404.2 / 23
p0 -504.9 ± 3.131
p1 1.987 ± 0.001894
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

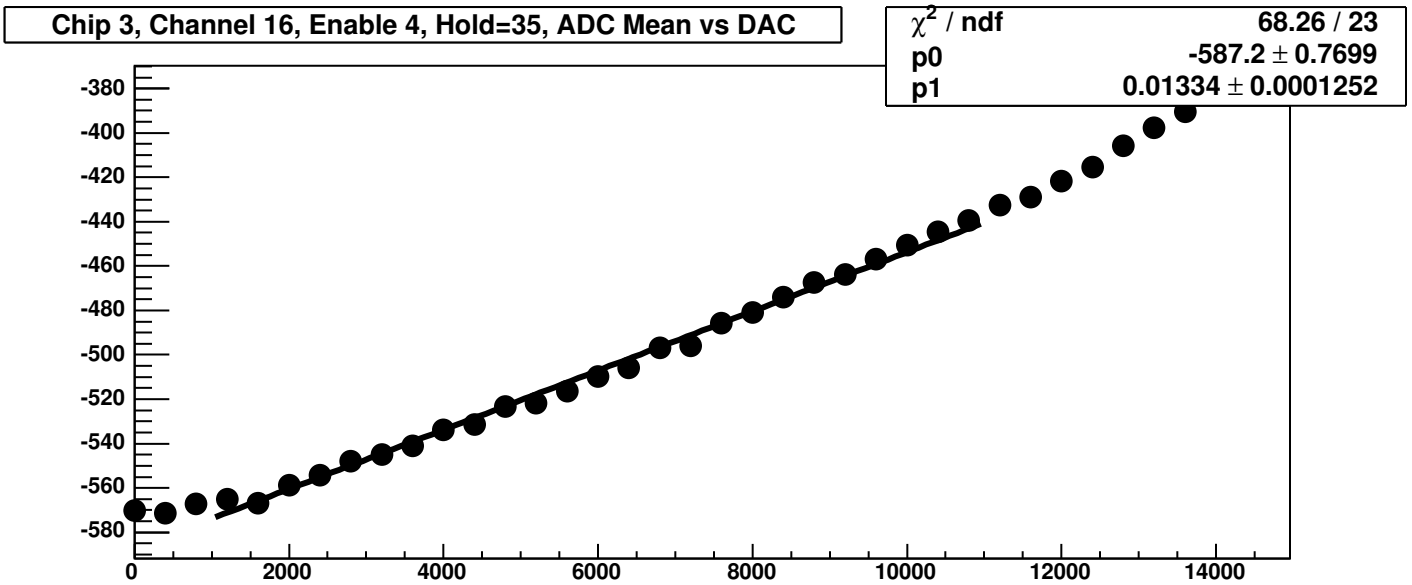

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

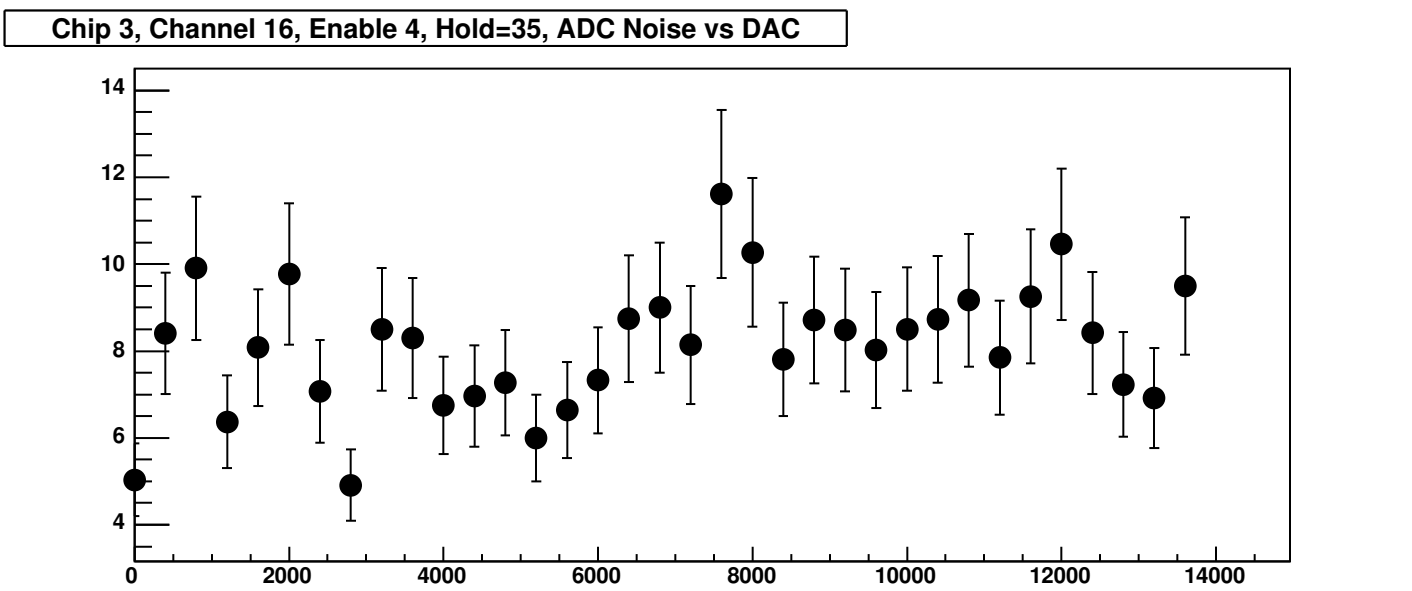

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

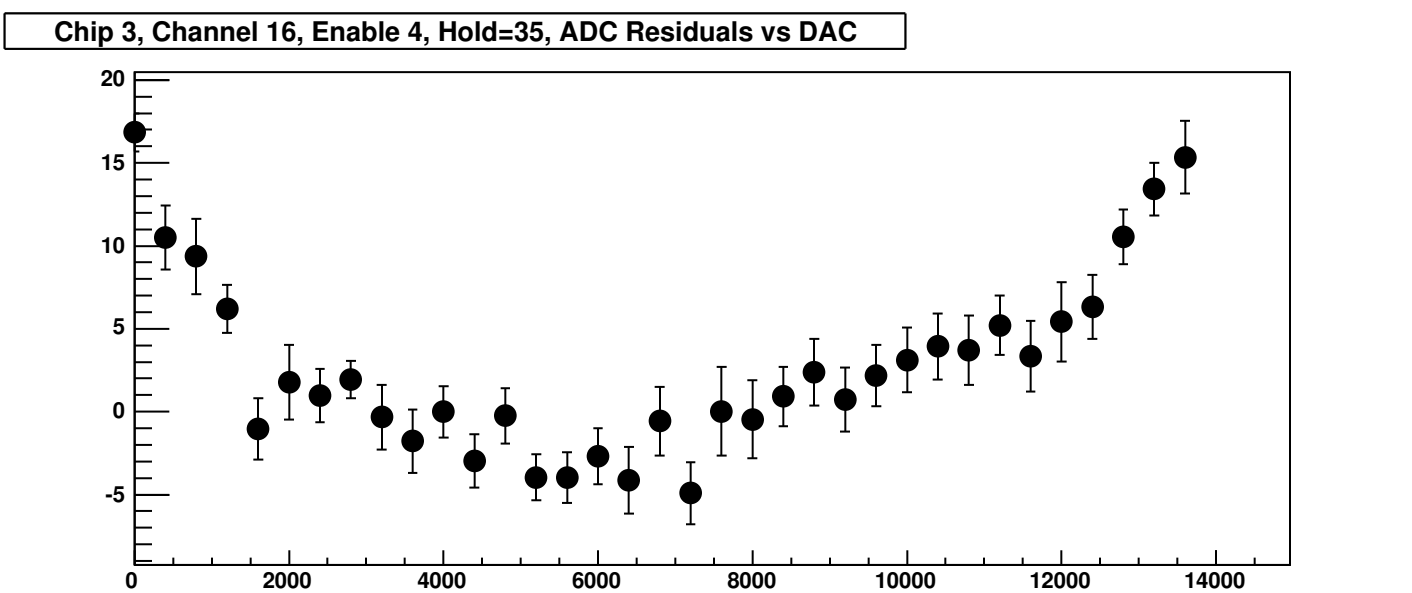

Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.82 / 23
p0 -592.4 ± 0.7685
p1 0.01085 ± 0.0001203
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

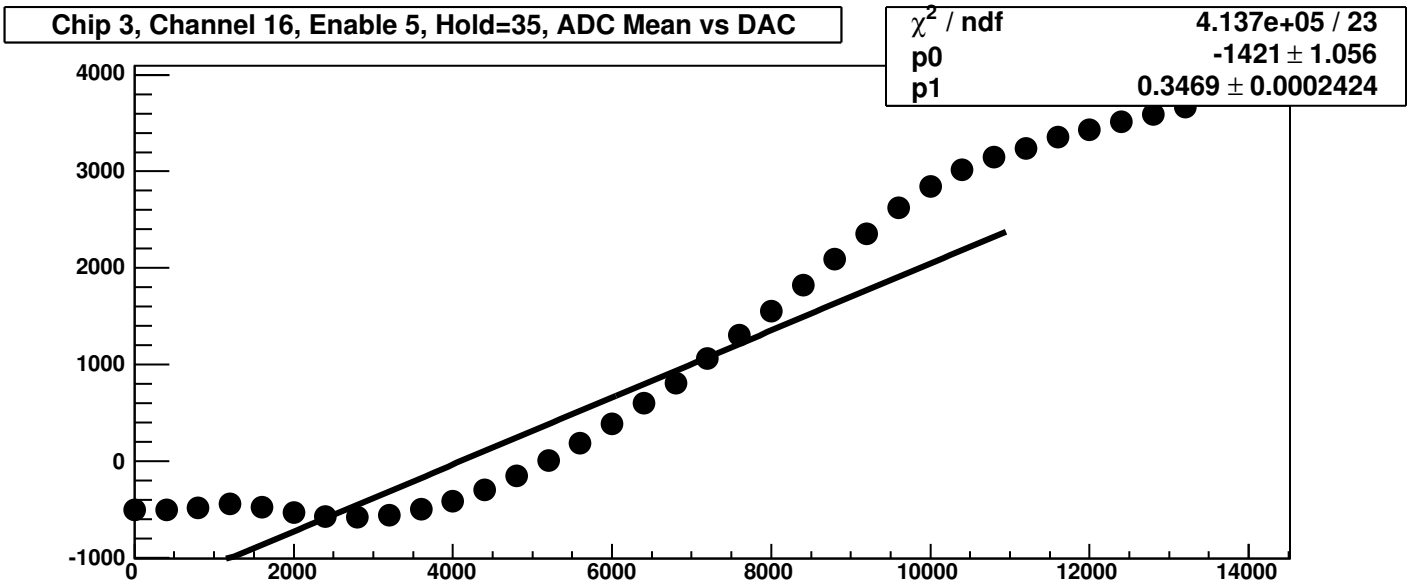

Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

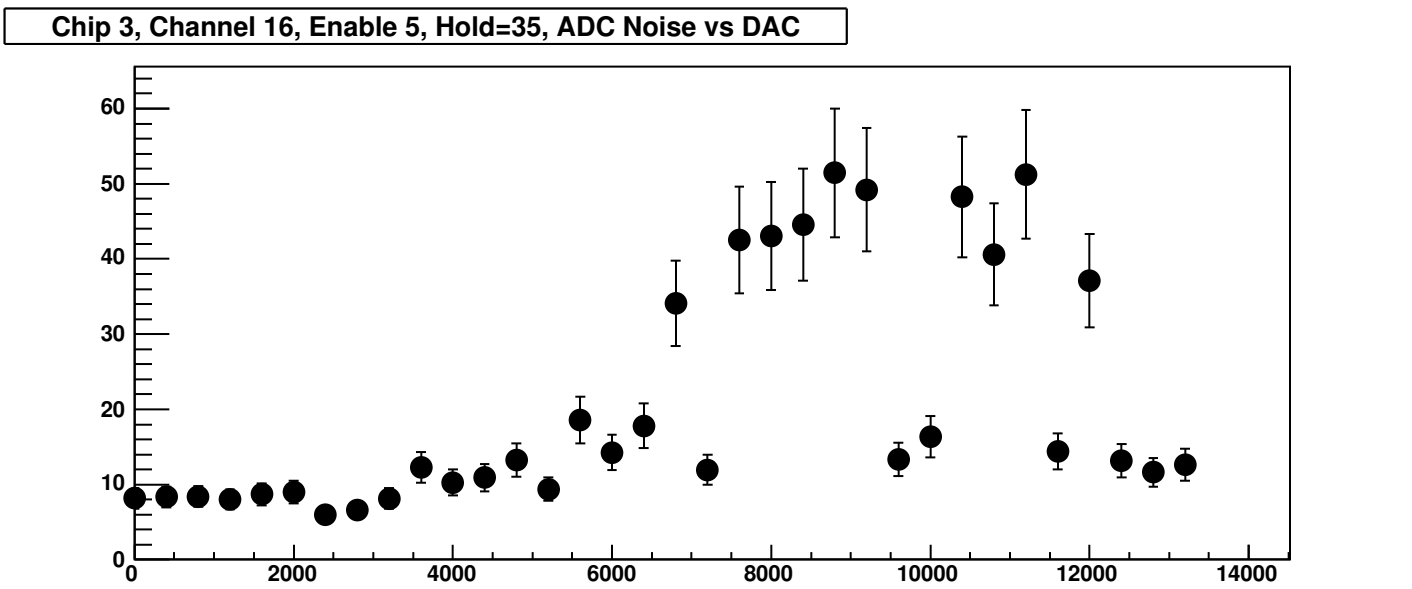

Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

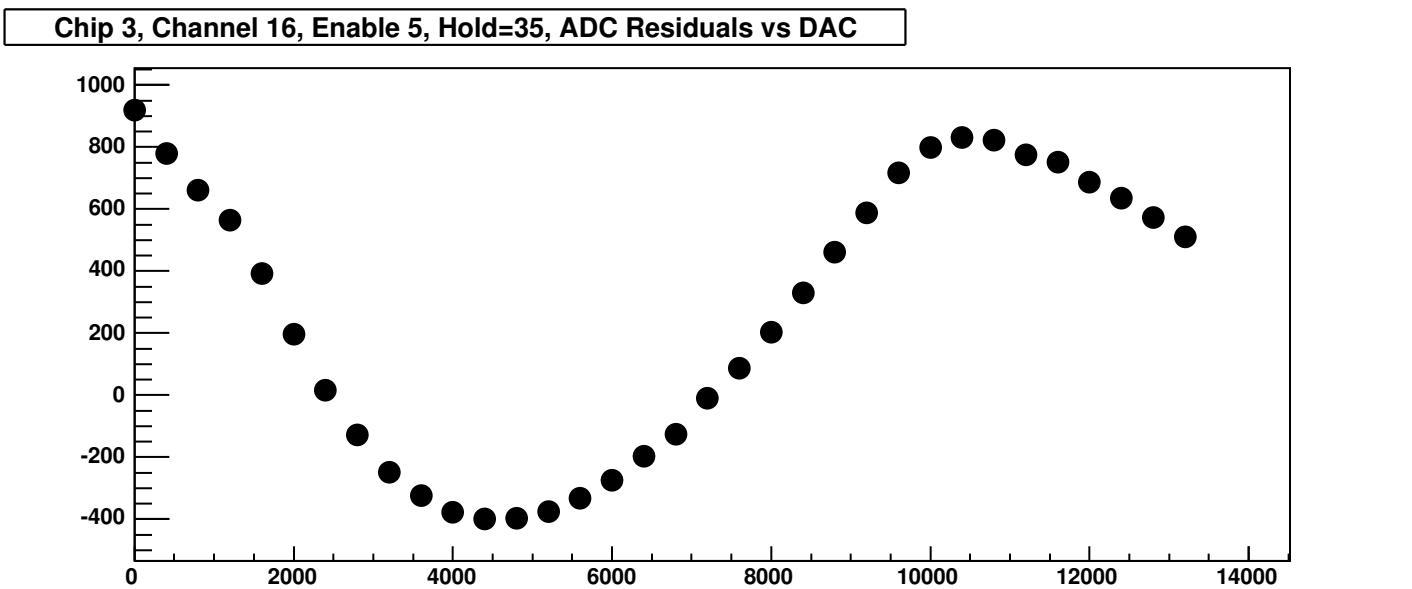

Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 68.26 / 23
p0 -587.2 ± 0.7699
p1 0.01334 ± 0.0001252
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

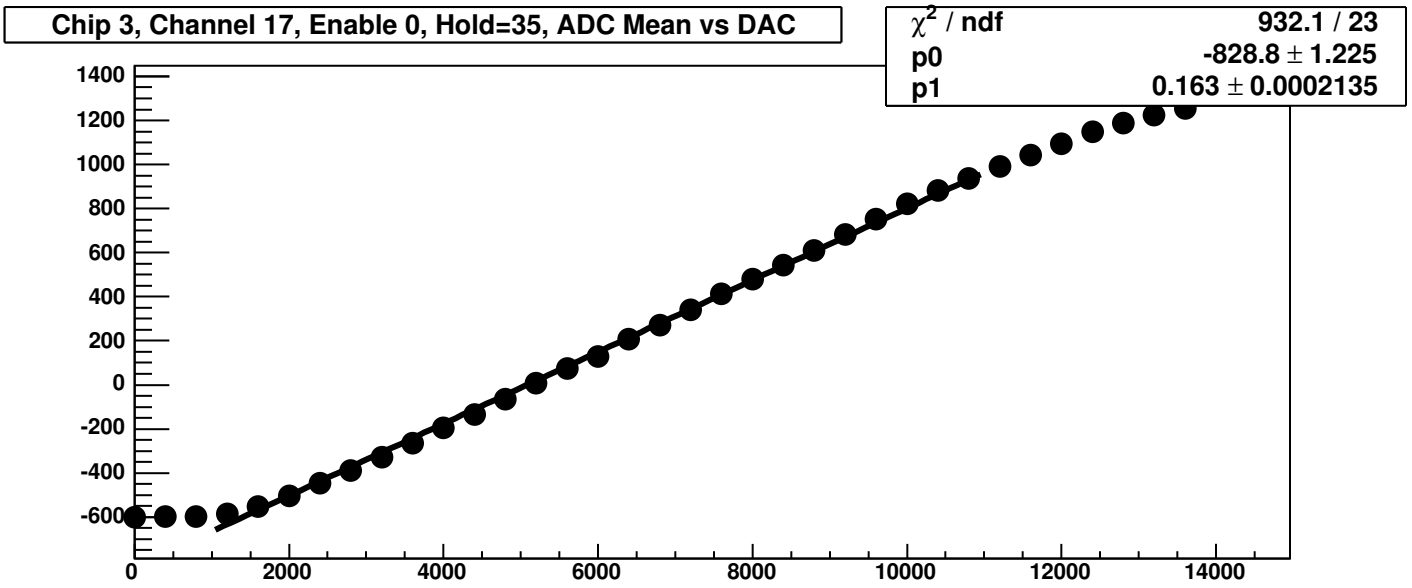

Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

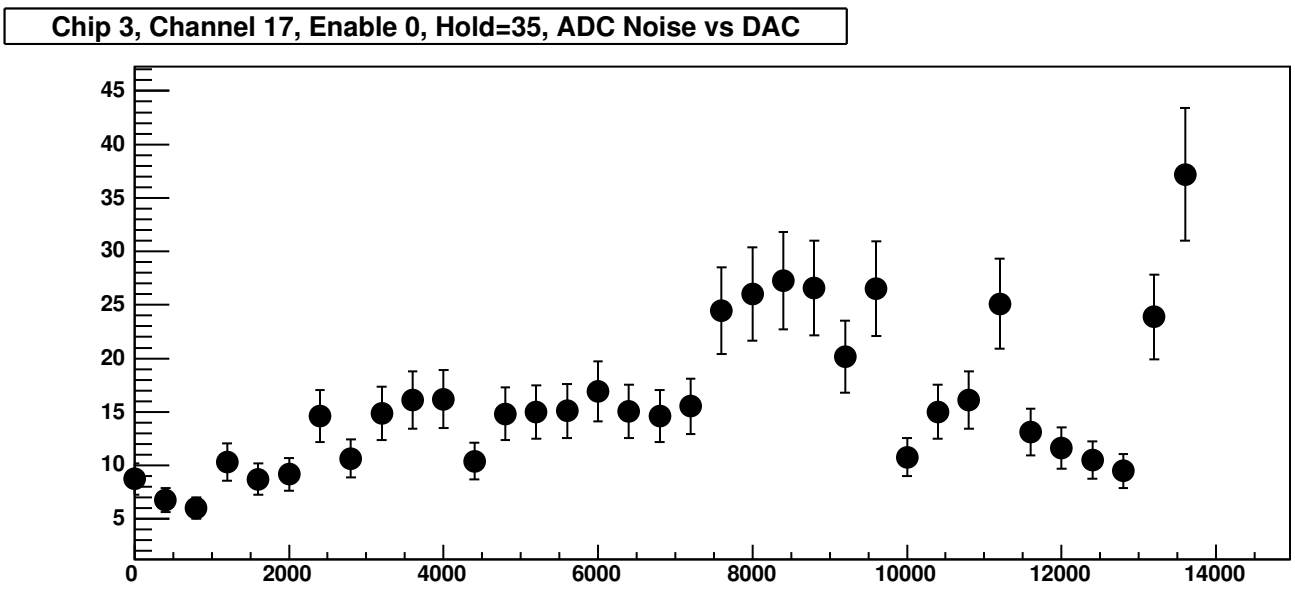

Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

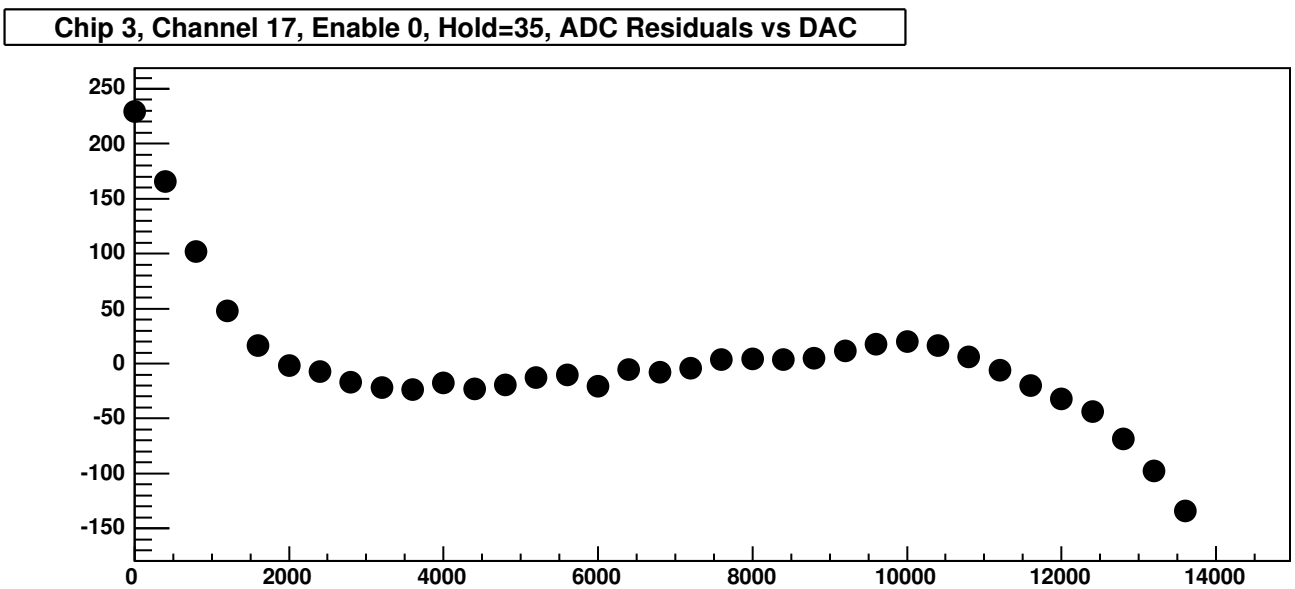

Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.137e+05 / 23
p0 -1421 ± 1.056
p1 0.3469 ± 0.0002424
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

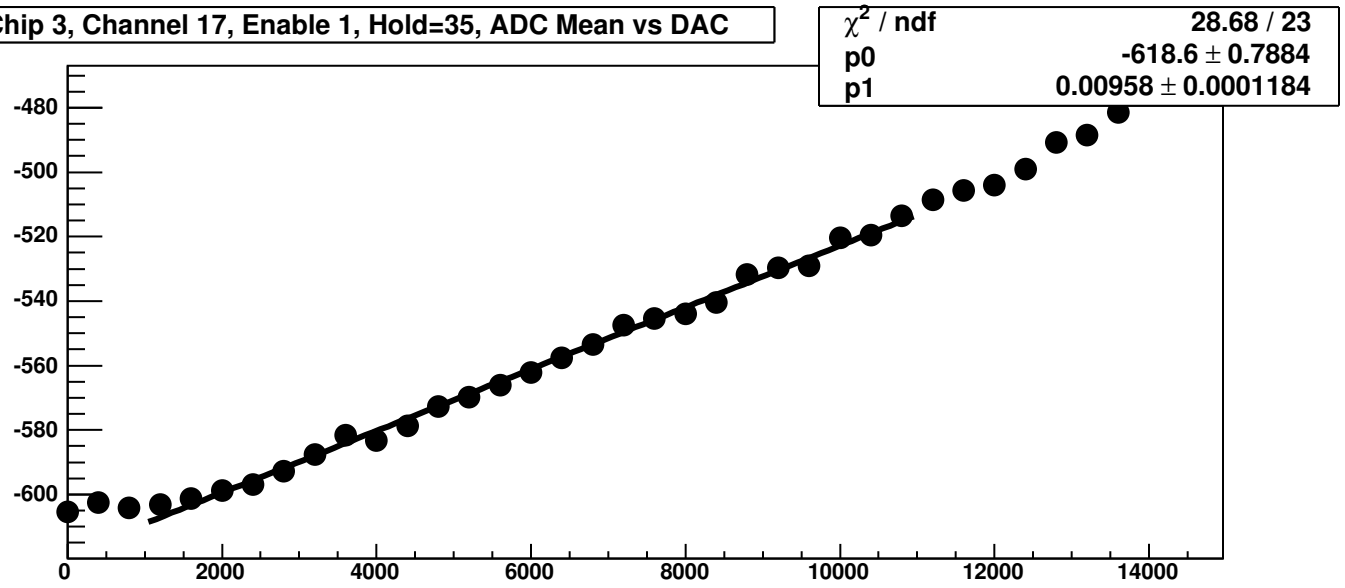

Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 932.1 / 23
p0 -828.8 ± 1.225
p1 0.163 ± 0.0002135
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

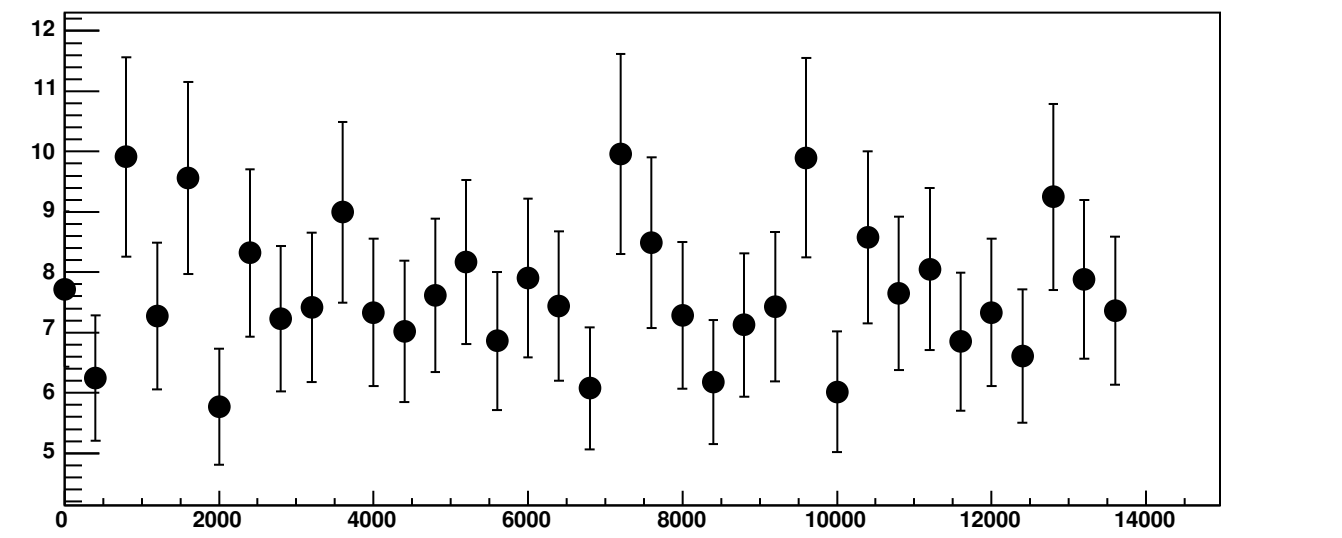

Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

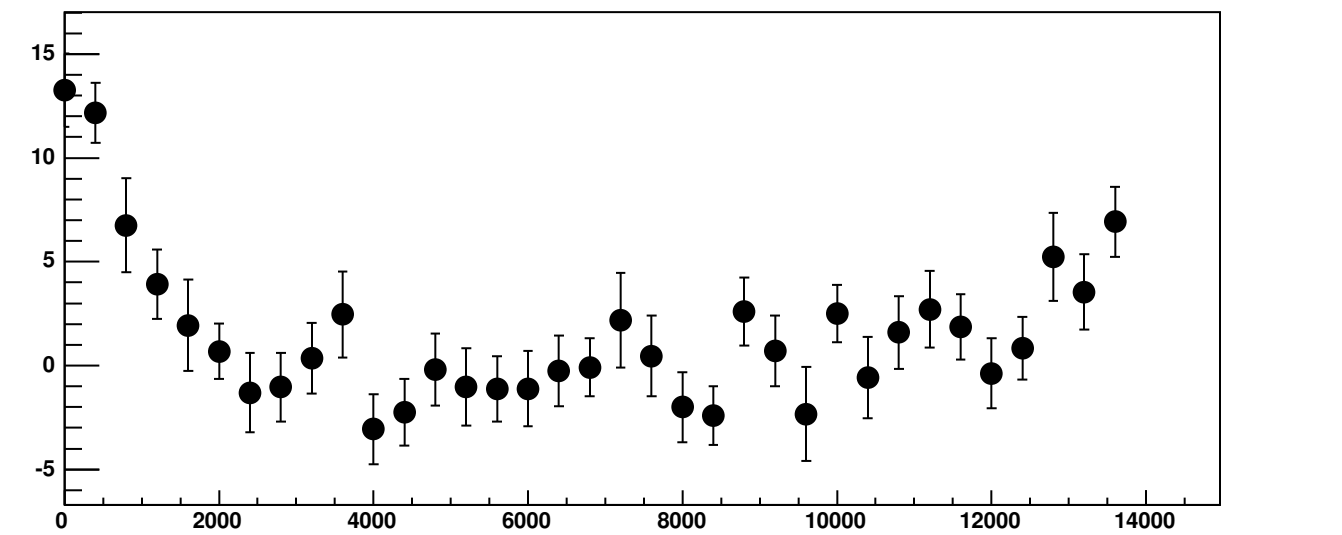
Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

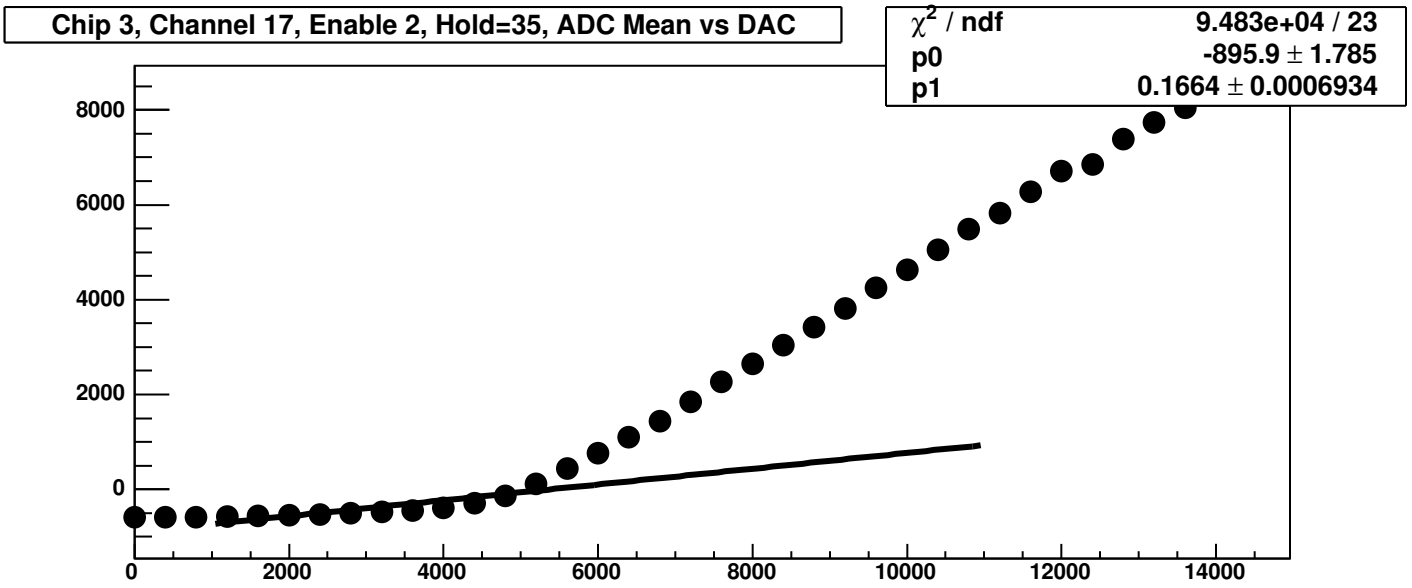


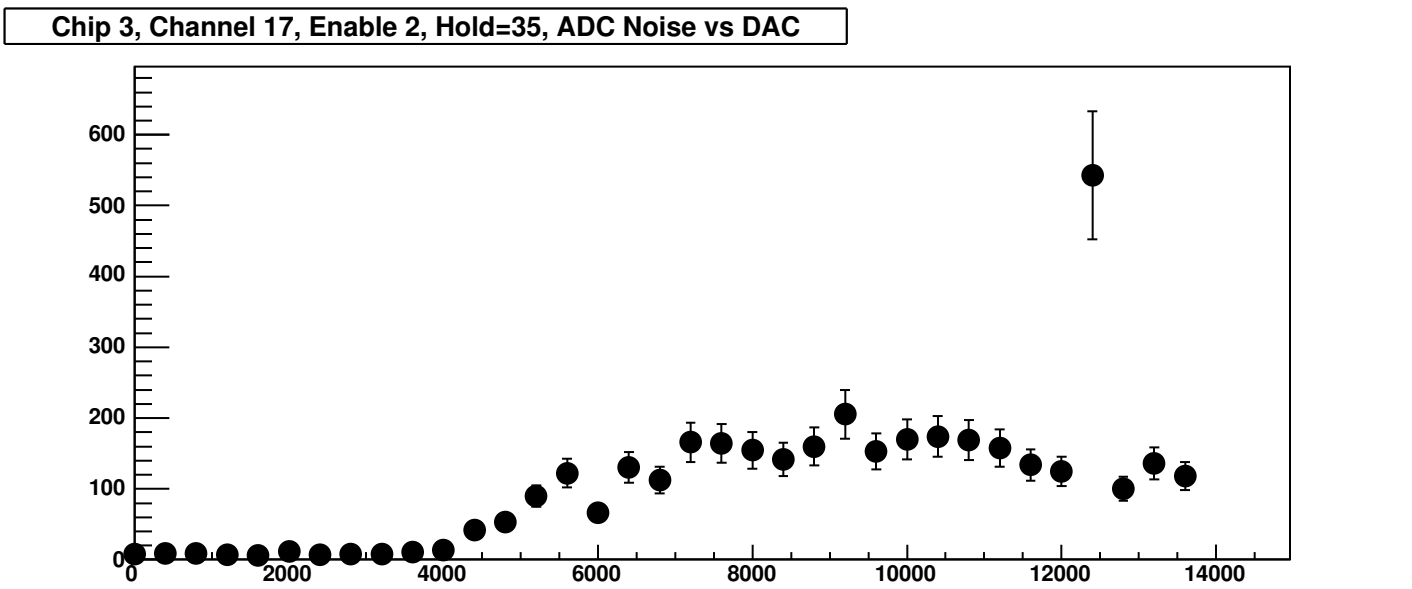
Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

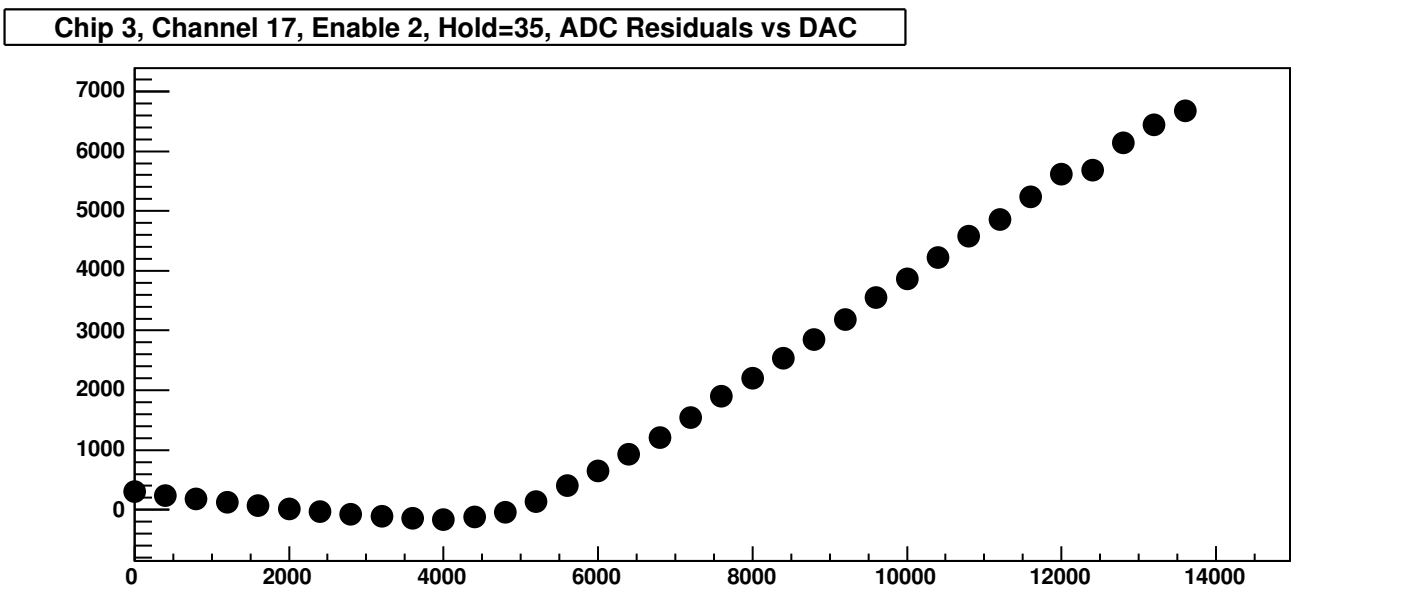


Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

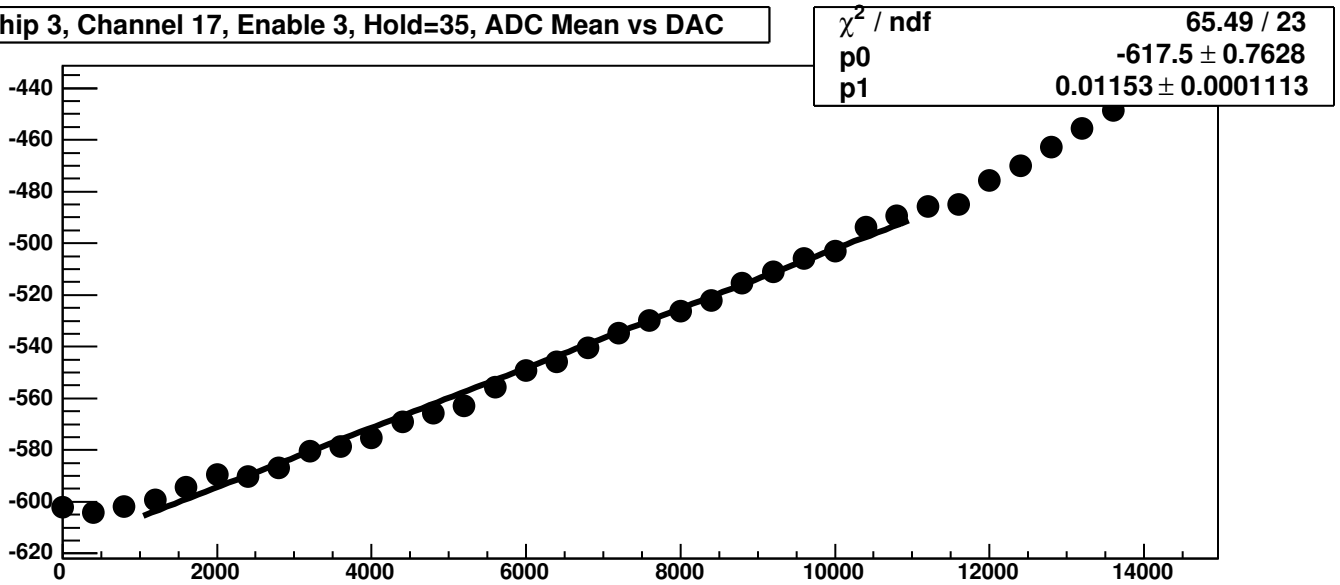



Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 9.483e+04 / 23
p0 -895.9 ± 1.785
p1 0.1664 ± 0.0006934
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

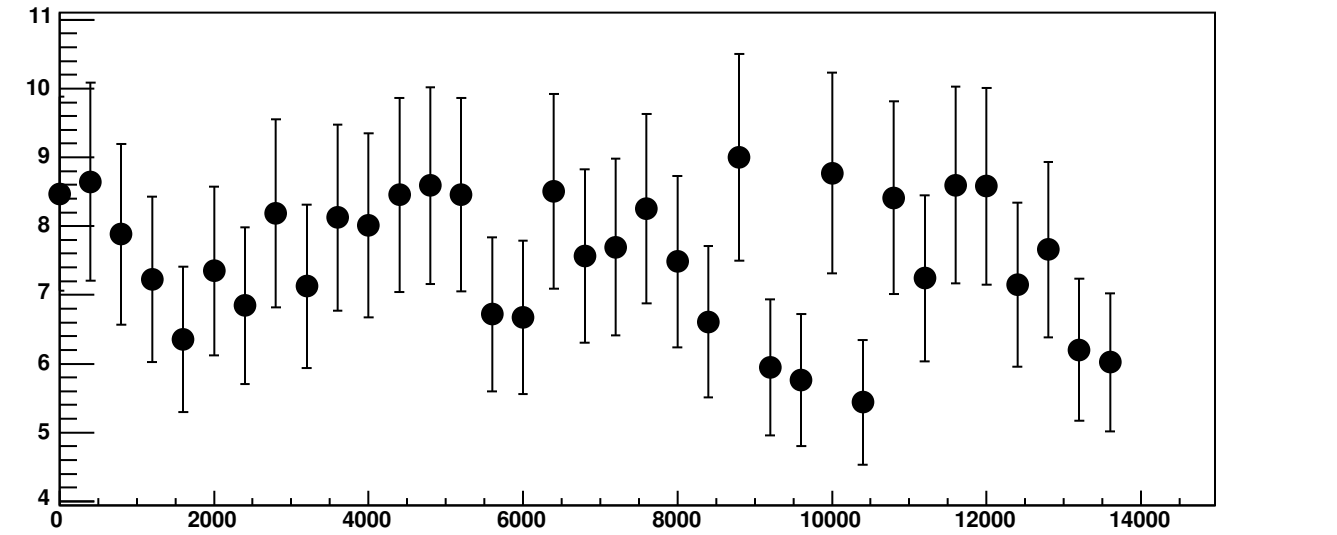

Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

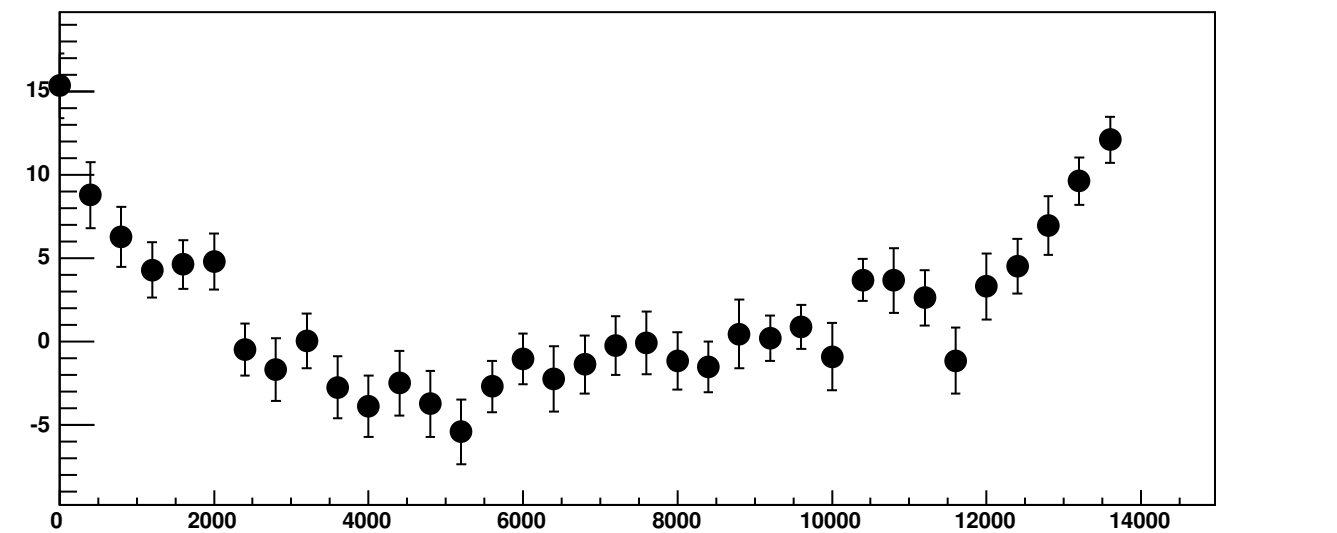
Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

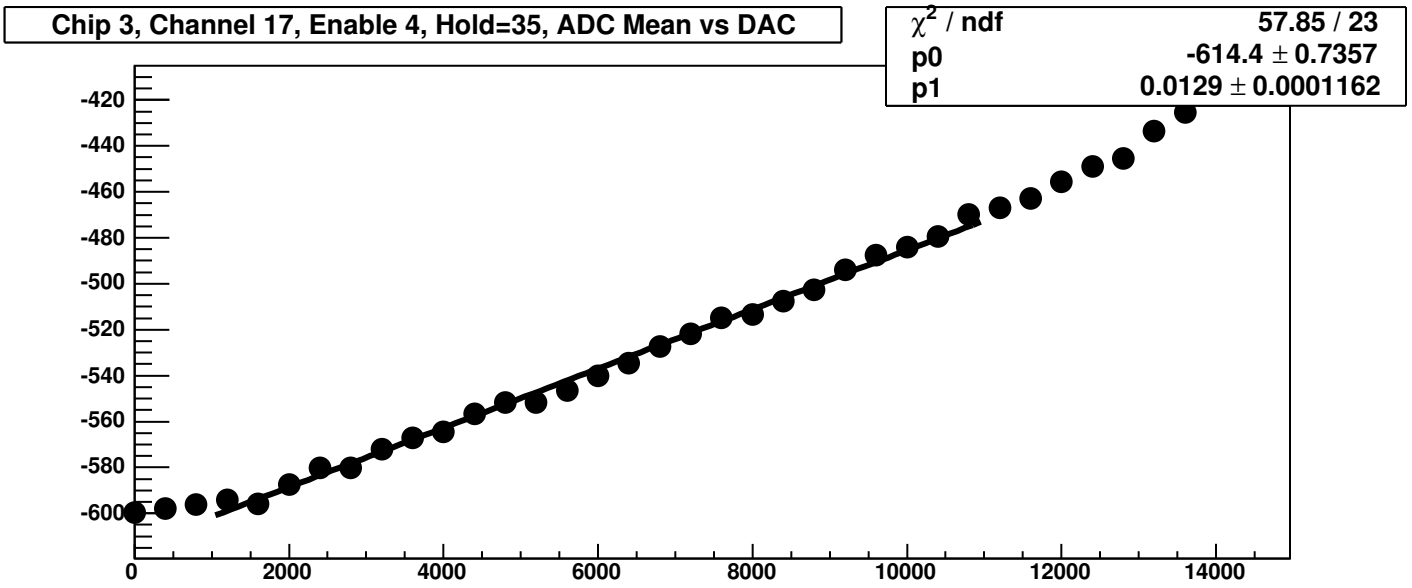


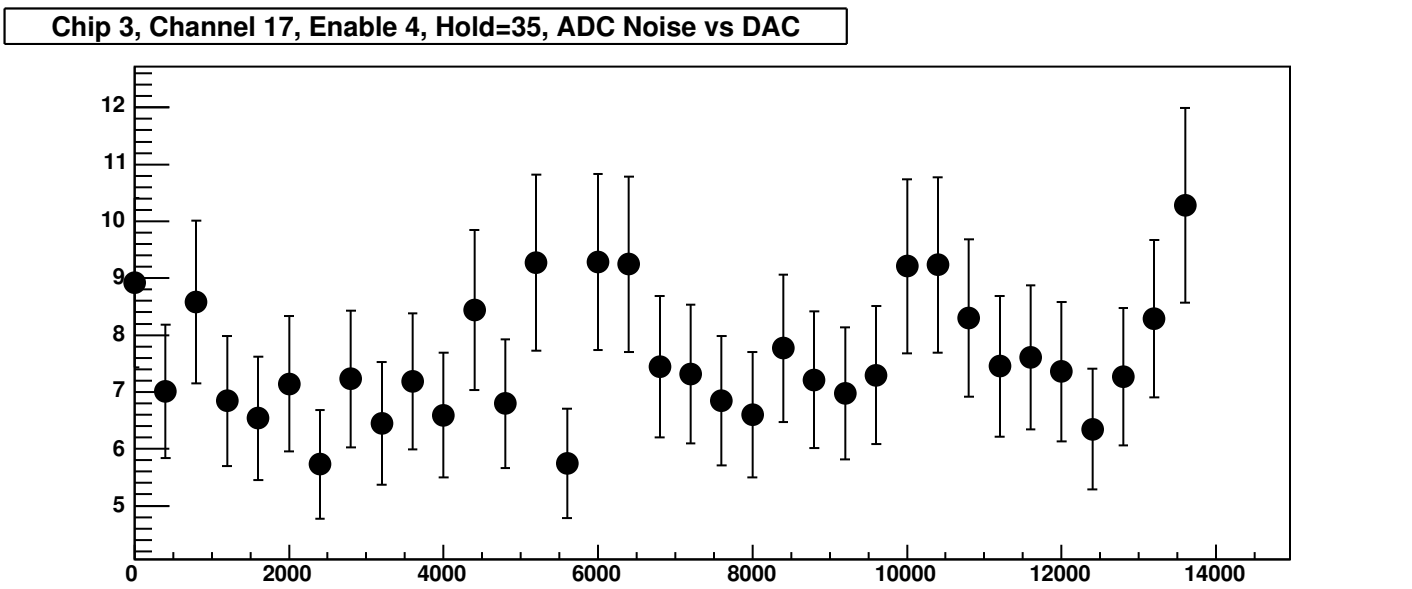
Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

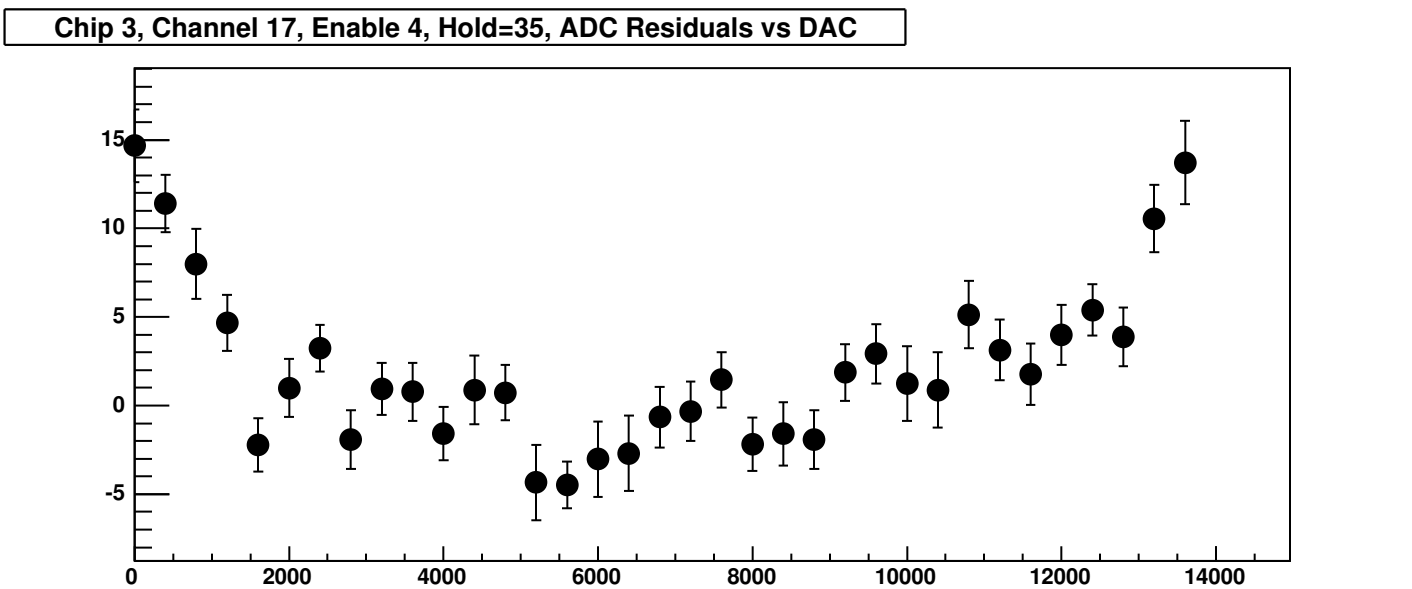


Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

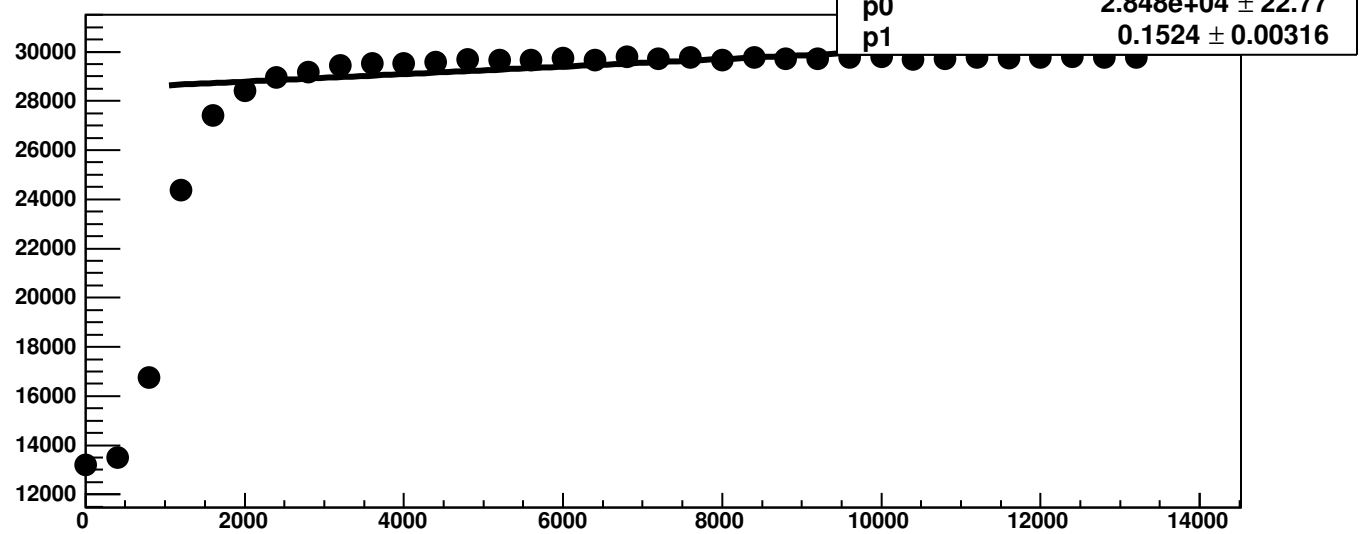



Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 57.85 / 23
p0 -614.4 ± 0.7357
p1 0.0129 ± 0.0001162
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

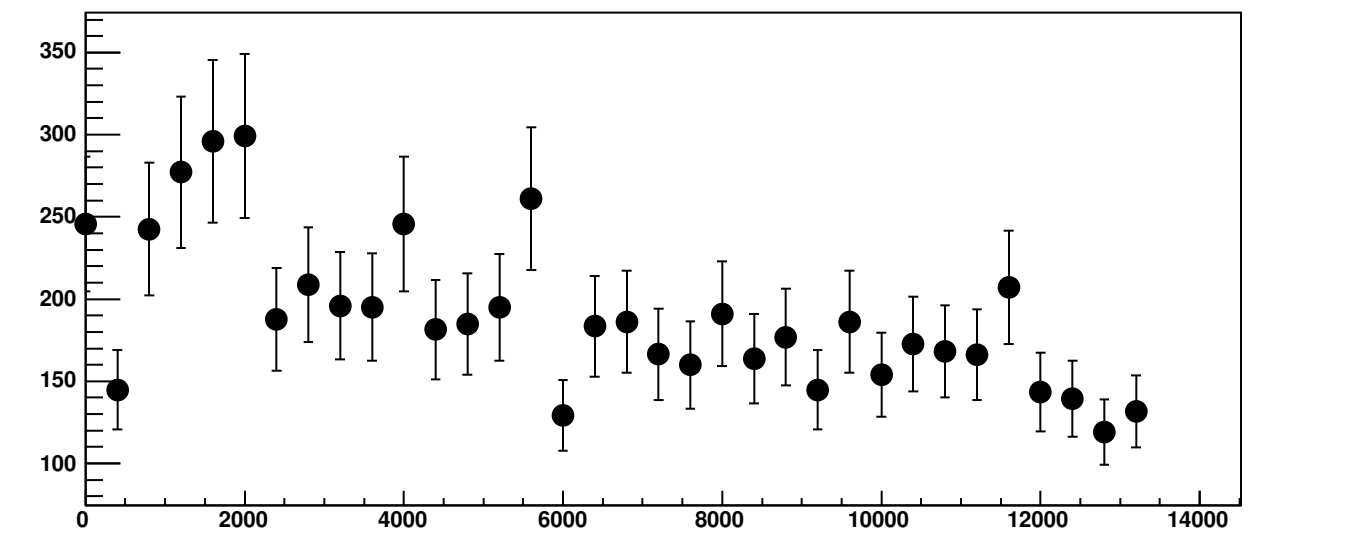

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

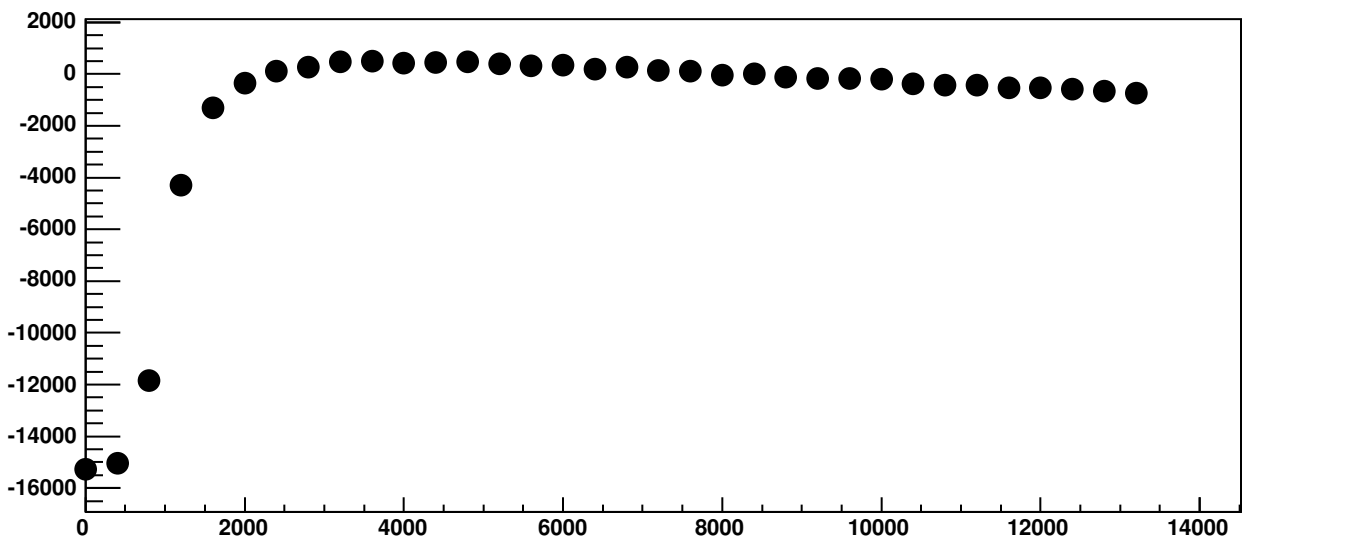
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC

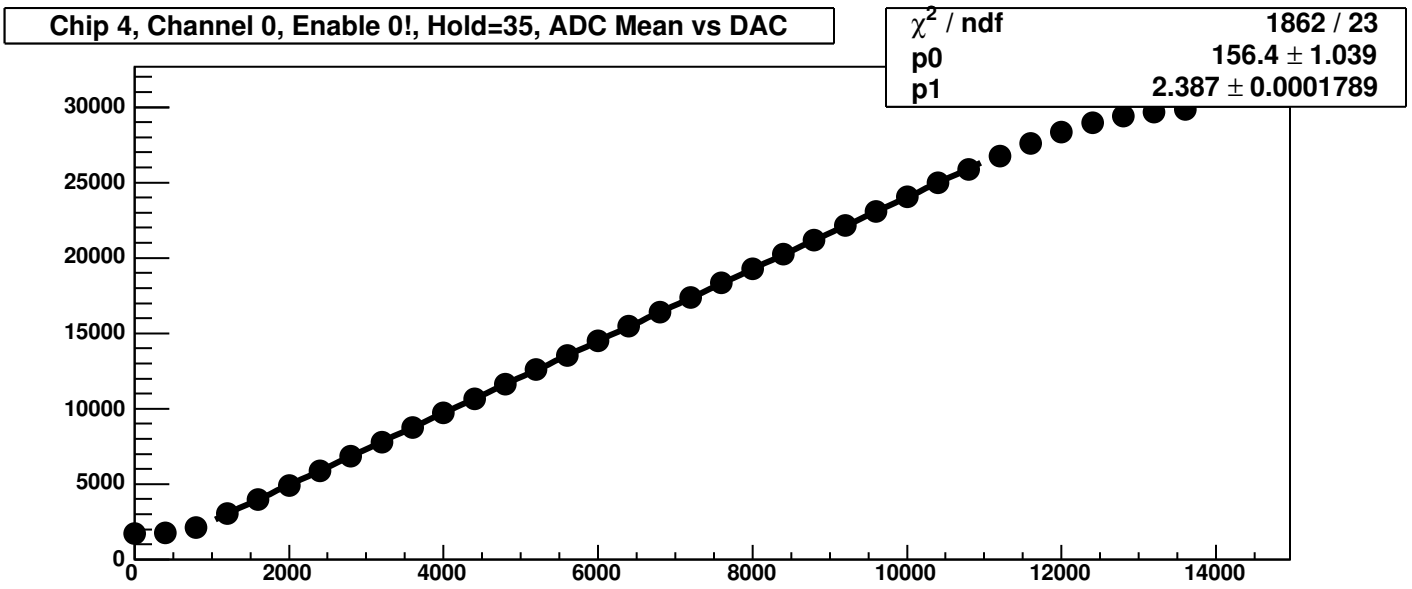


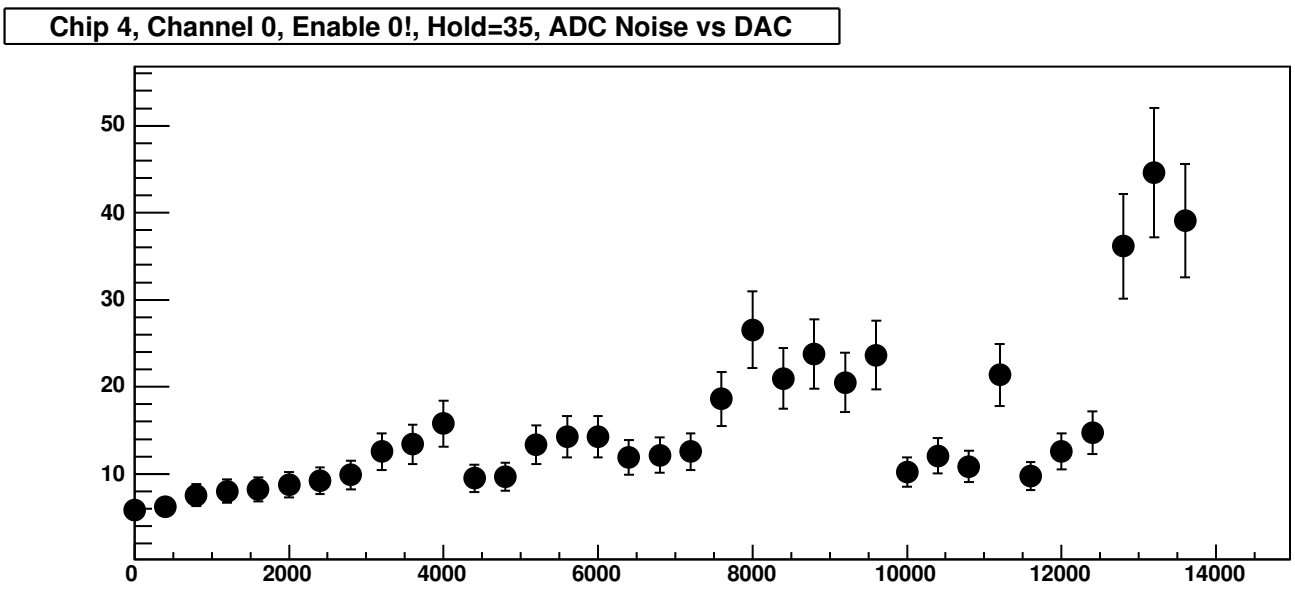
Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC

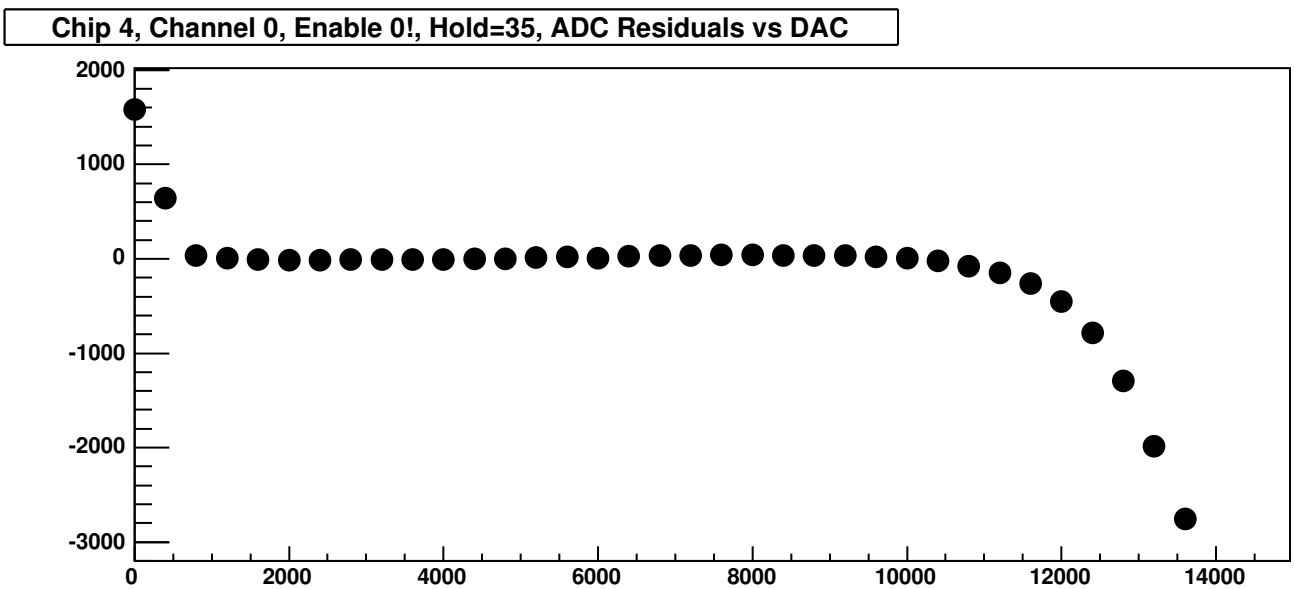


Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC

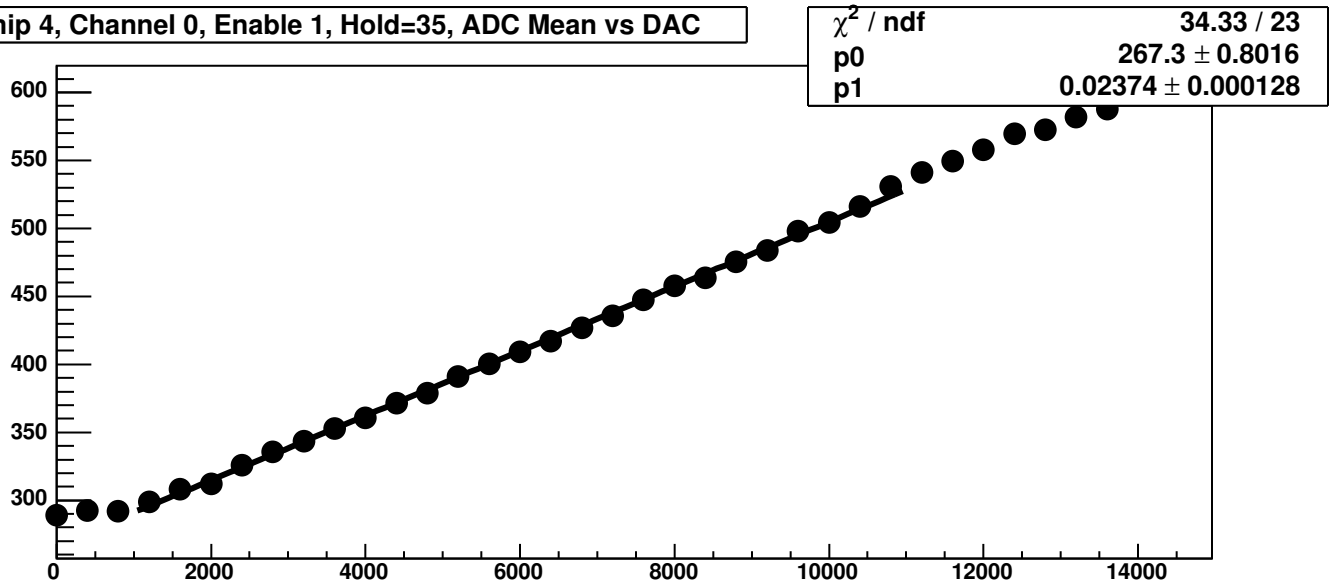



Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1862 / 23
p0 156.4 ± 1.039
p1 2.387 ± 0.0001789
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

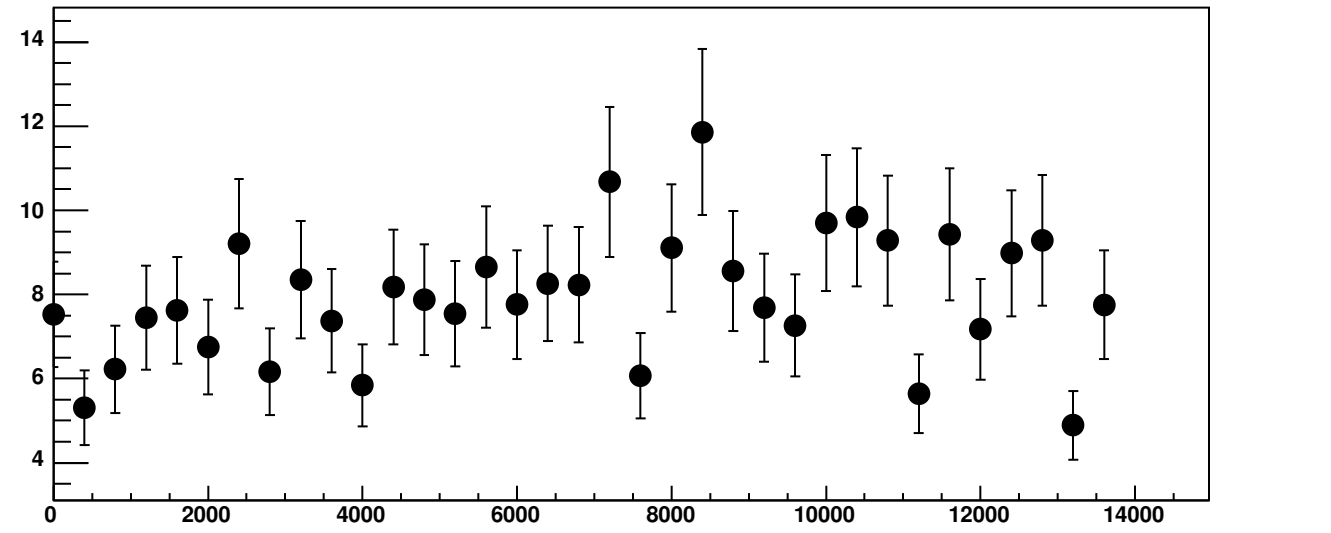

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

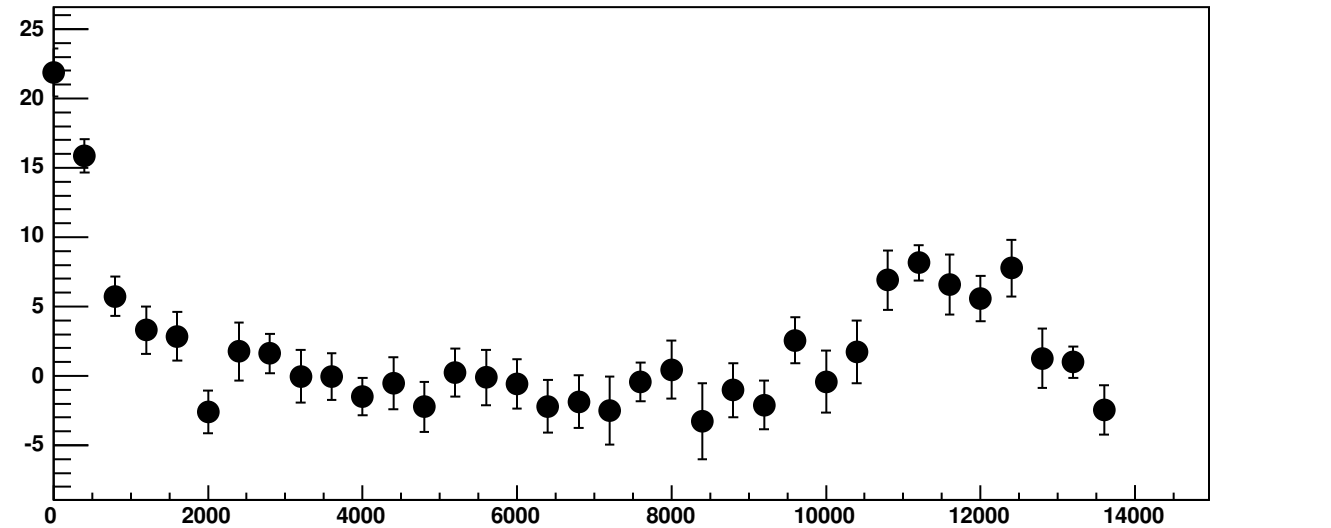
Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

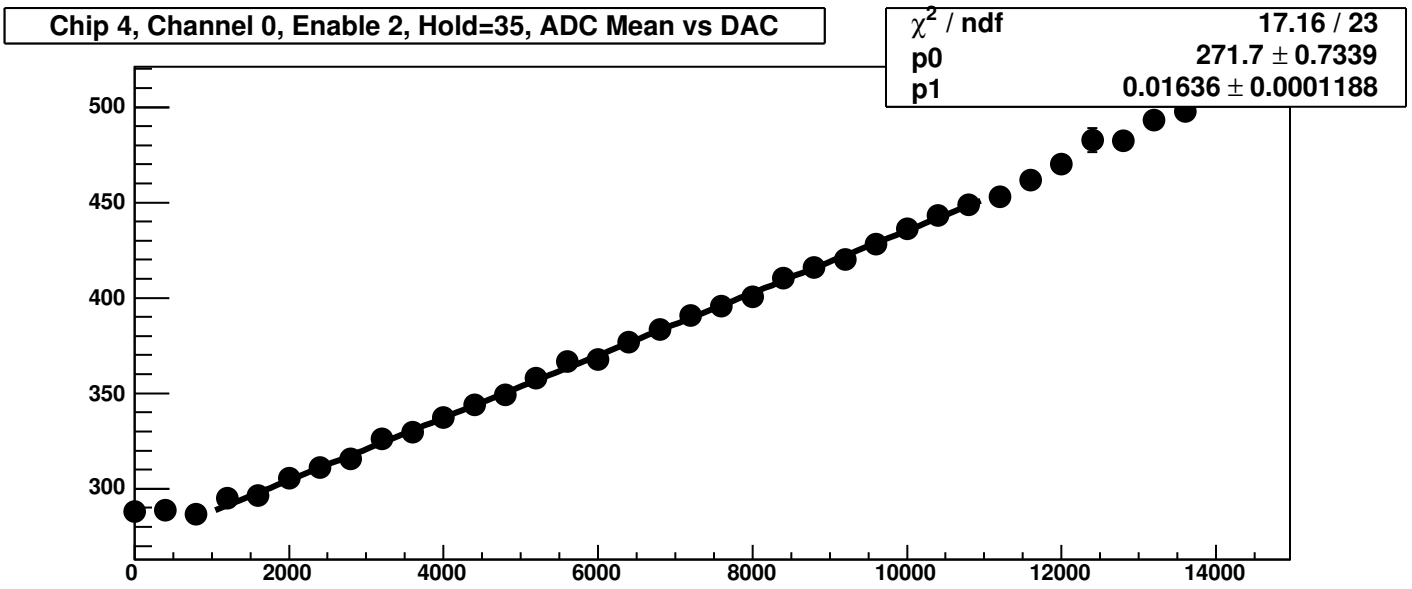


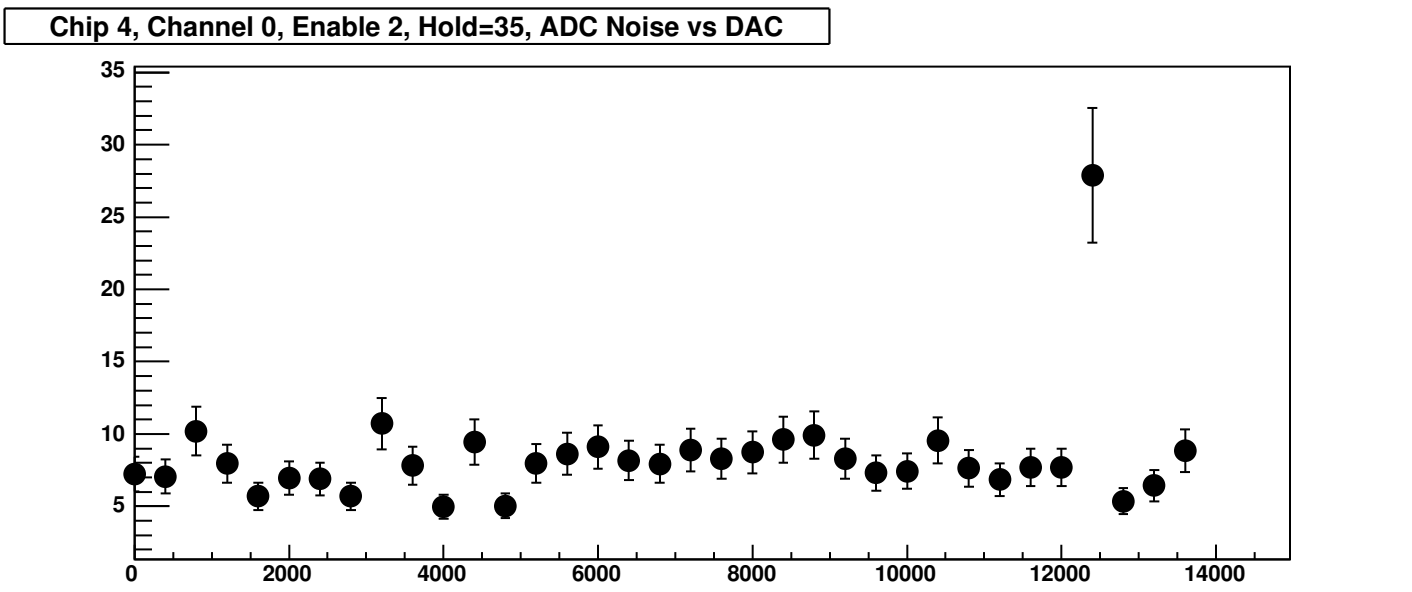
Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

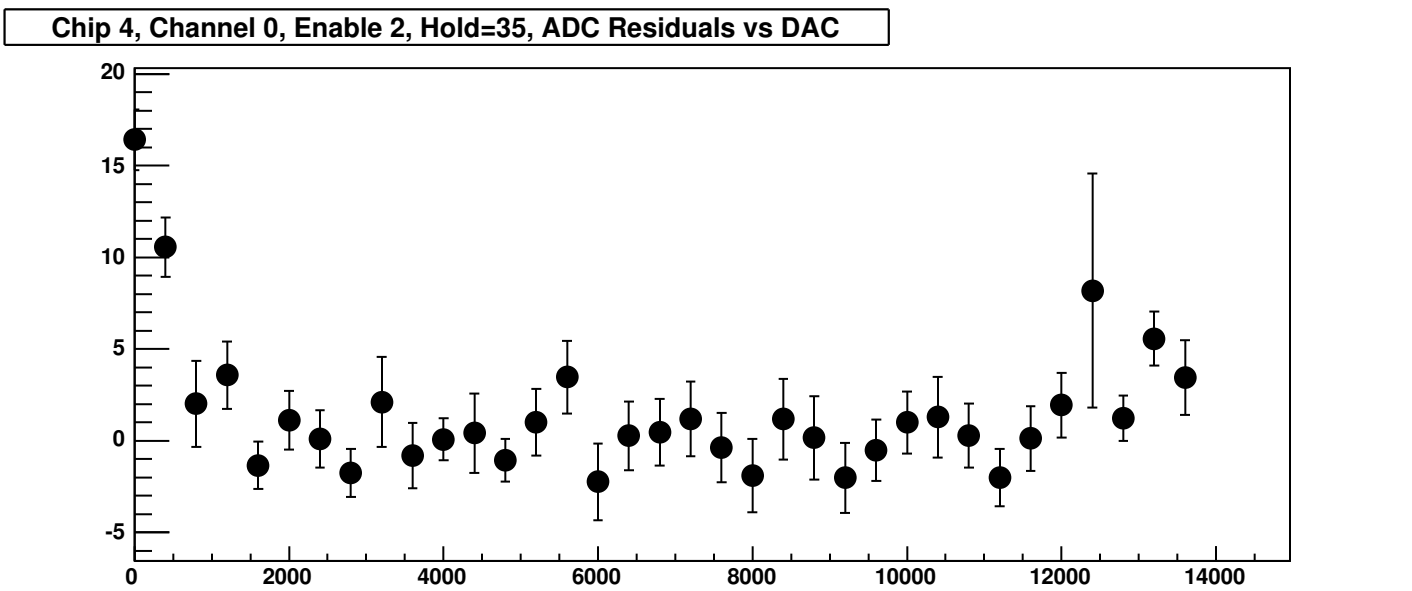


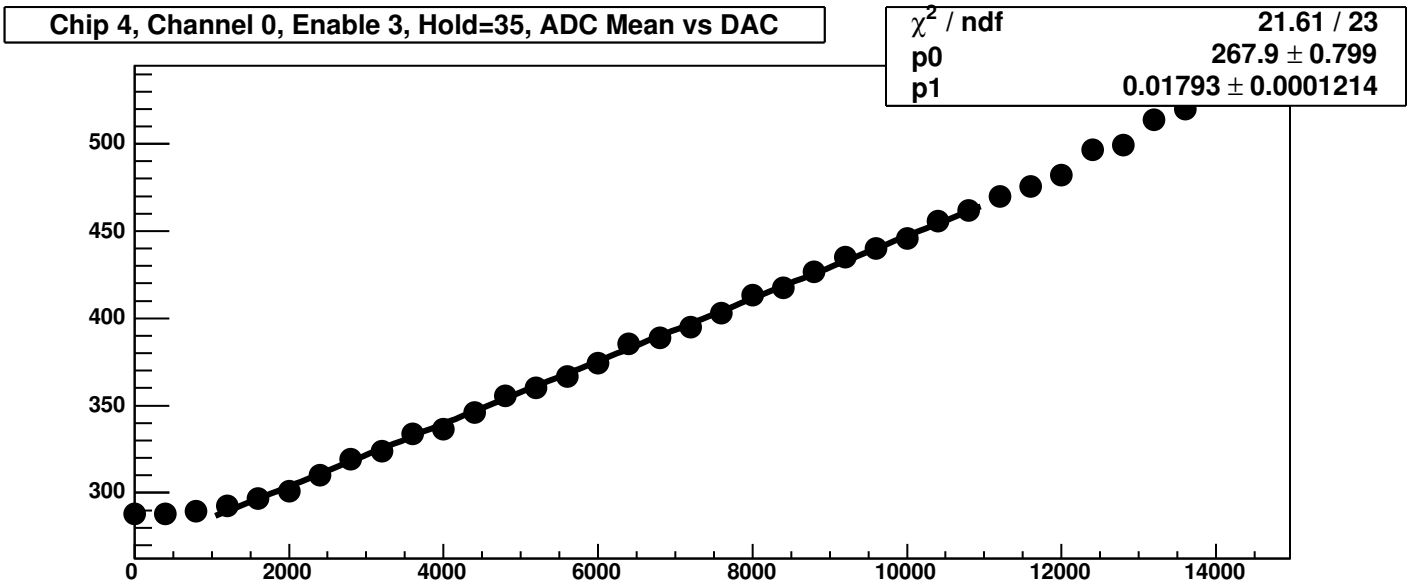
Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

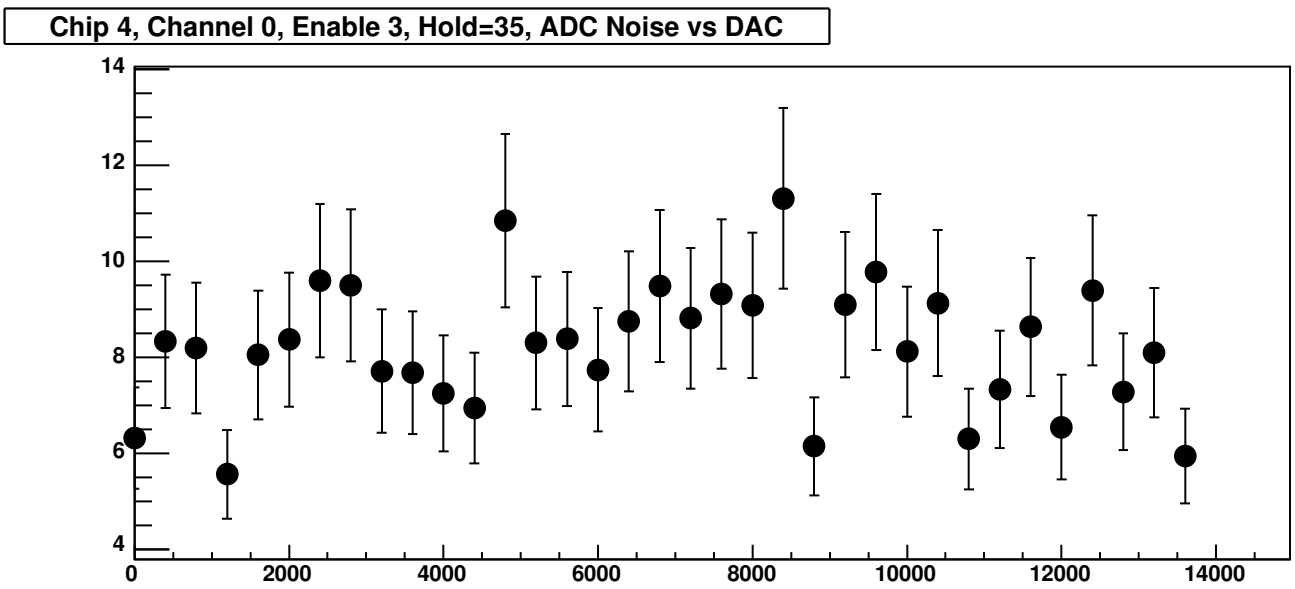


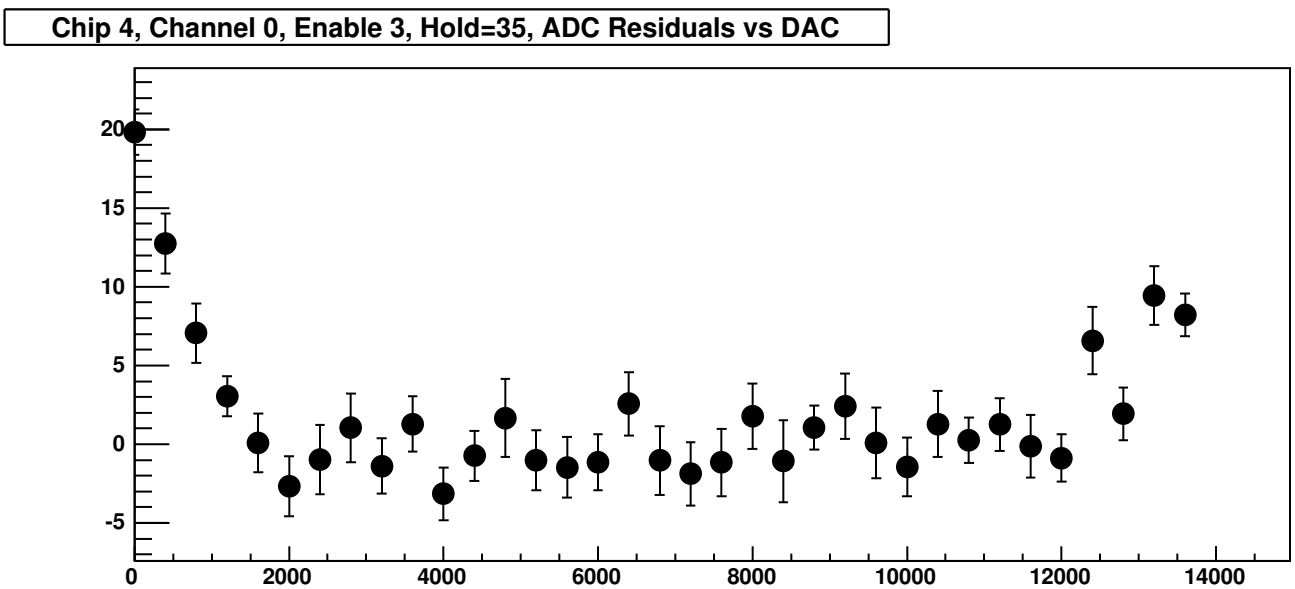

Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.16 / 23
p0 271.7 ± 0.7339
p1 0.01636 ± 0.0001188
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

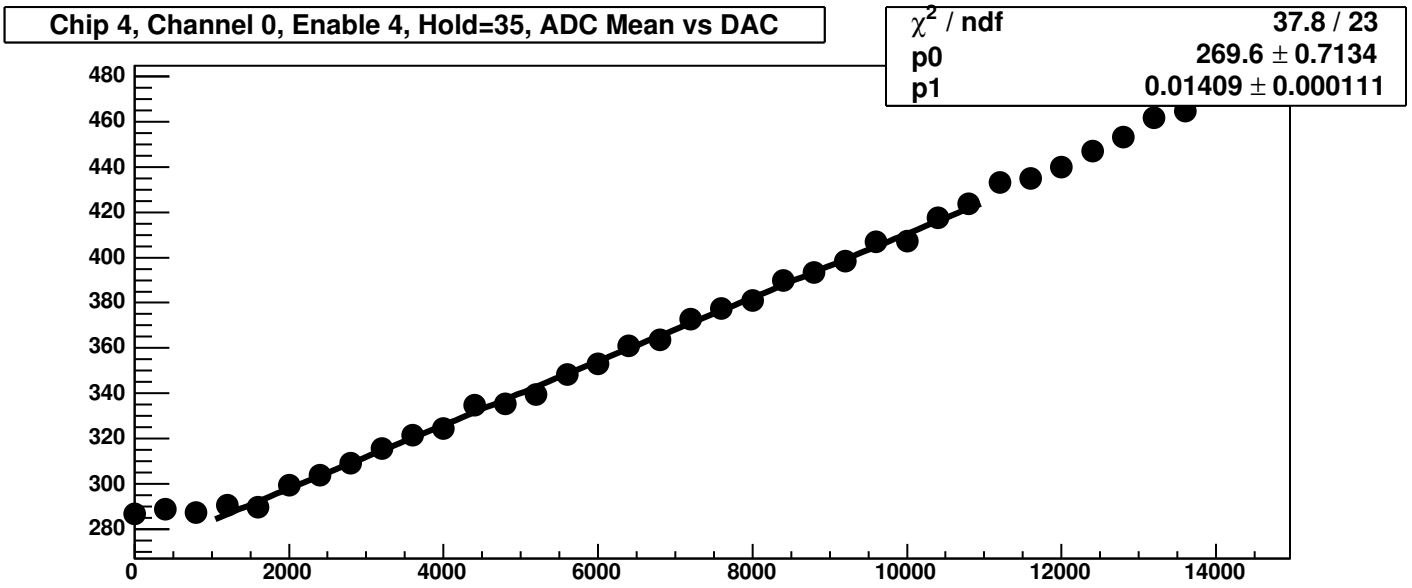

Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

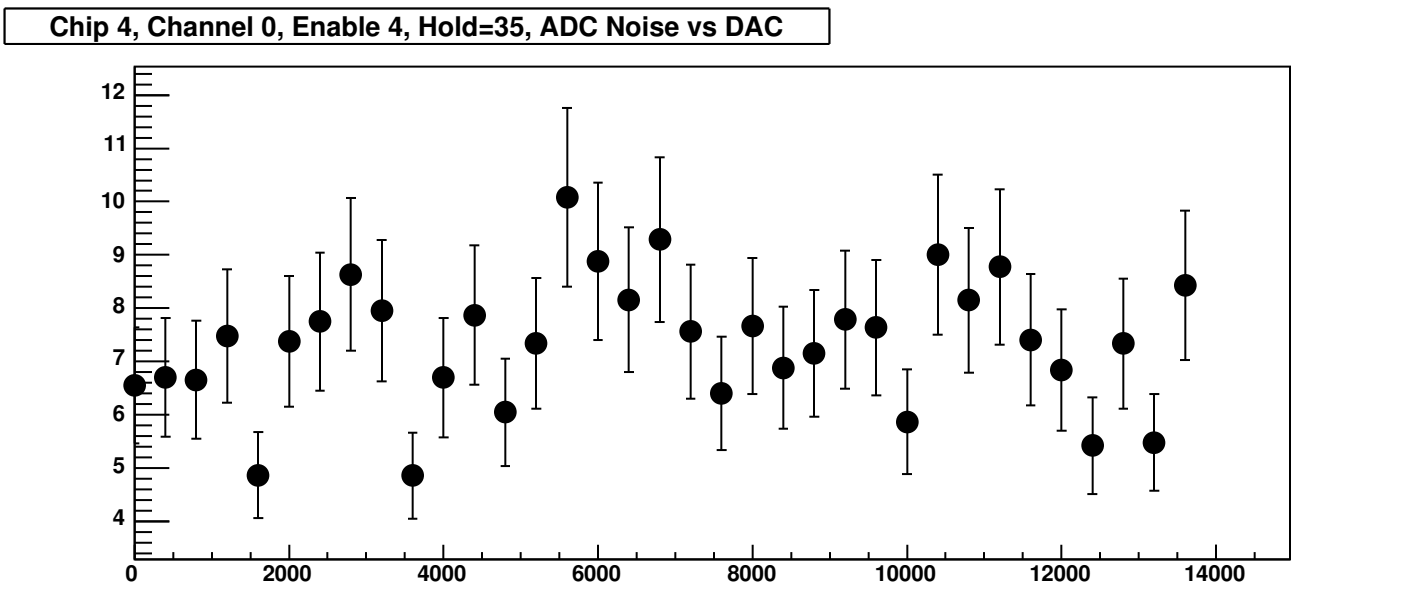

Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

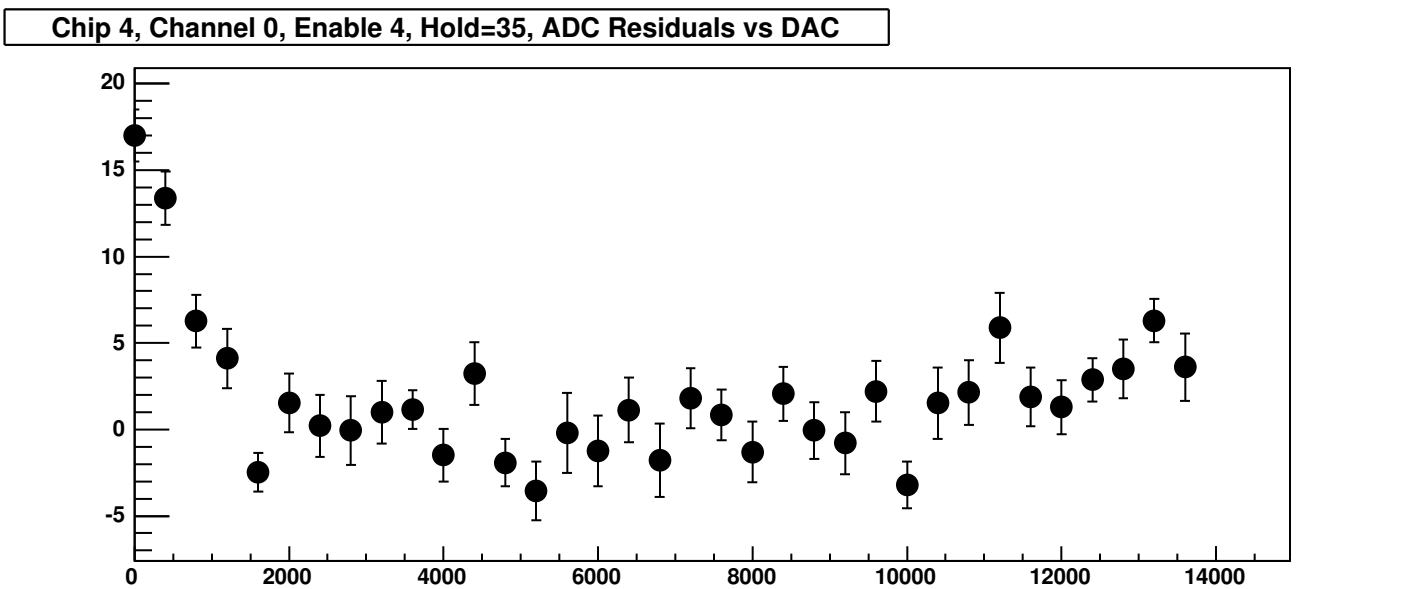

Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.61 / 23
p0 267.9 ± 0.799
p1 0.01793 ± 0.0001214
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

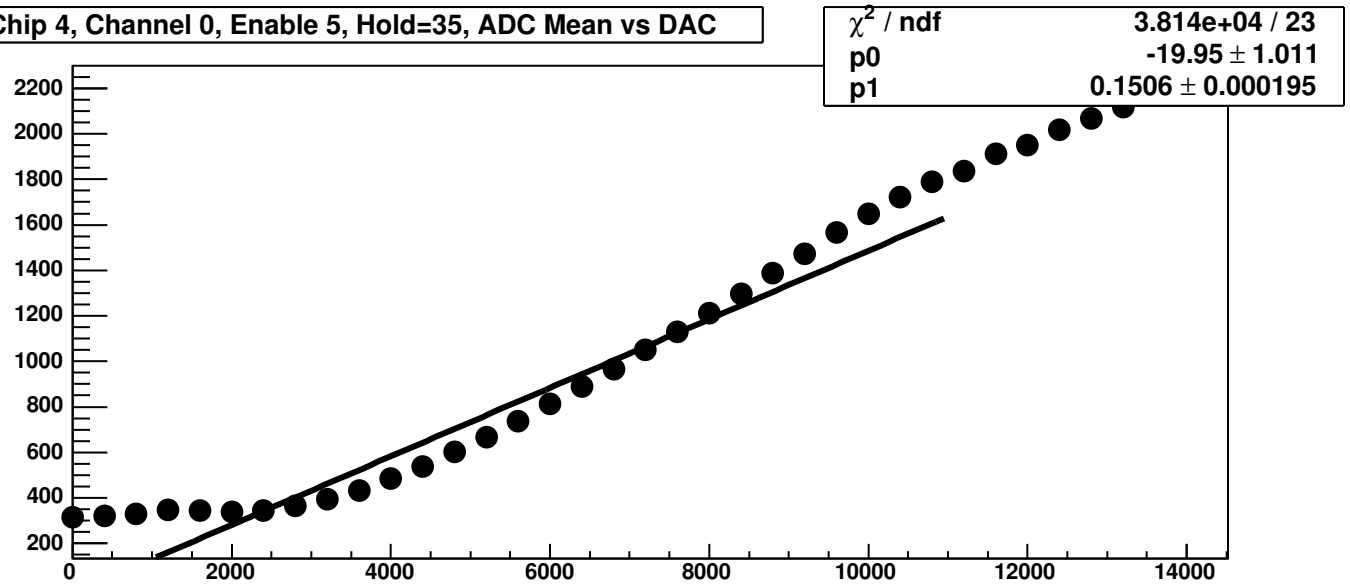

Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.8 / 23
p0 269.6 ± 0.7134
p1 0.01409 ± 0.000111
480
460
440
420
400
380
360
340
320
300
280
0
2000
4000
6000
8000
10000
12000
14000

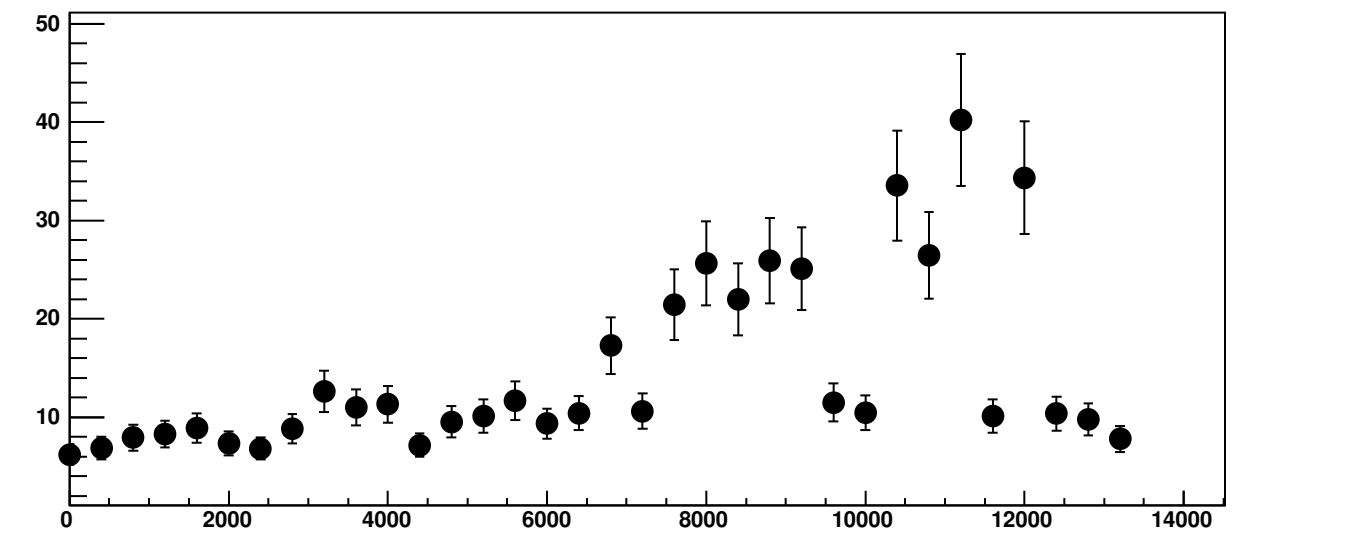

Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

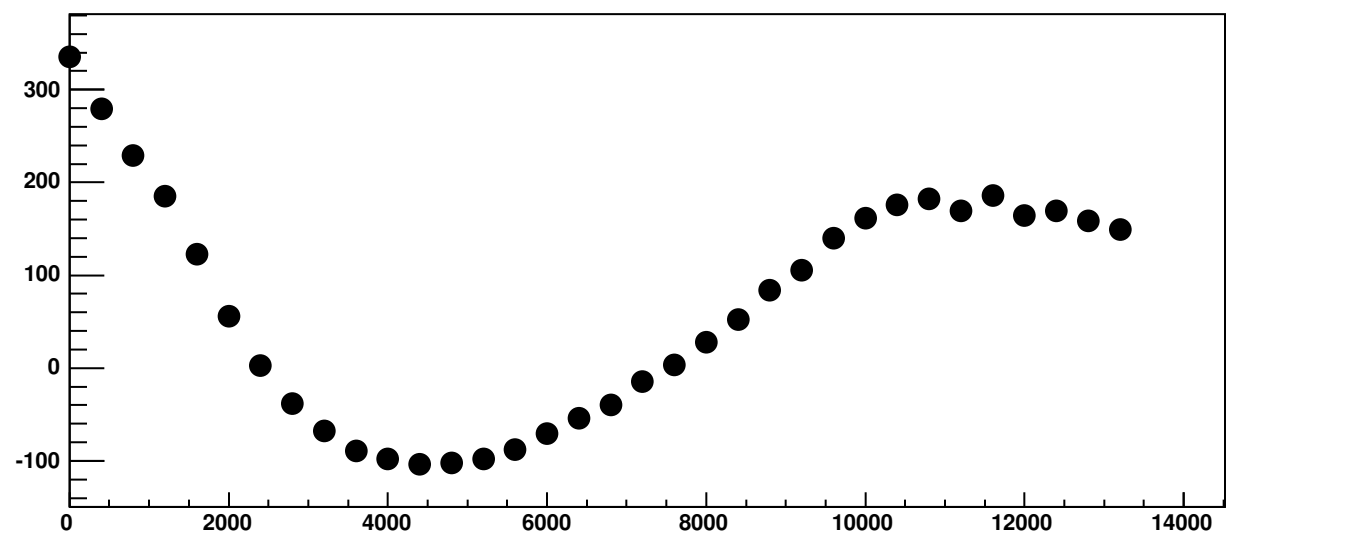
Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

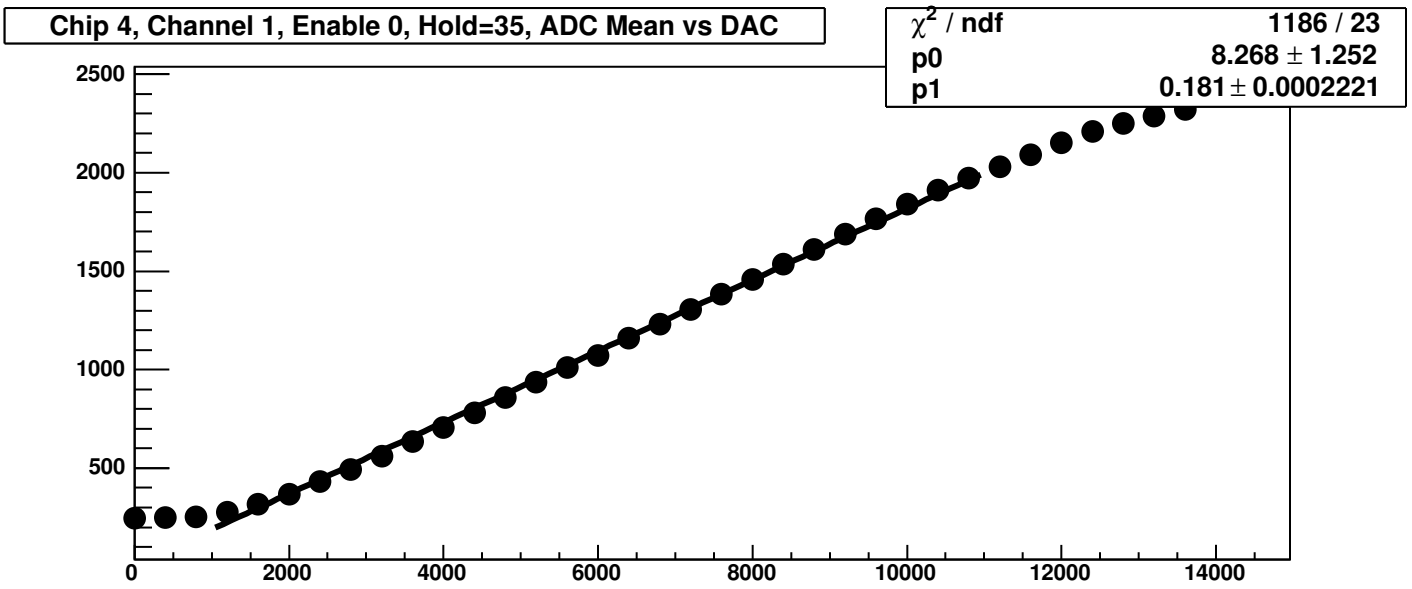


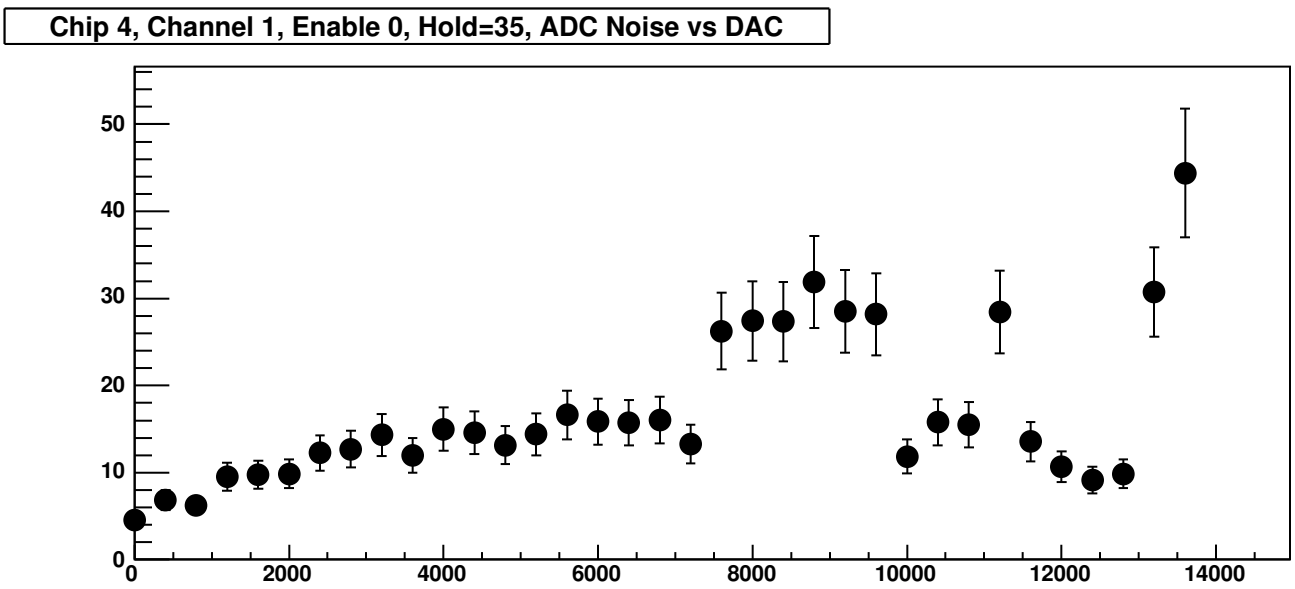
Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

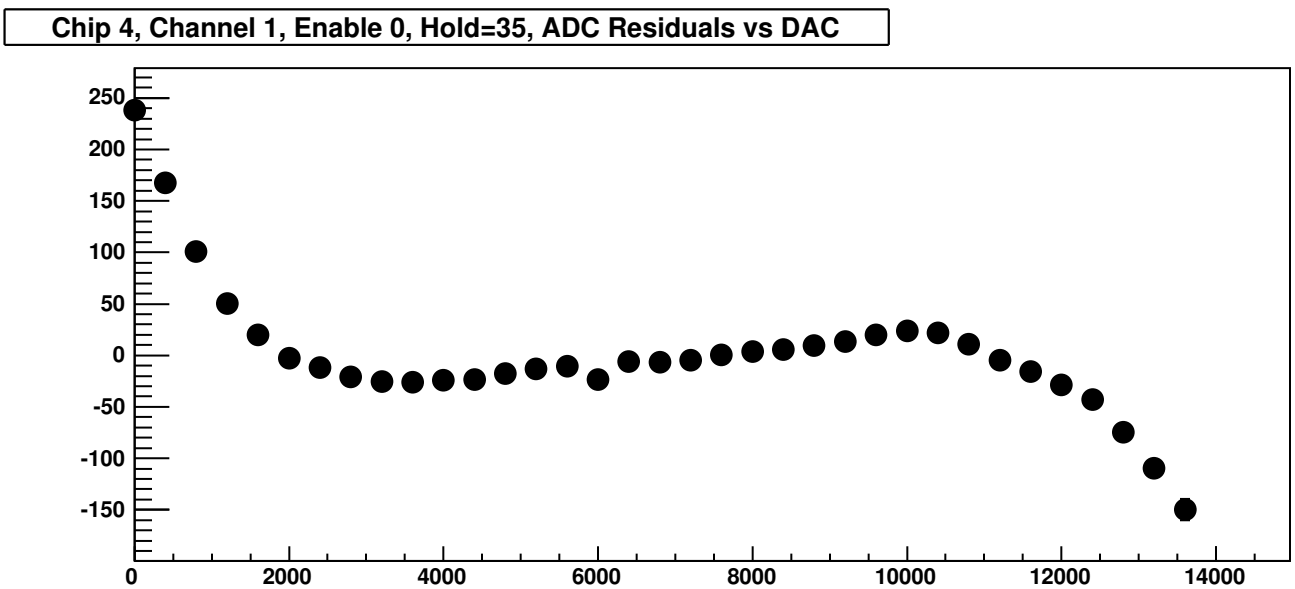


Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

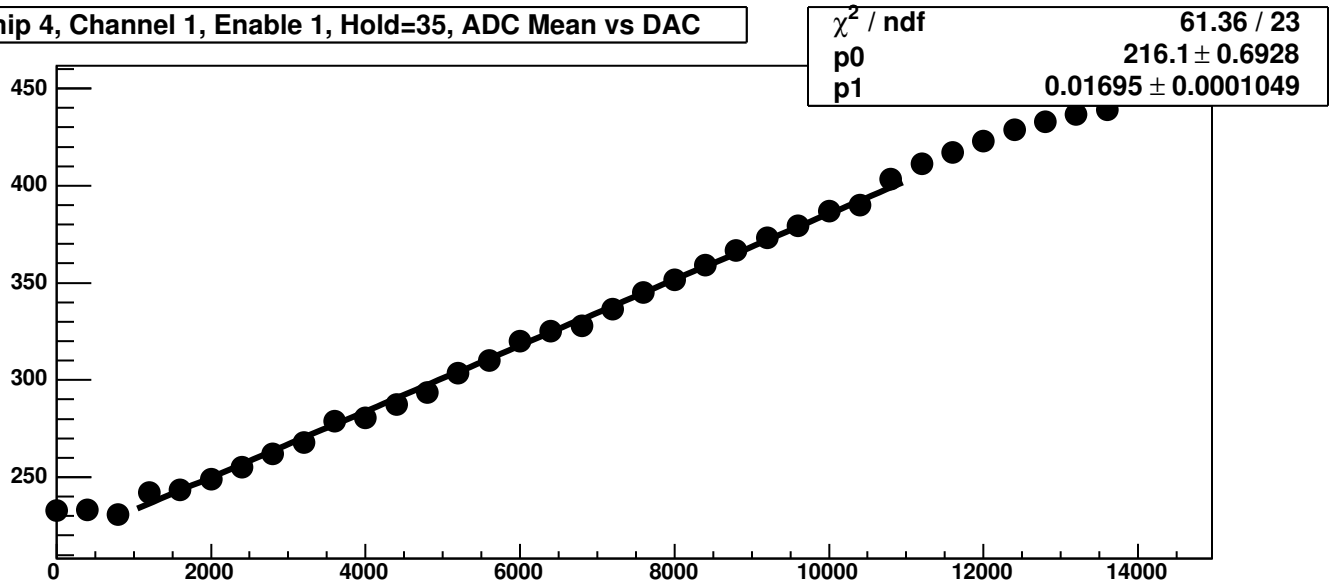



Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1186 / 23
p0 8.268 ± 1.252
p1 0.181 ± 0.0002221
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

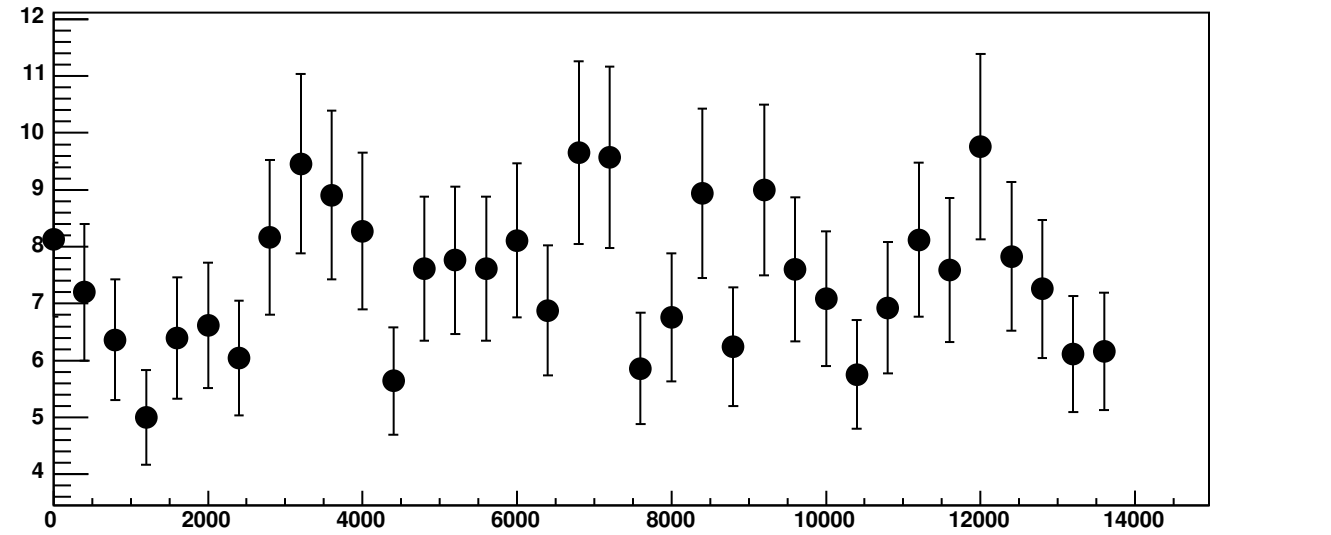

Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

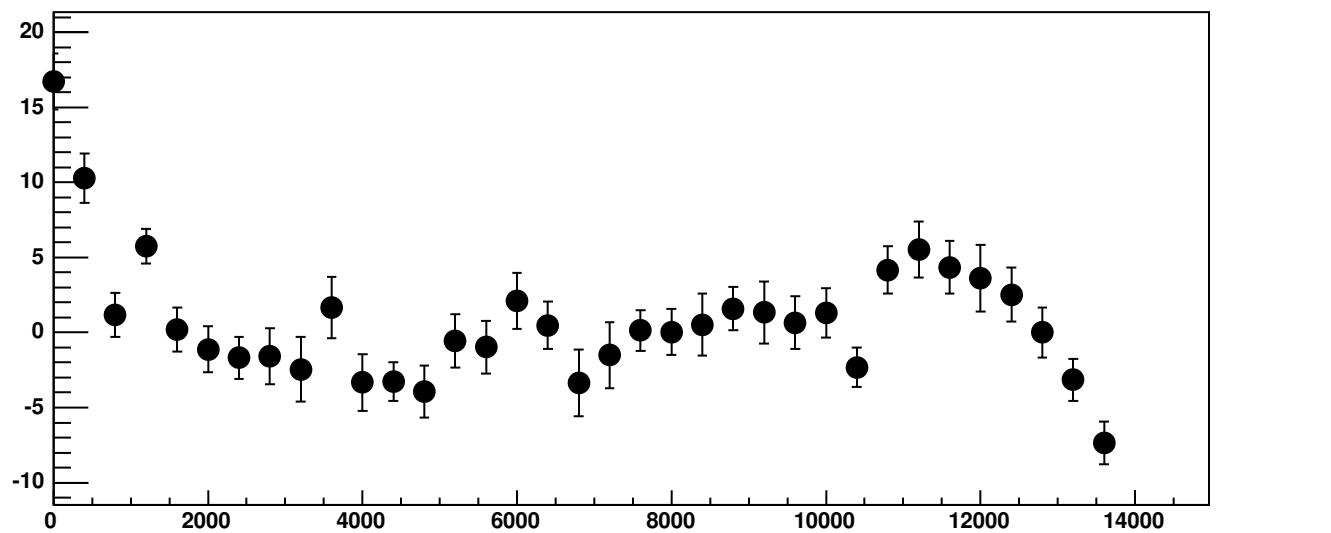
Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



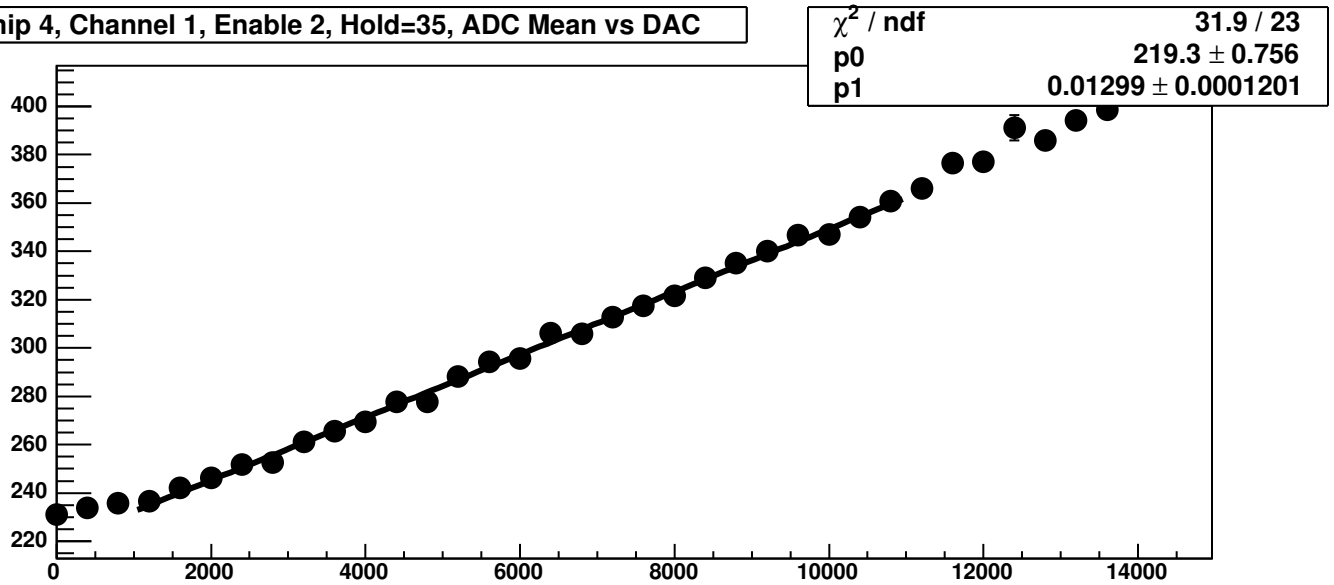
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



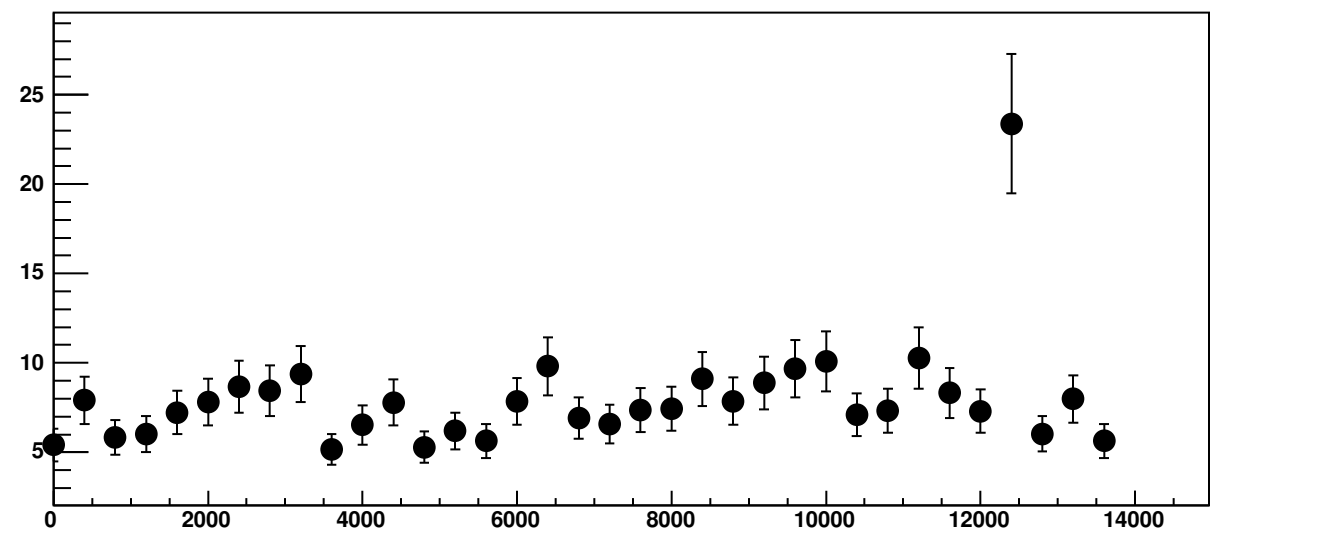
Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



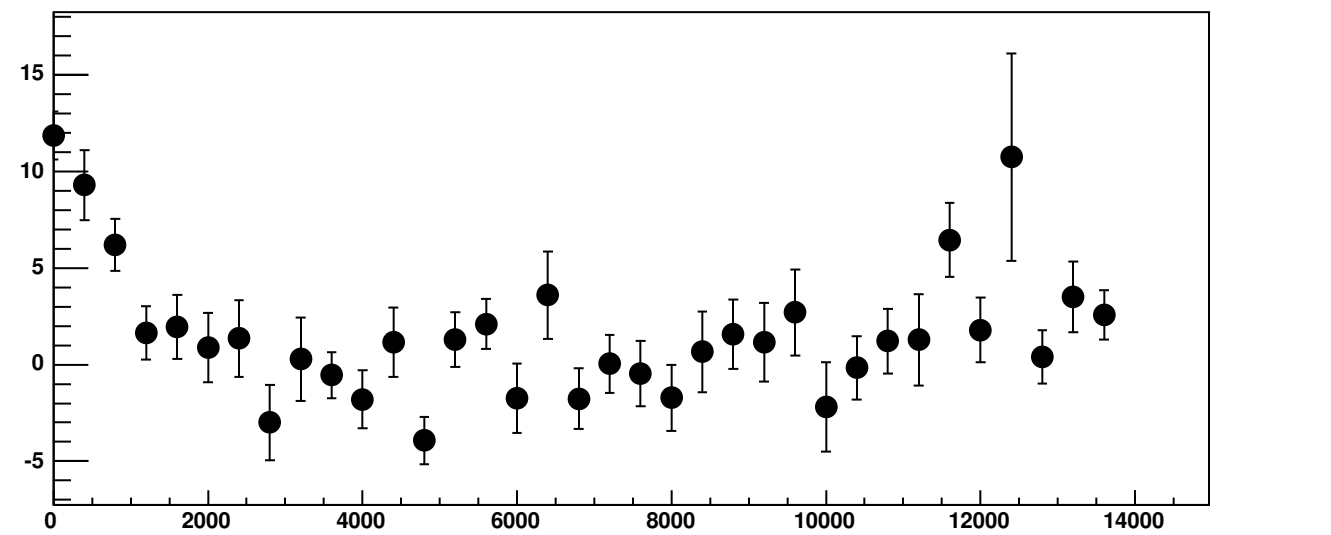
Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

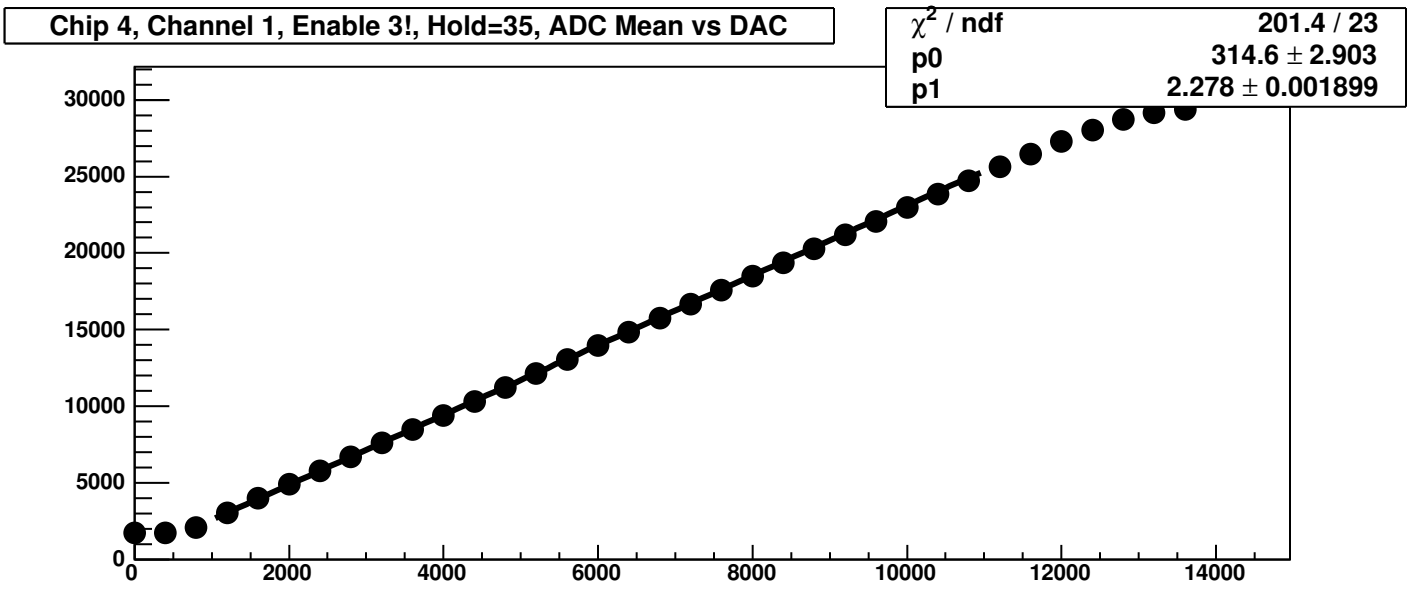


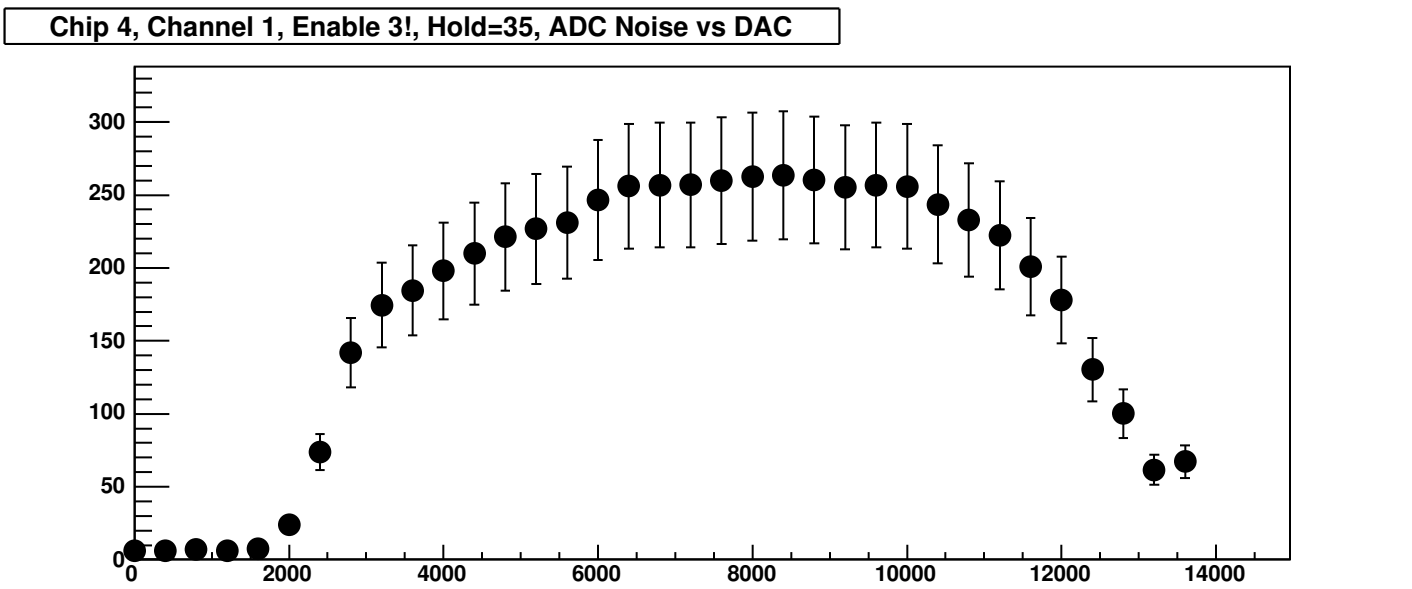
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

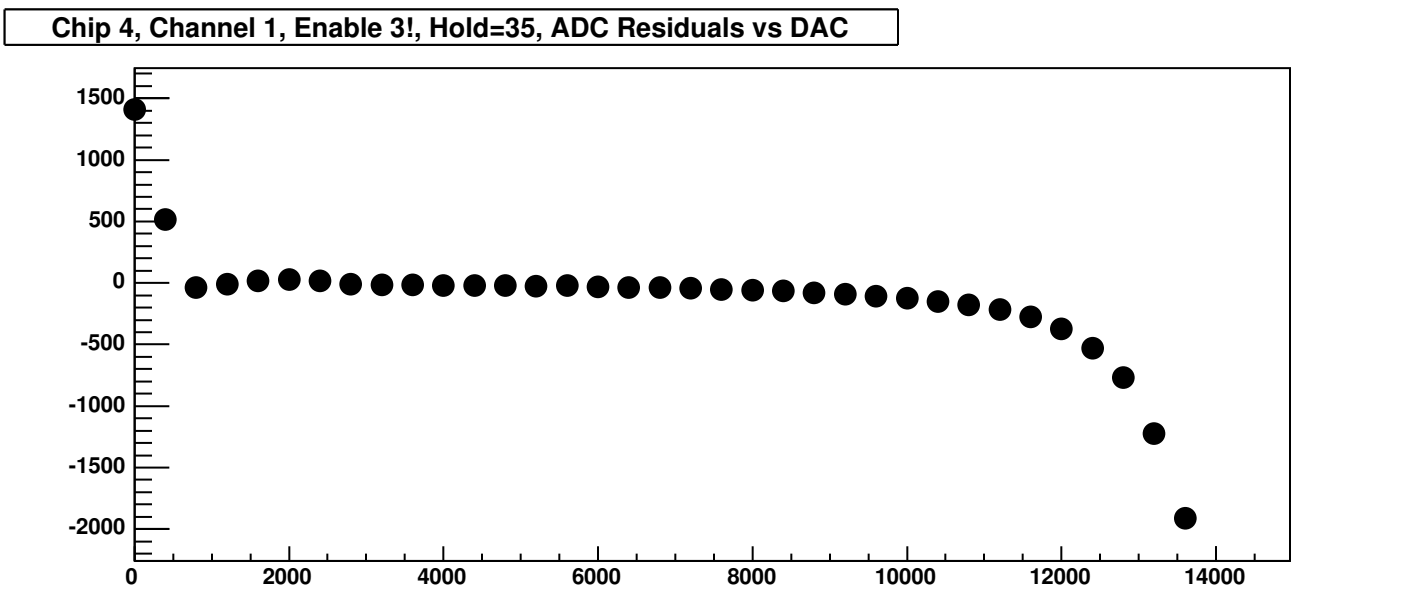


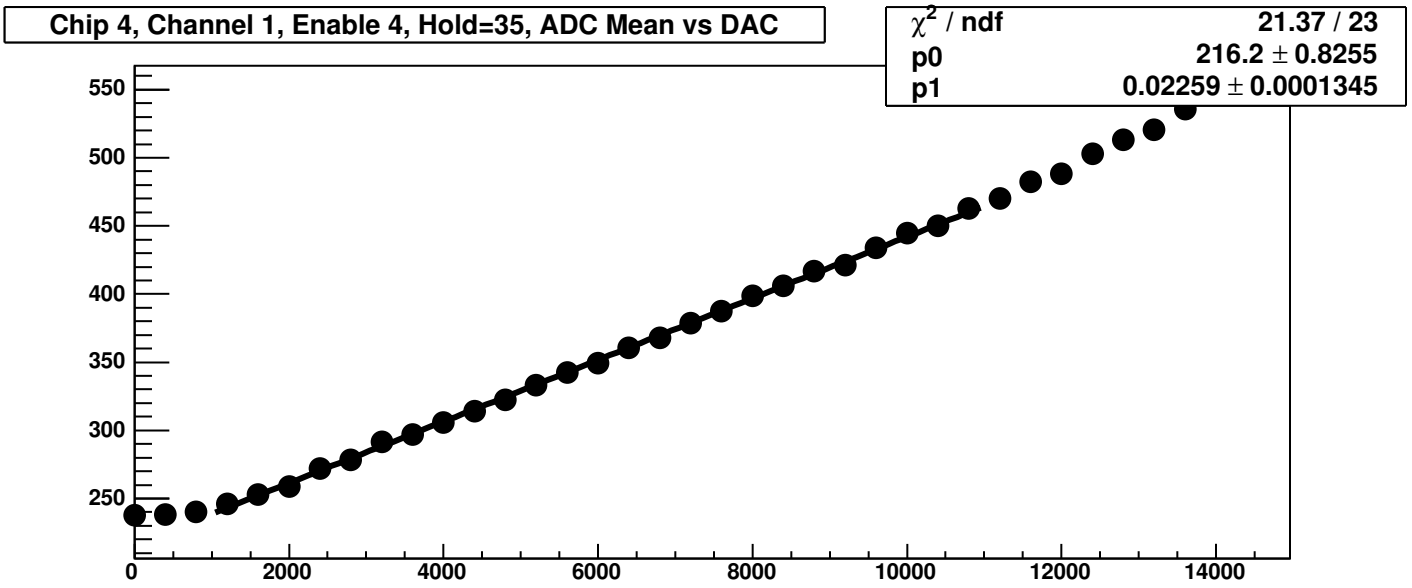
Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

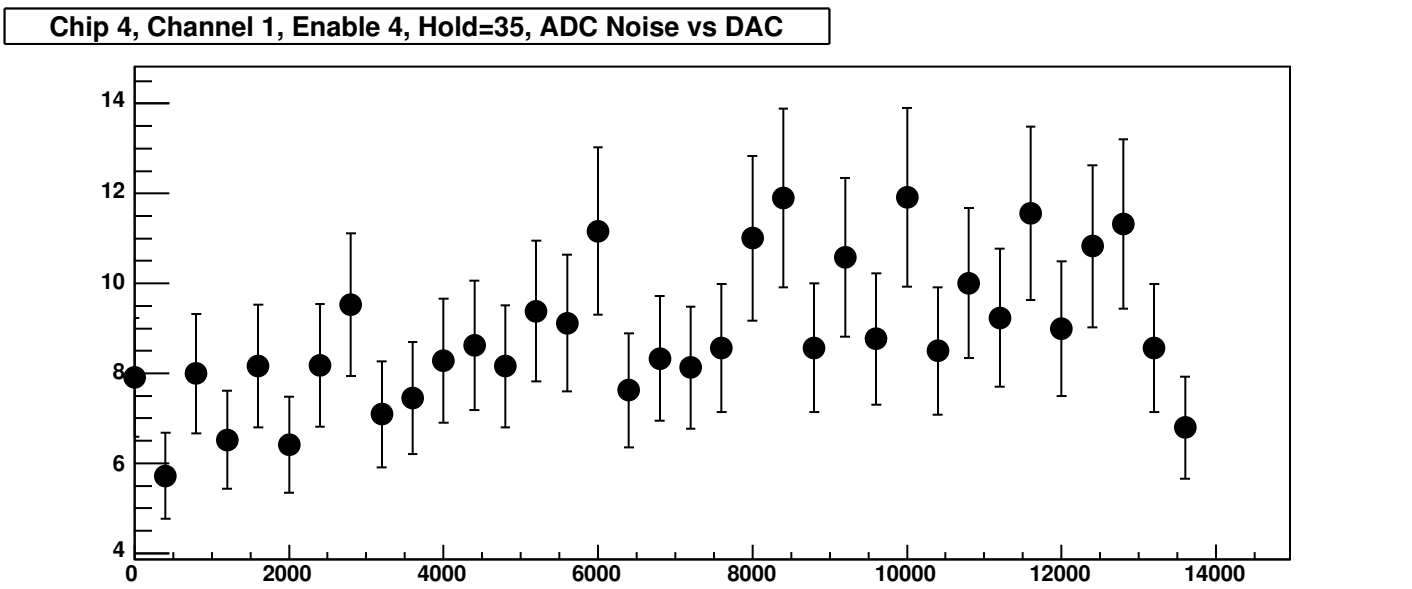


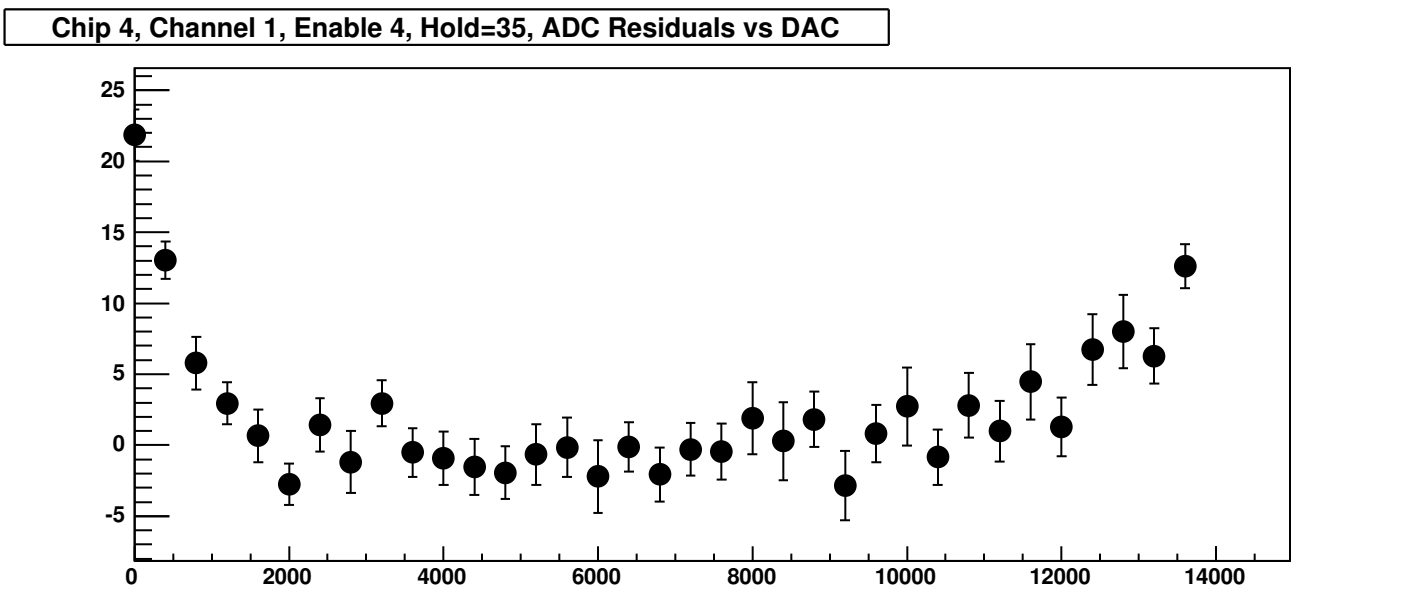

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 201.4 / 23
p0 314.6 ± 2.903
p1 2.278 ± 0.001899
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

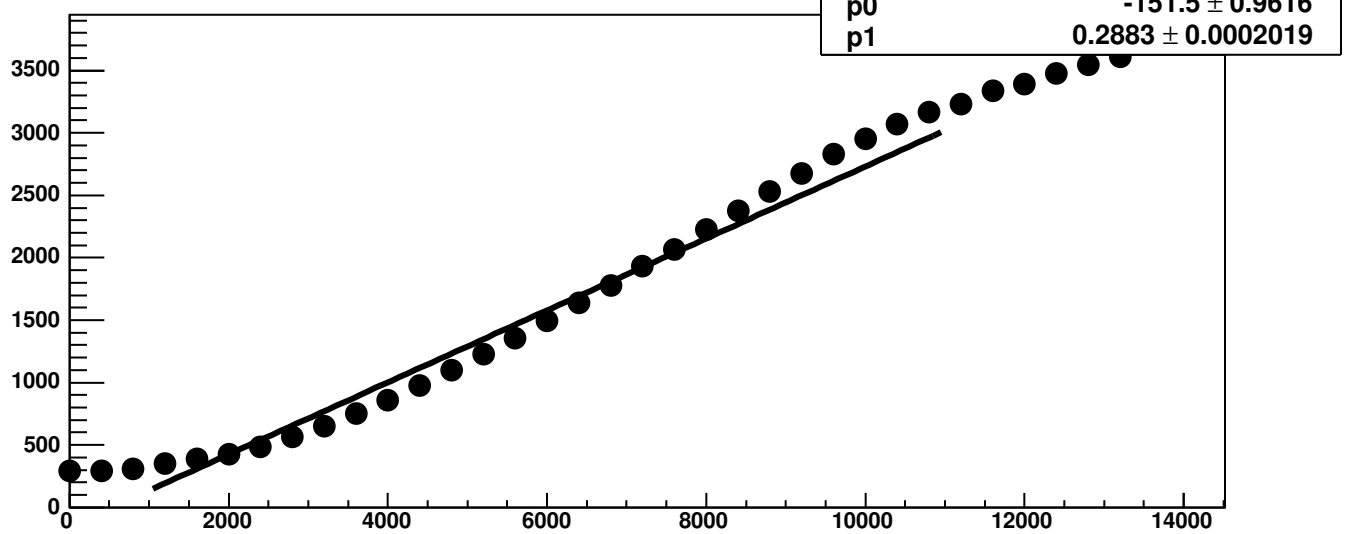

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.37 / 23
p0 216.2 ± 0.8255
p1 0.02259 ± 0.0001345
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

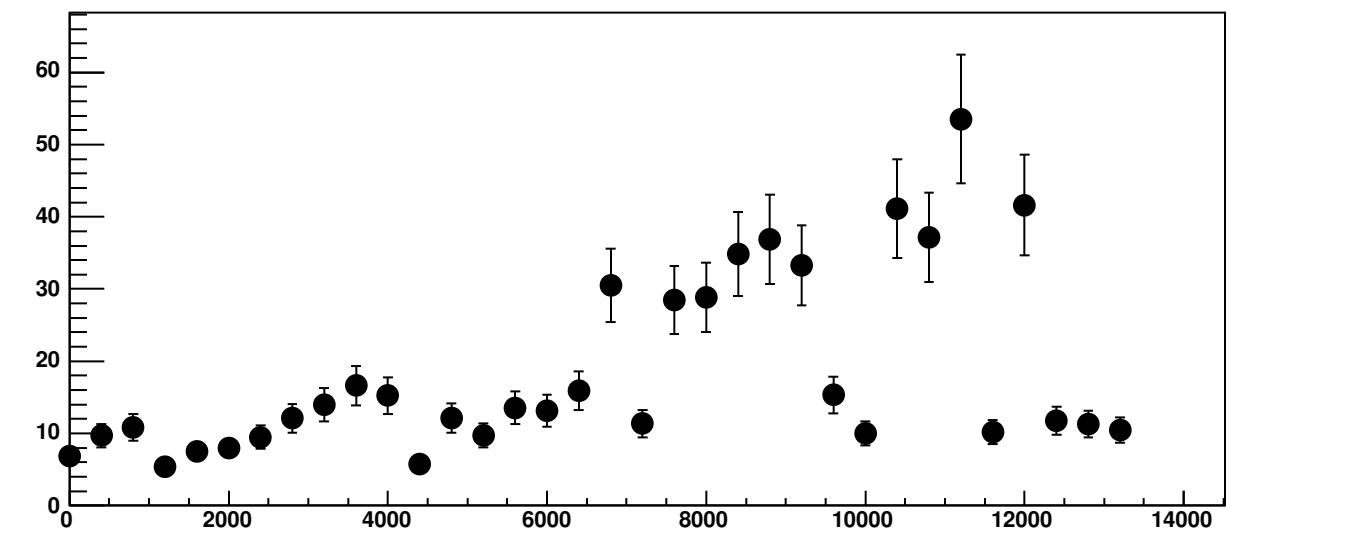

Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

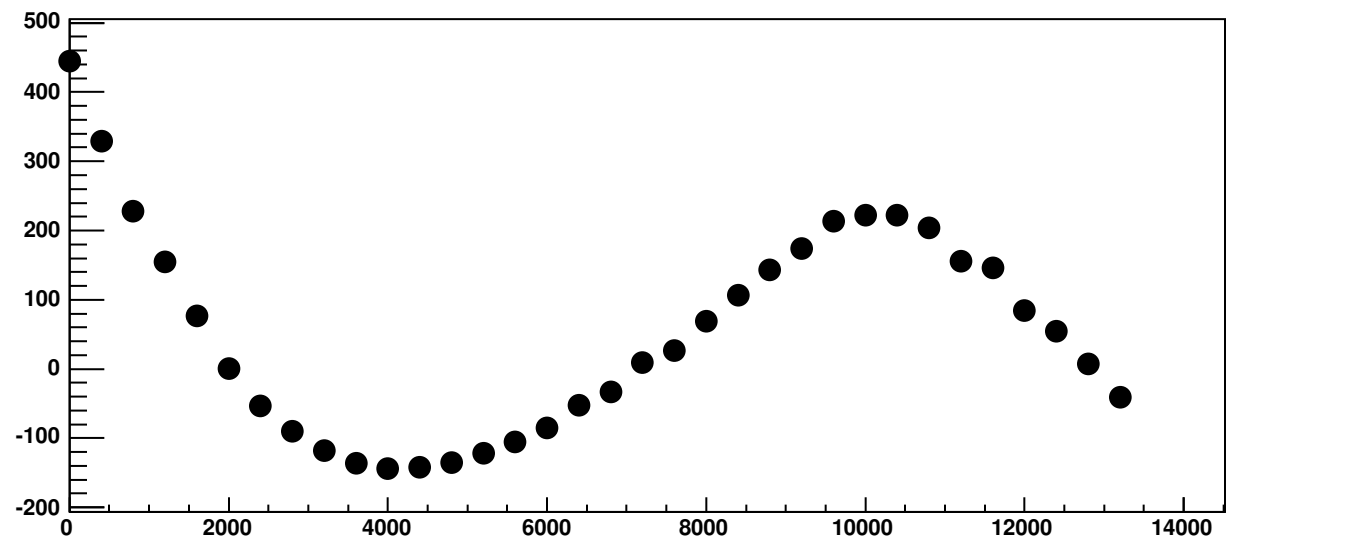
Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

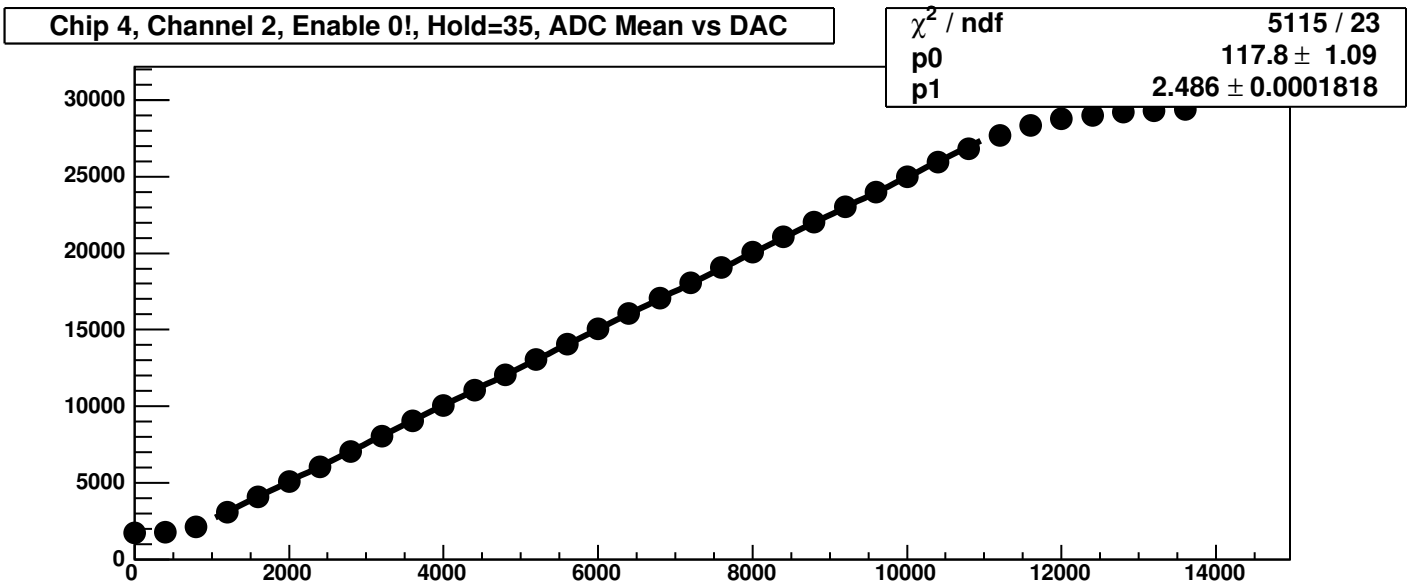


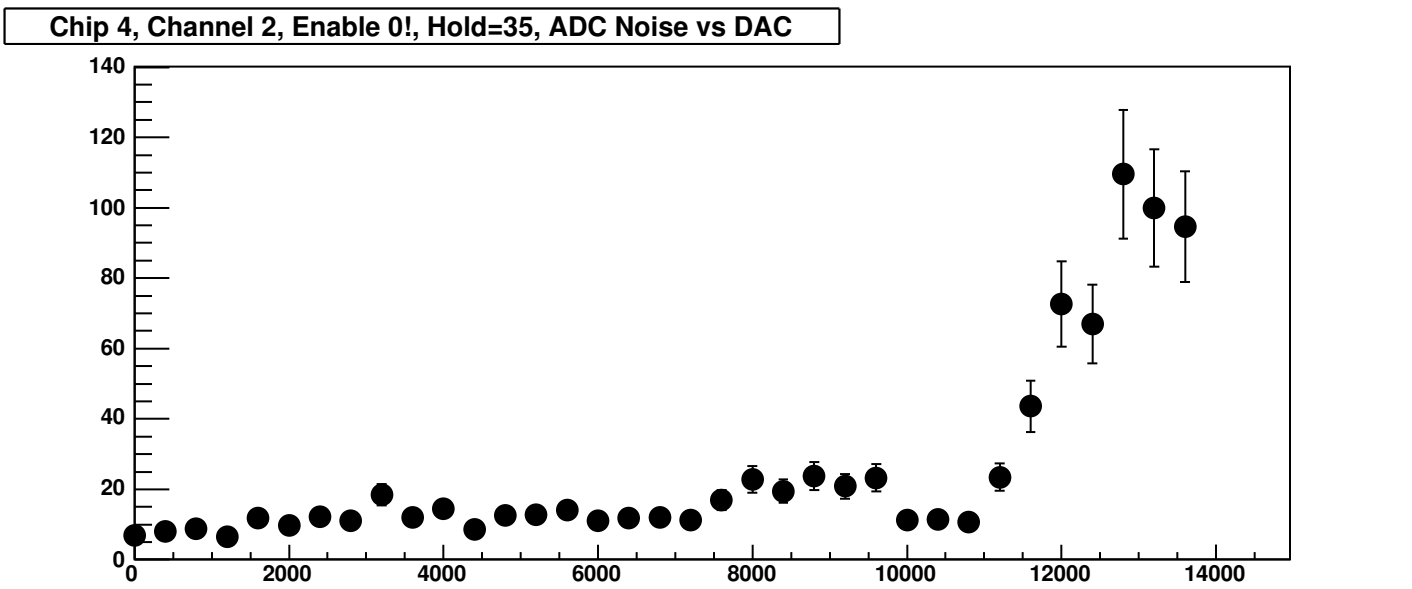
Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

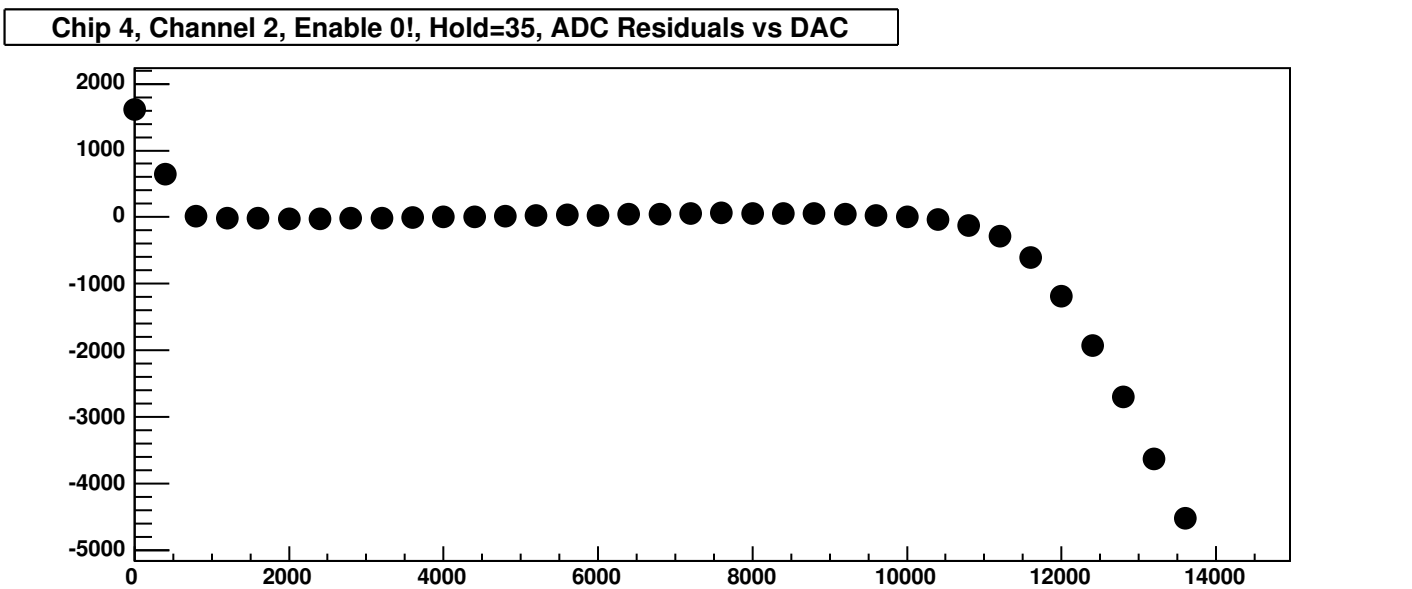


Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

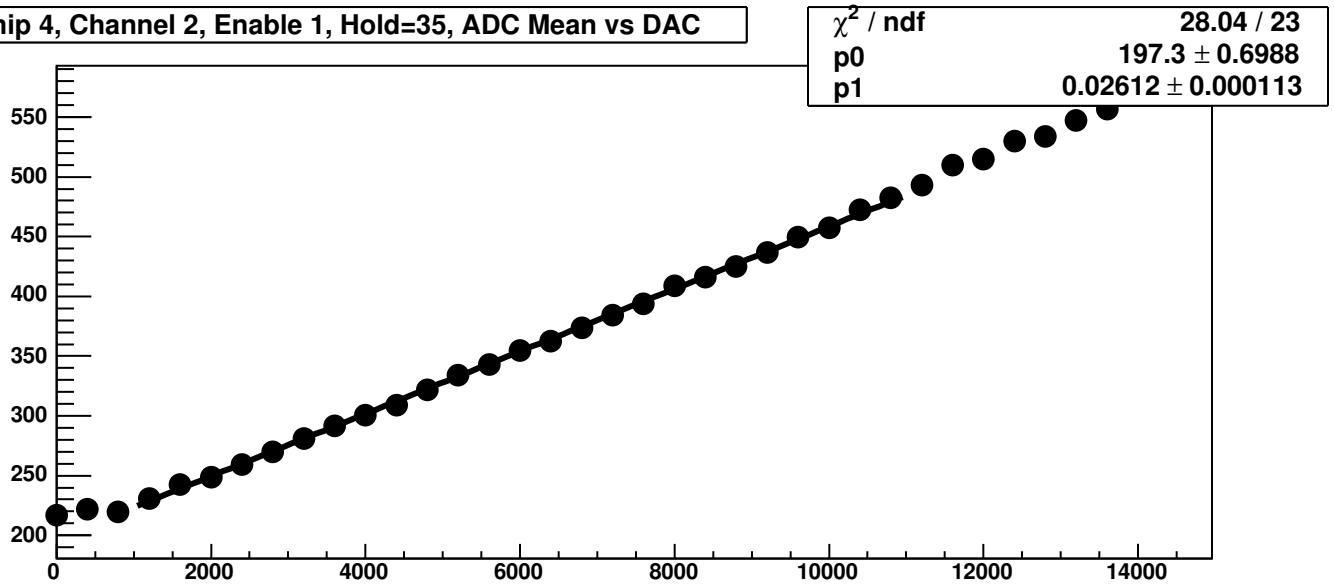



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 5115 / 23
p0 117.8 ± 1.09
p1 2.486 ± 0.0001818
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

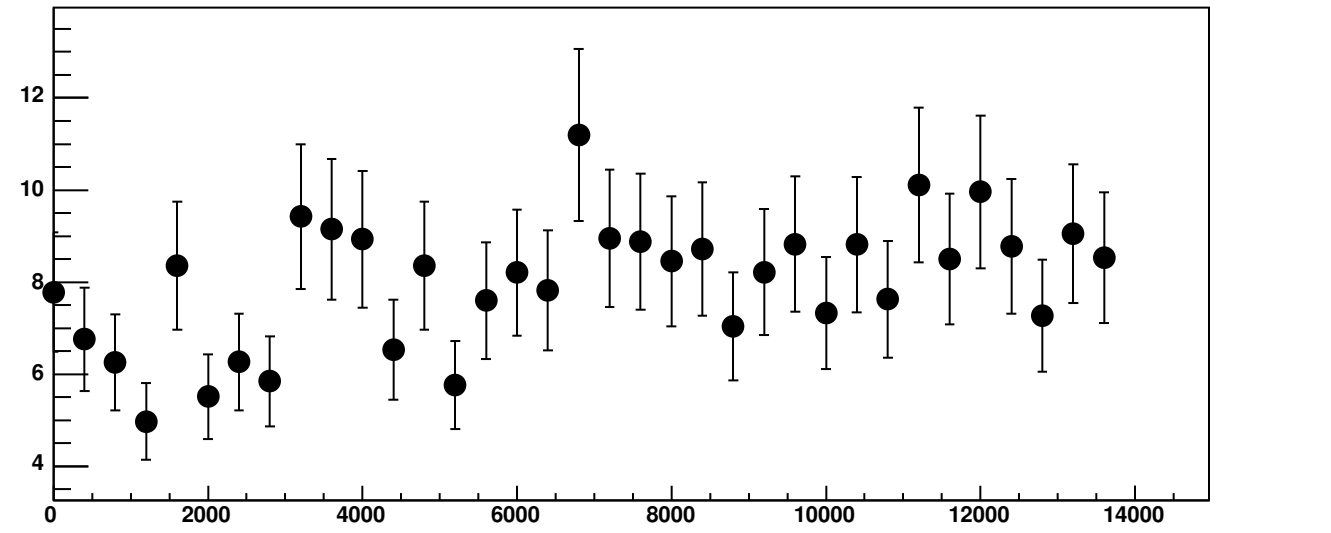

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

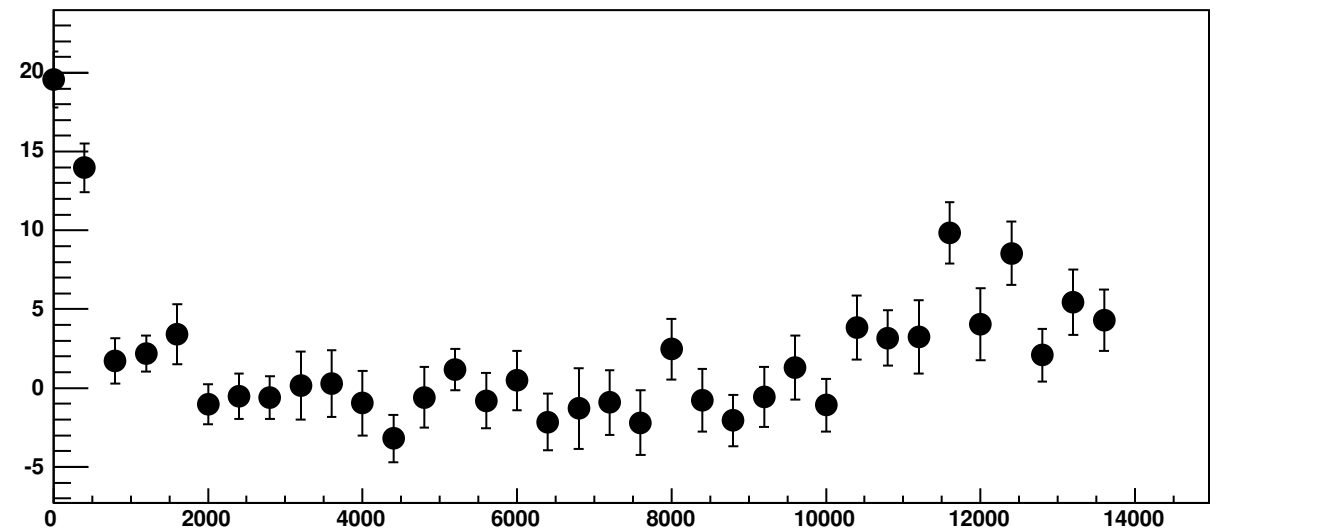
Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



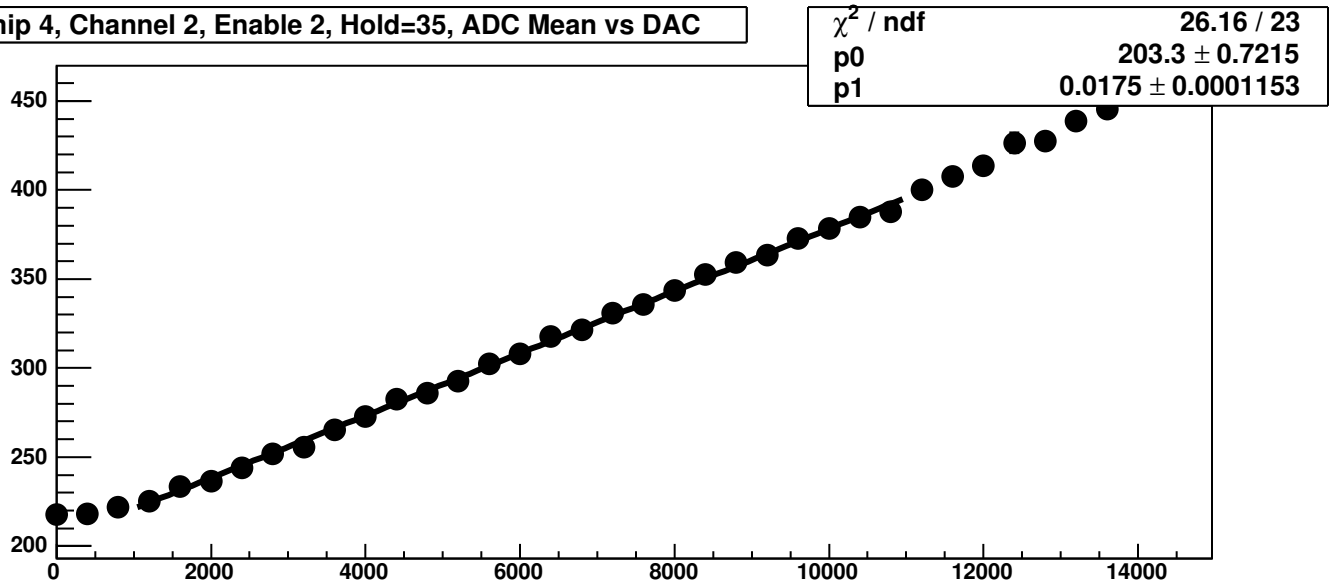
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



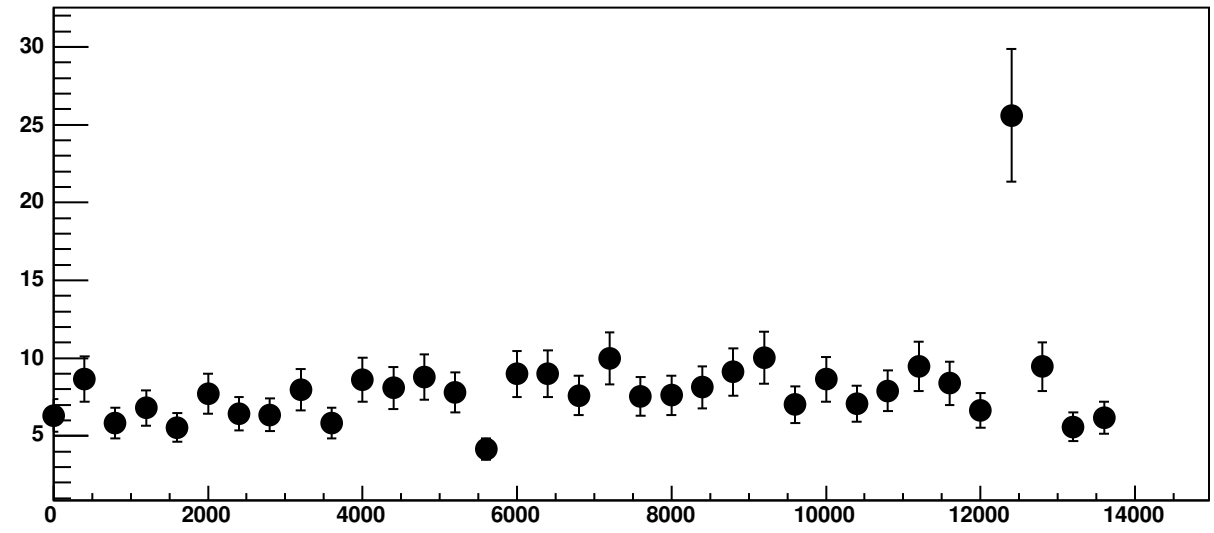
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



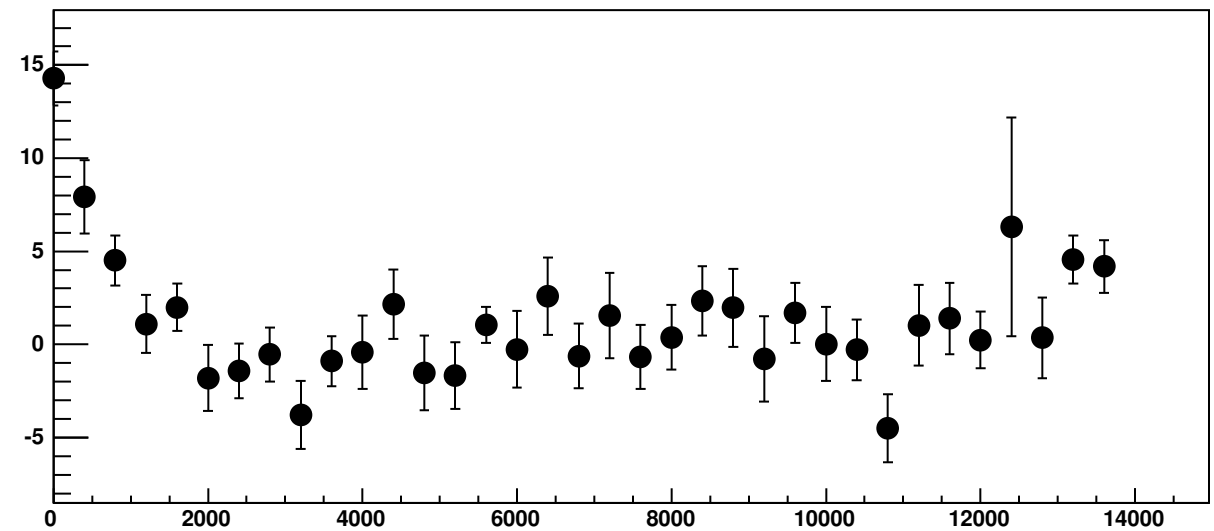
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

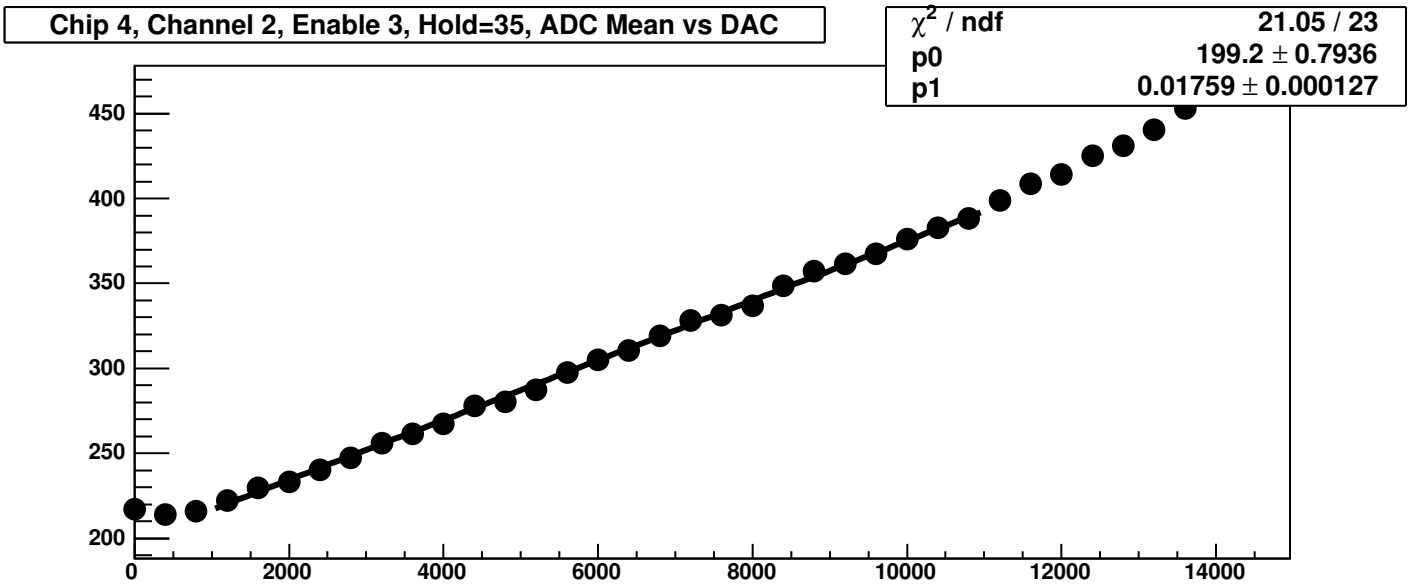


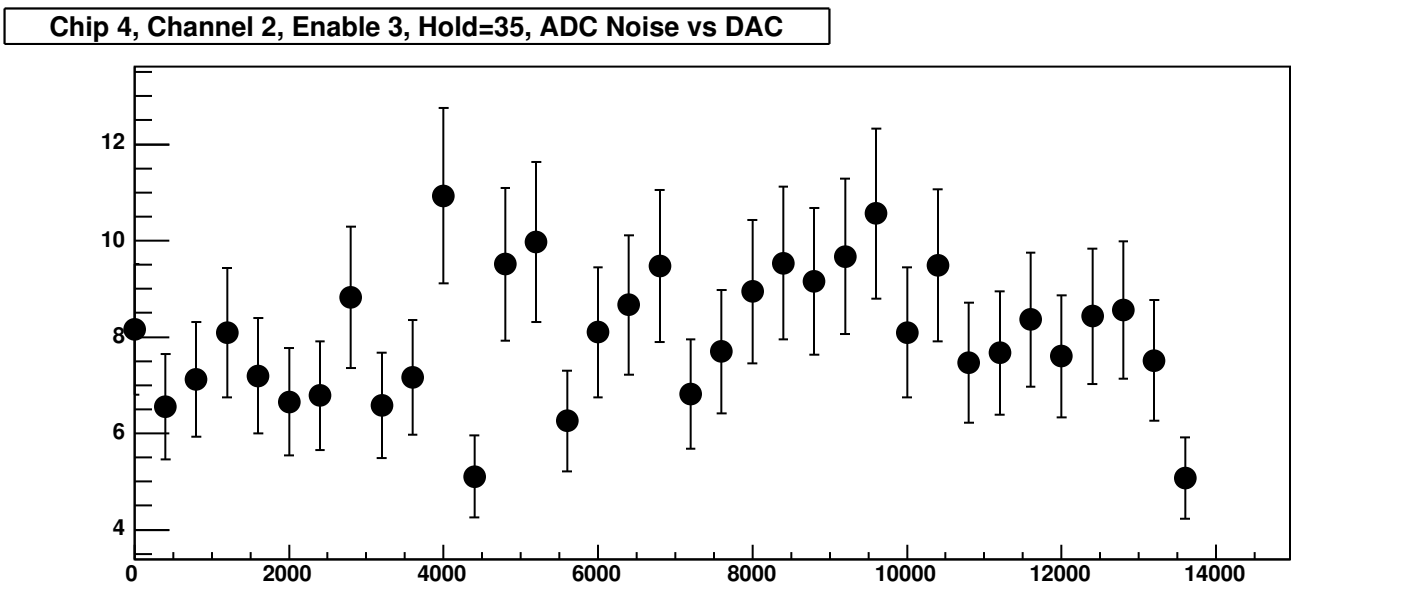
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

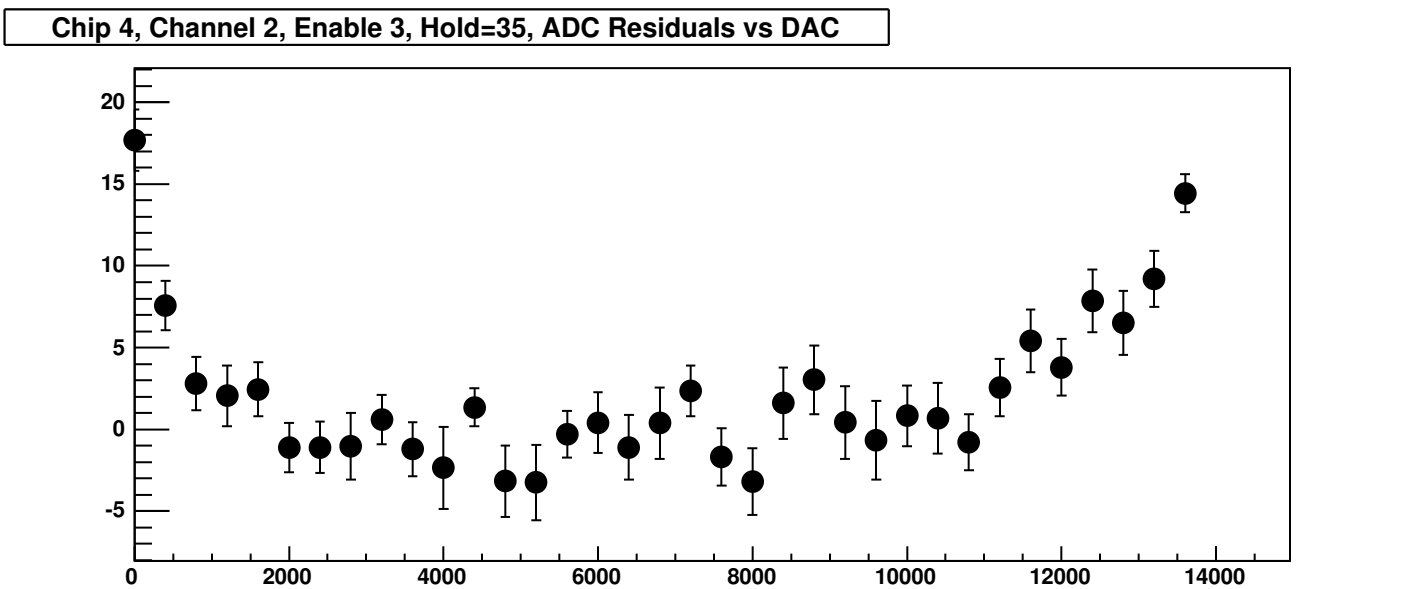


Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

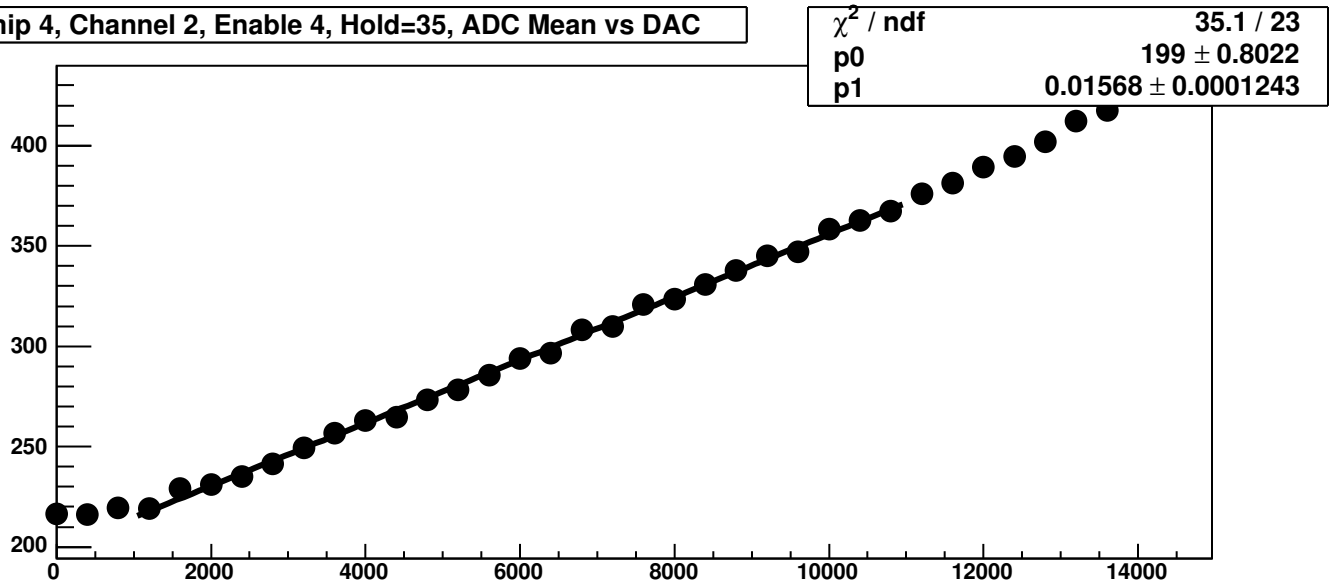



Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.05 / 23
p0 199.2 ± 0.7936
p1 0.01759 ± 0.000127
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

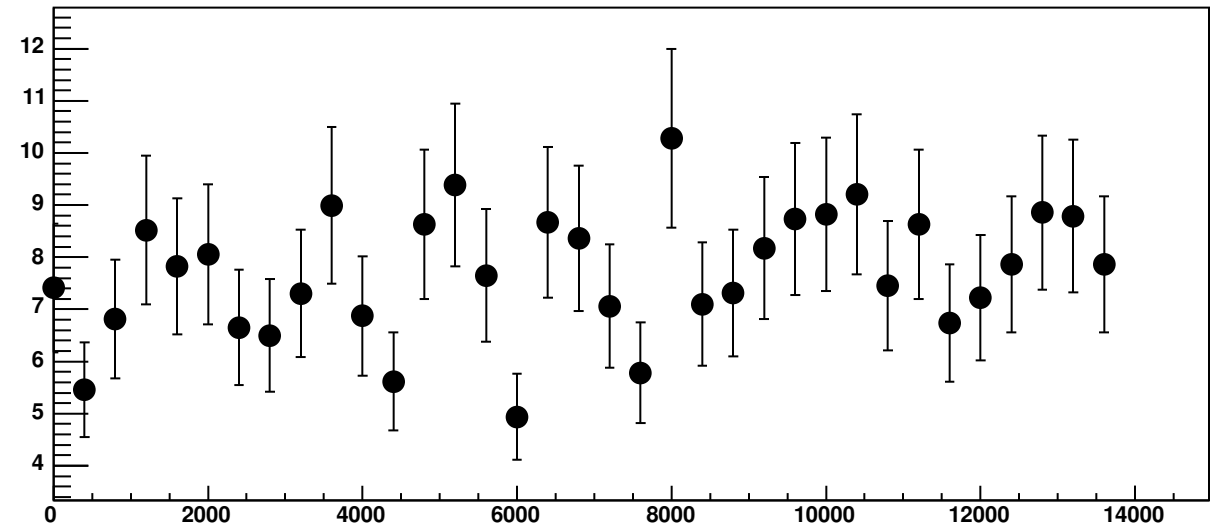

Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

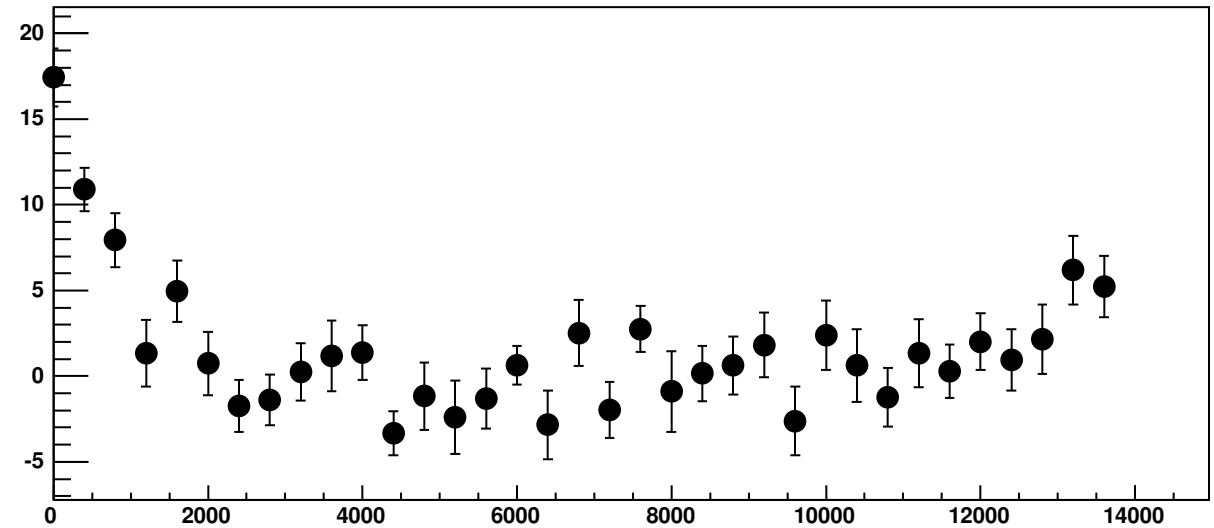
Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



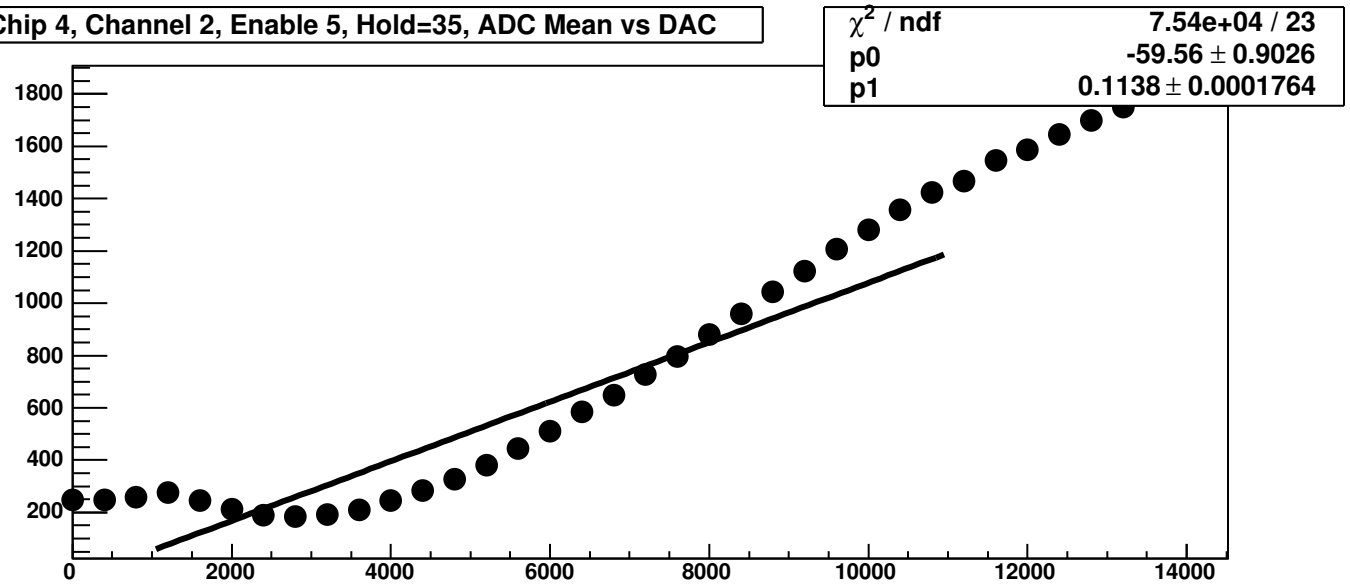
Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



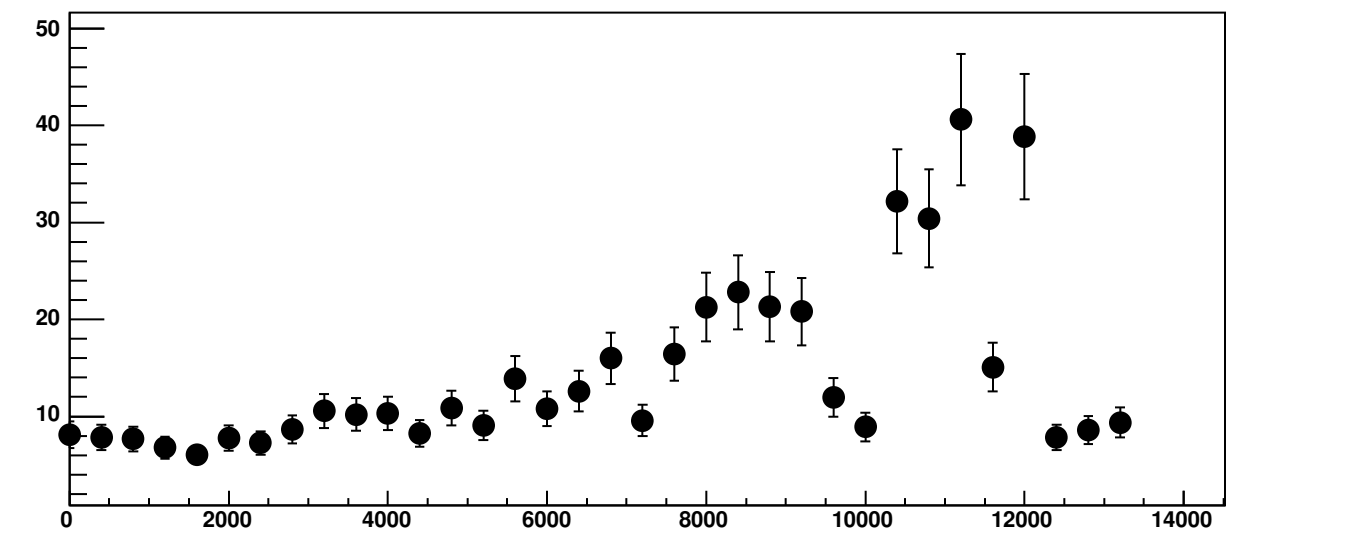
Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



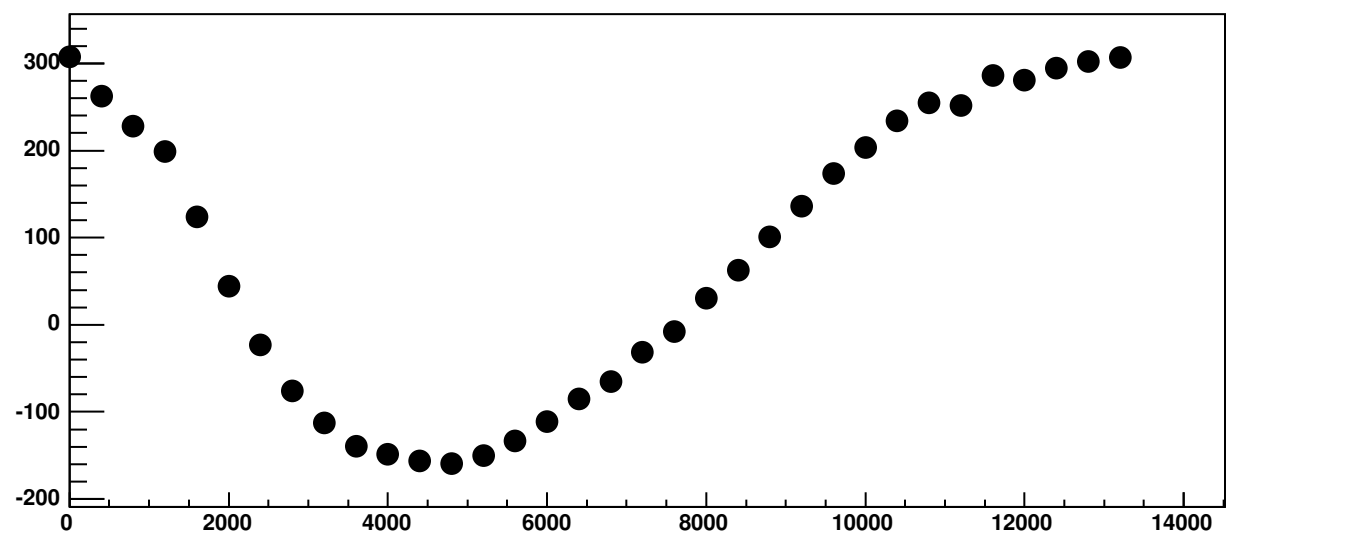
Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

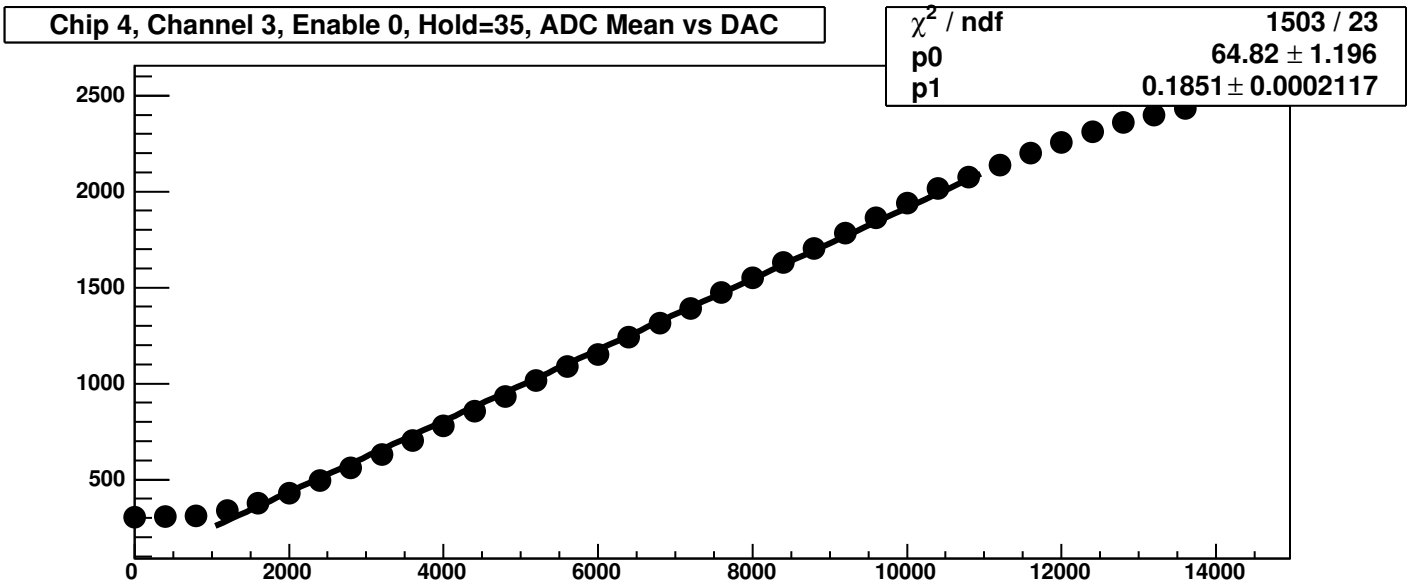


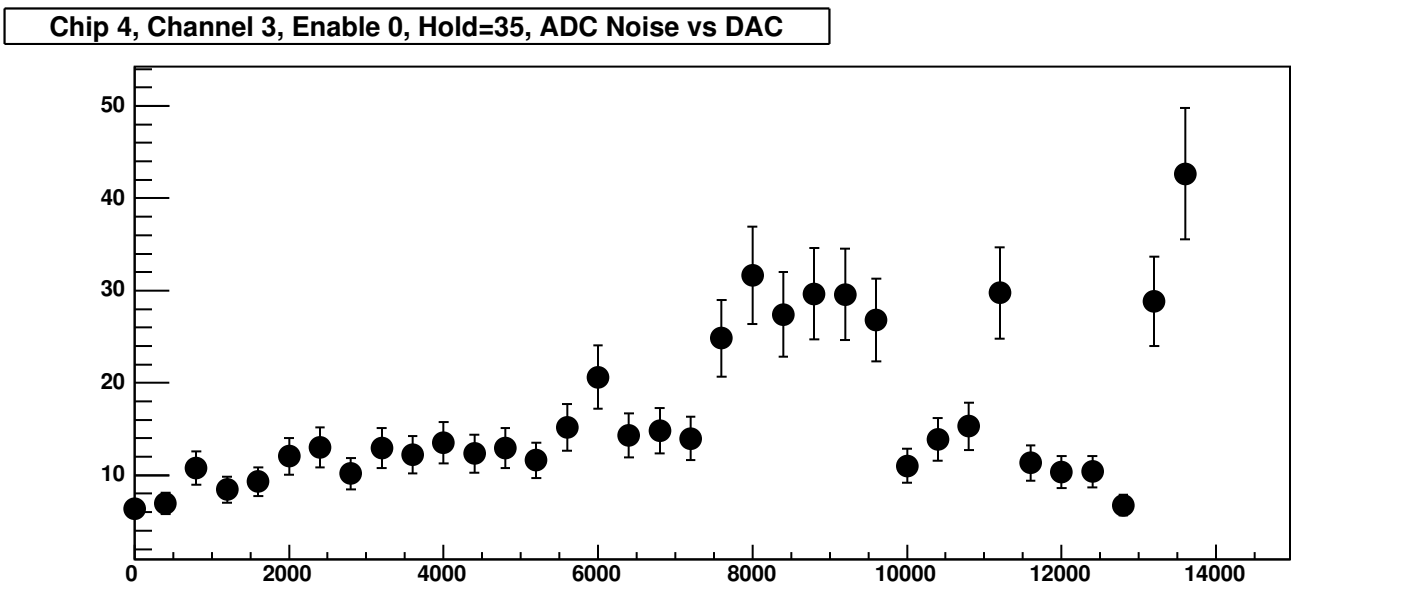
Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

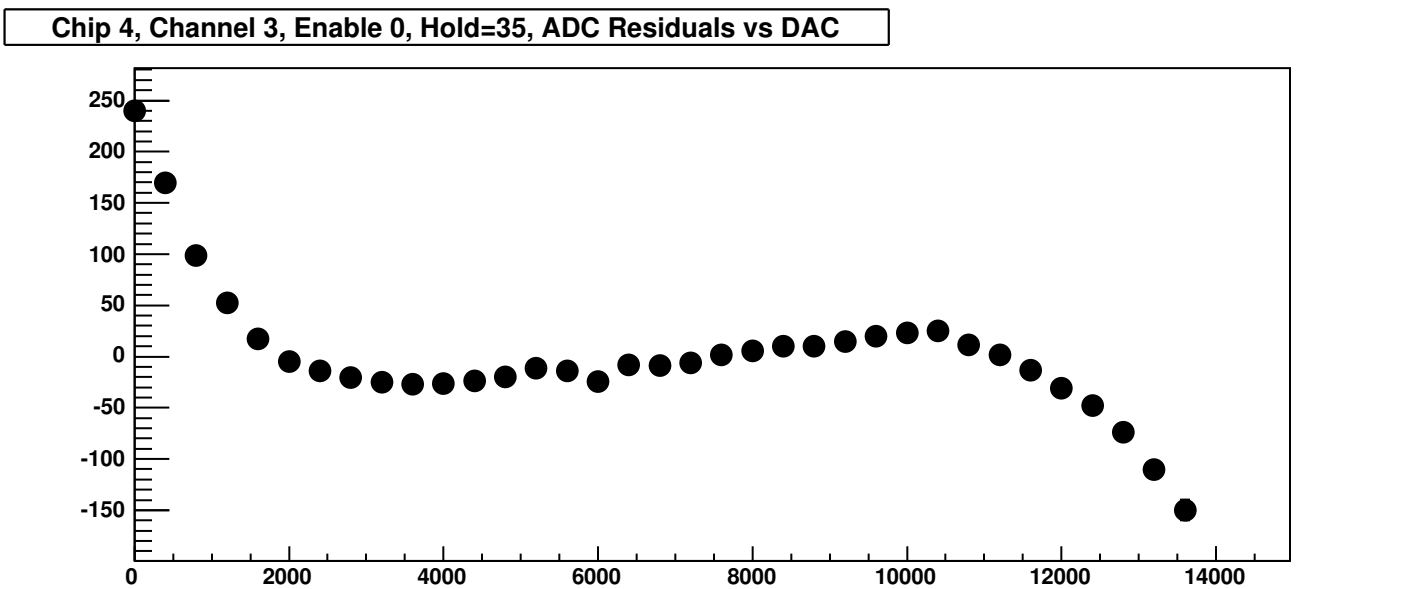


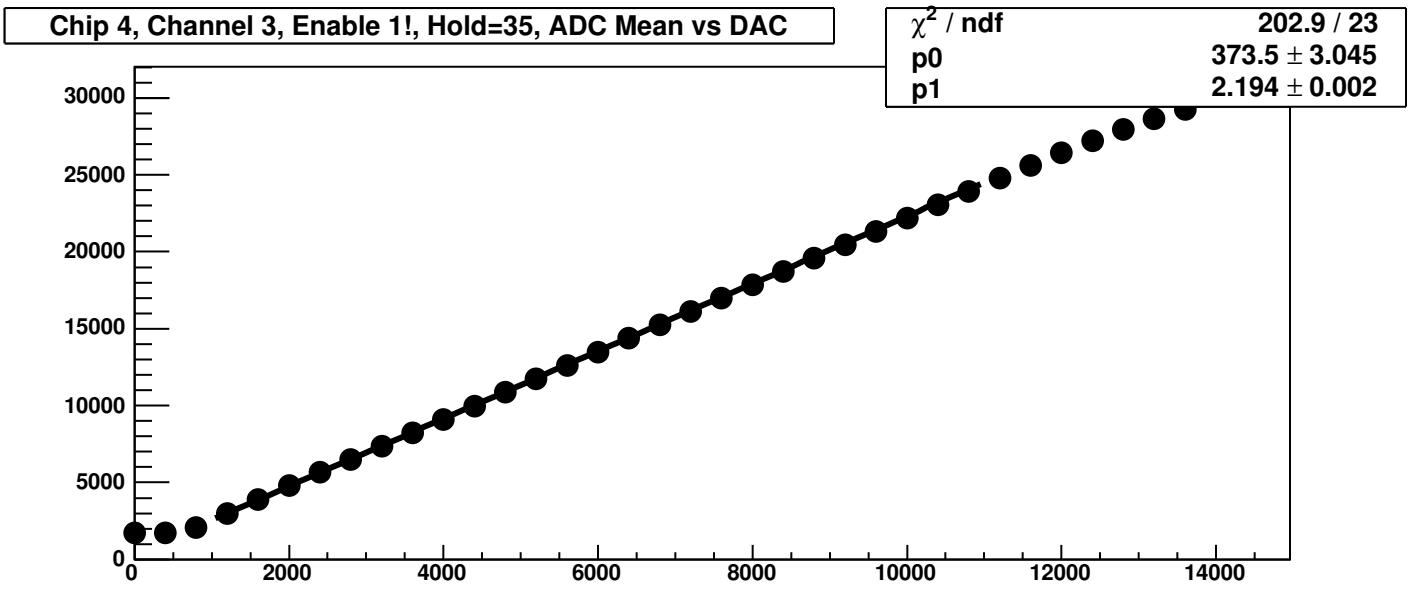
Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

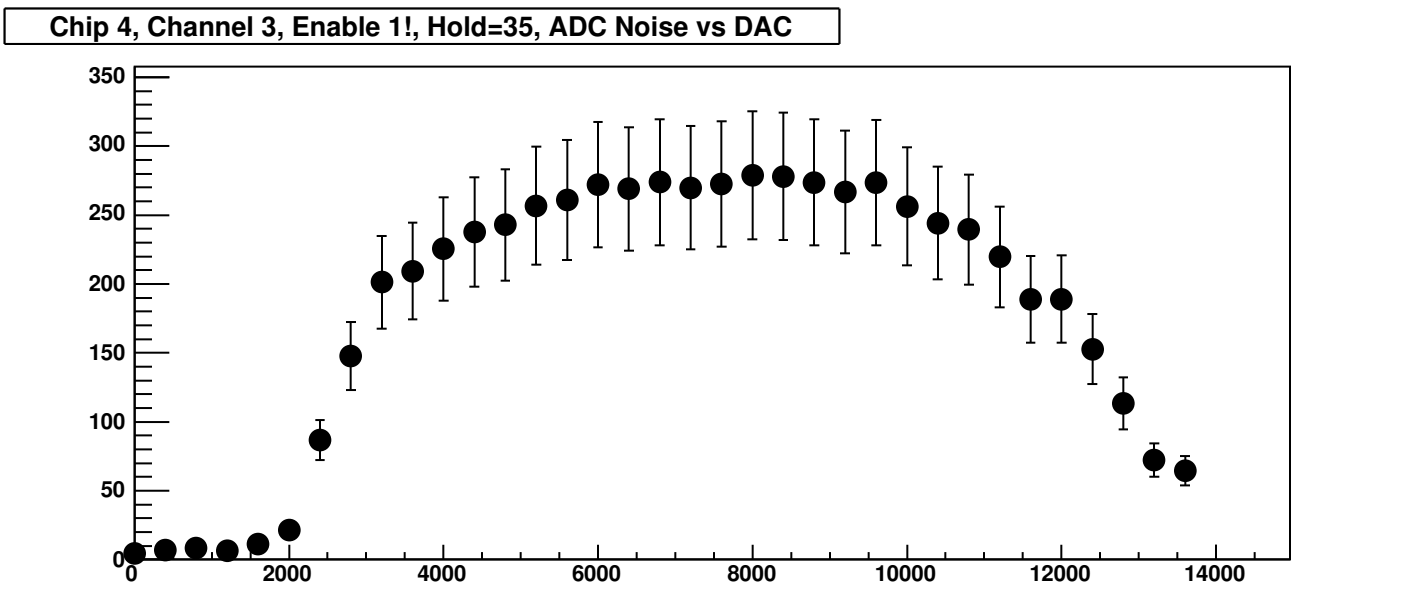


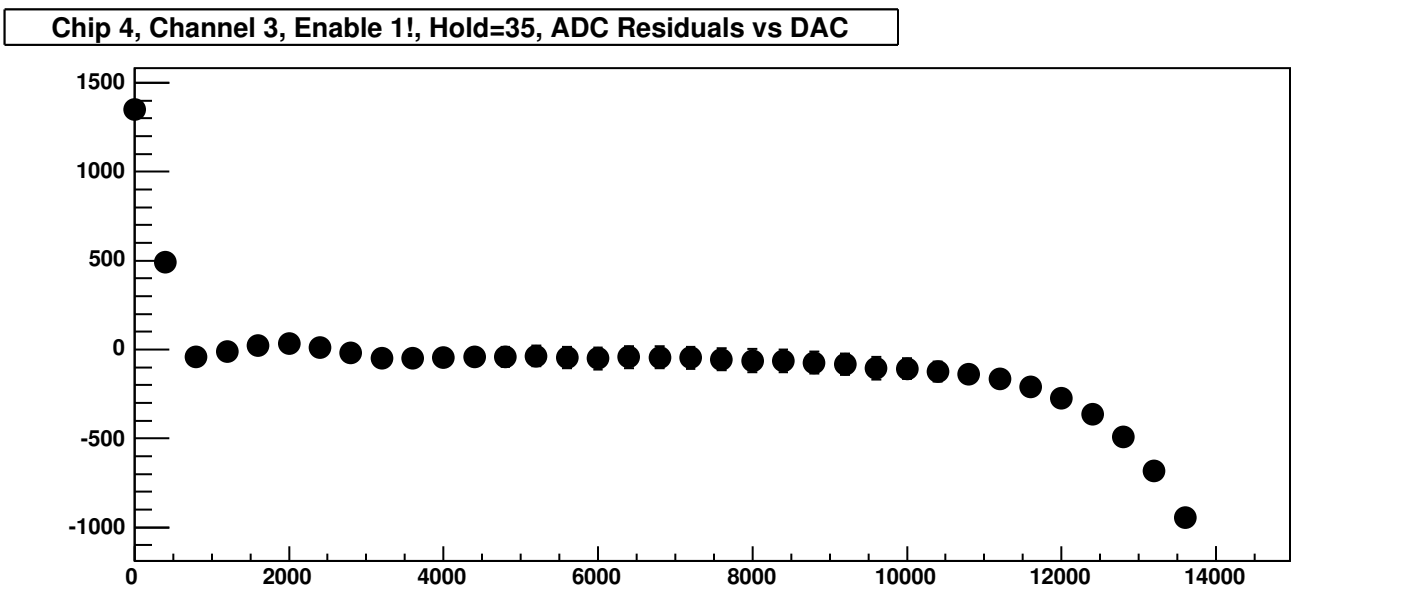

Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1503 / 23
p0 64.82 ± 1.196
p1 0.1851 ± 0.0002117
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

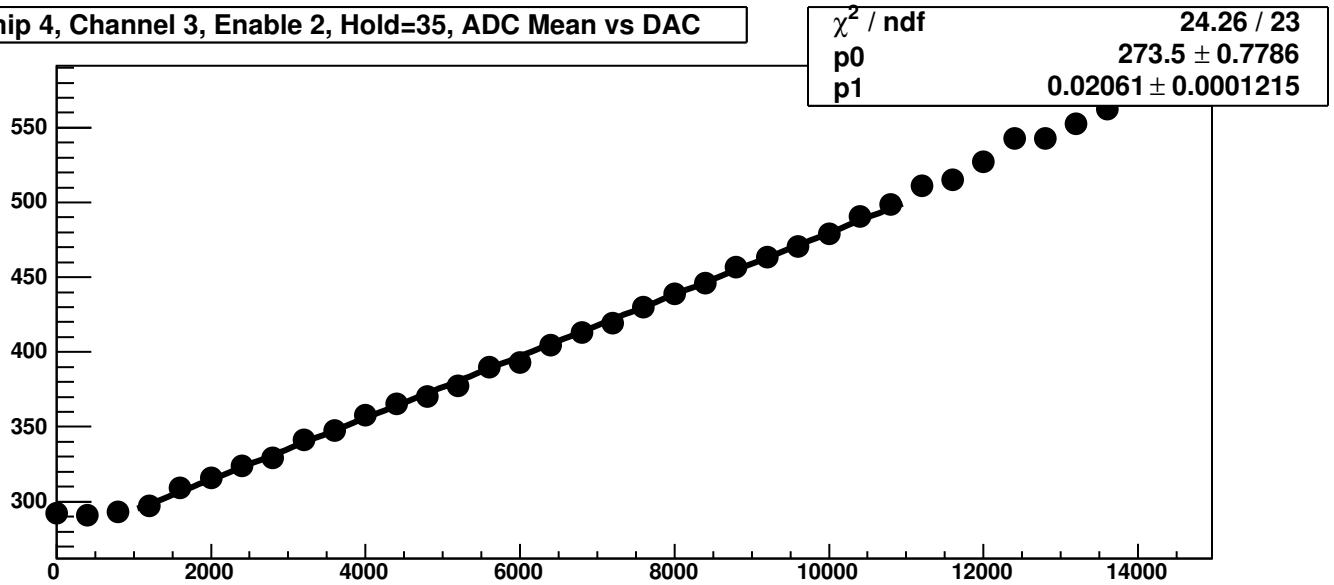

Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 202.9 / 23
p0 373.5 ± 3.045
p1 2.194 ± 0.002
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

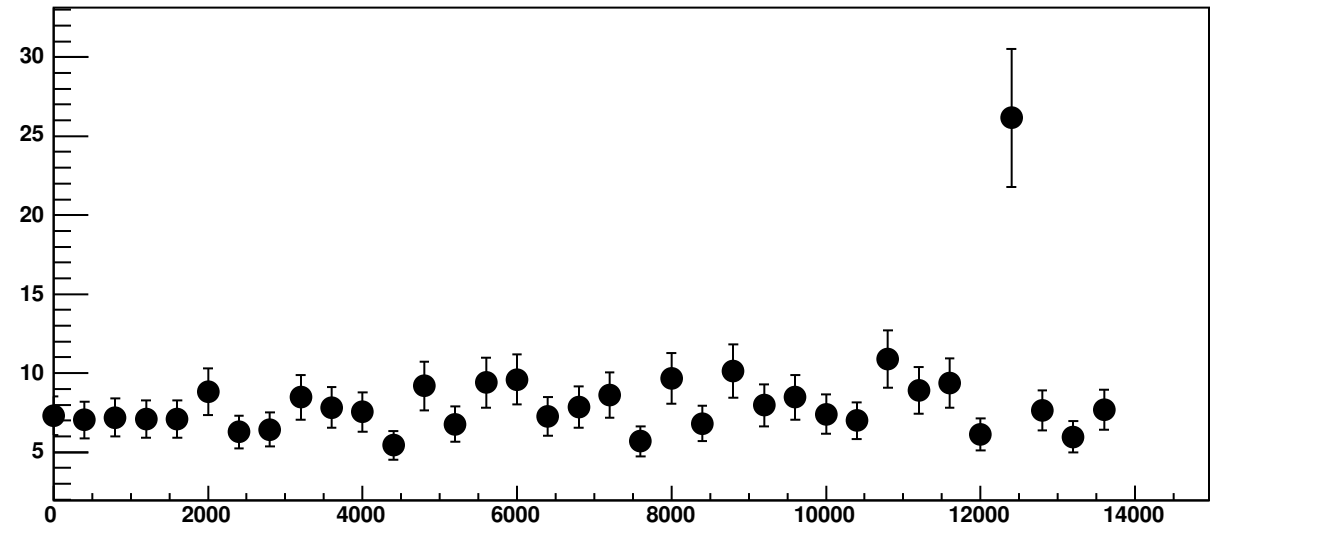

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

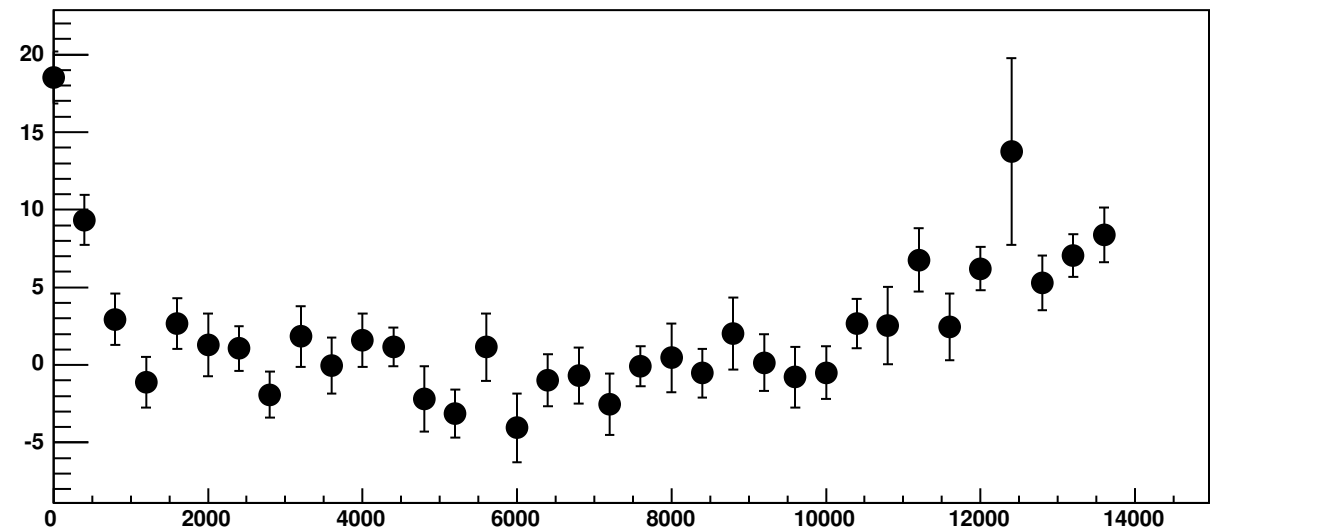
Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

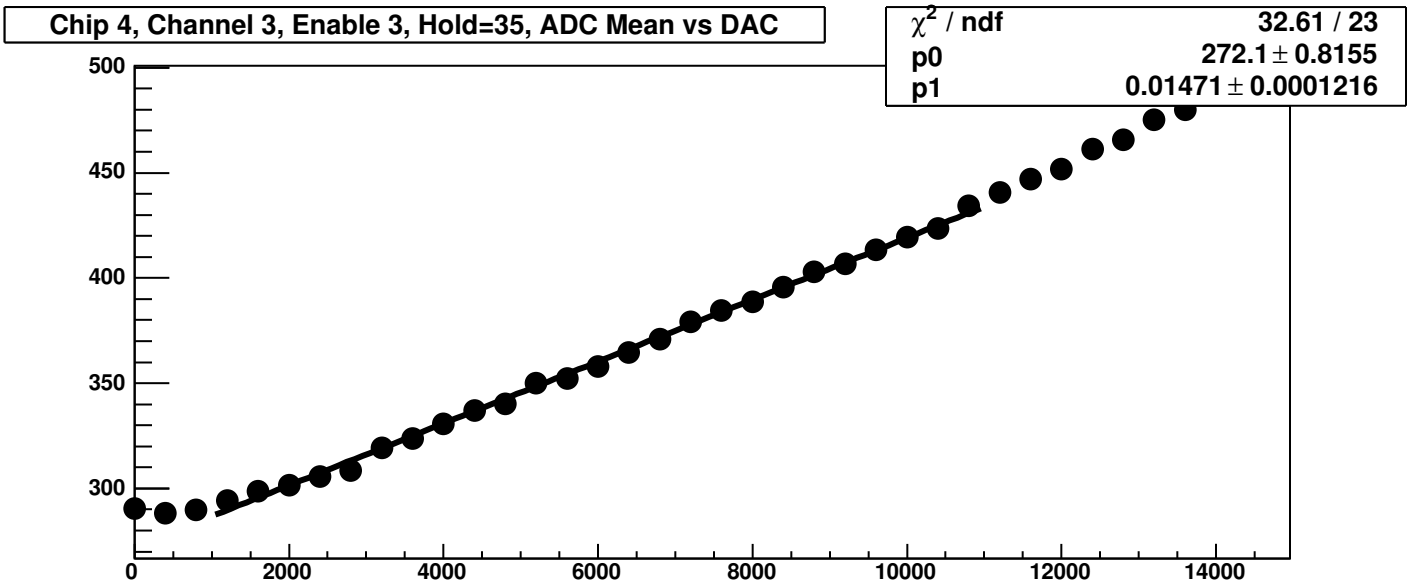


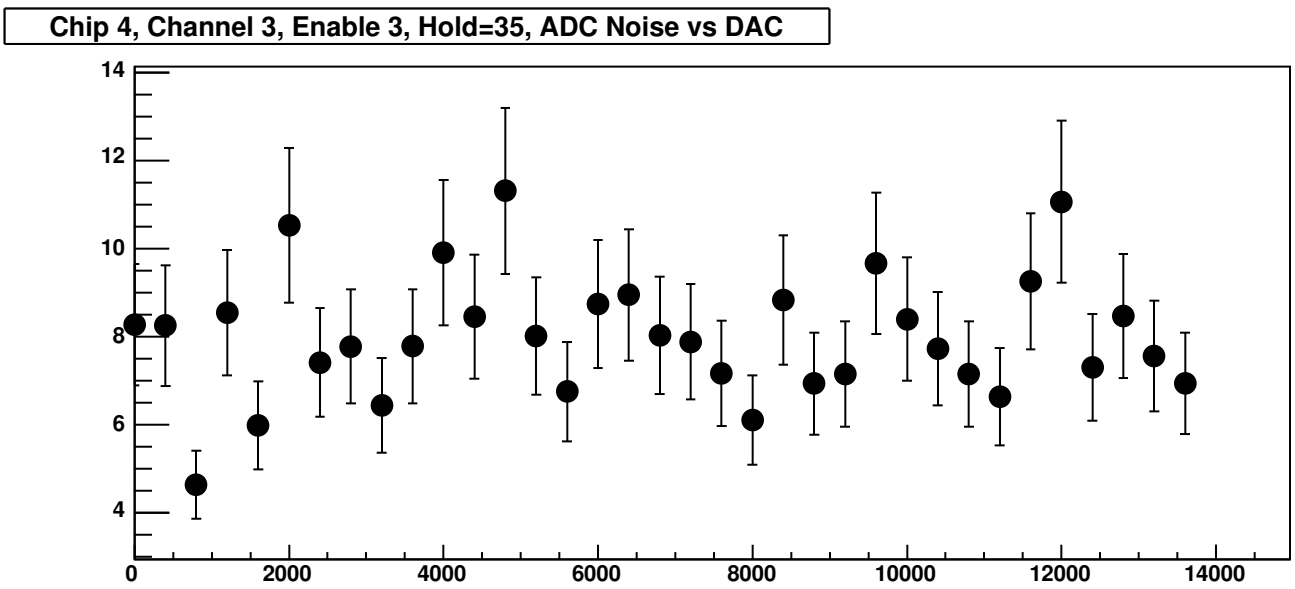
Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

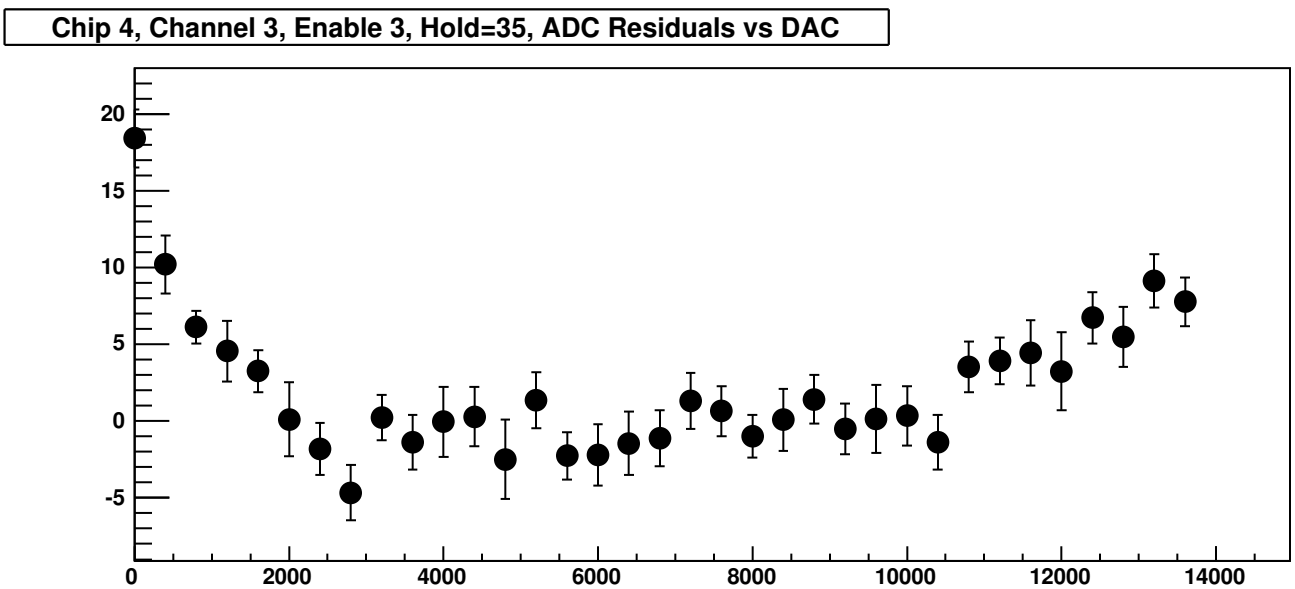


Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

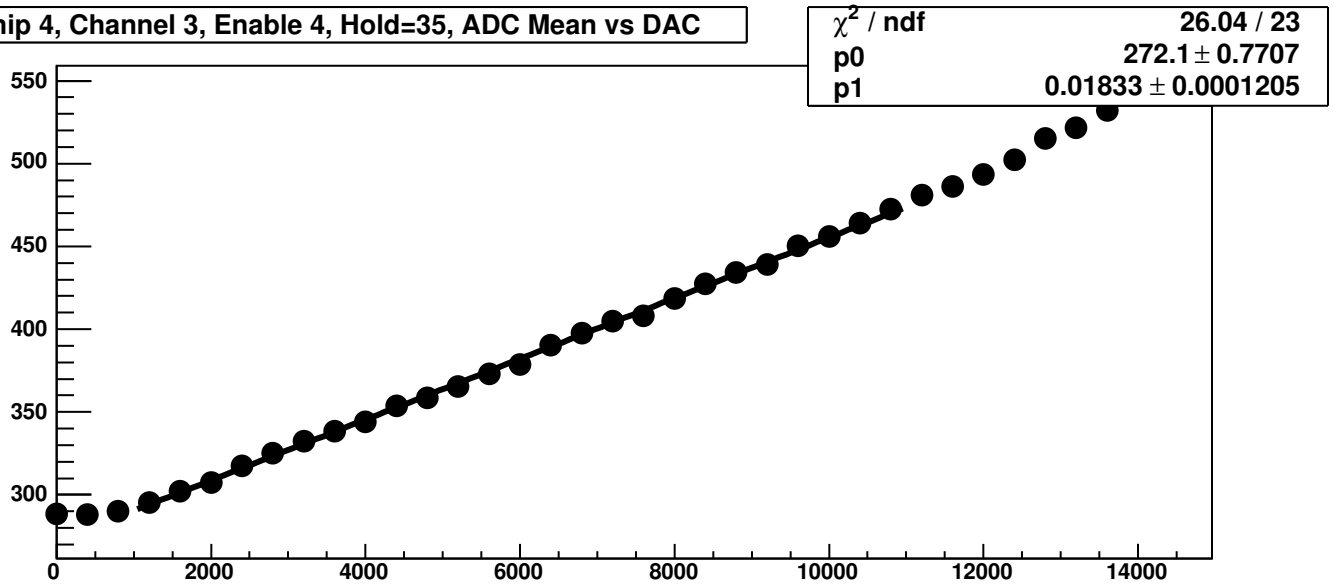



Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.61 / 23
p0 272.1 ± 0.8155
p1 0.01471 ± 0.0001216
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

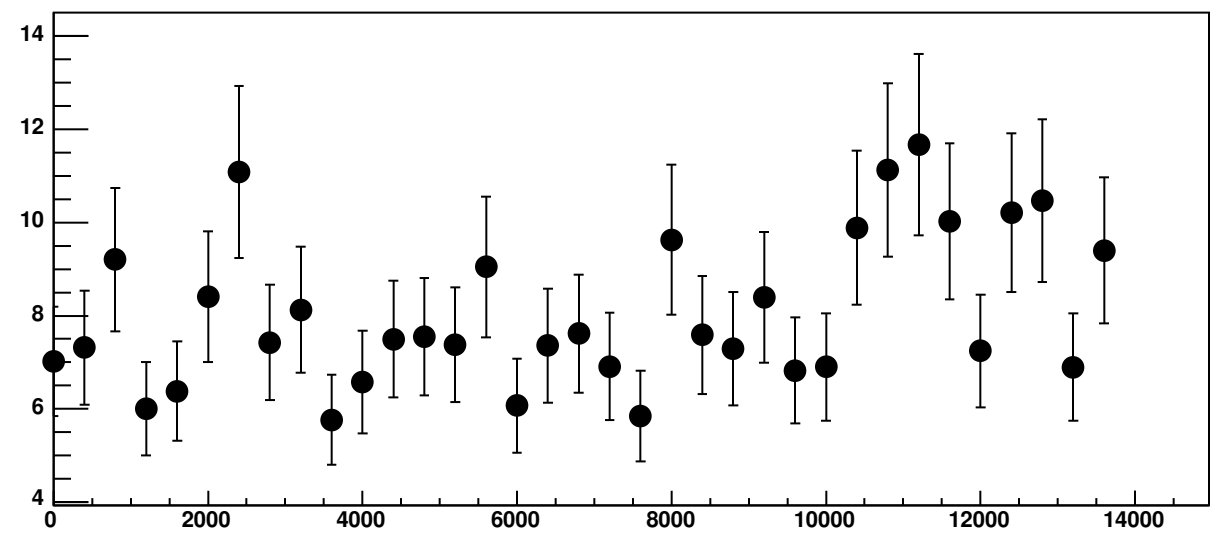

Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

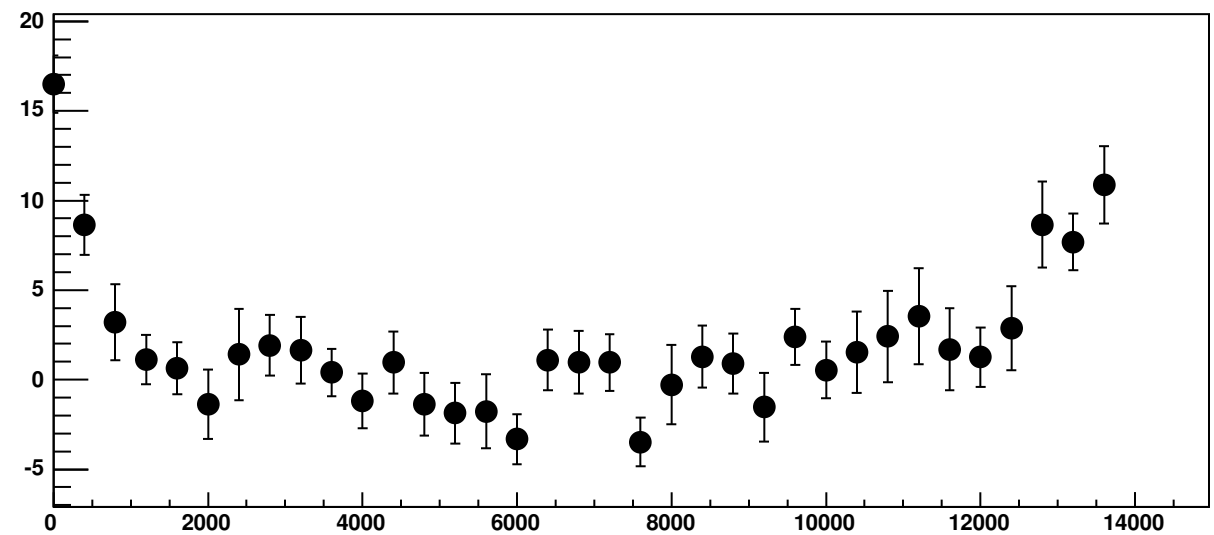
Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC



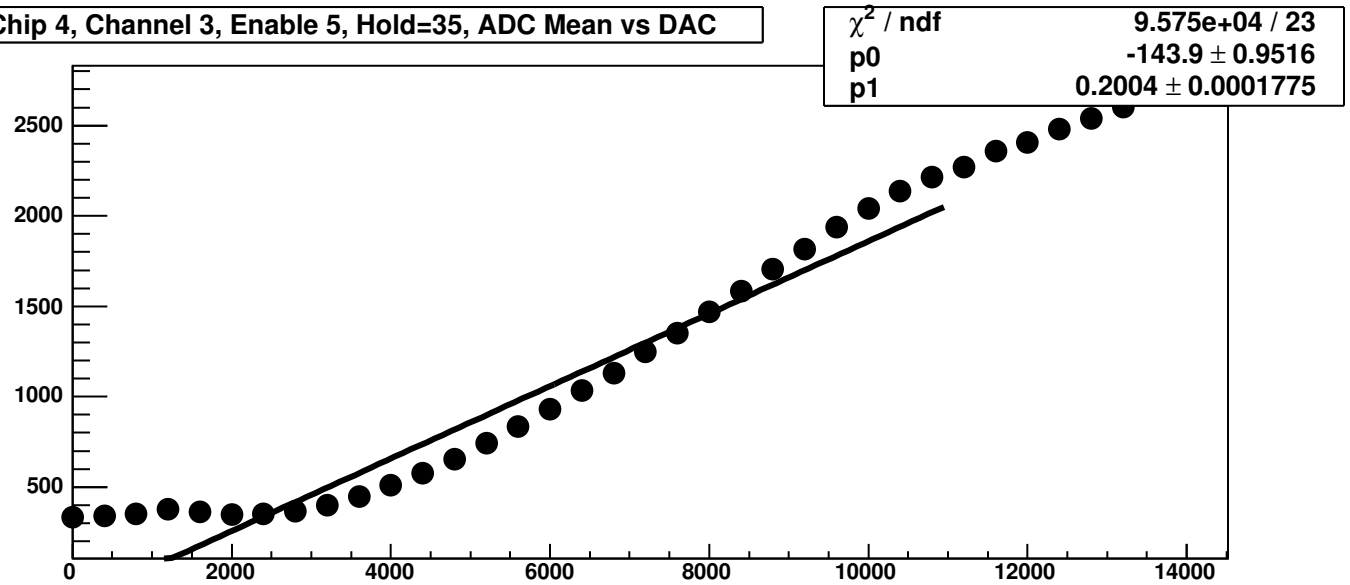
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC



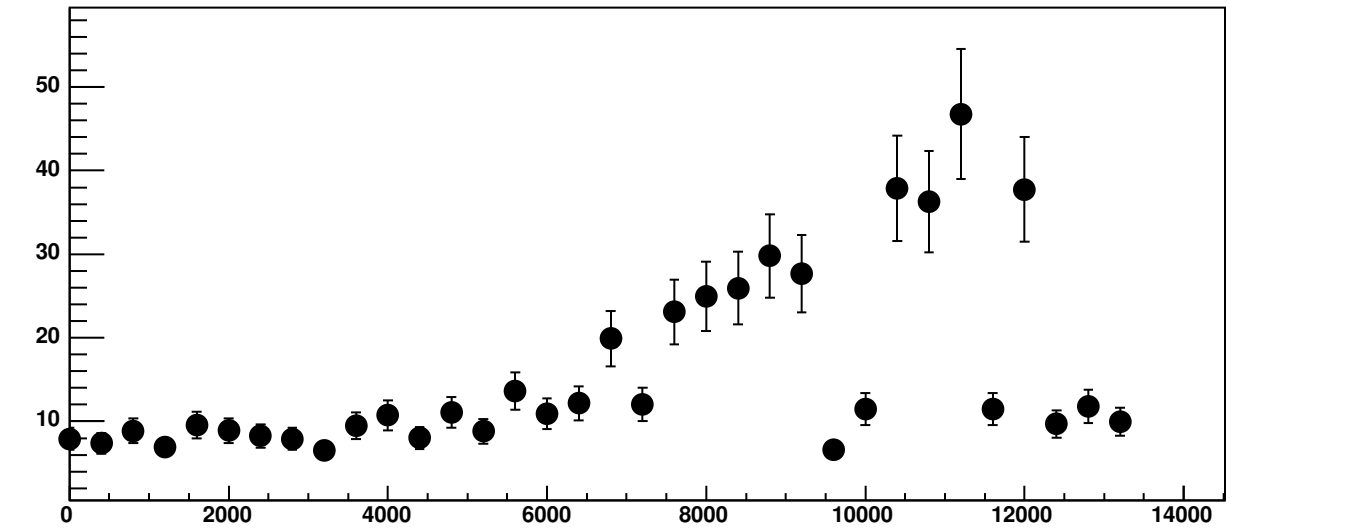
Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



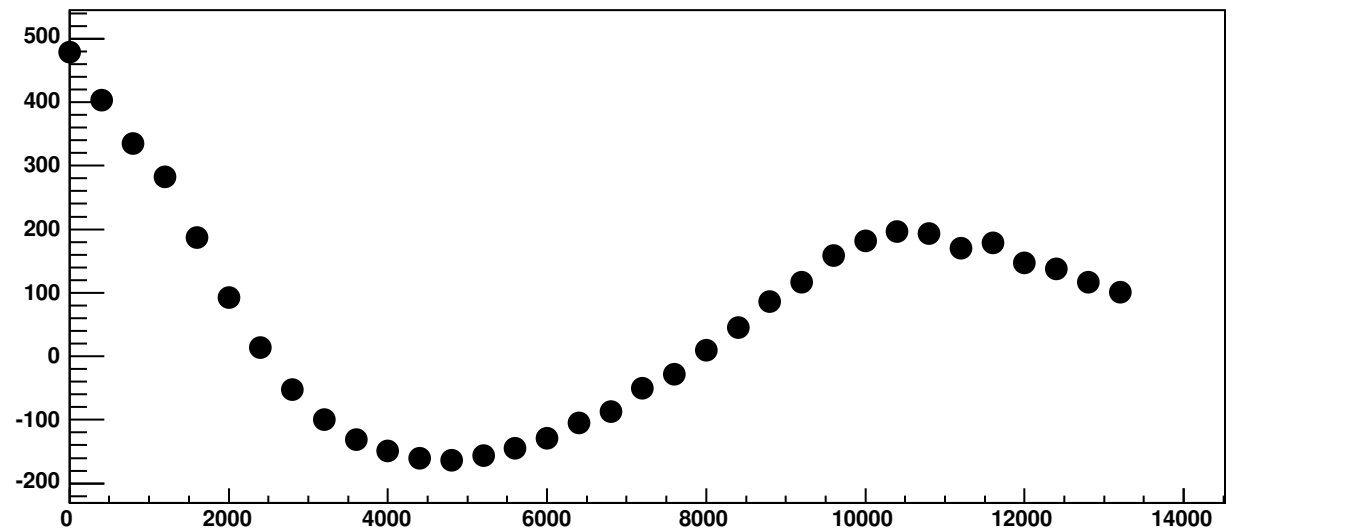
Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

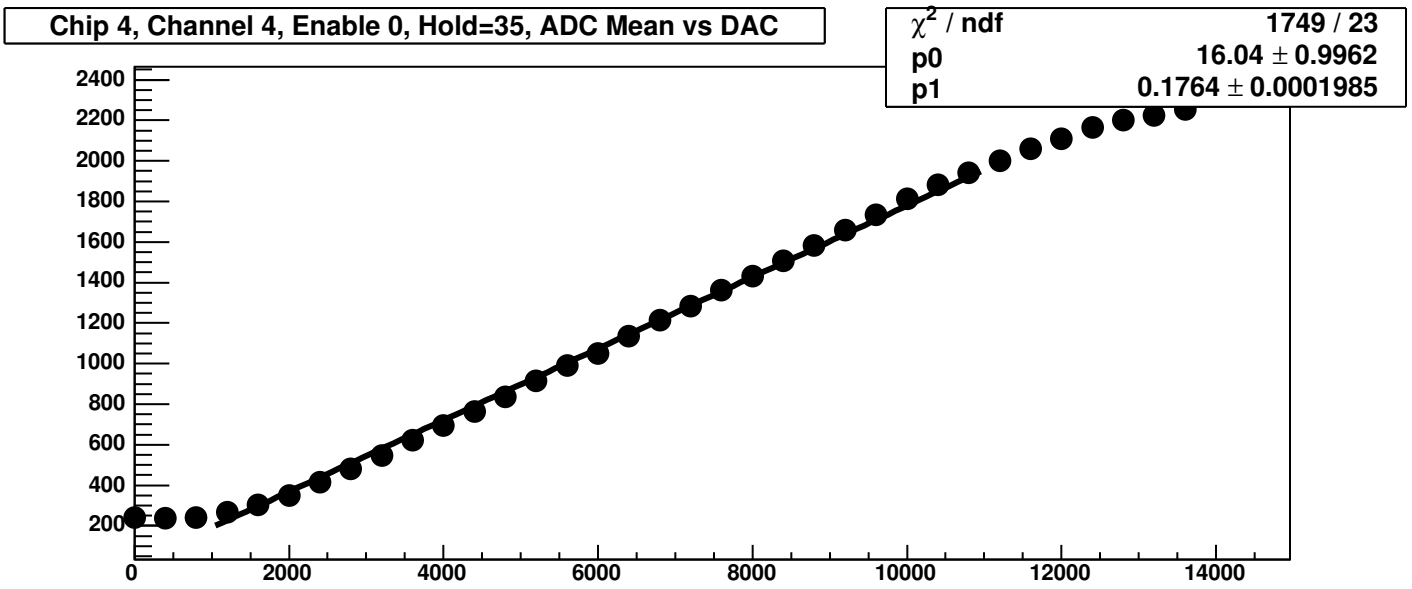


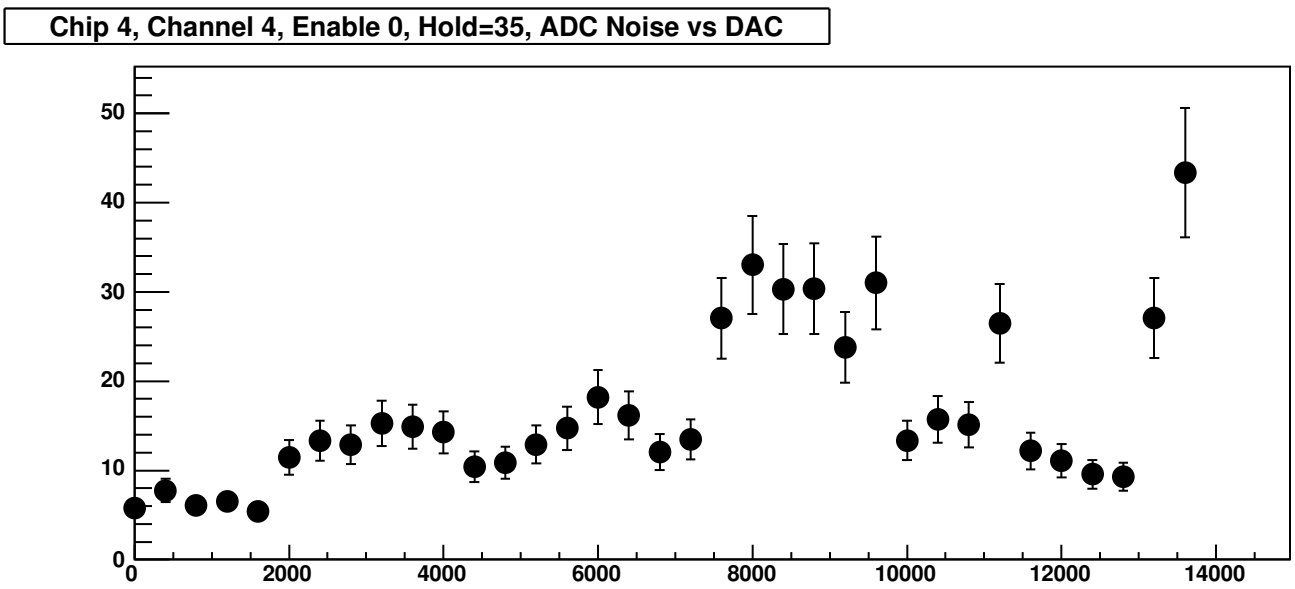
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

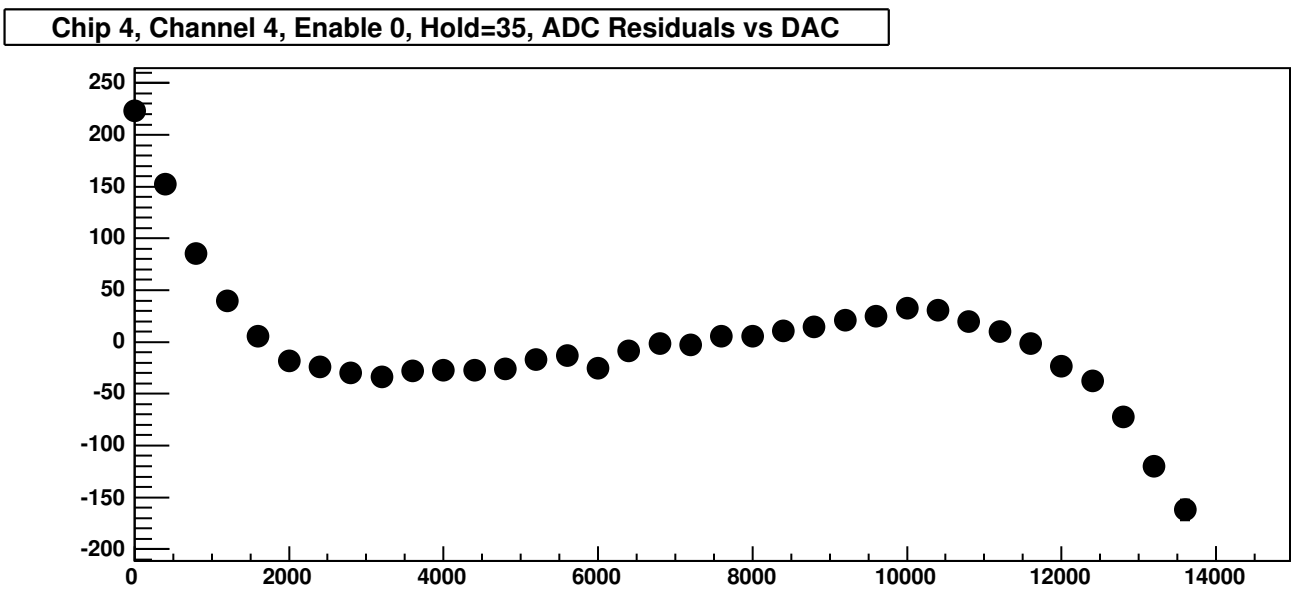


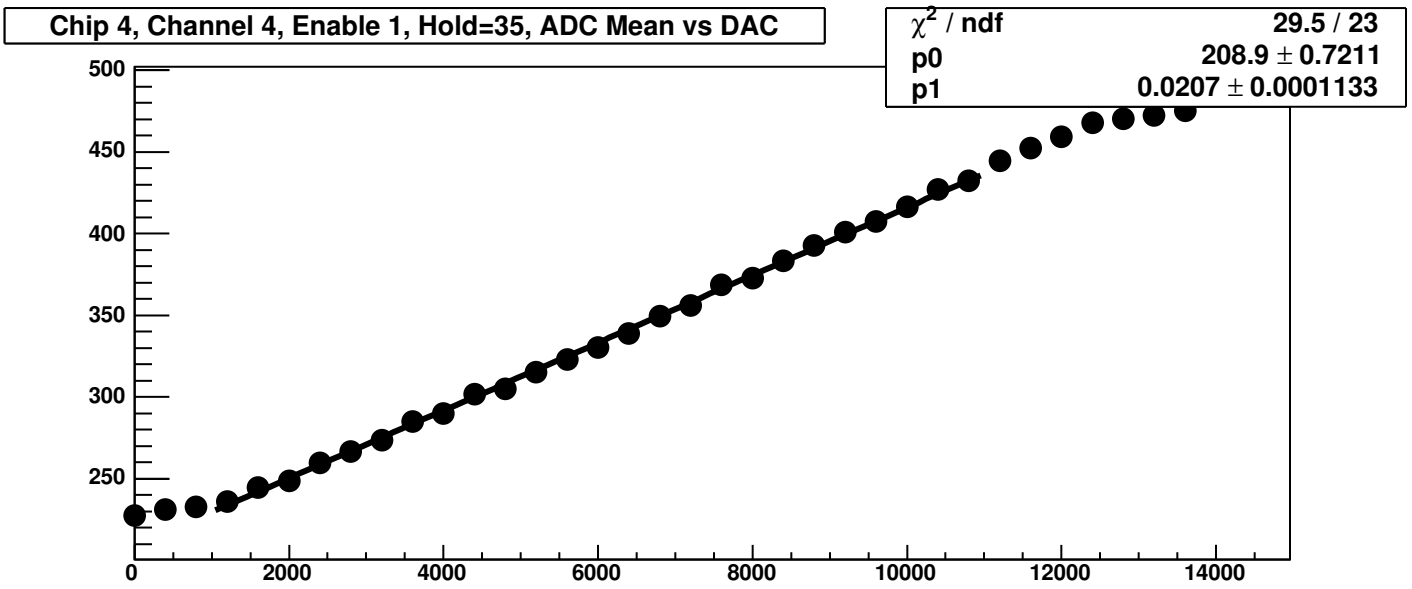
Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

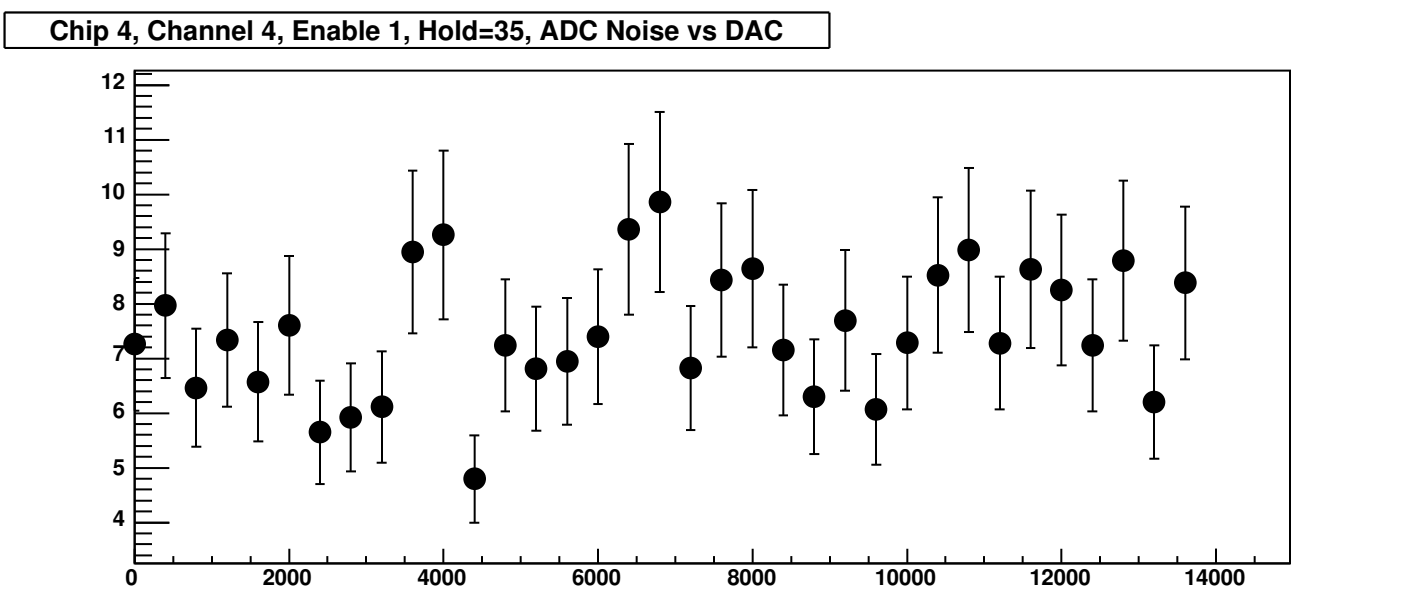


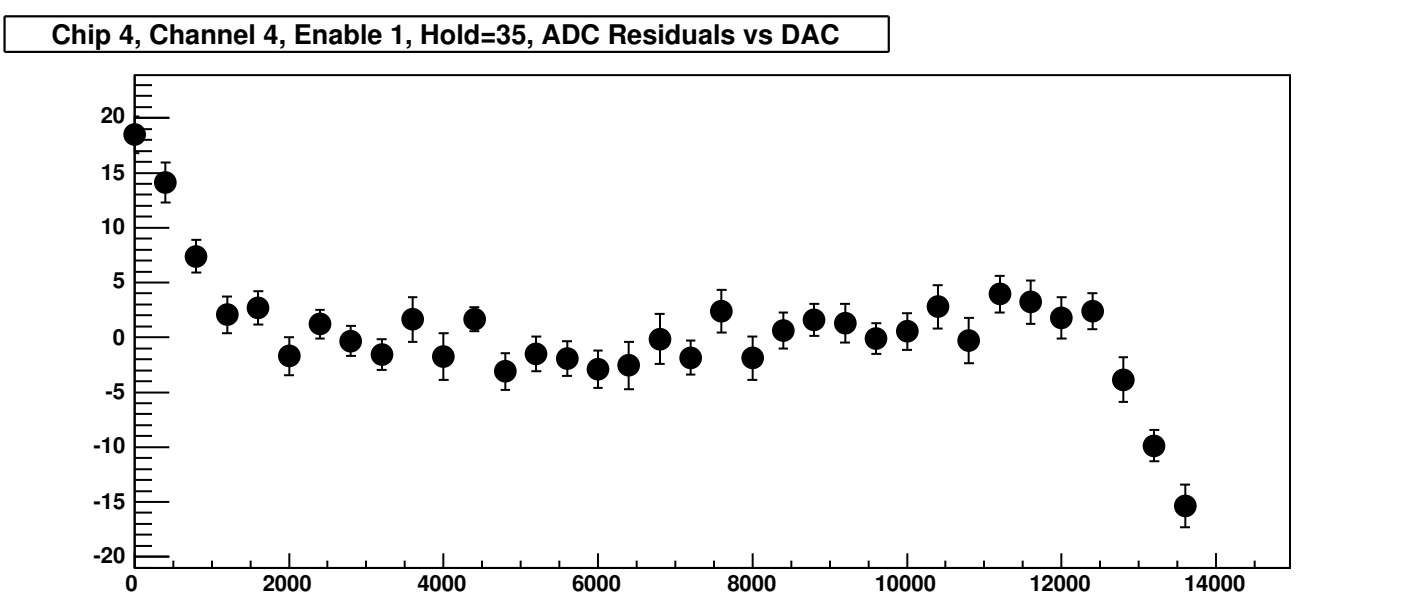

Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1749 / 23
p0 16.04 ± 0.9962
p1 0.1764 ± 0.0001985
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

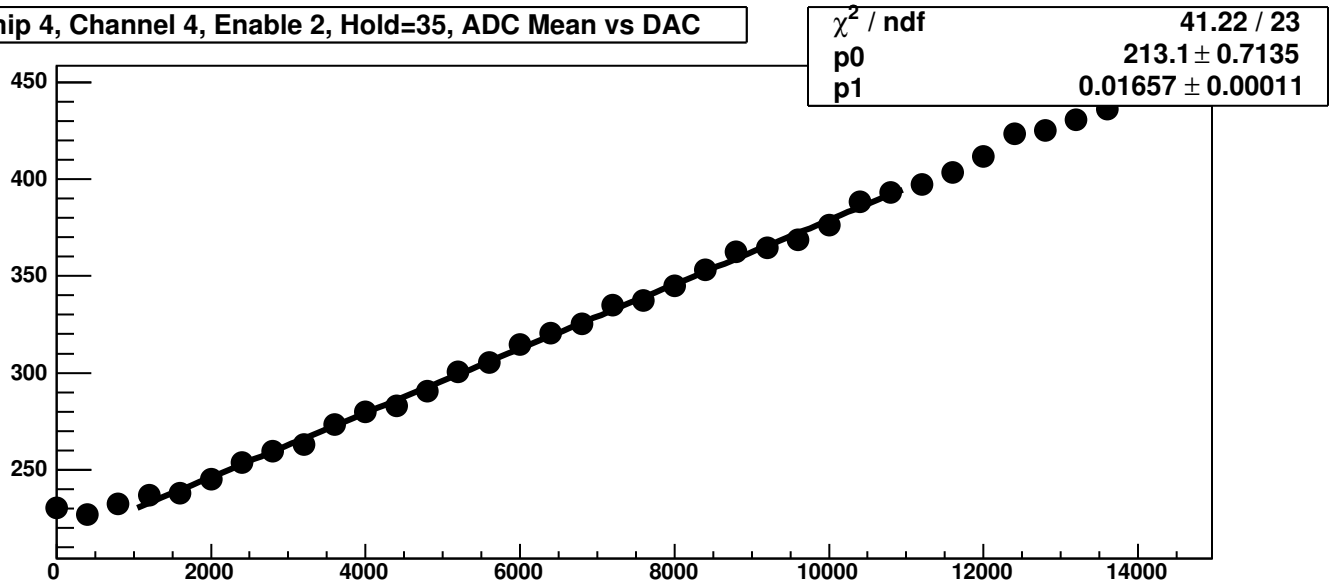

Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.5 / 23
p0 208.9 ± 0.7211
p1 0.0207 ± 0.0001133
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

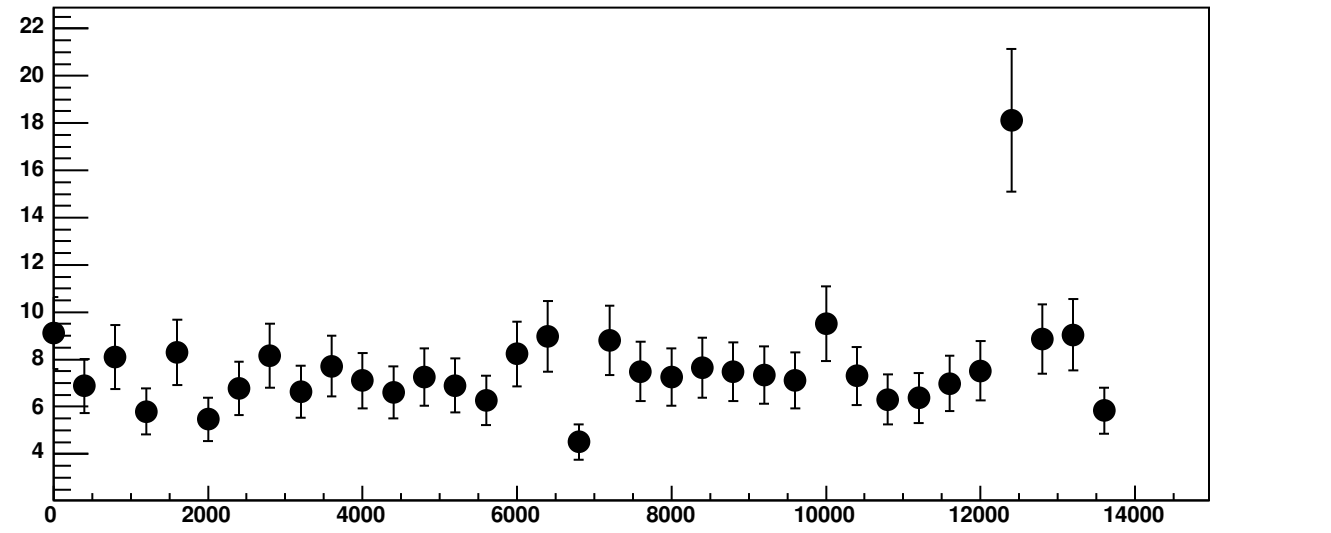

Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

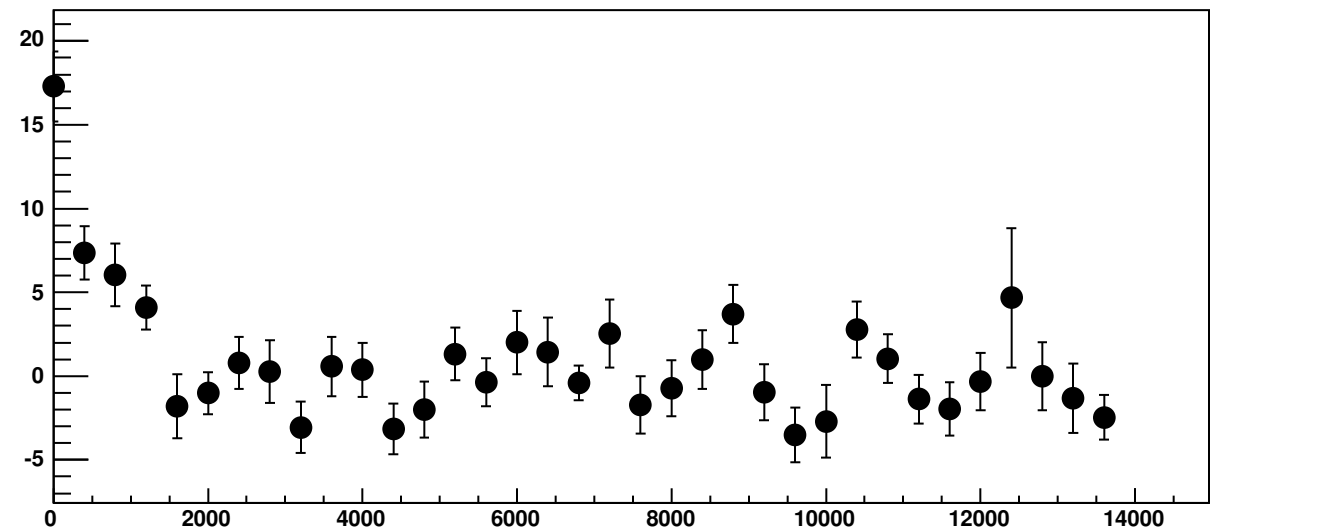
Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

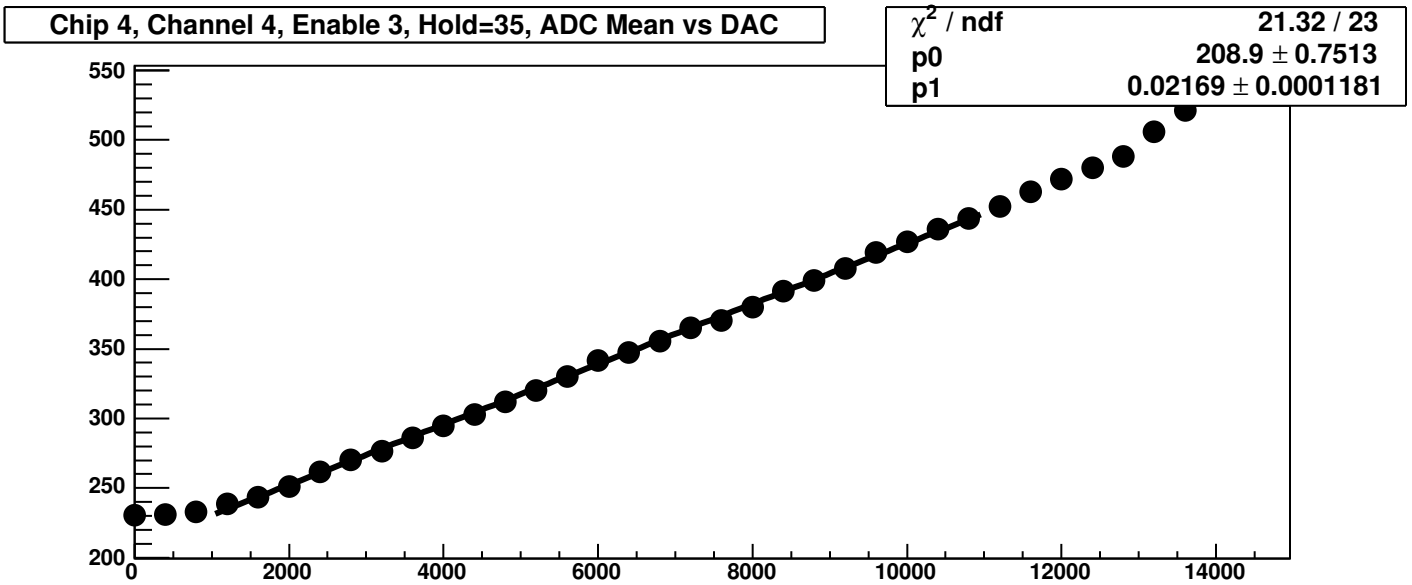


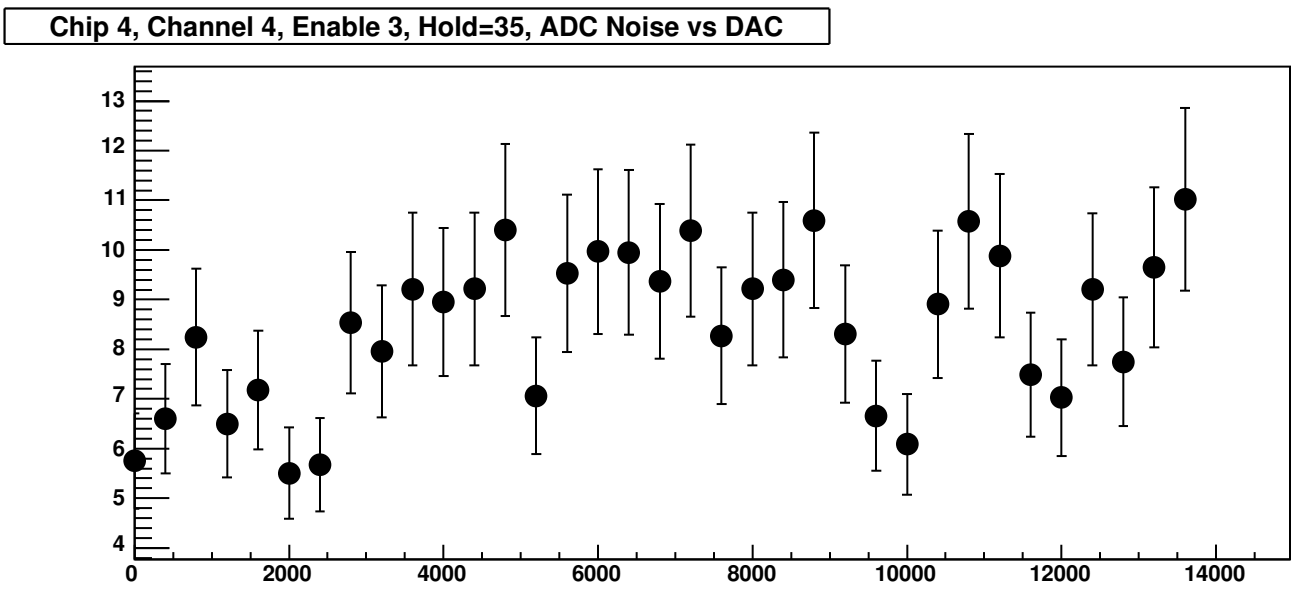
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

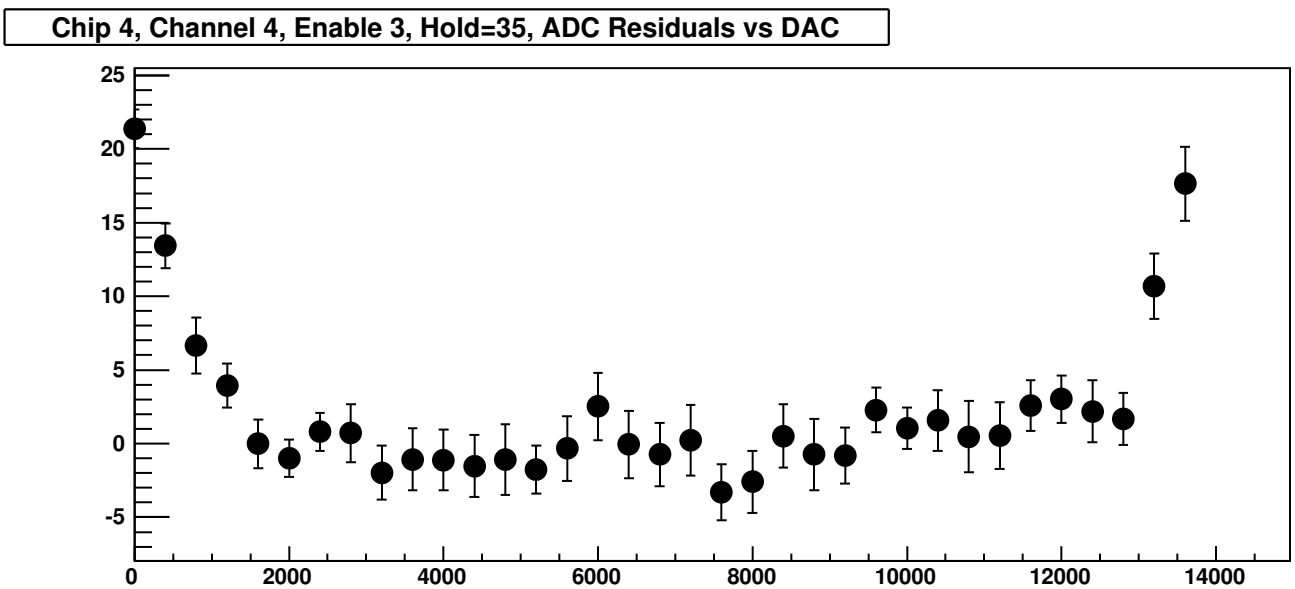


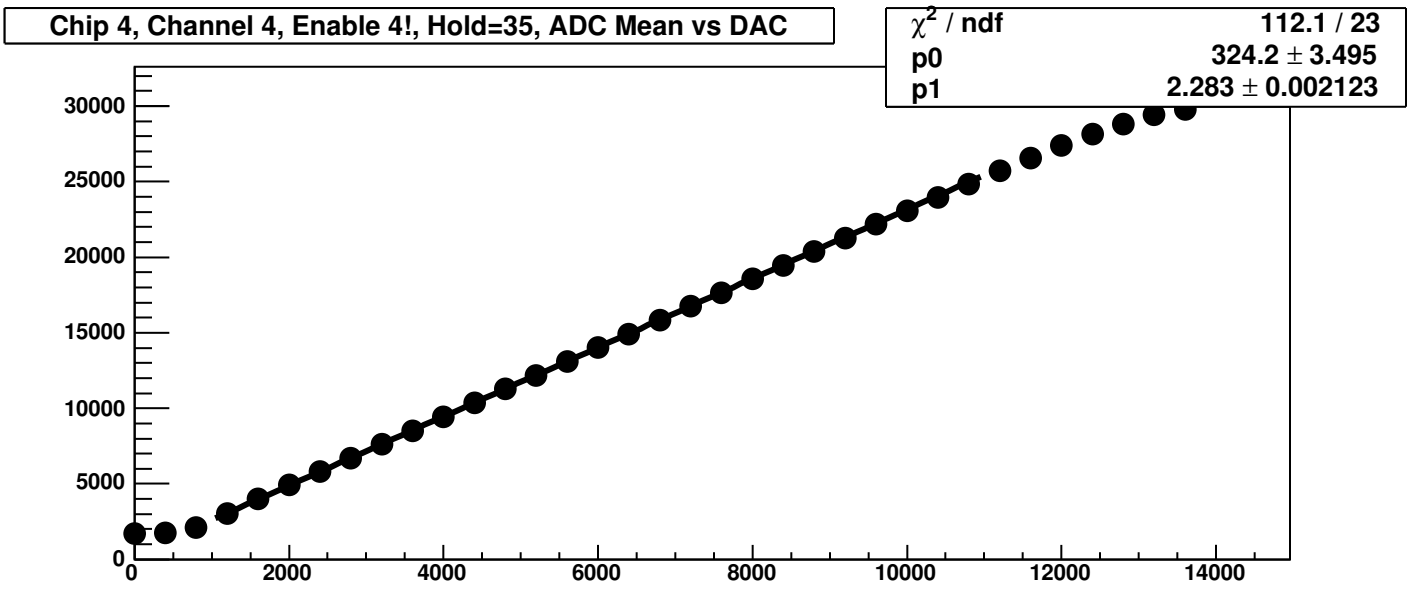
Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

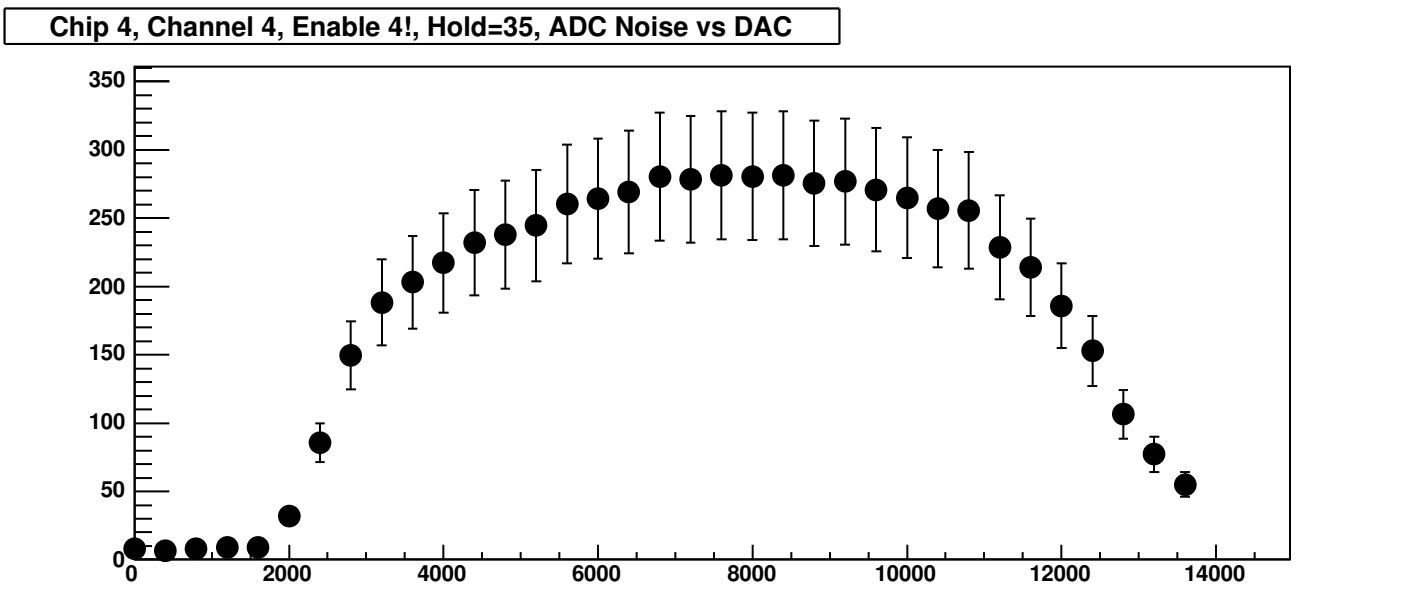


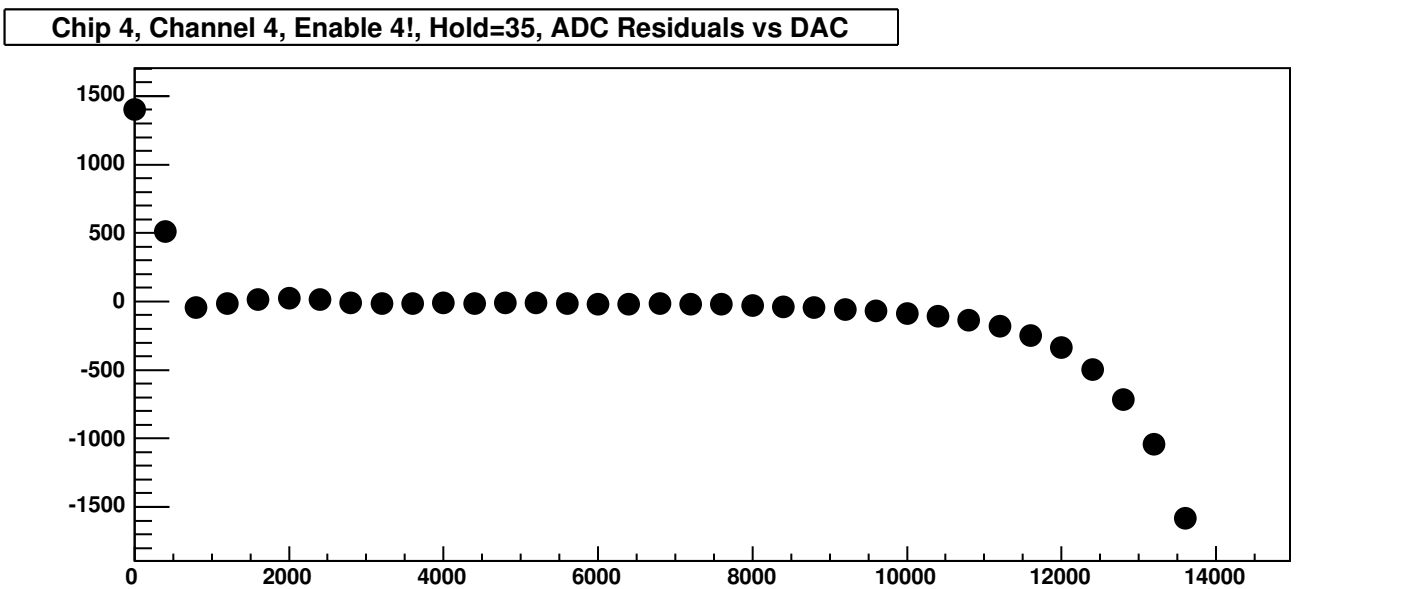

Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.32 / 23
p0 208.9 ± 0.7513
p1 0.02169 ± 0.0001181
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

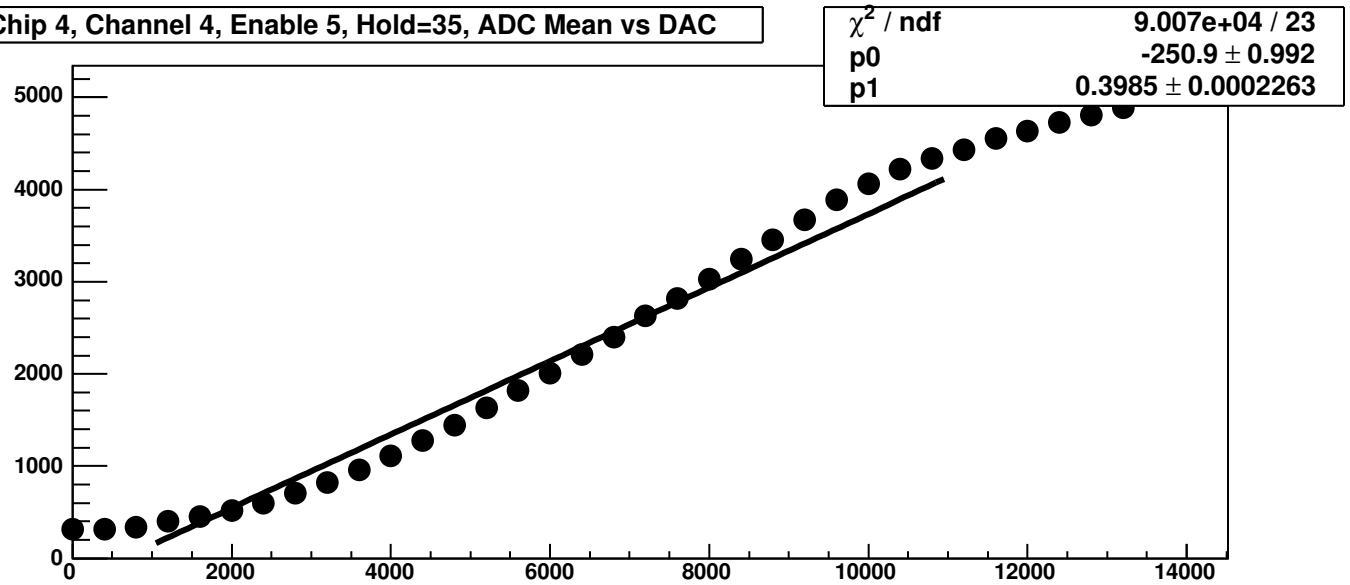

Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 112.1 / 23
p0 324.2 ± 3.495
p1 2.283 ± 0.002123
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

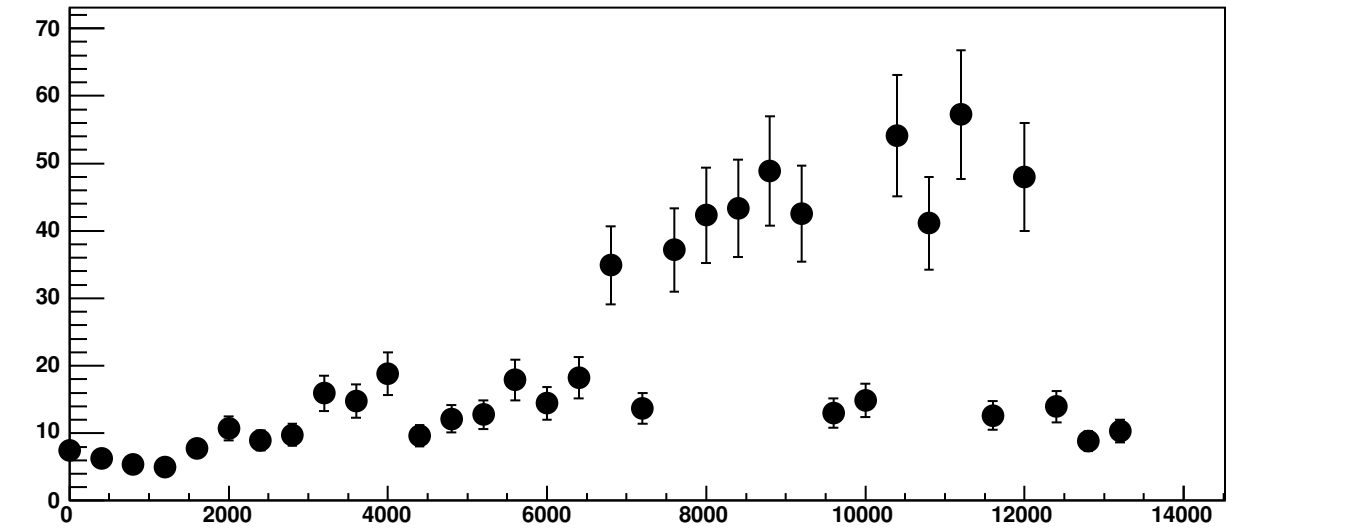

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

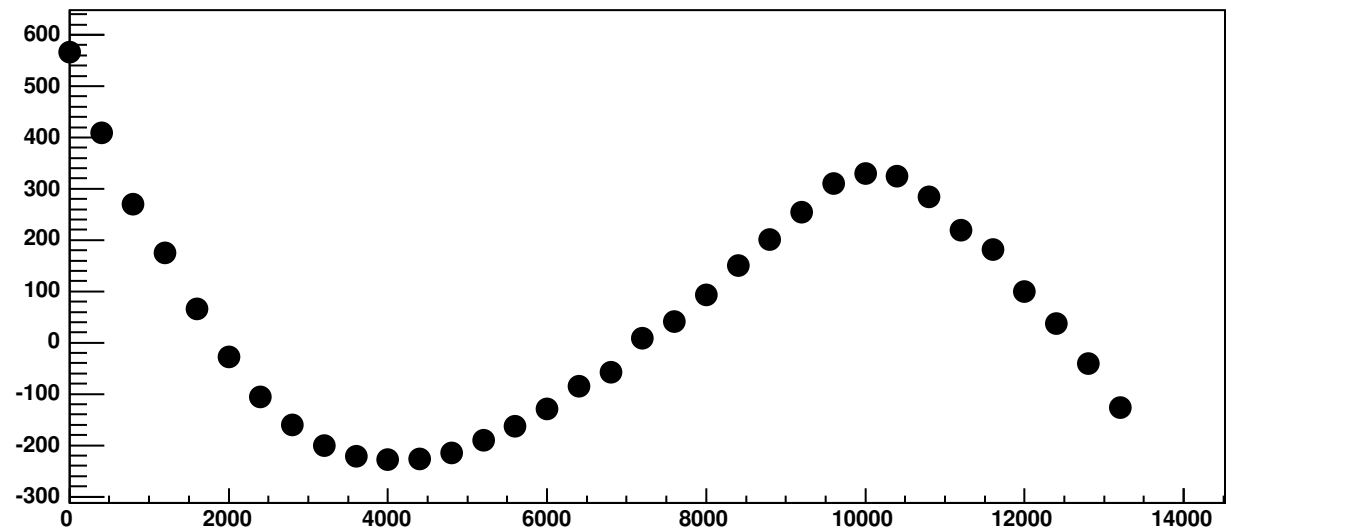
Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

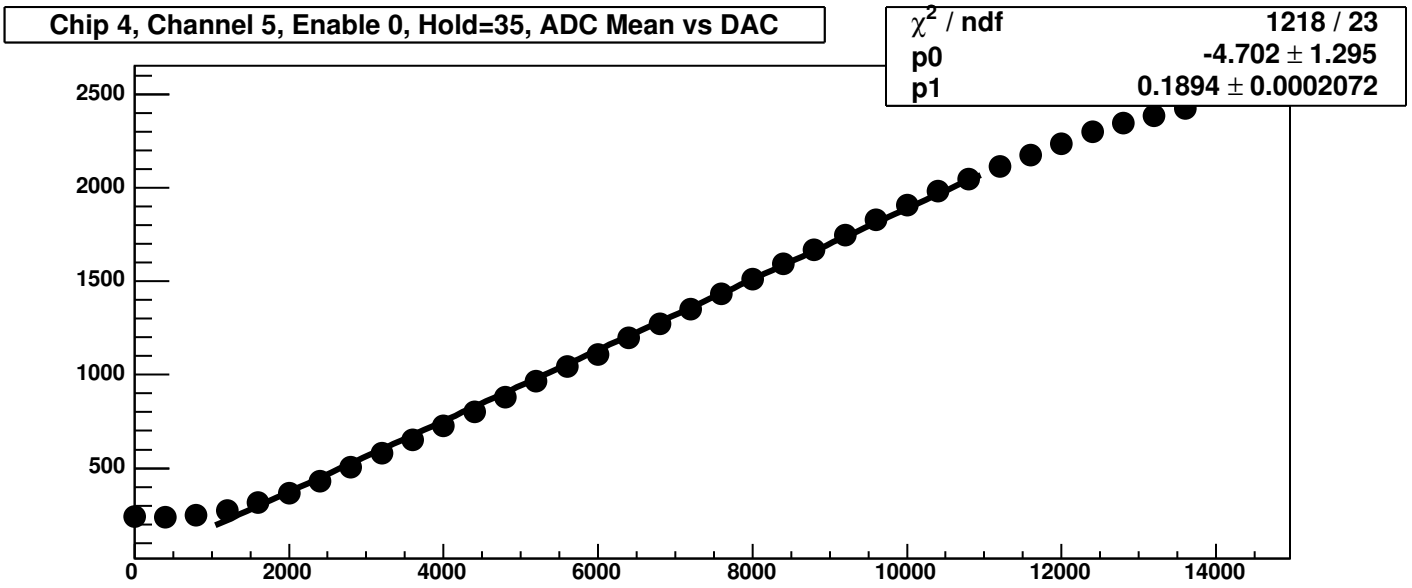


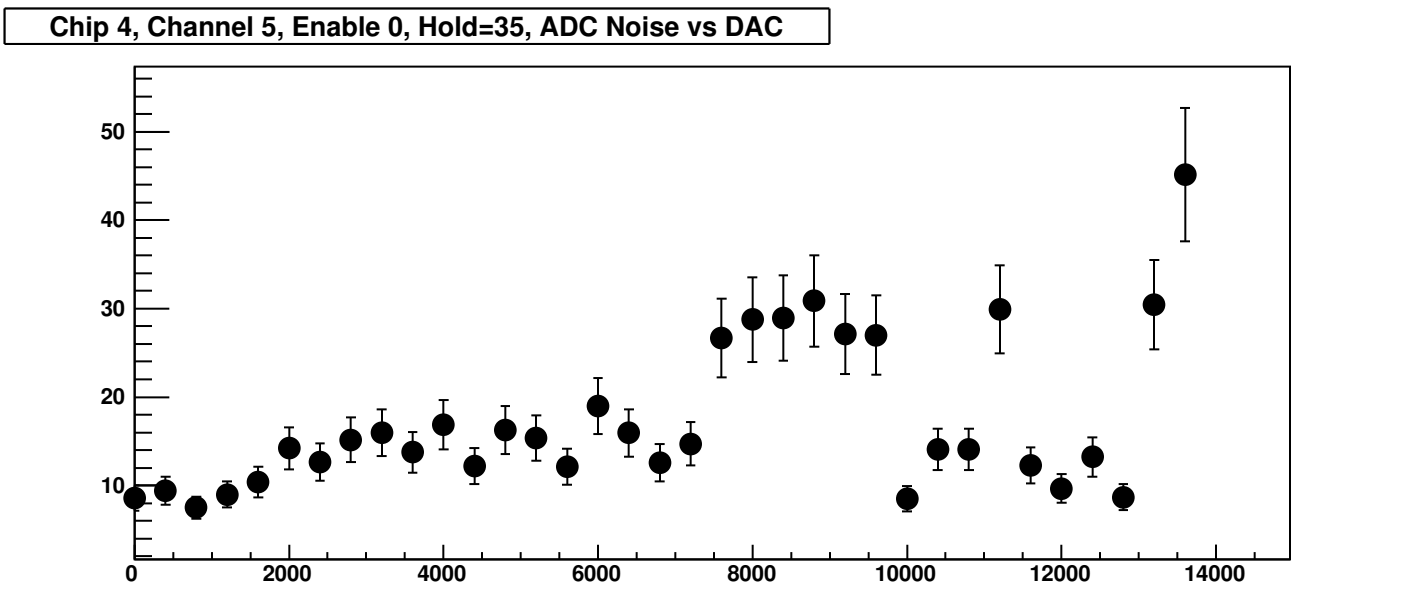
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

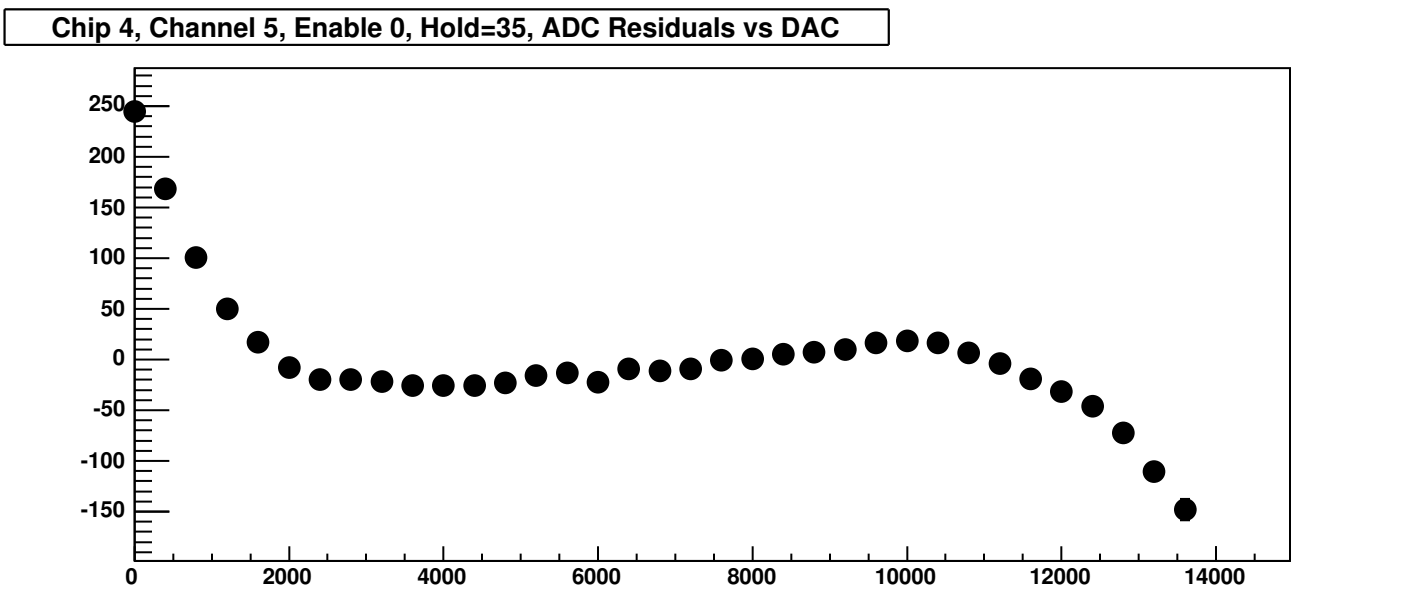


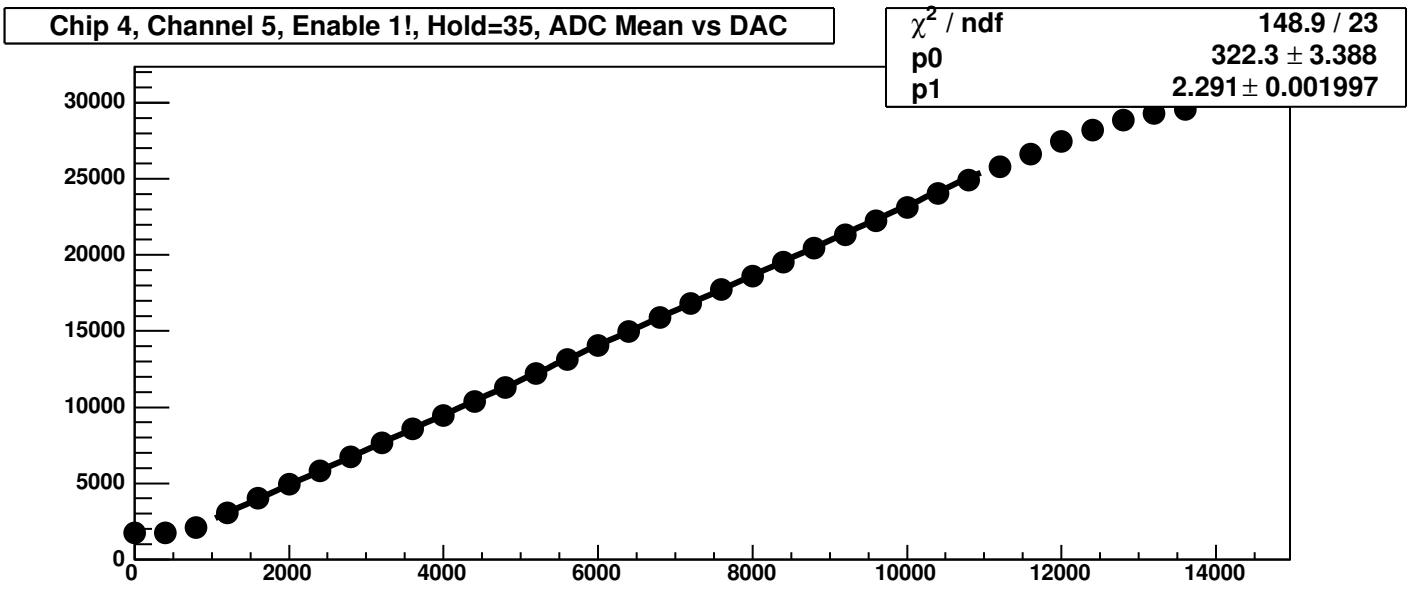
Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

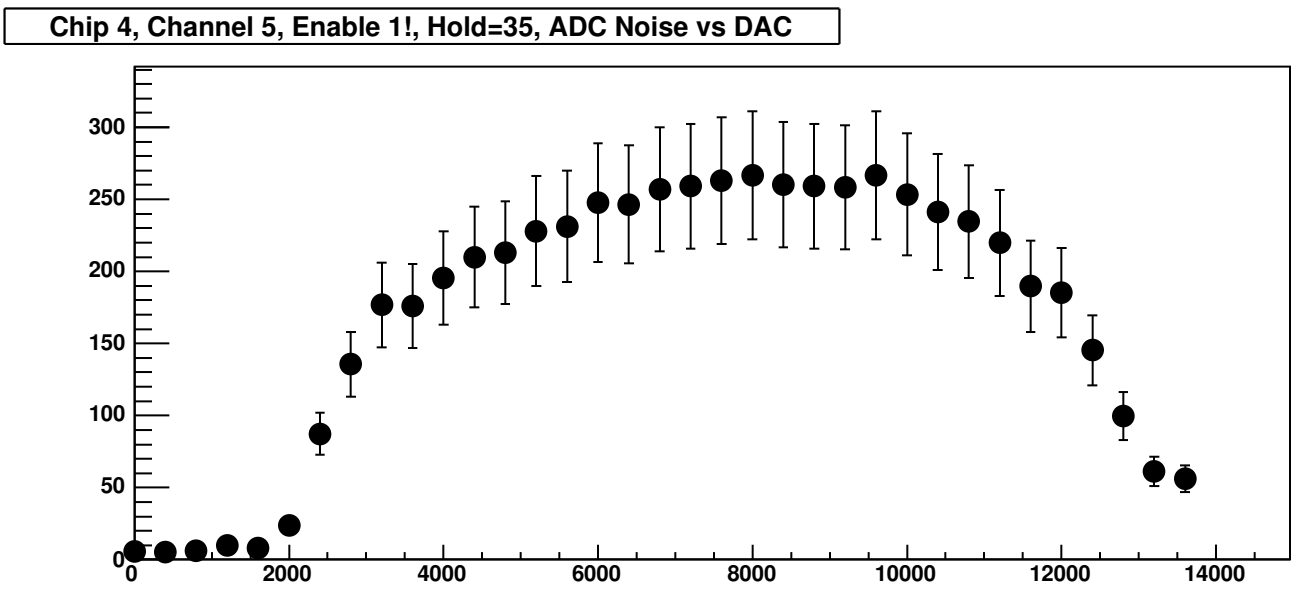


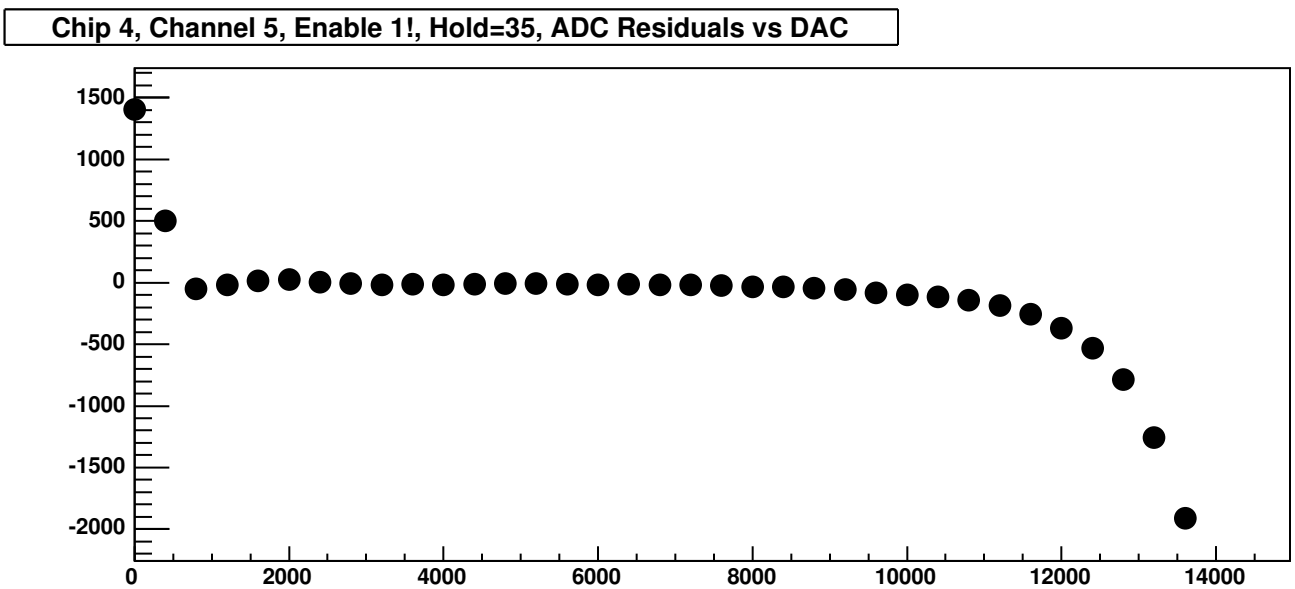

Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1218 / 23
p0 -4.702 ± 1.295
p1 0.1894 ± 0.0002072
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

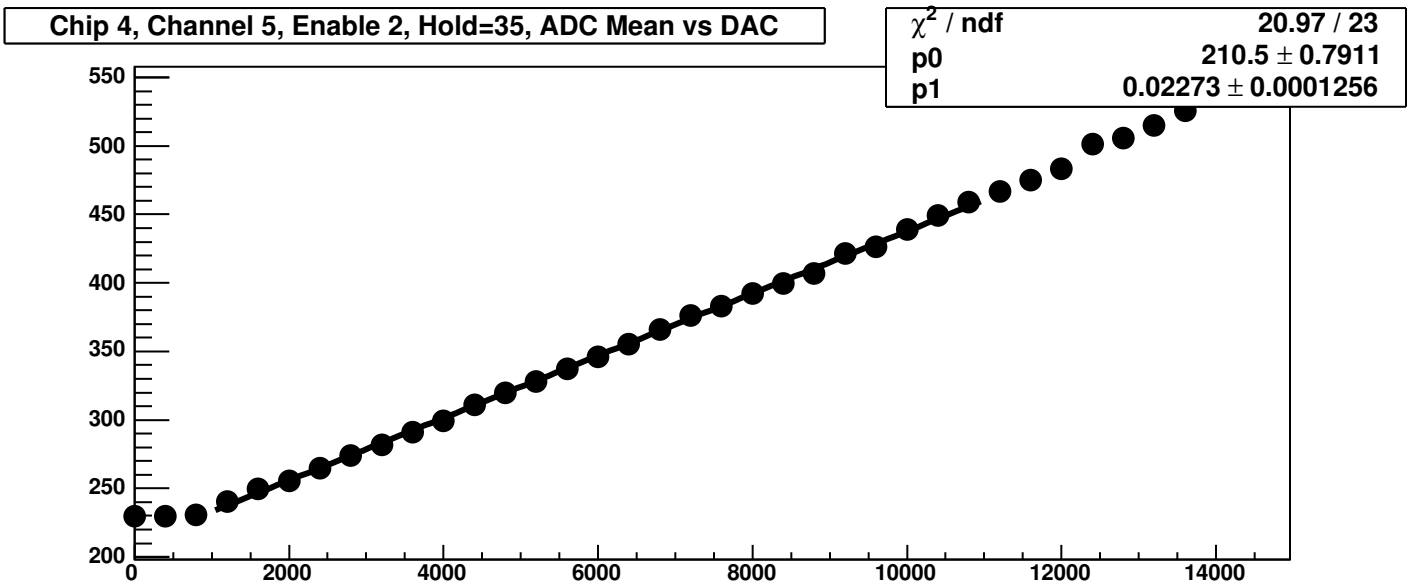

Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

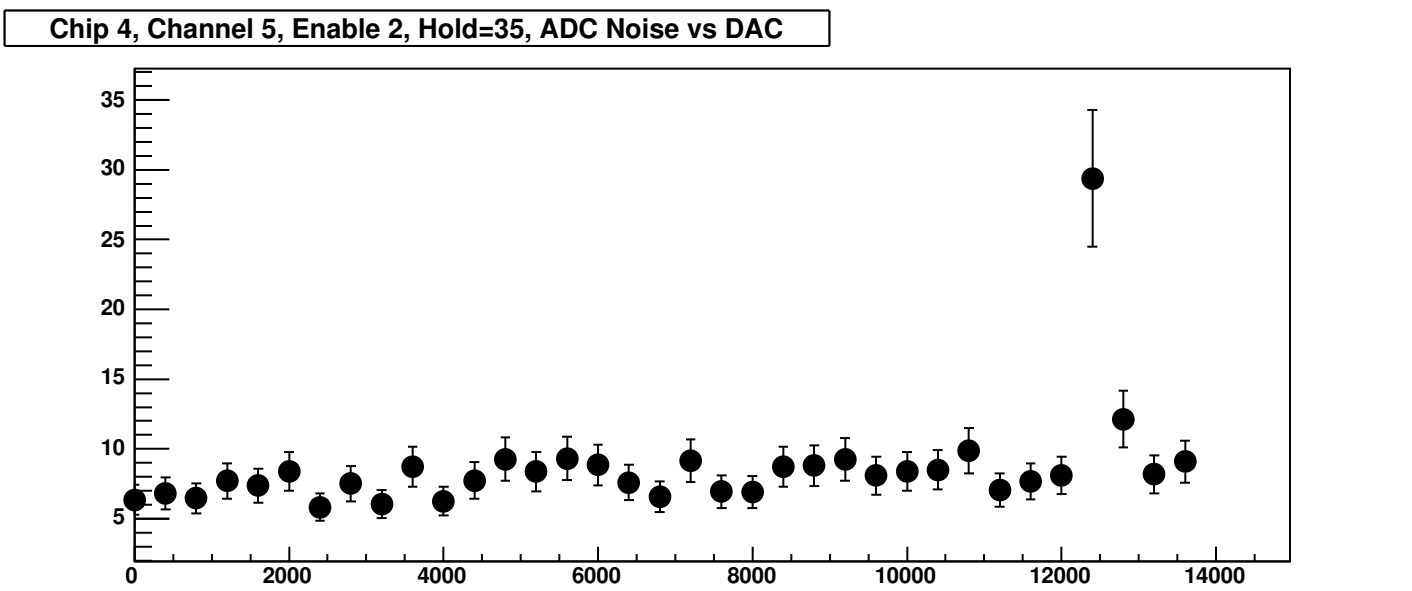

Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

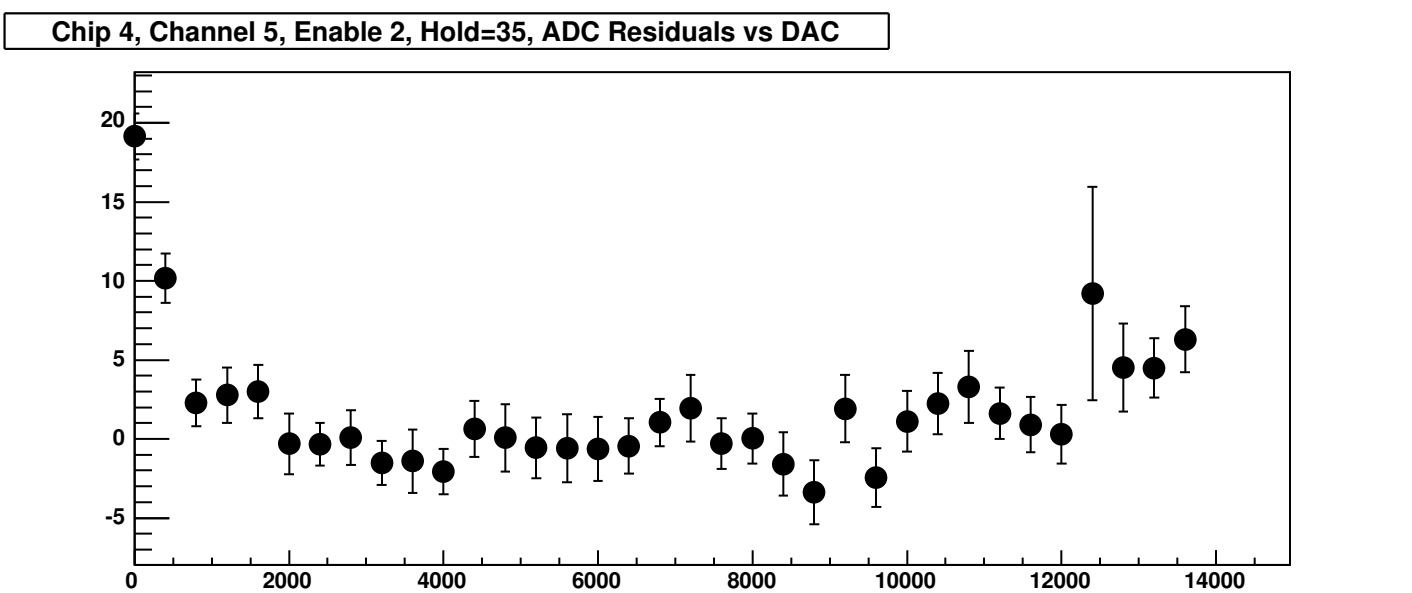

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 148.9 / 23
p0 322.3 ± 3.388
p1 2.291 ± 0.001997
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

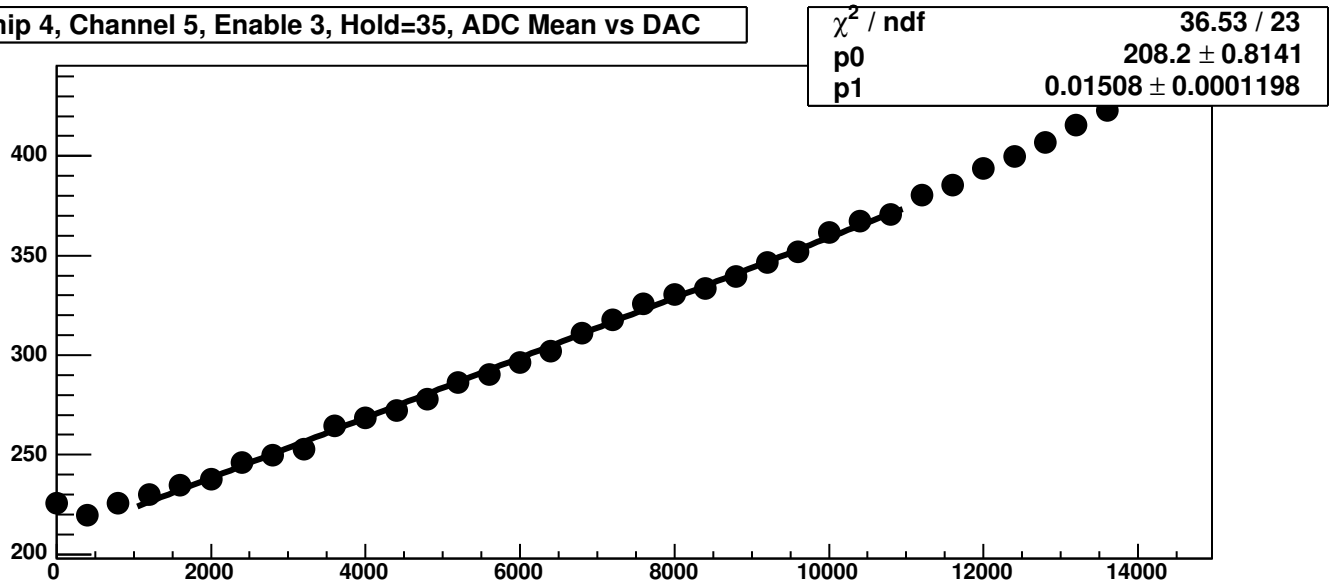

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.97 / 23
p0 210.5 ± 0.7911
p1 0.02273 ± 0.0001256
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

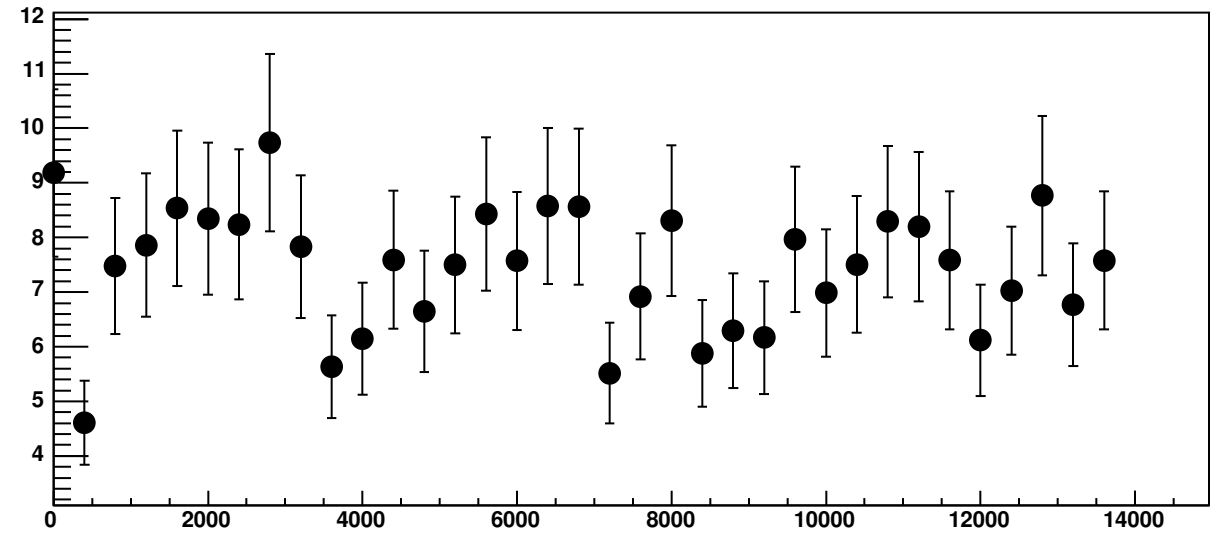

Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

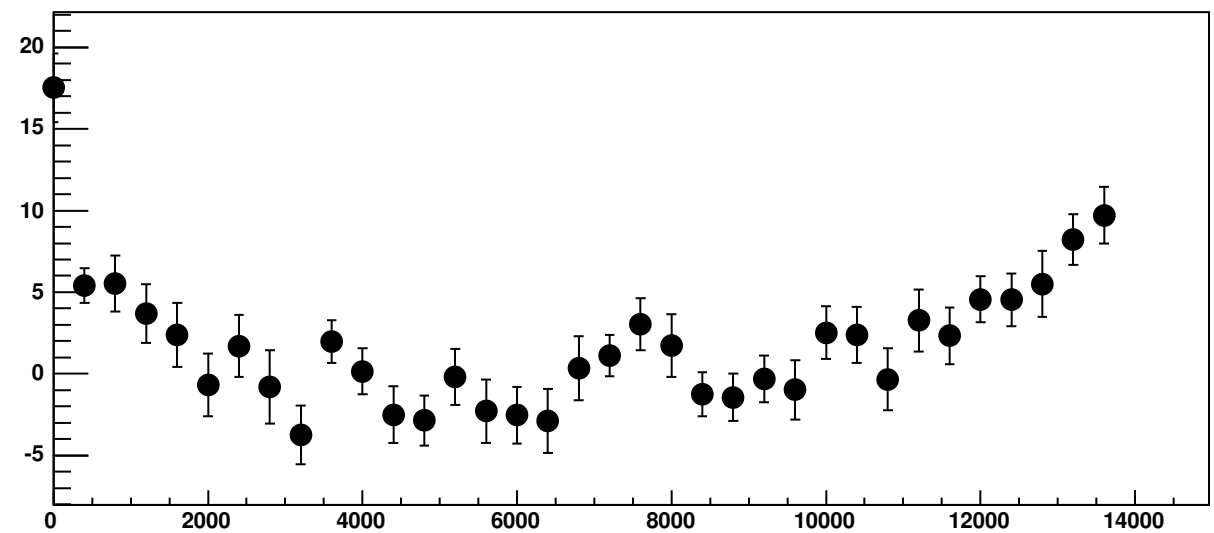
Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

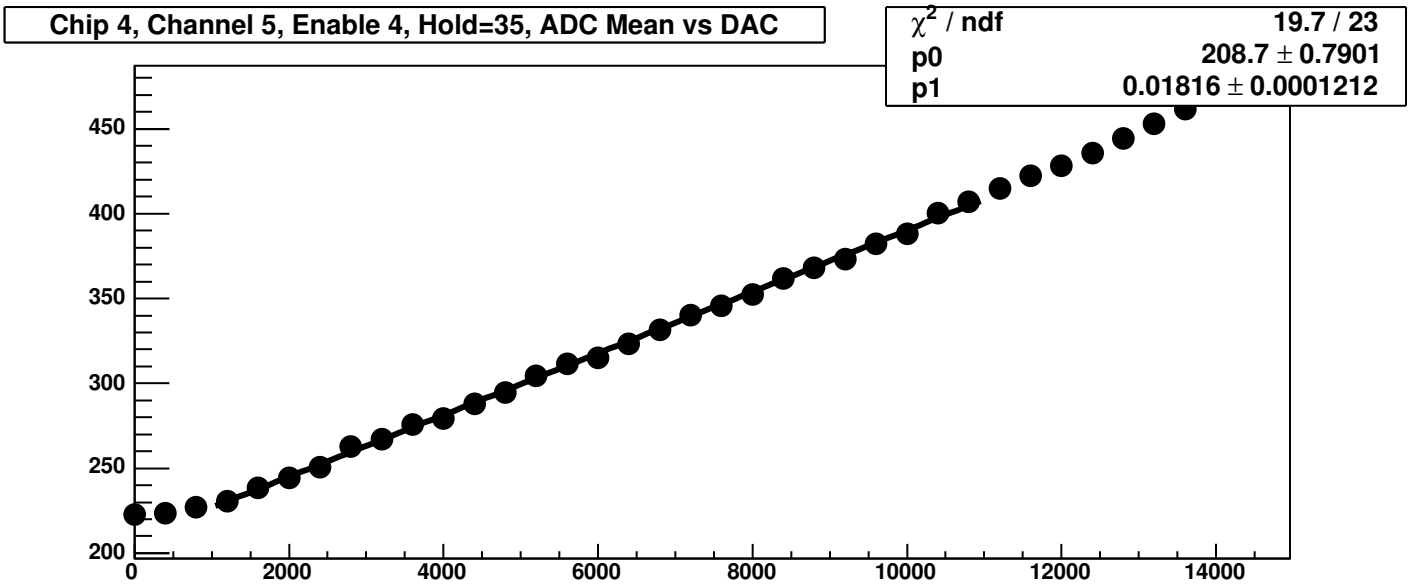


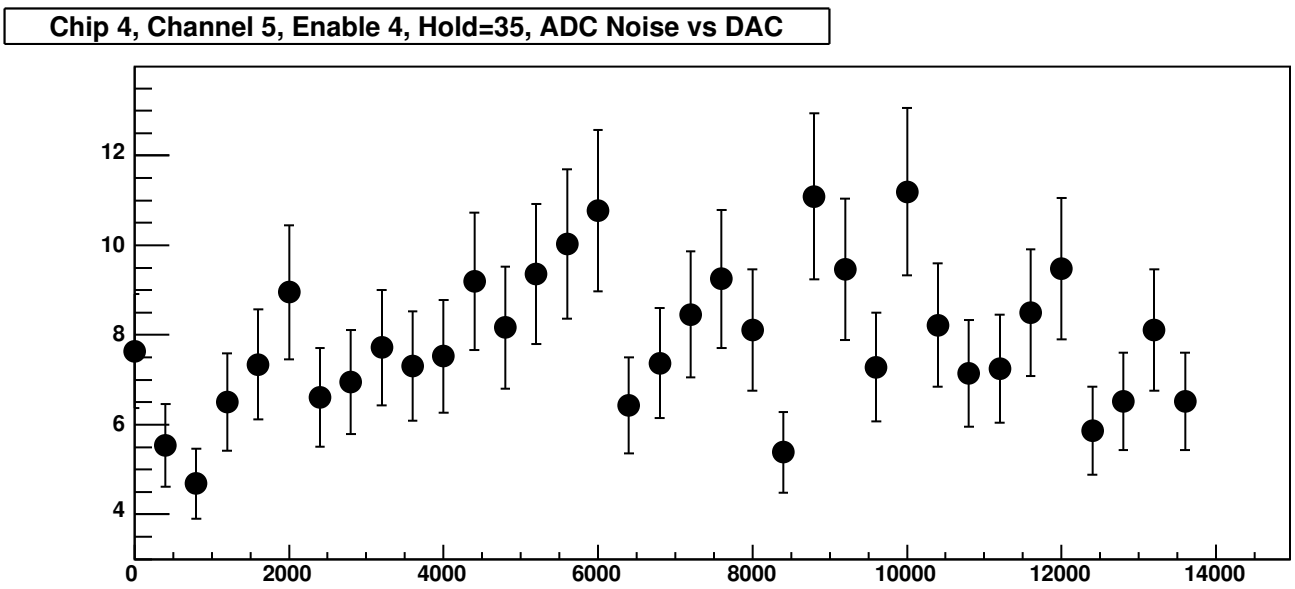
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

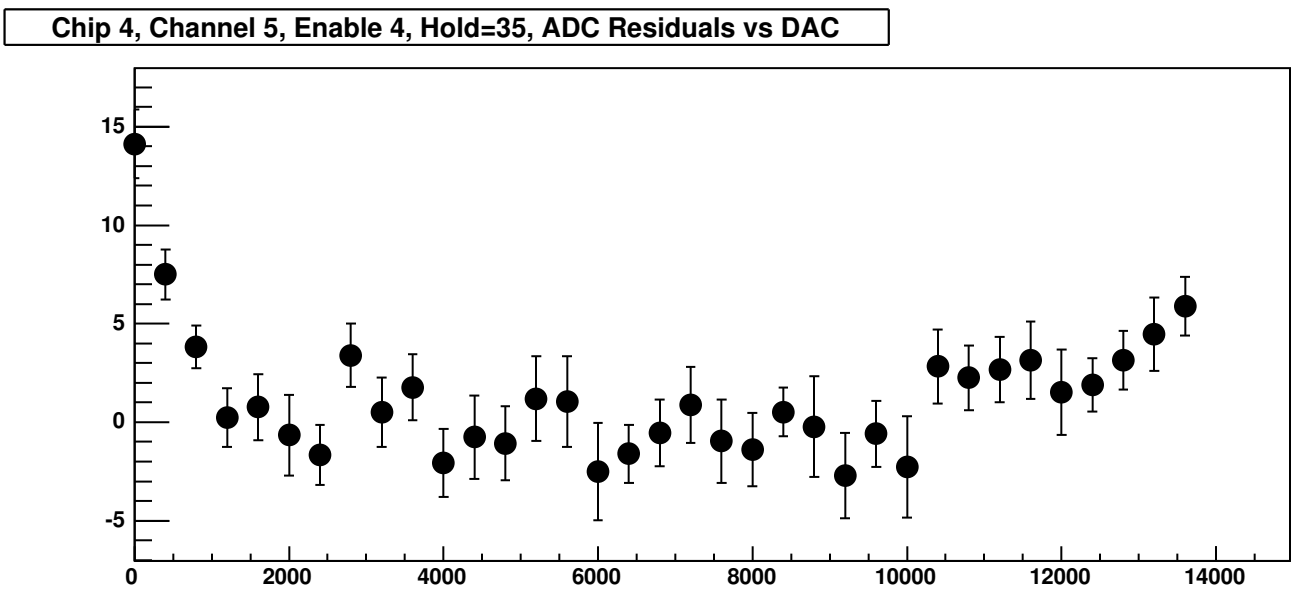


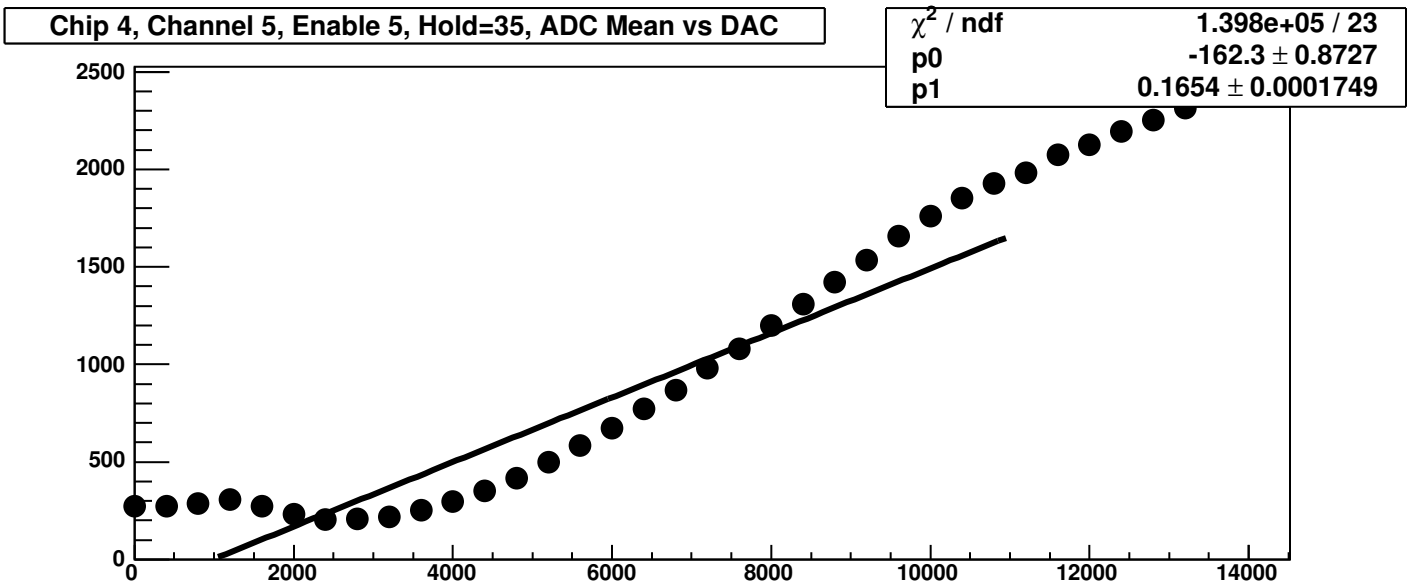
Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

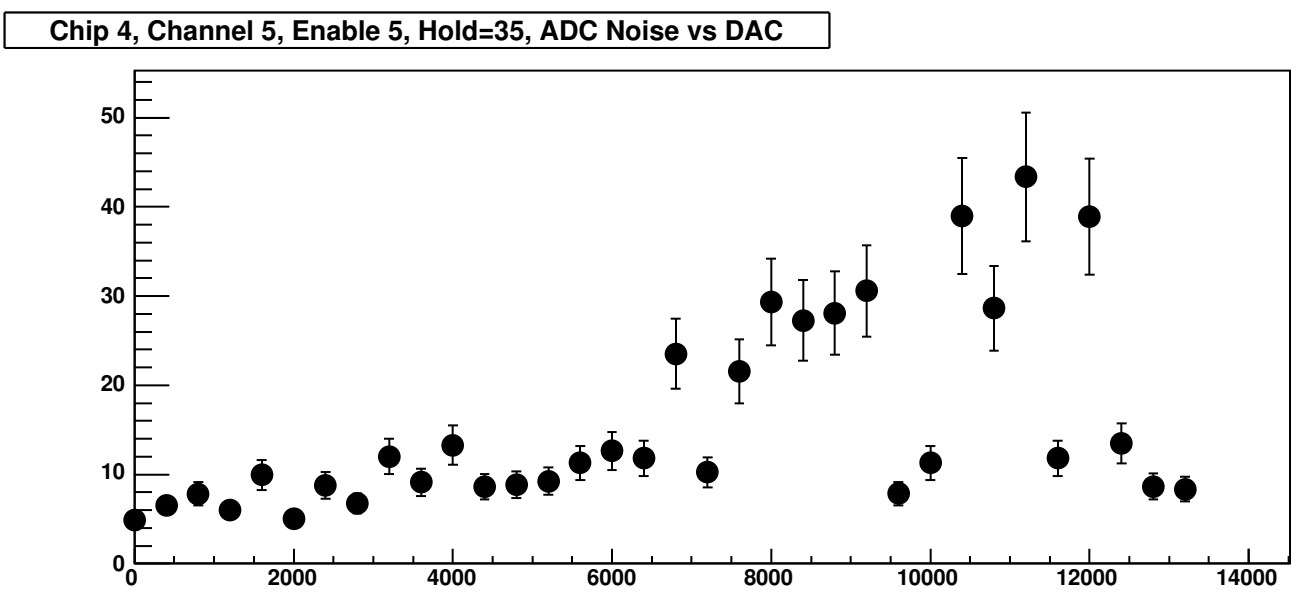


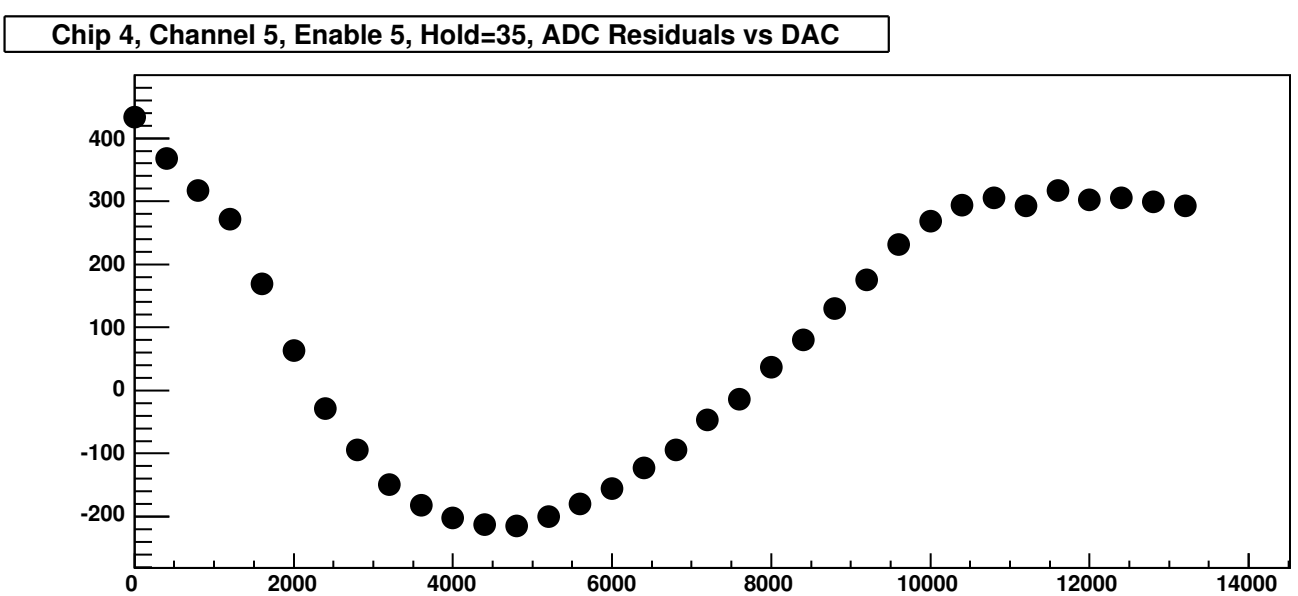

Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.7 / 23
p0 208.7 ± 0.7901
p1 0.01816 ± 0.0001212
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

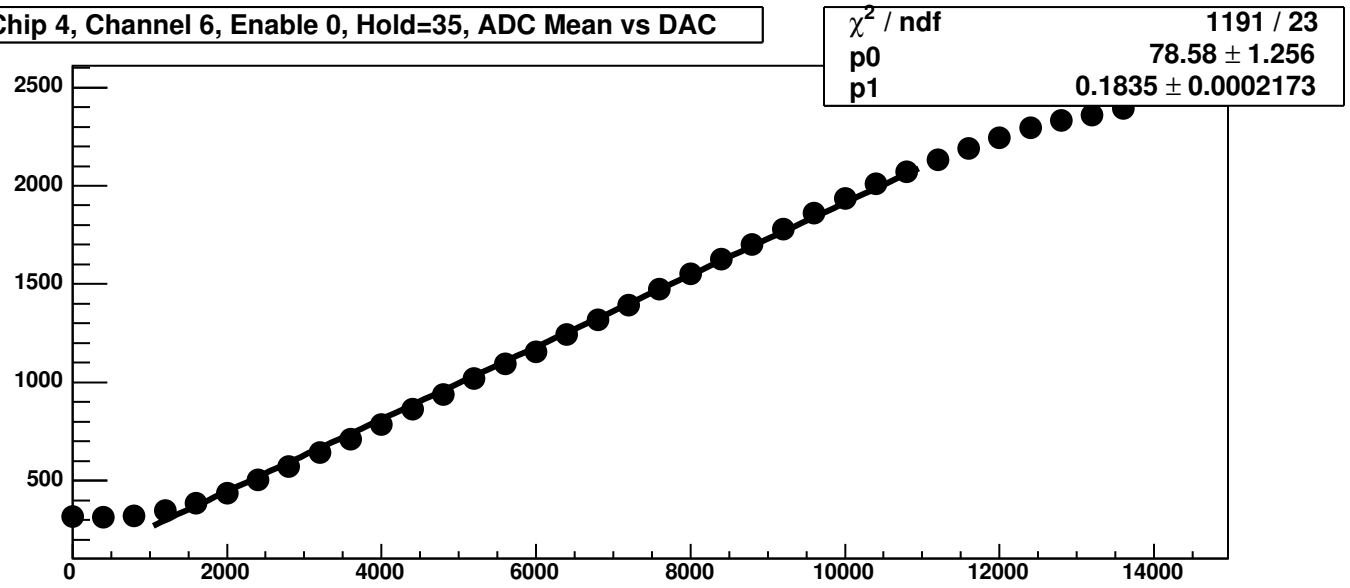

Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.398e+05 / 23
p0 -162.3 ± 0.8727
p1 0.1654 ± 0.0001749
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

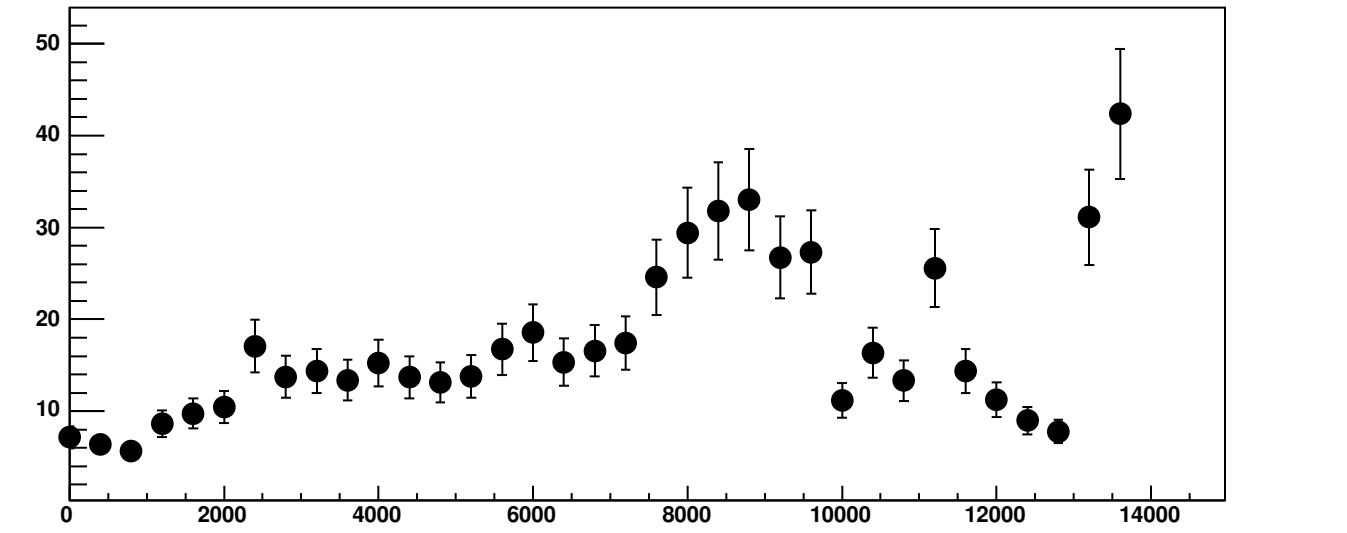

Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

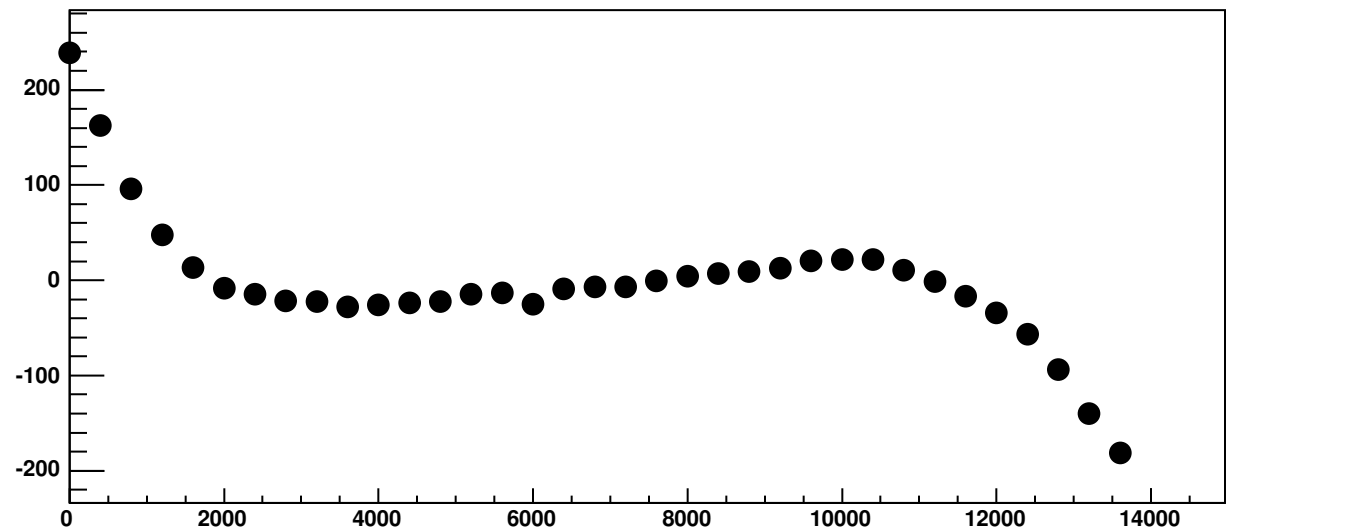
Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



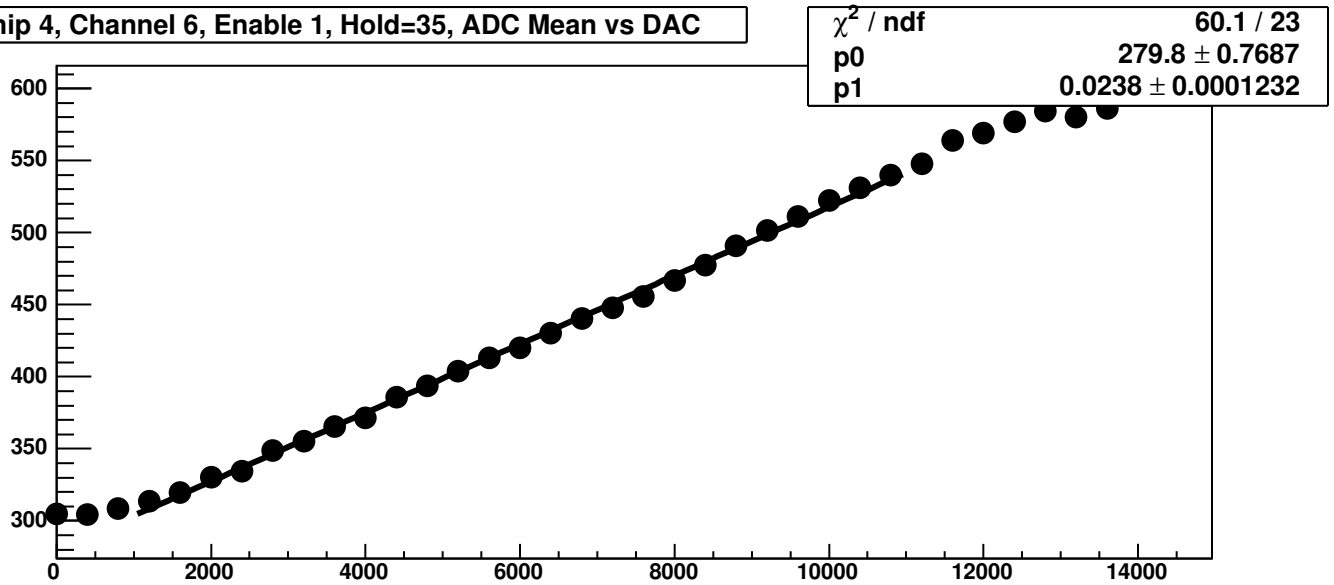
Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



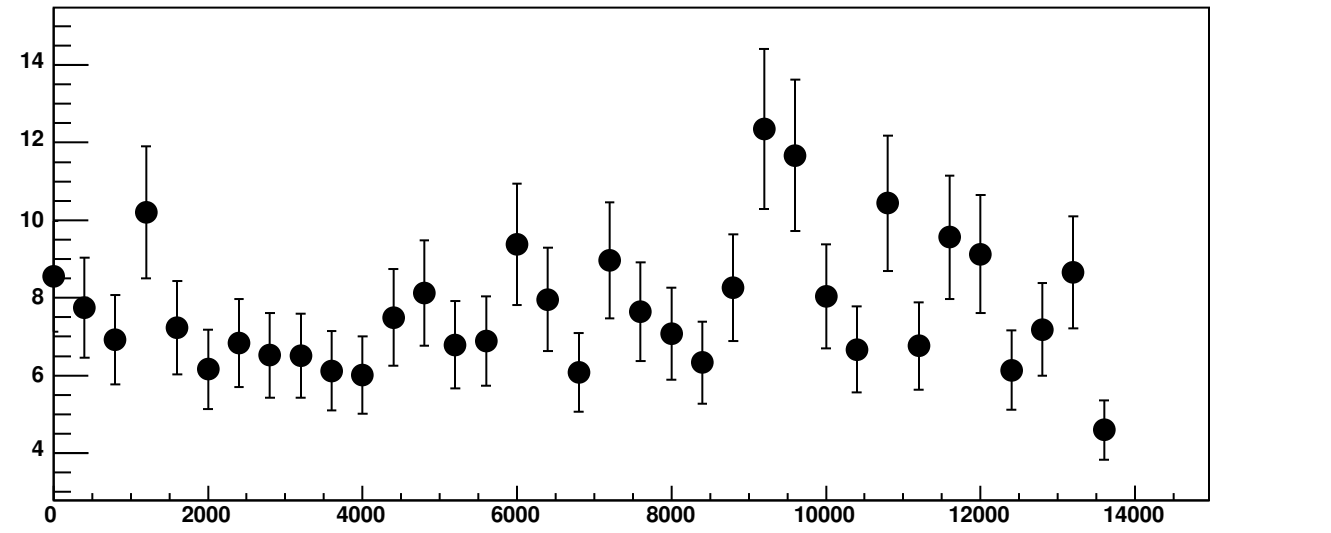
Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



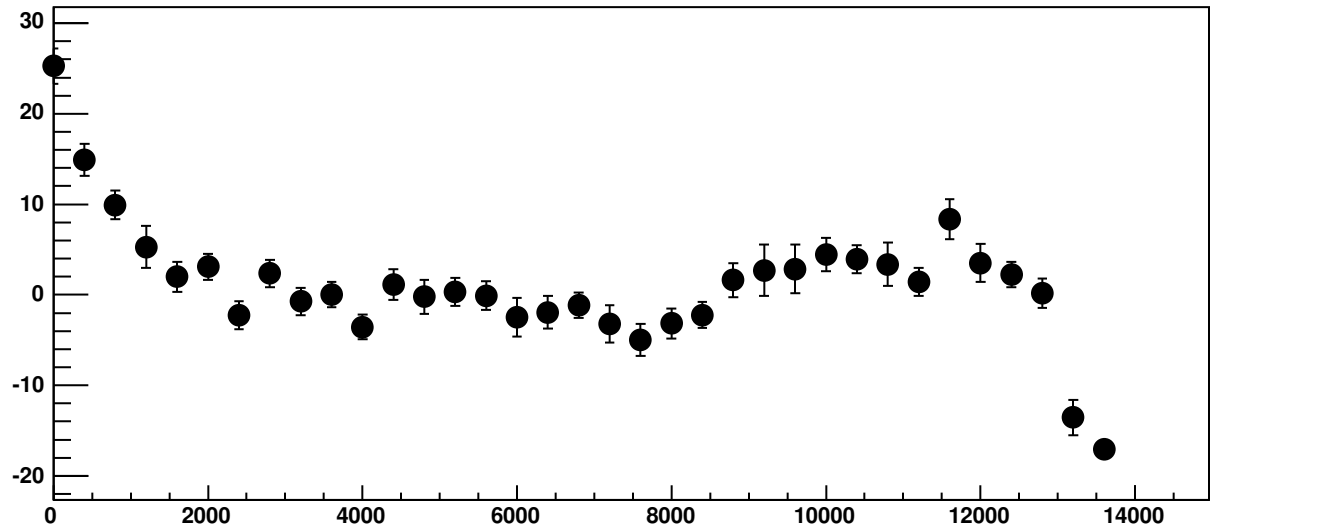
Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

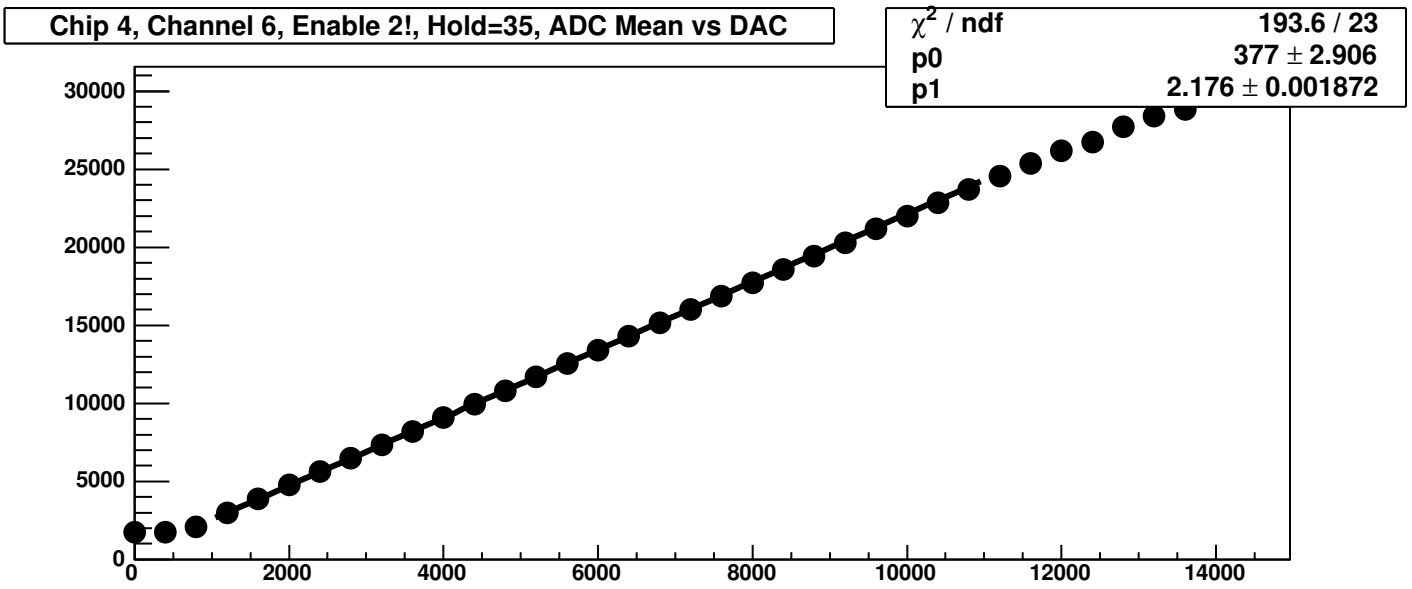


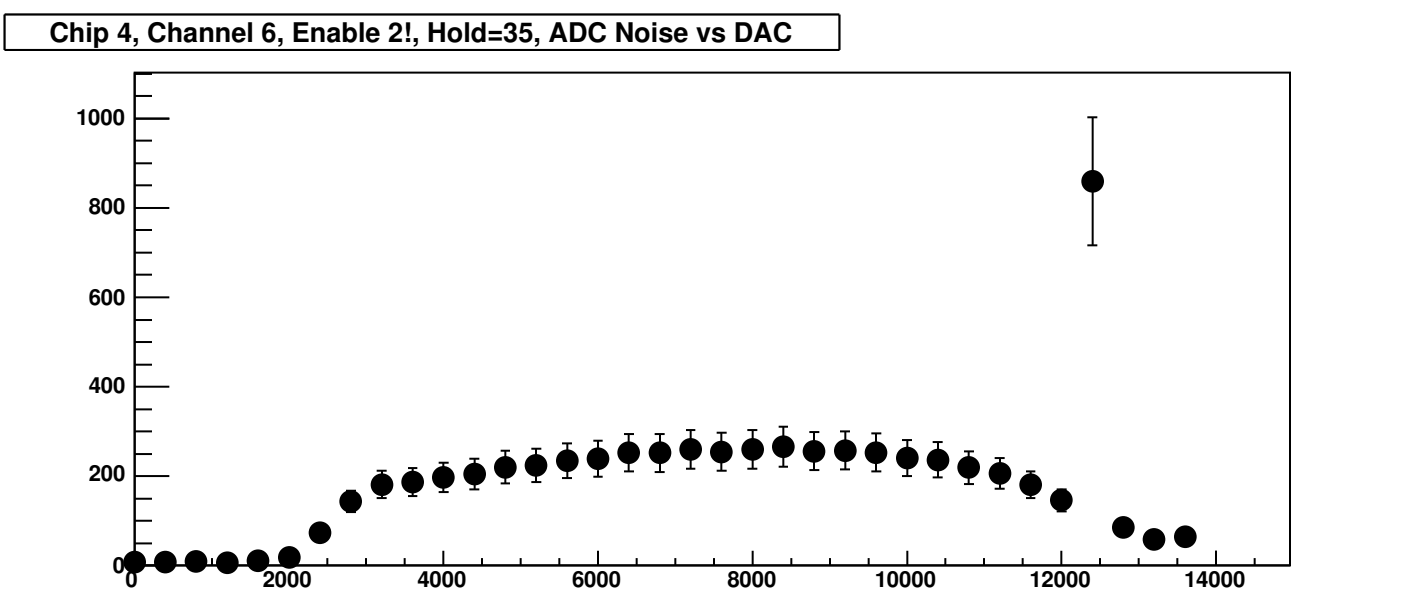
Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

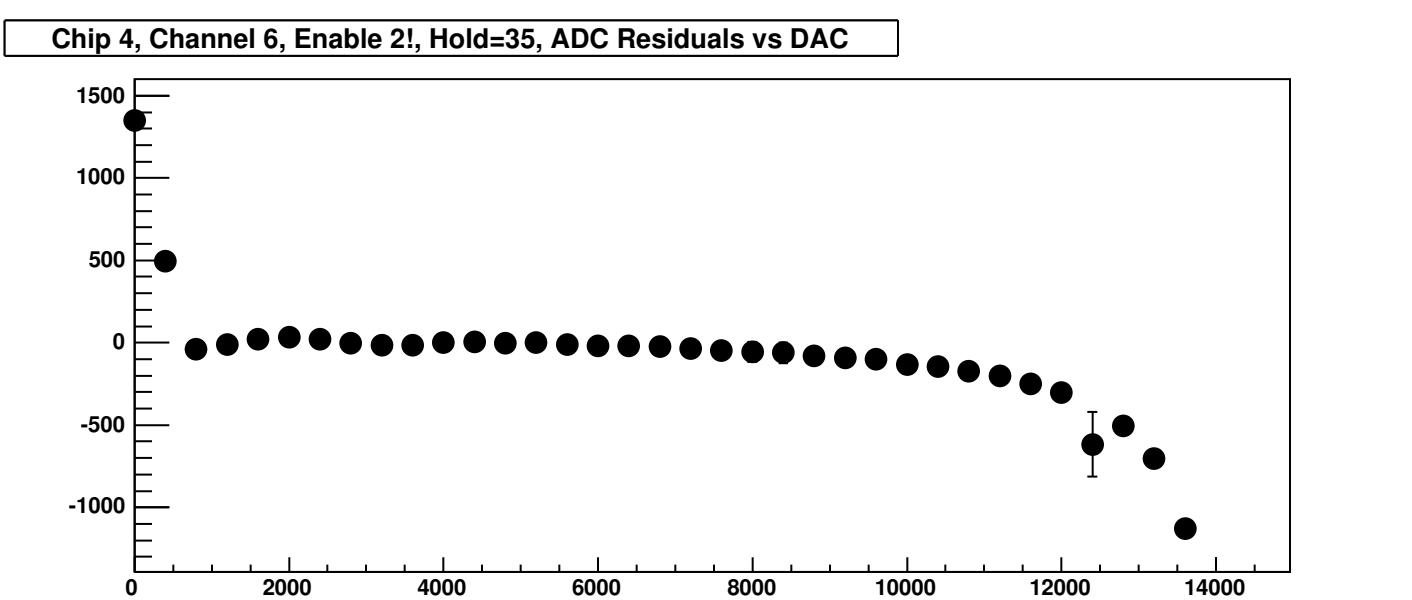


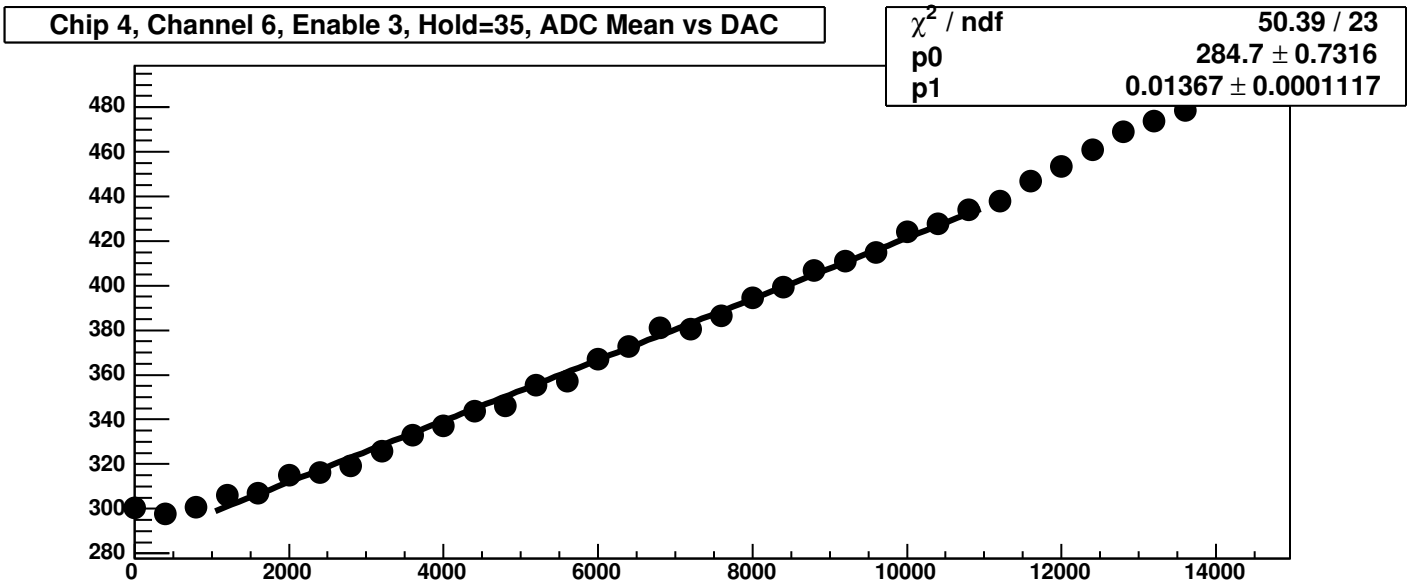
Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

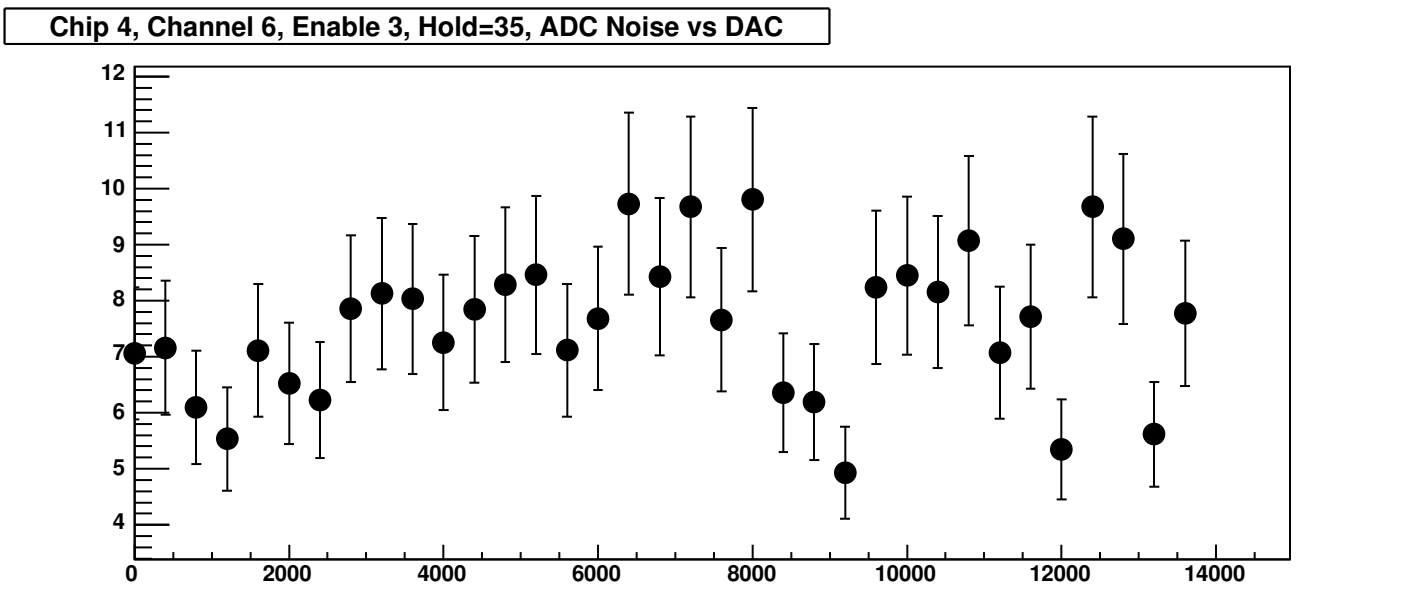


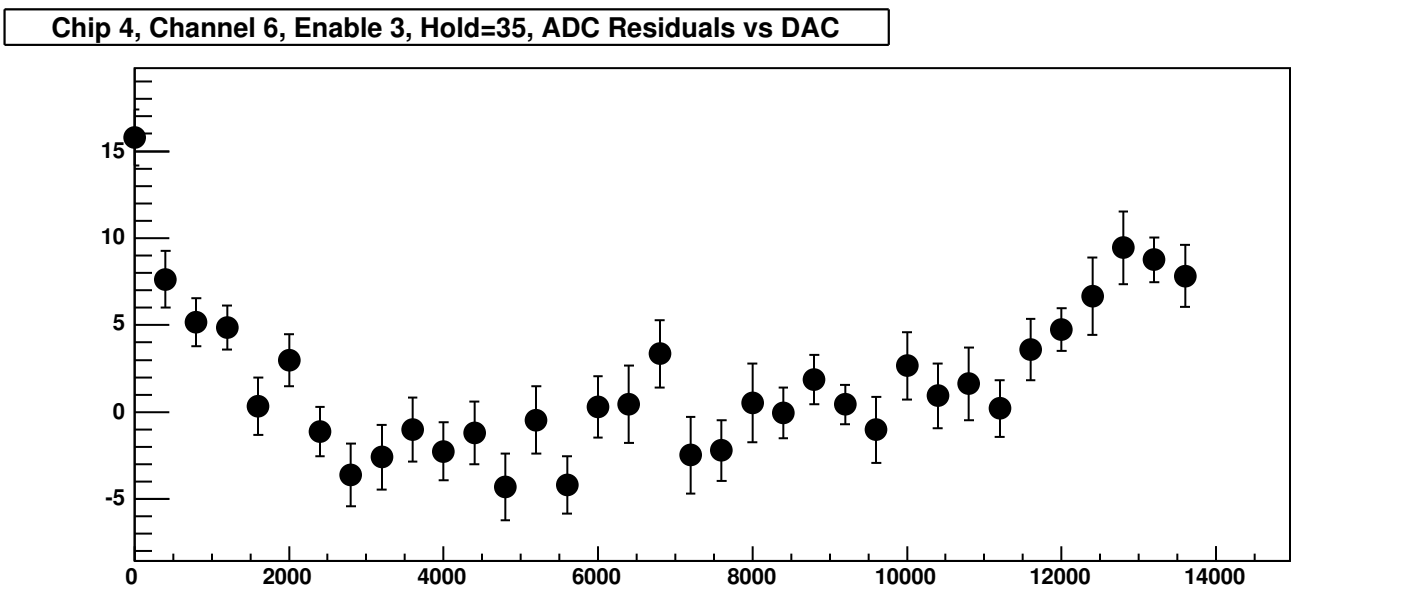

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 193.6 / 23
p0 377 ± 2.906
p1 2.176 ± 0.001872
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

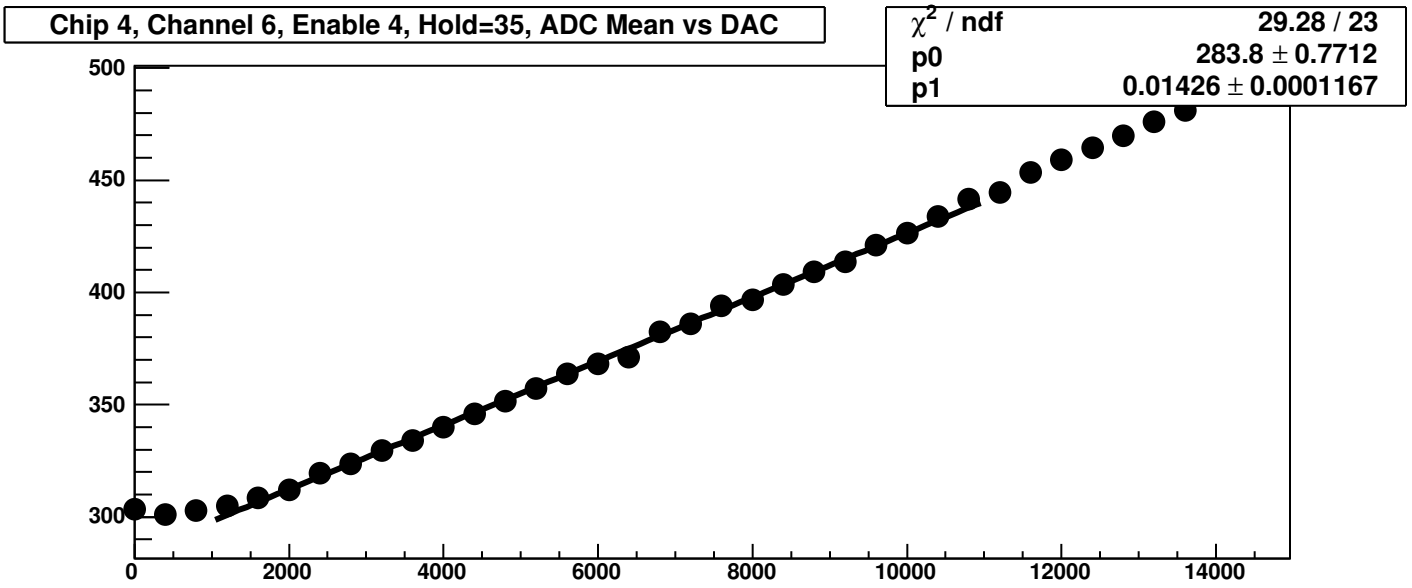

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

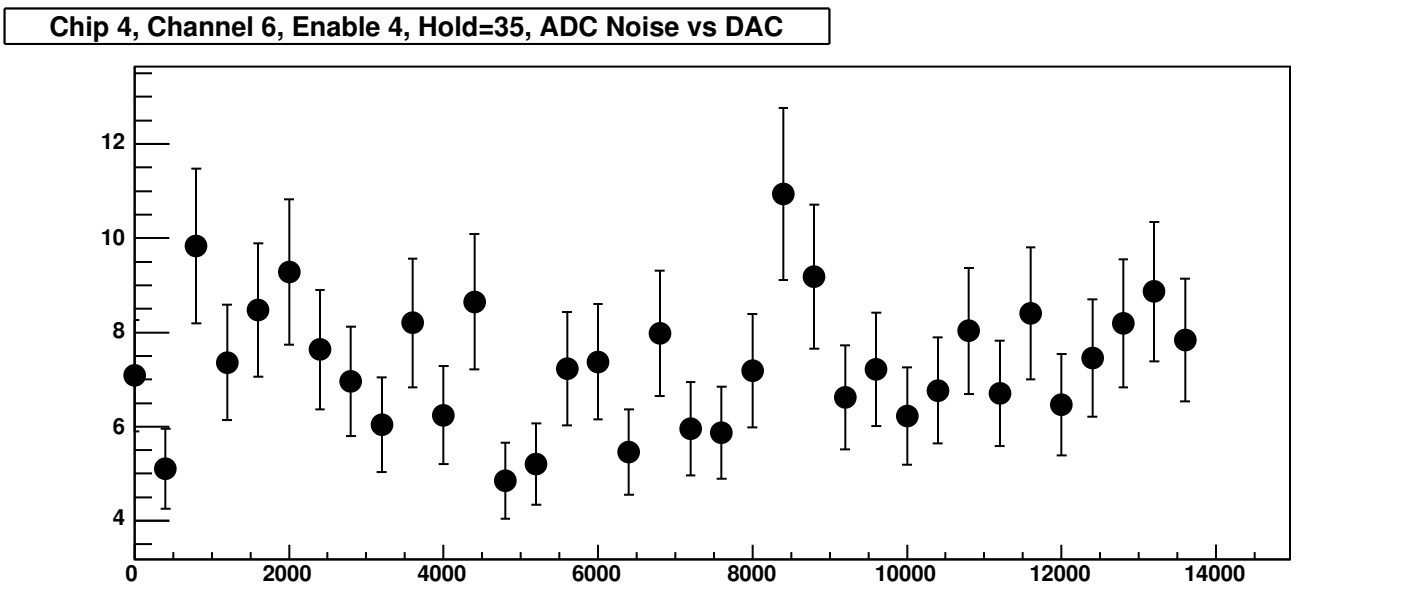

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

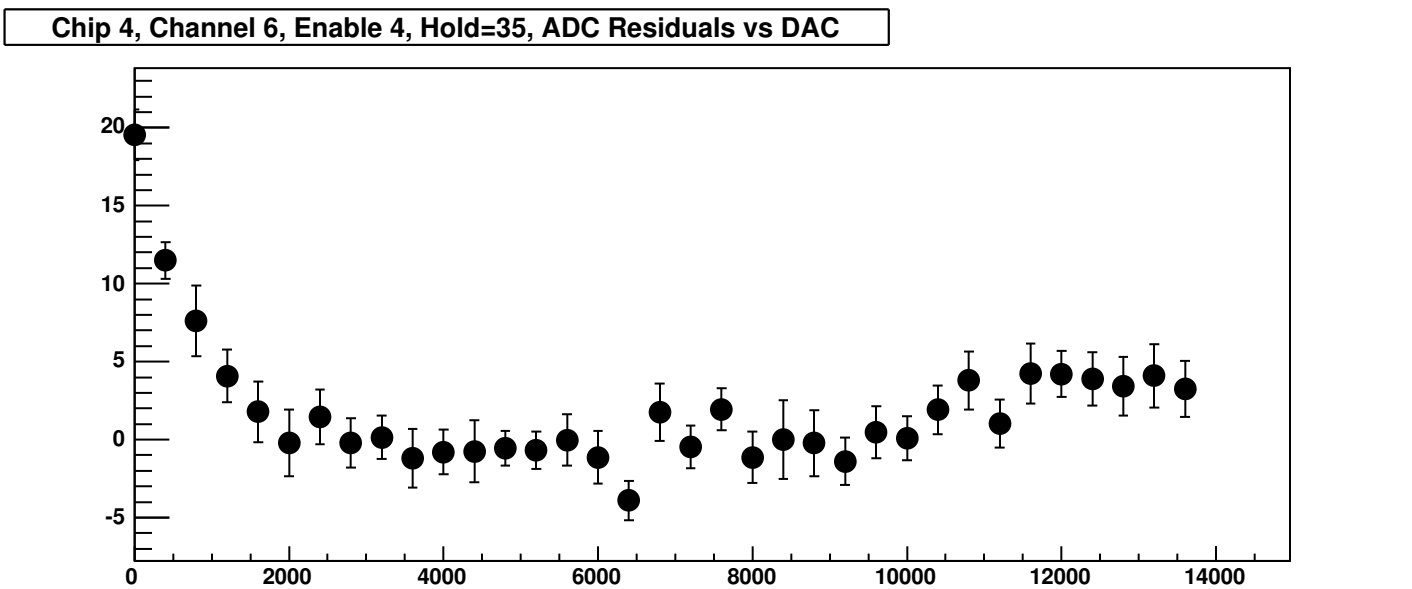

Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.39 / 23
p0 284.7 ± 0.7316
p1 0.01367 ± 0.0001117
480
460
440
420
400
380
360
340
320
300
280
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

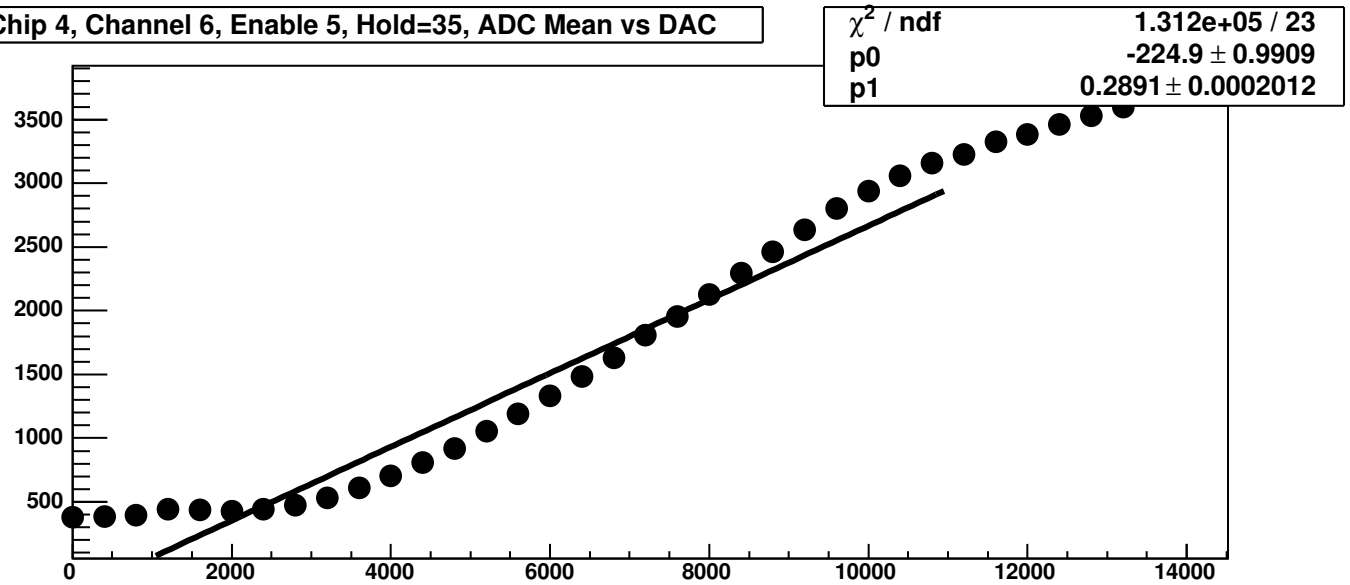

Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.28 / 23
p0 283.8 ± 0.7712
p1 0.01426 ± 0.0001167
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

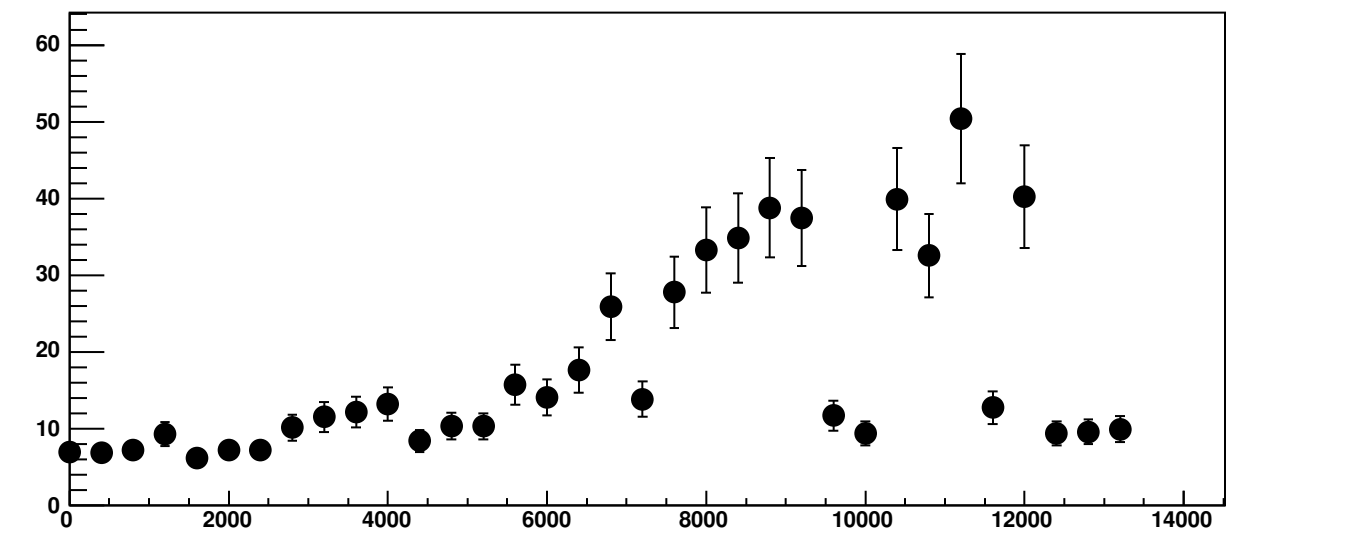

Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

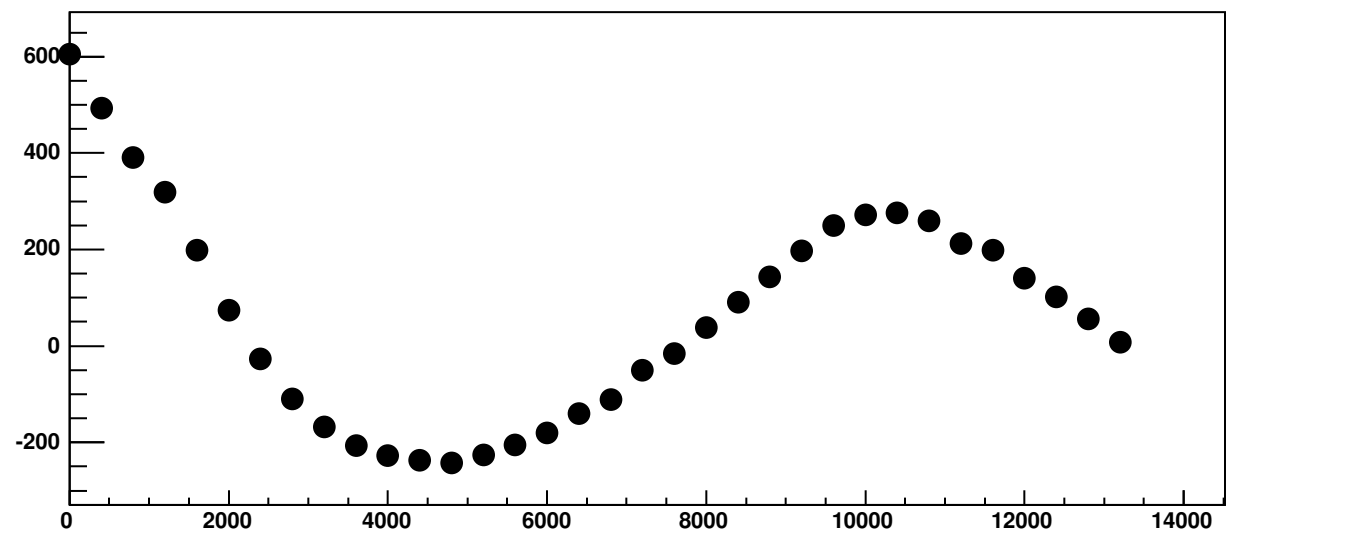
Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

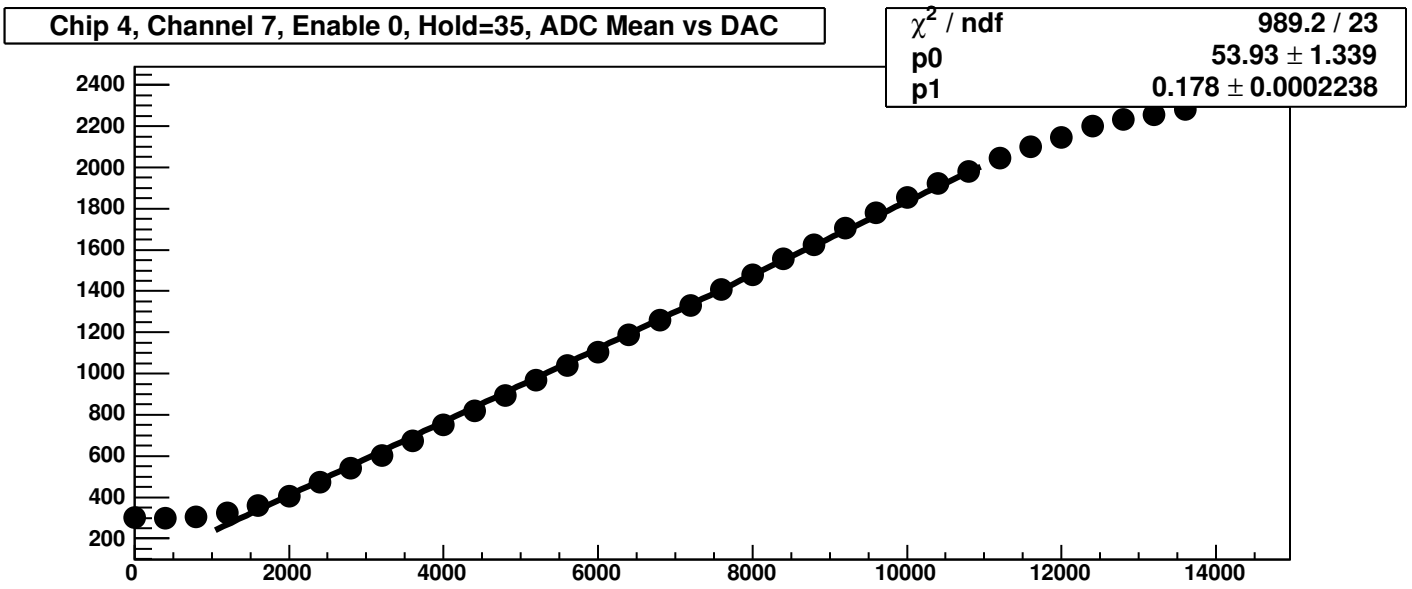


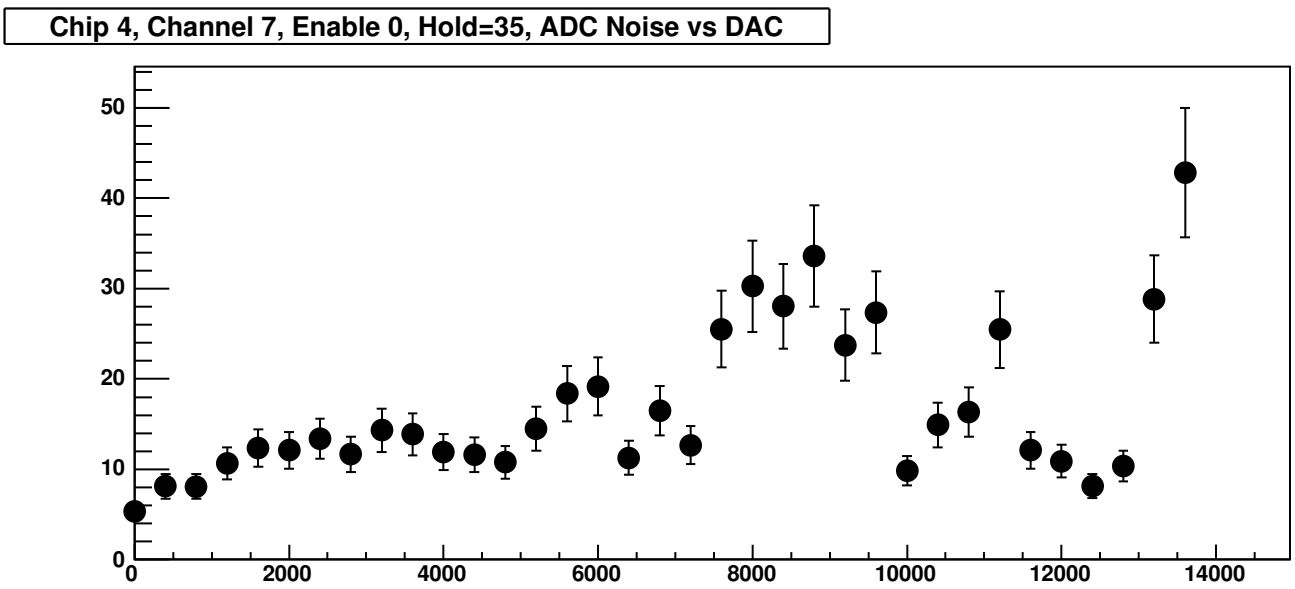
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC

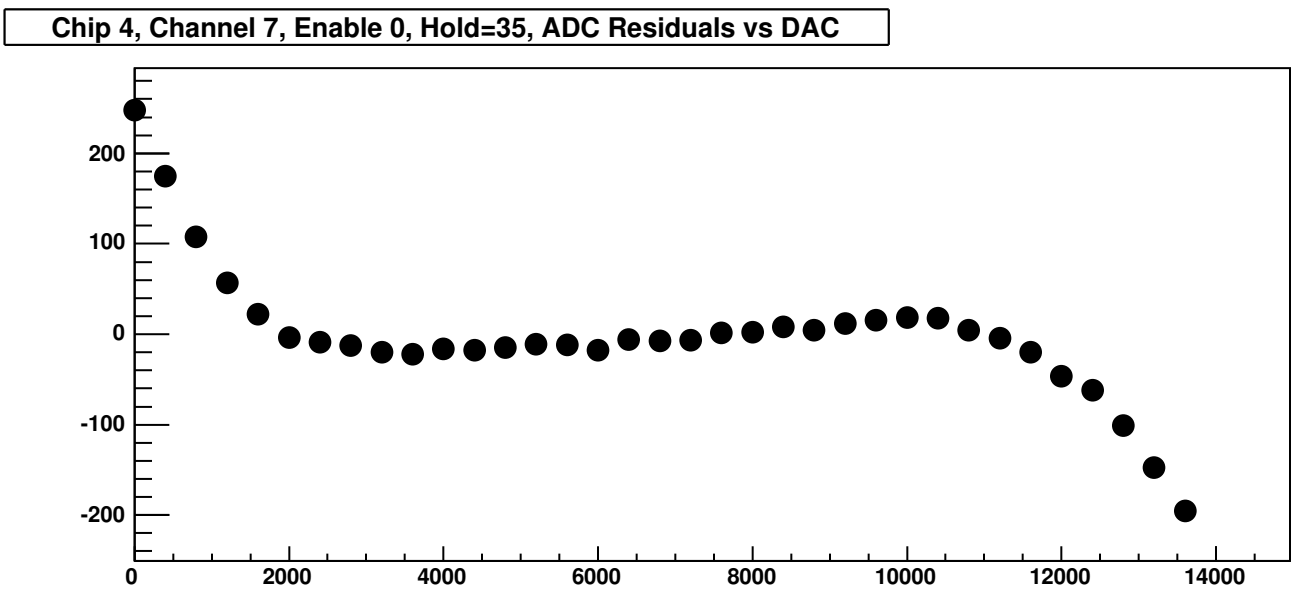


Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

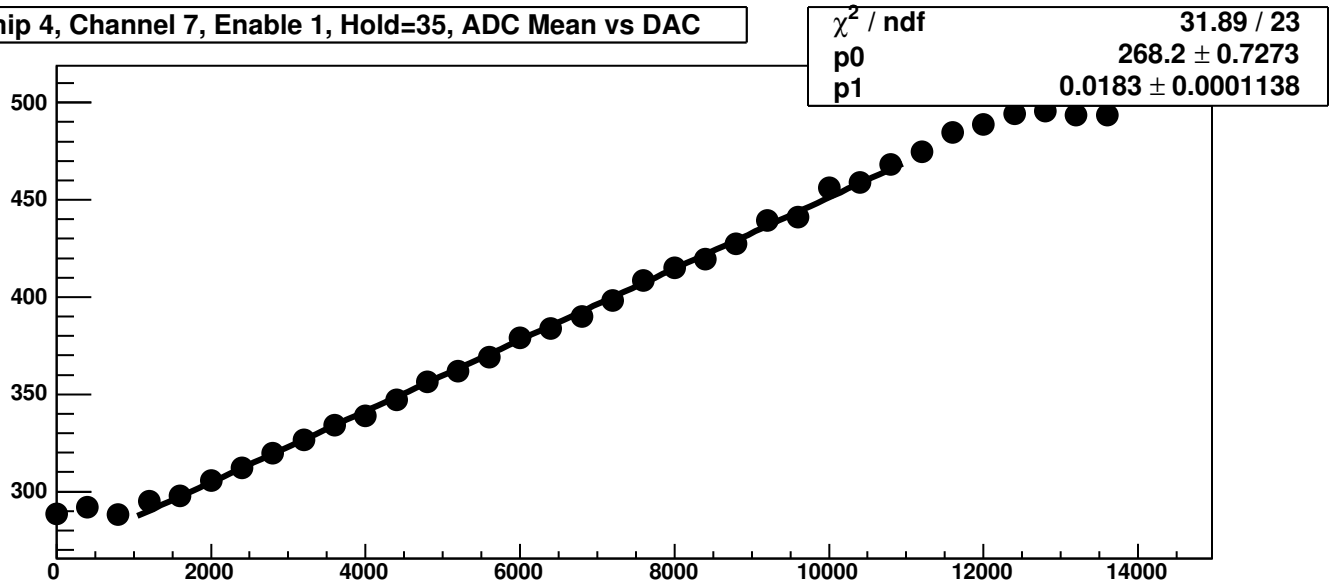



Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 989.2 / 23
p0 53.93 ± 1.339
p1 0.178 ± 0.0002238
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

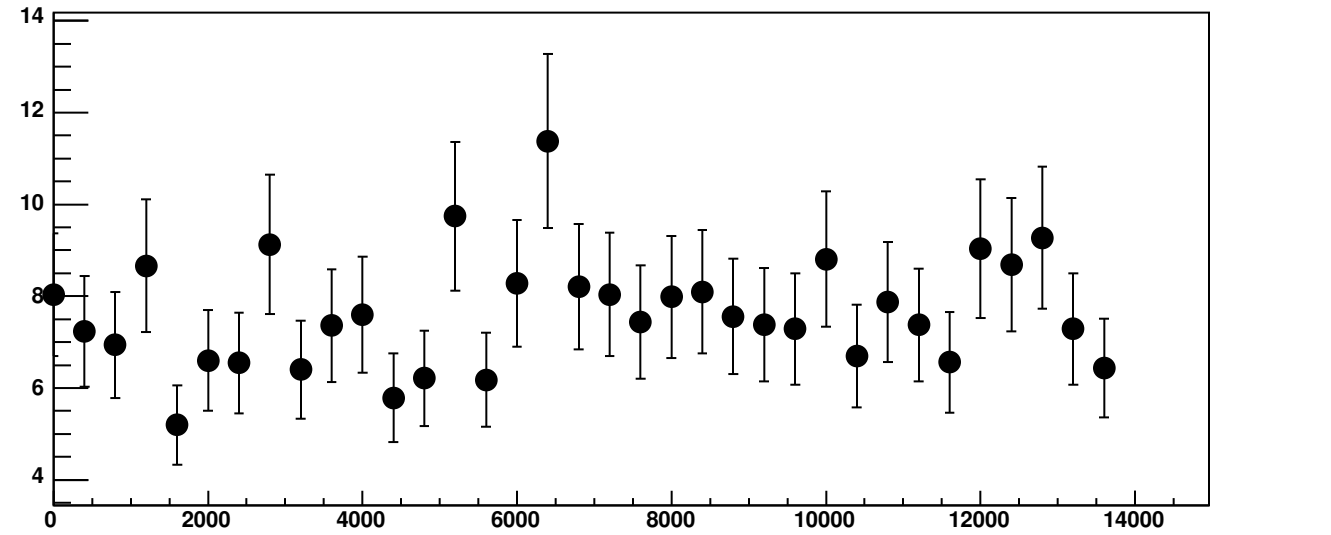

Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

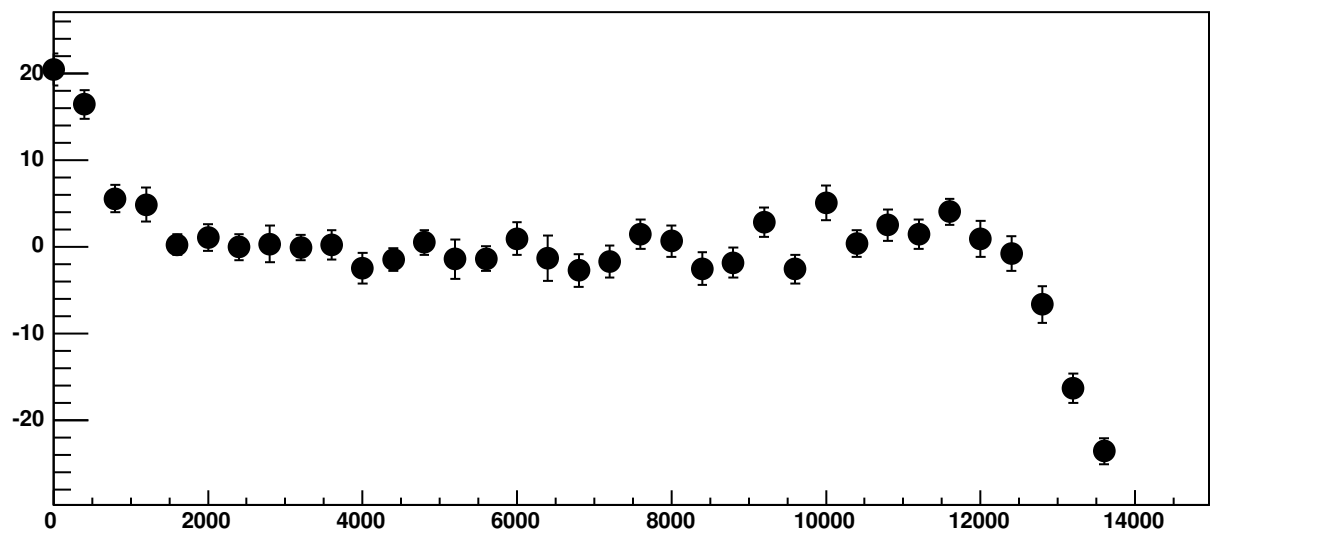
Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

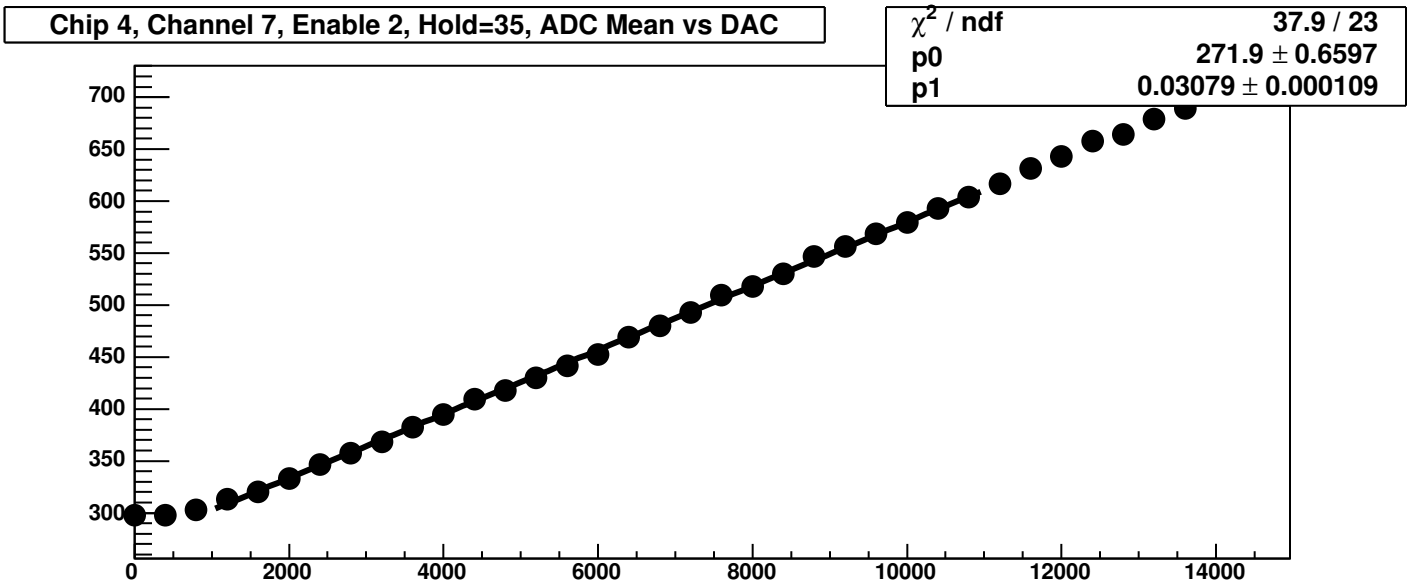


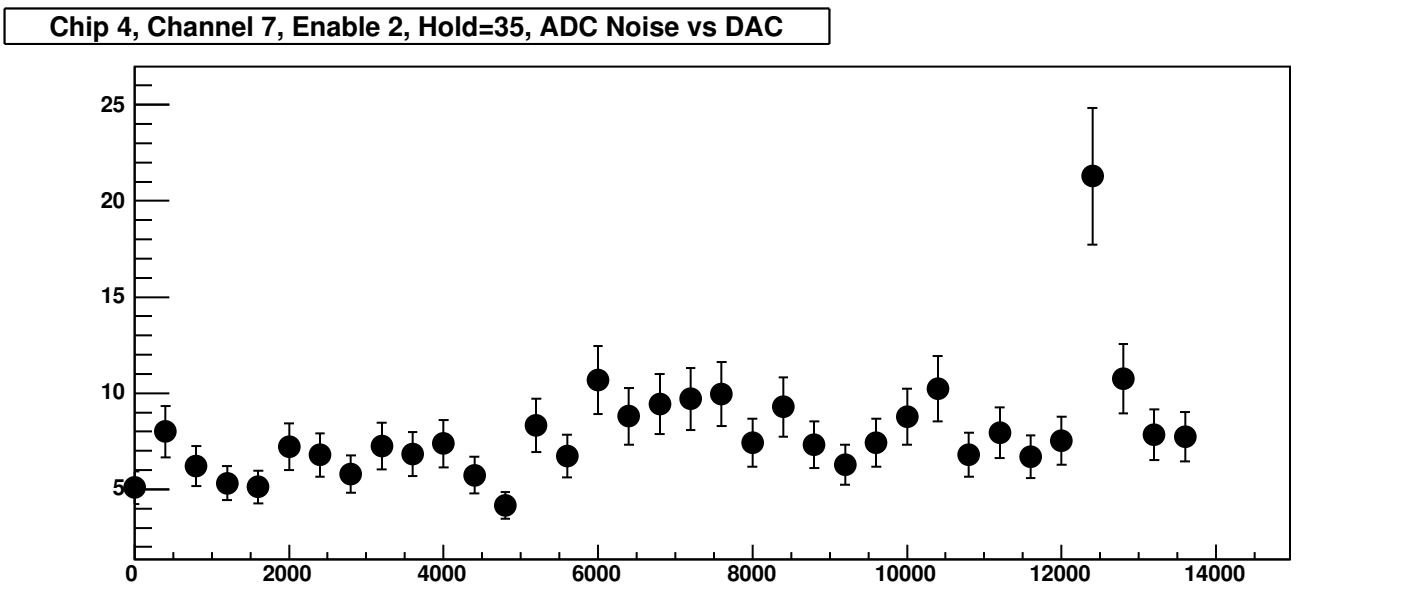
Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

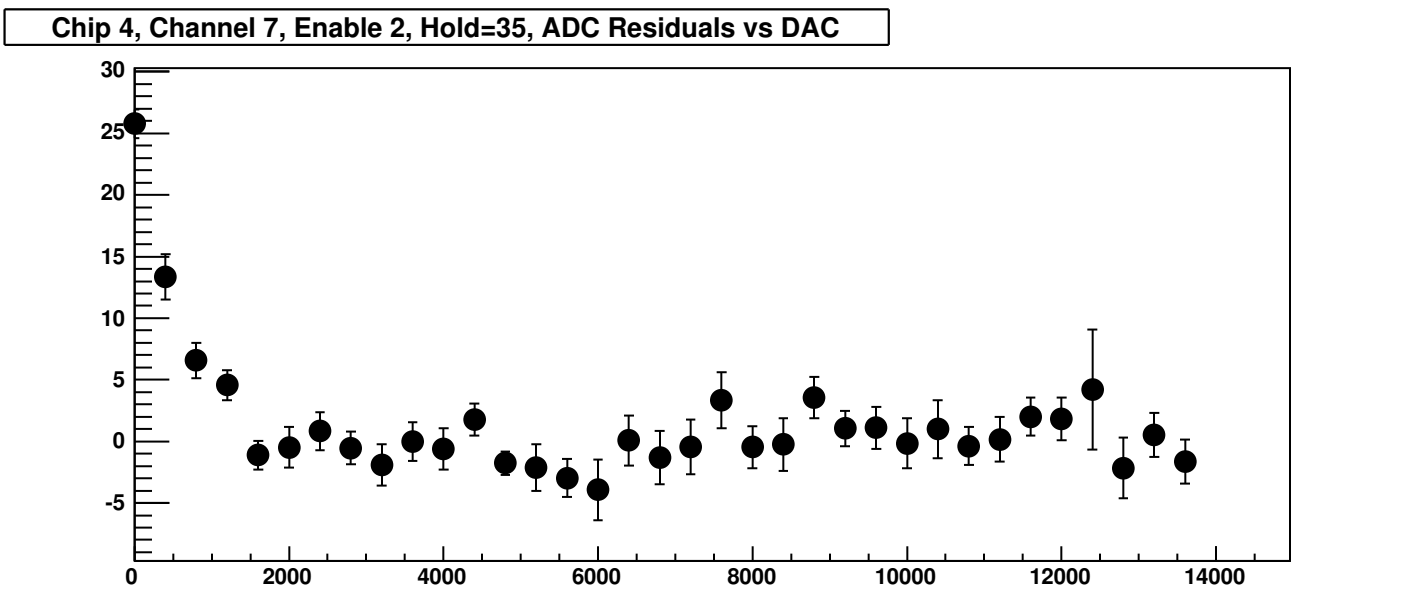


Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

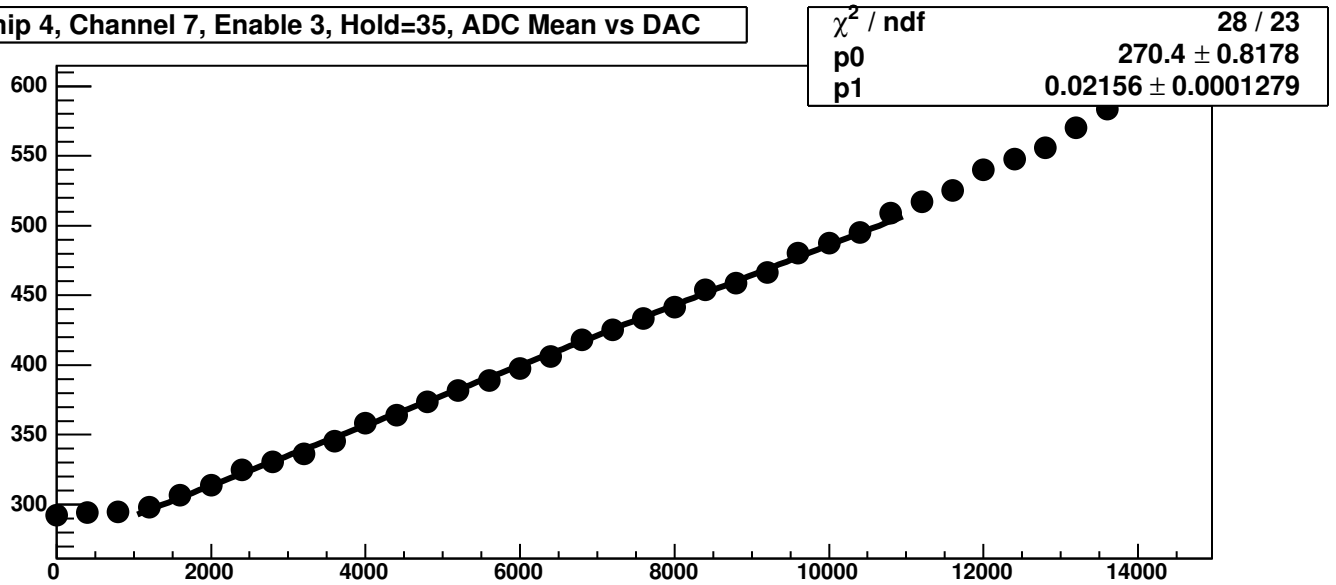



Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 37.9 / 23
p0 271.9 ± 0.6597
p1 0.03079 ± 0.000109
700
650
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

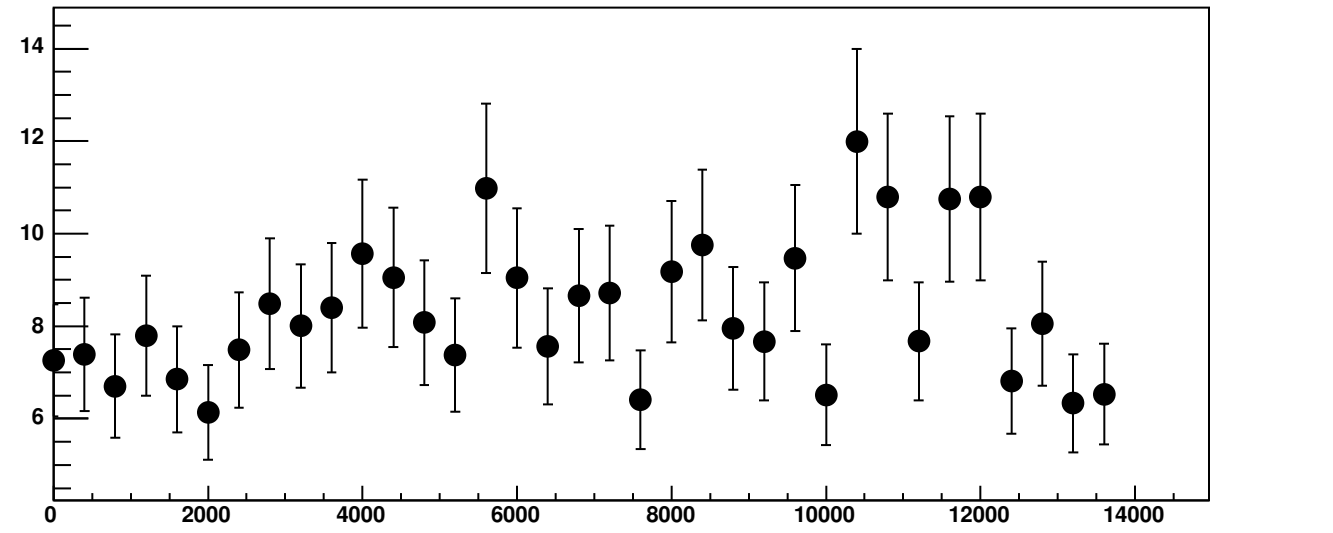

Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

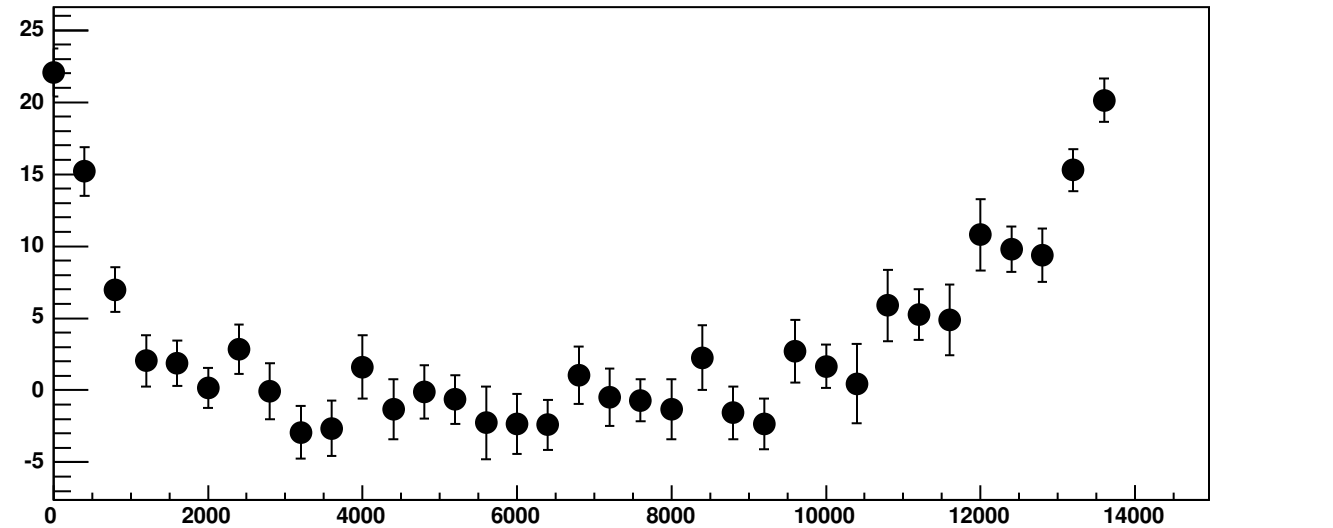
Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



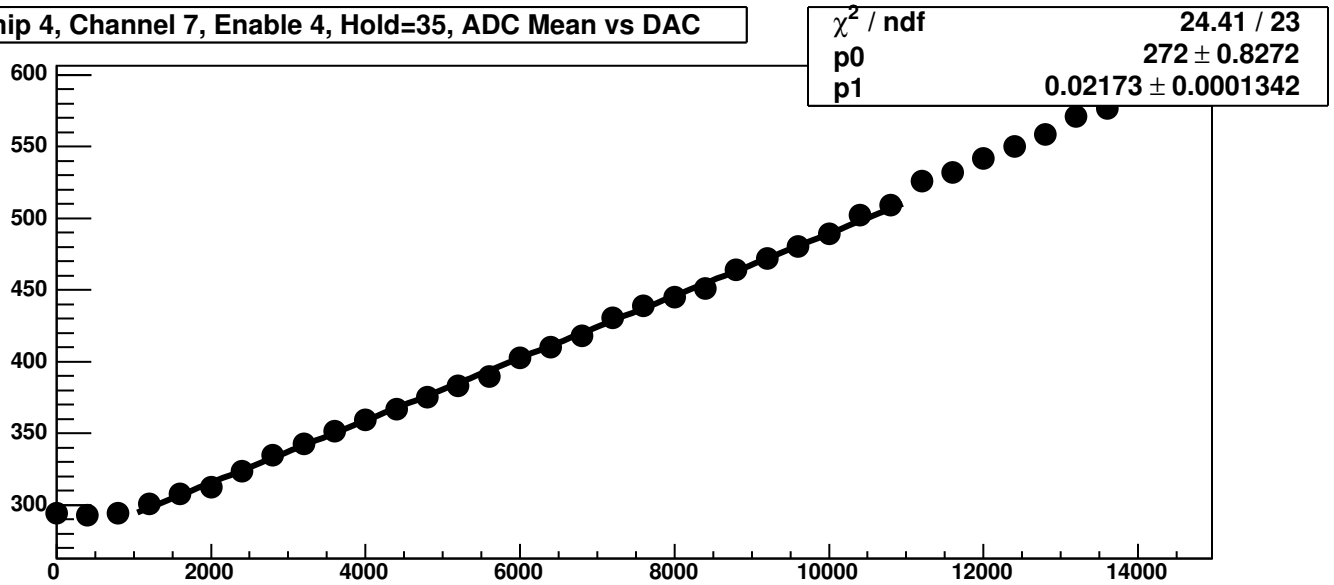
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



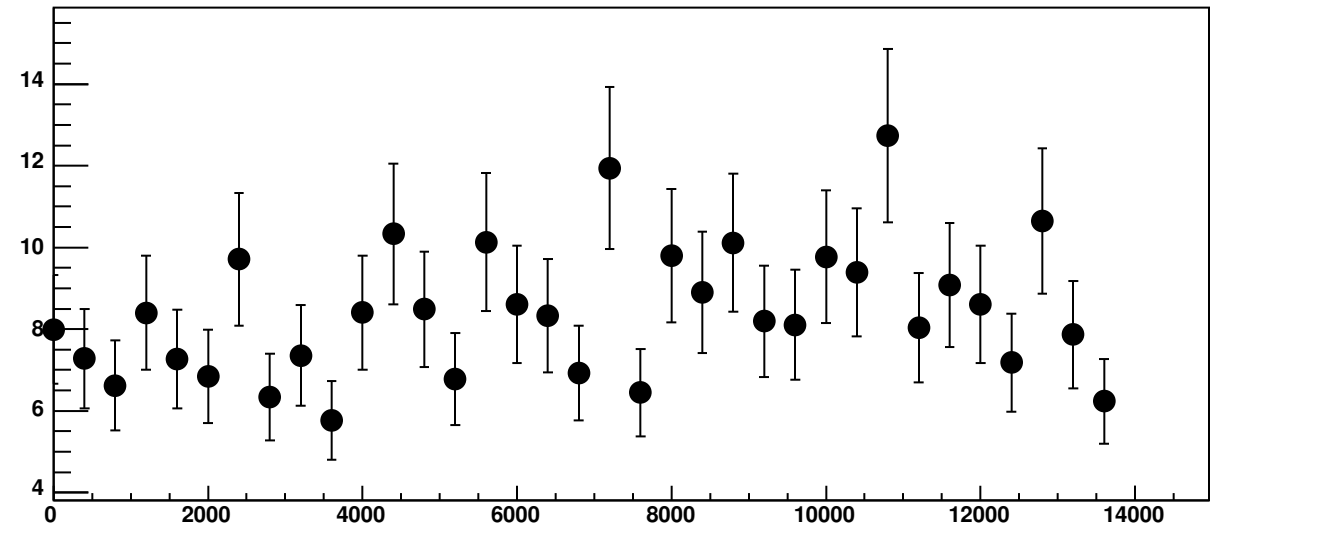
Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



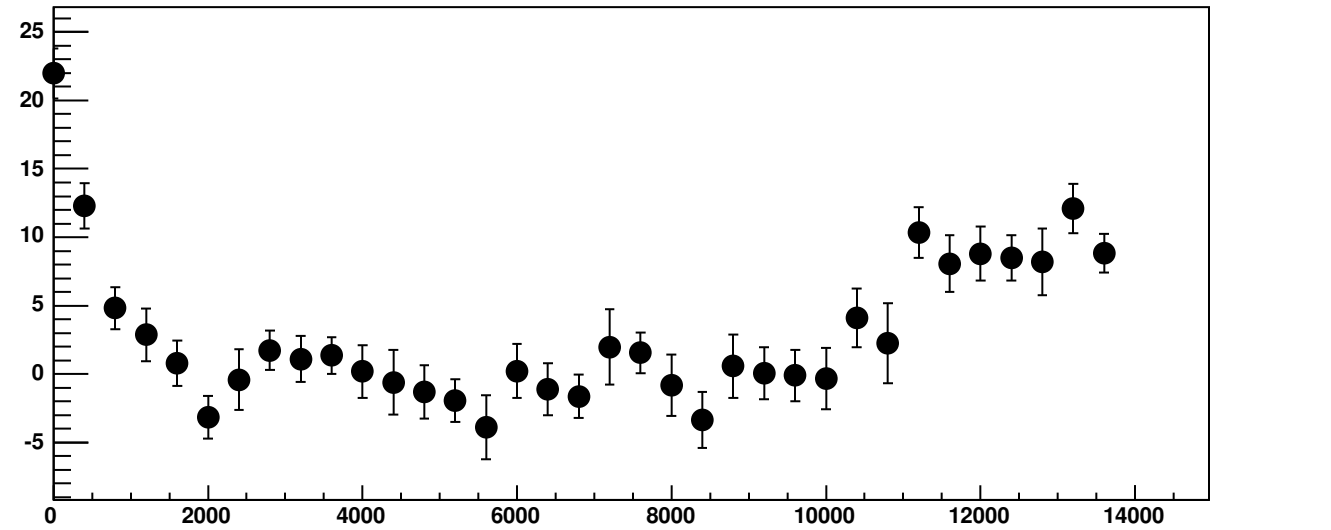
Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

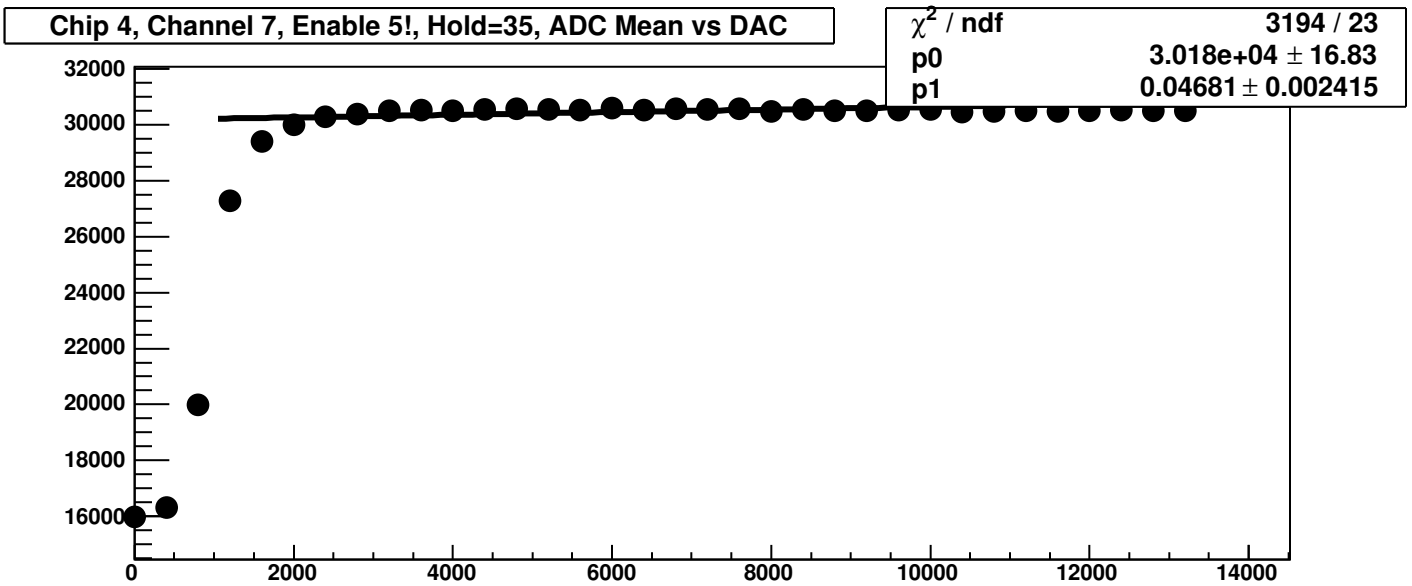


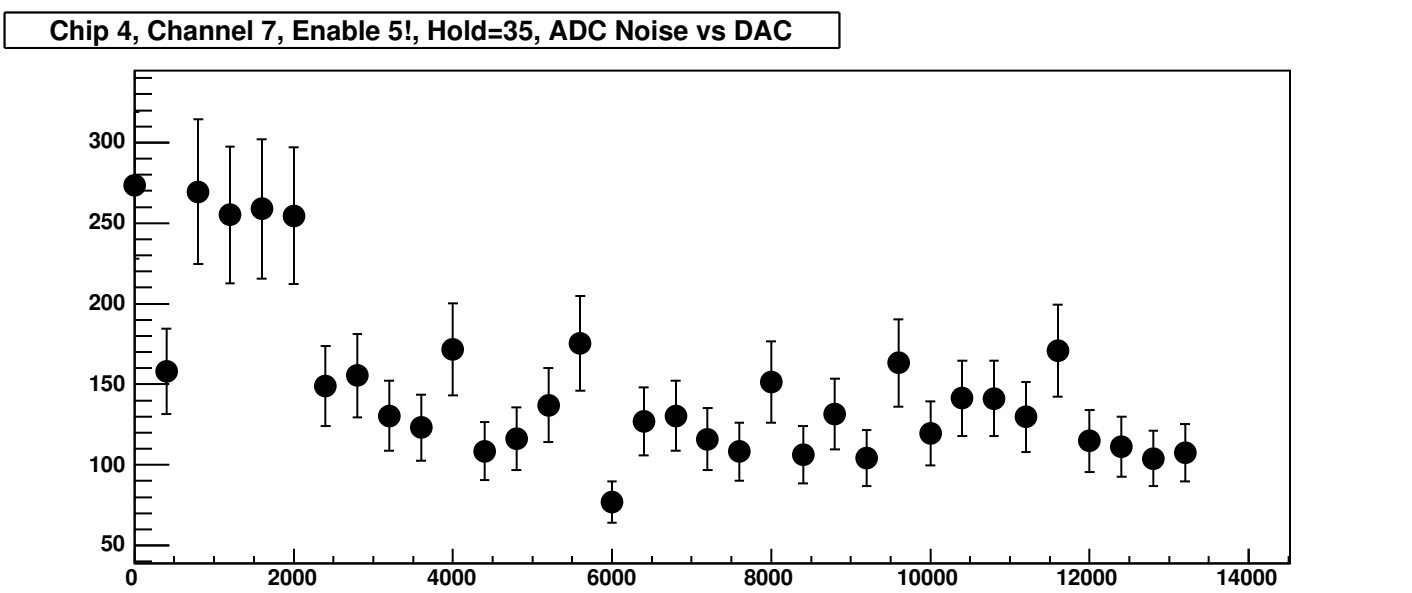
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

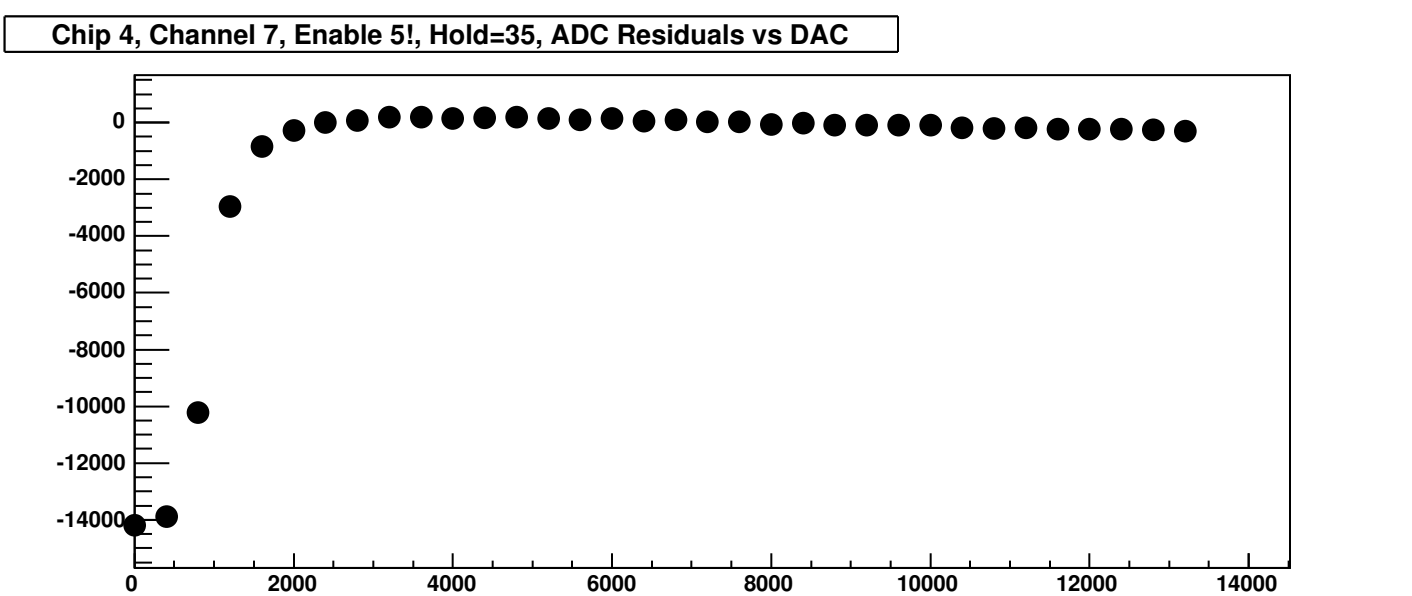


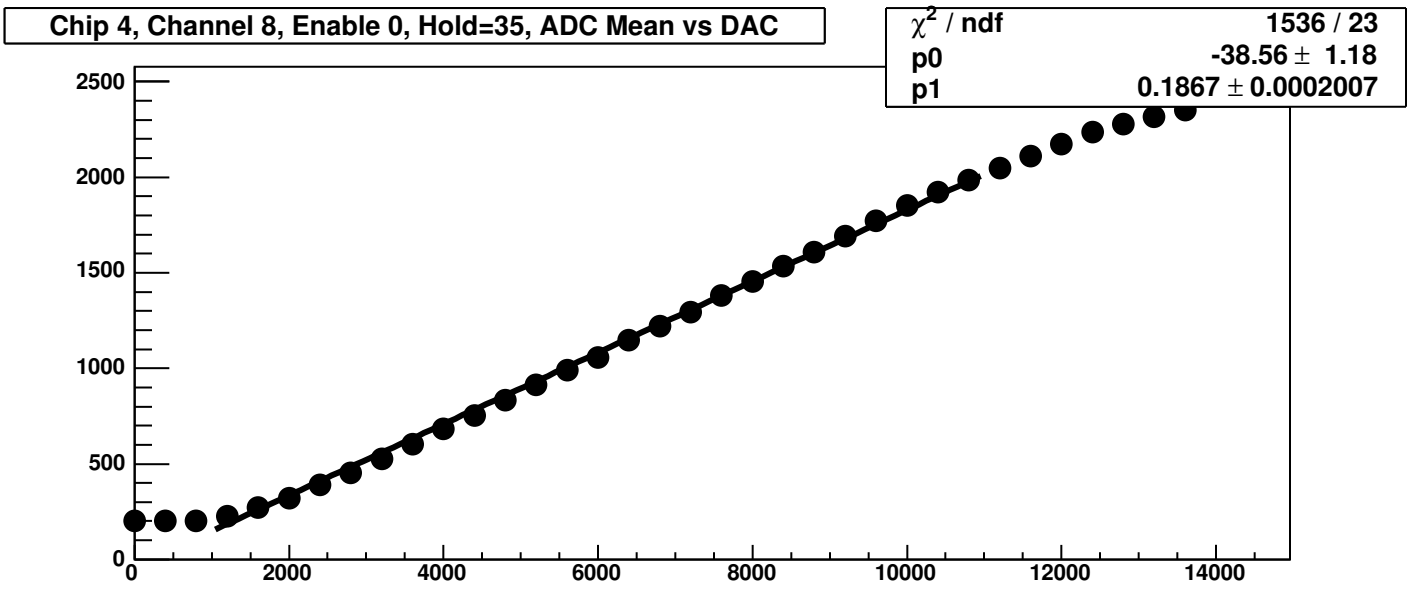
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

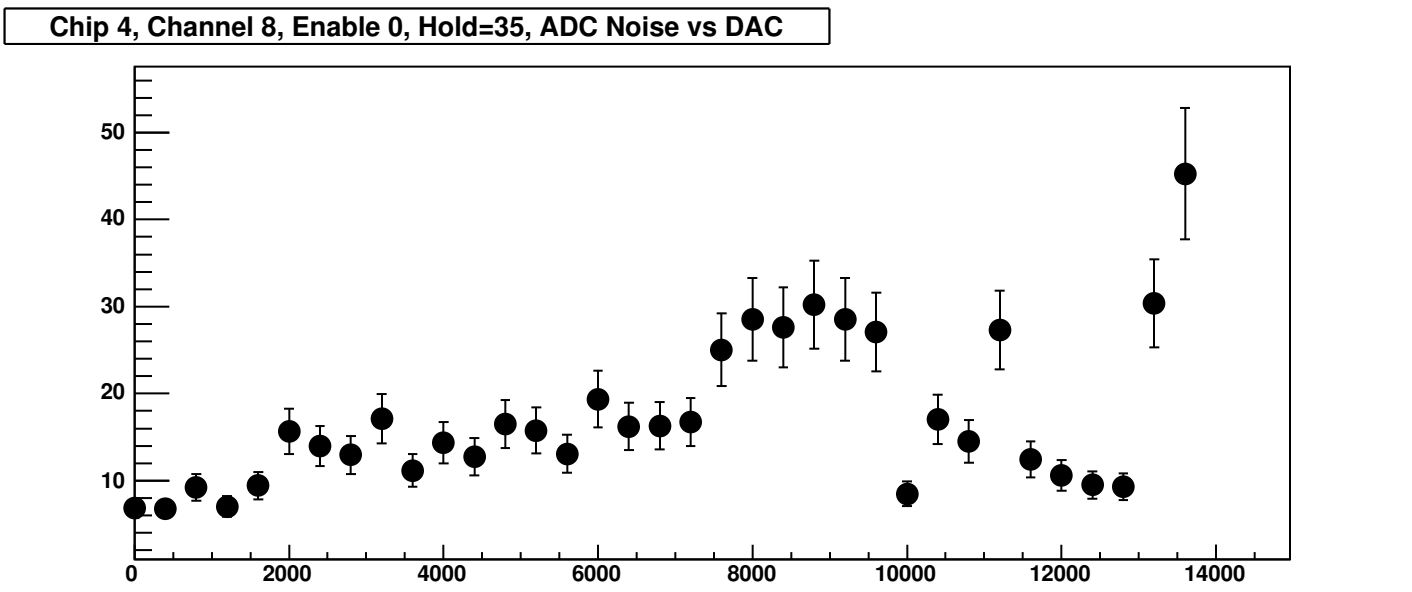


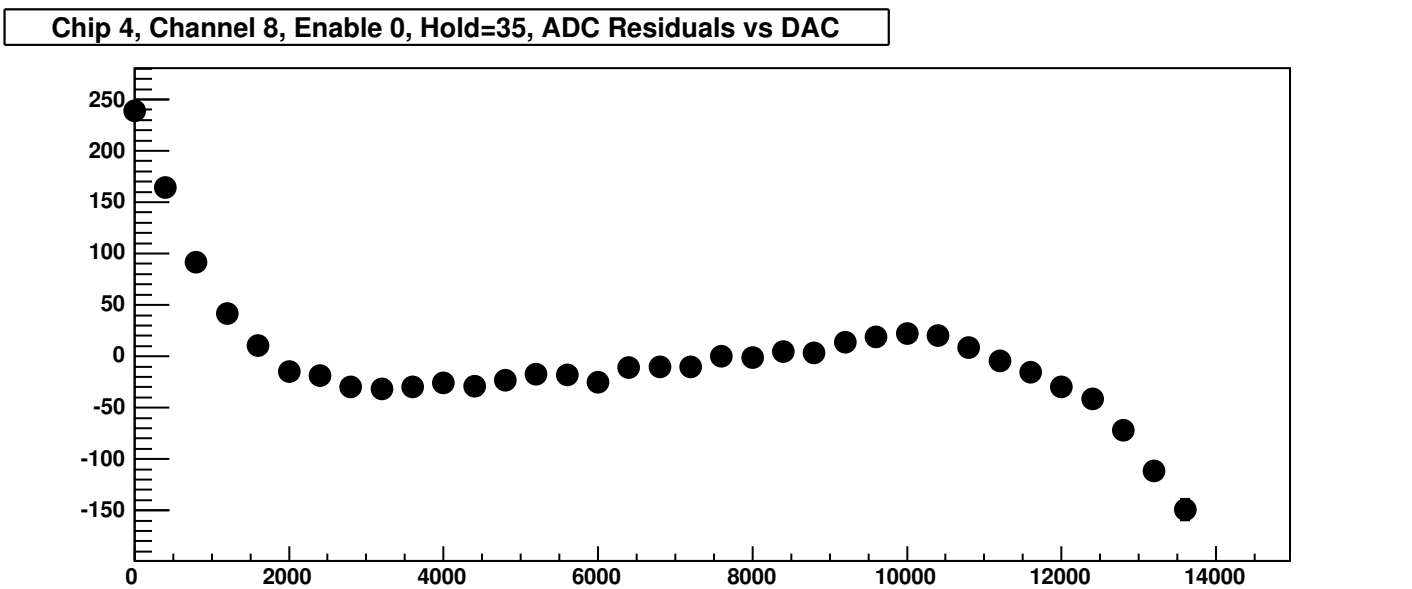

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3194 / 23
p0 3.018e+04 ± 16.83
p1 0.04681 ± 0.002415
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

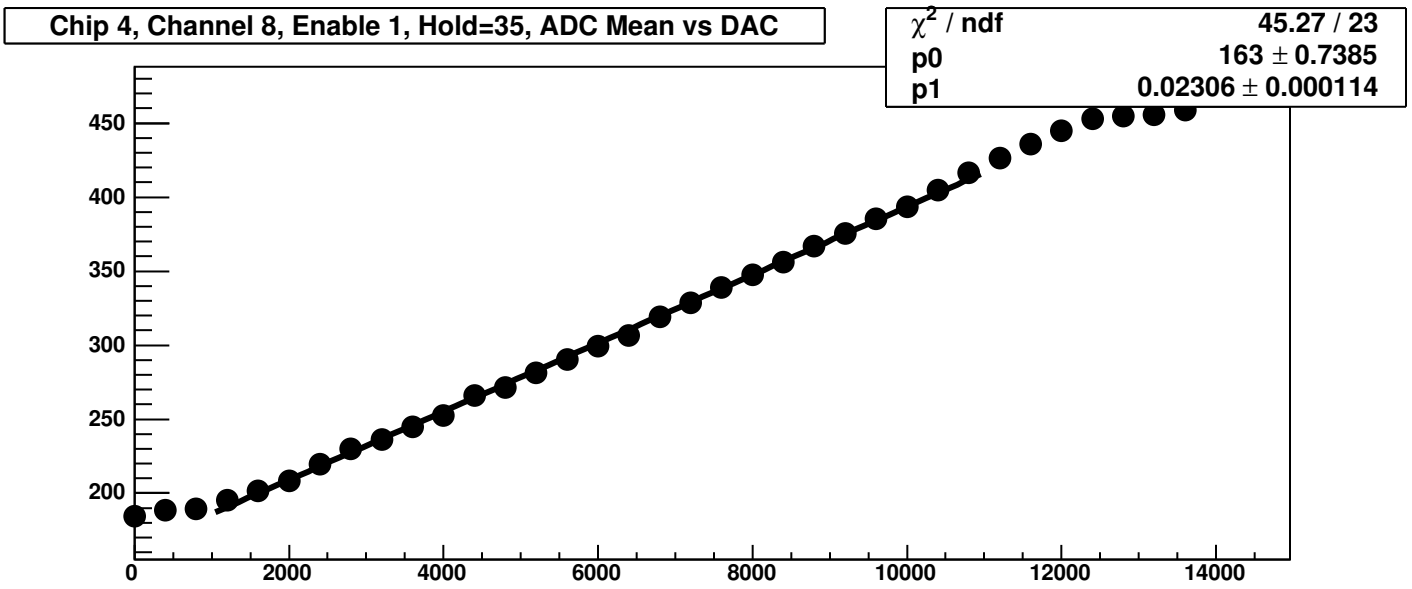

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

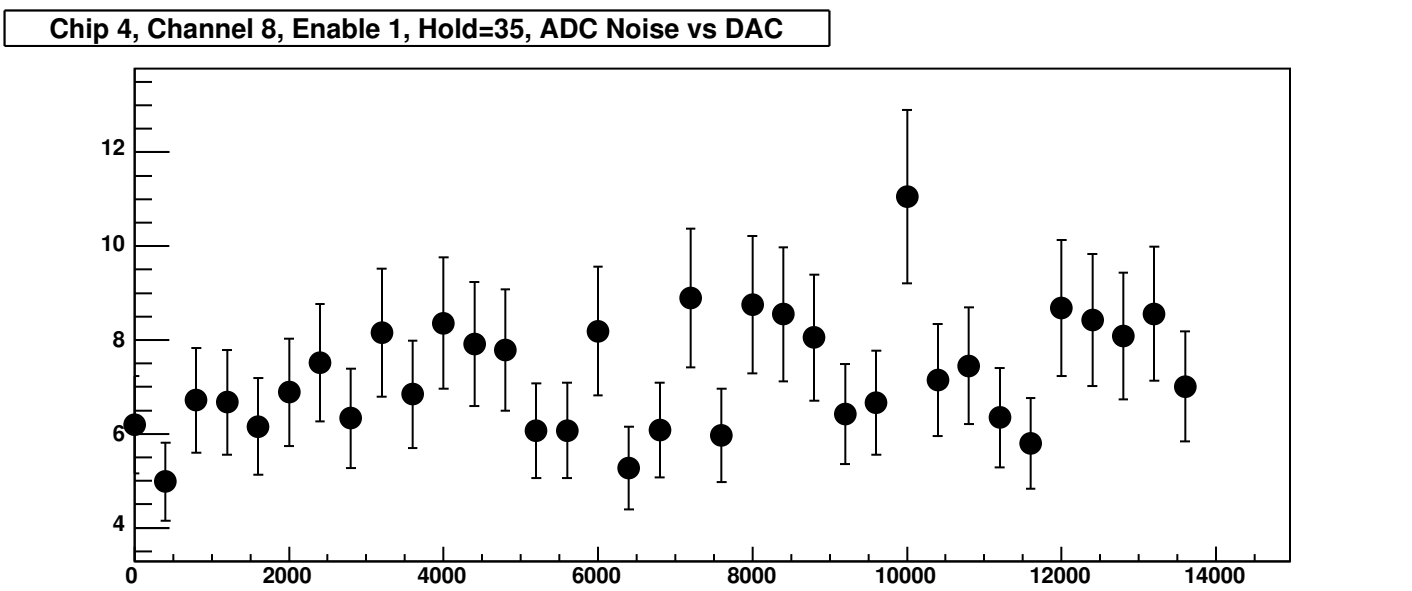

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

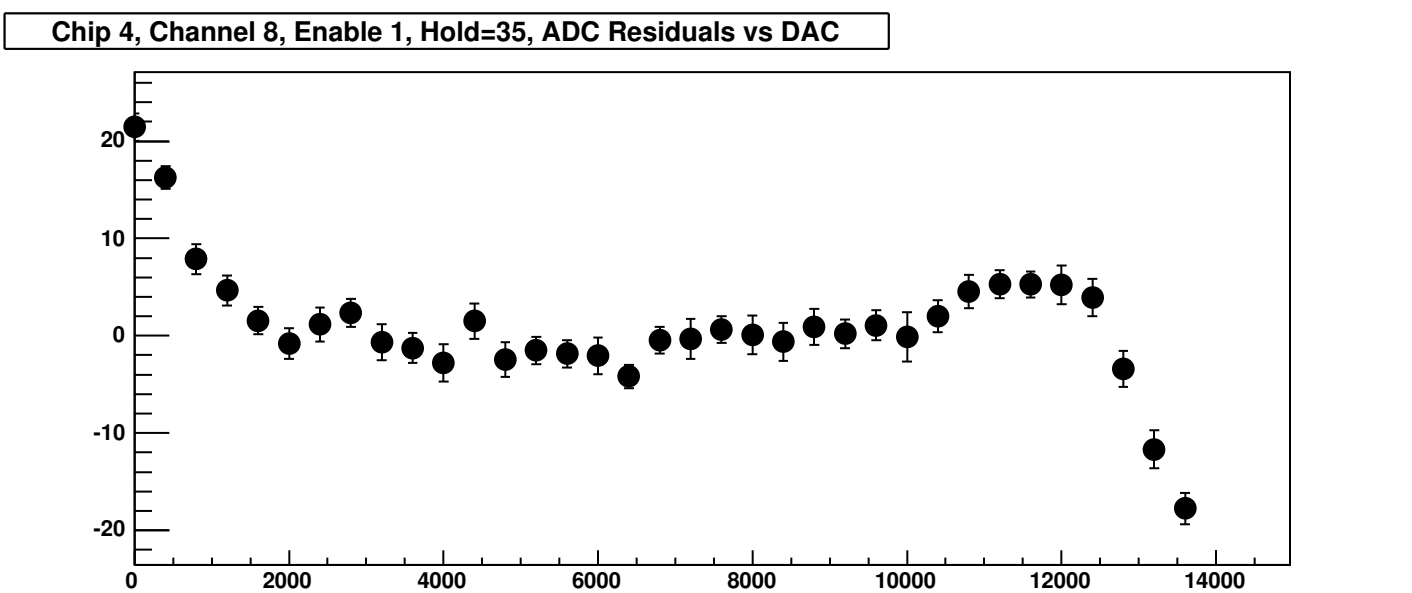

Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1536 / 23
p0 -38.56 ± 1.18
p1 0.1867 ± 0.0002007
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

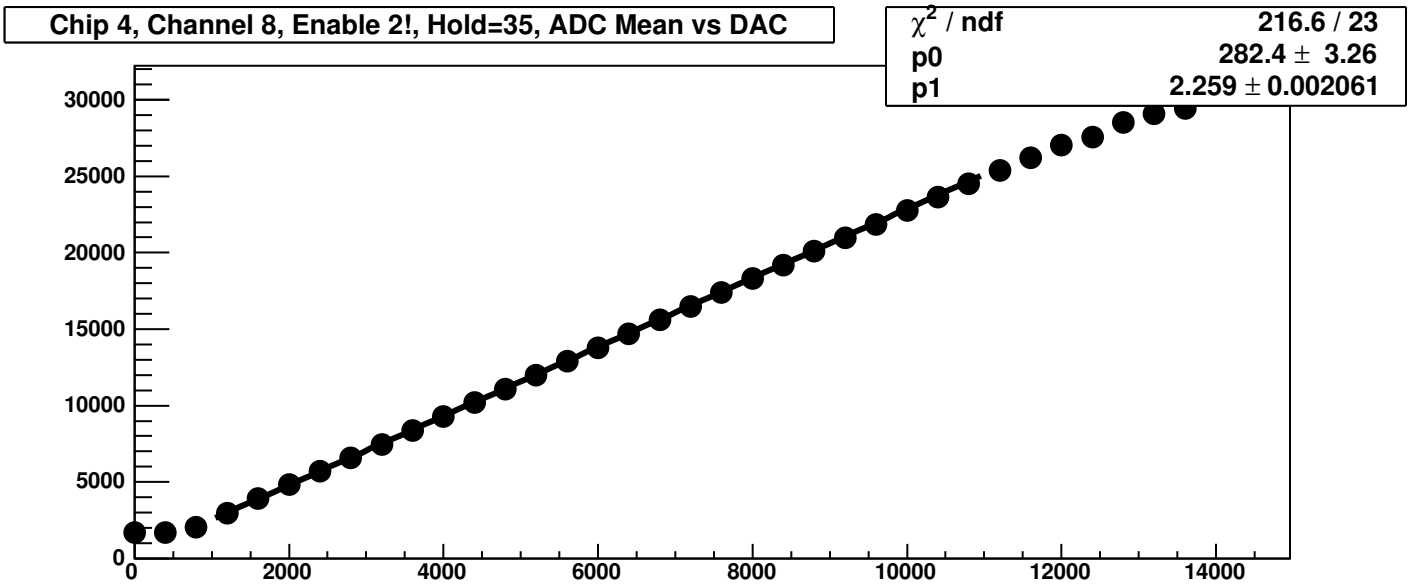

Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

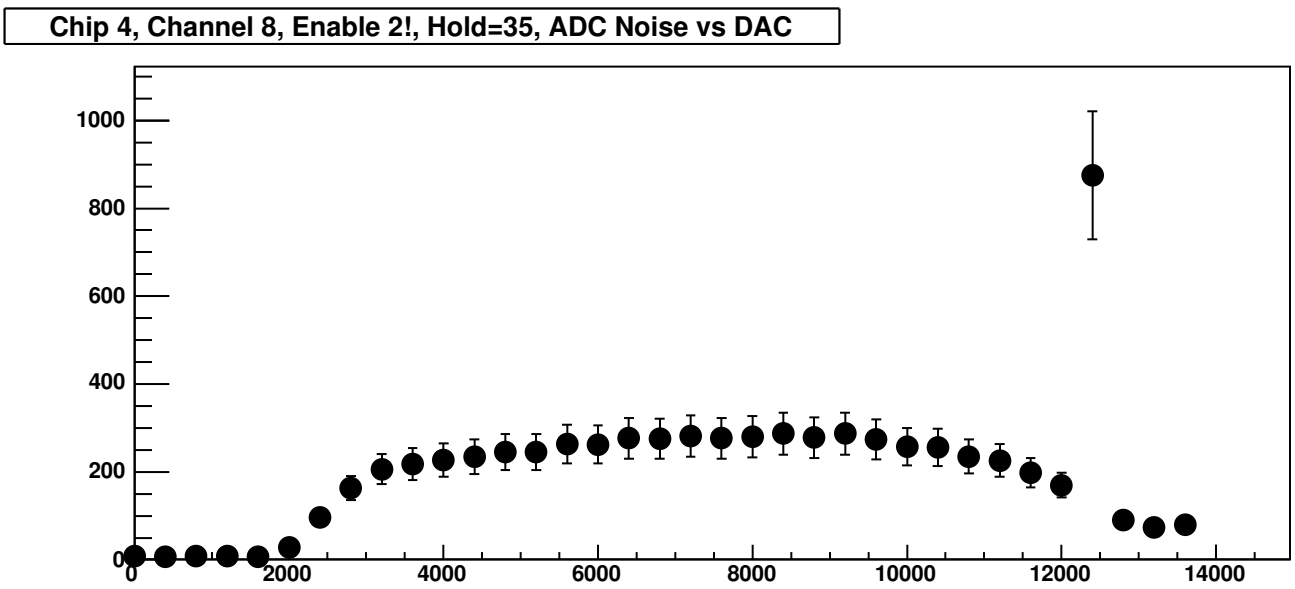

Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

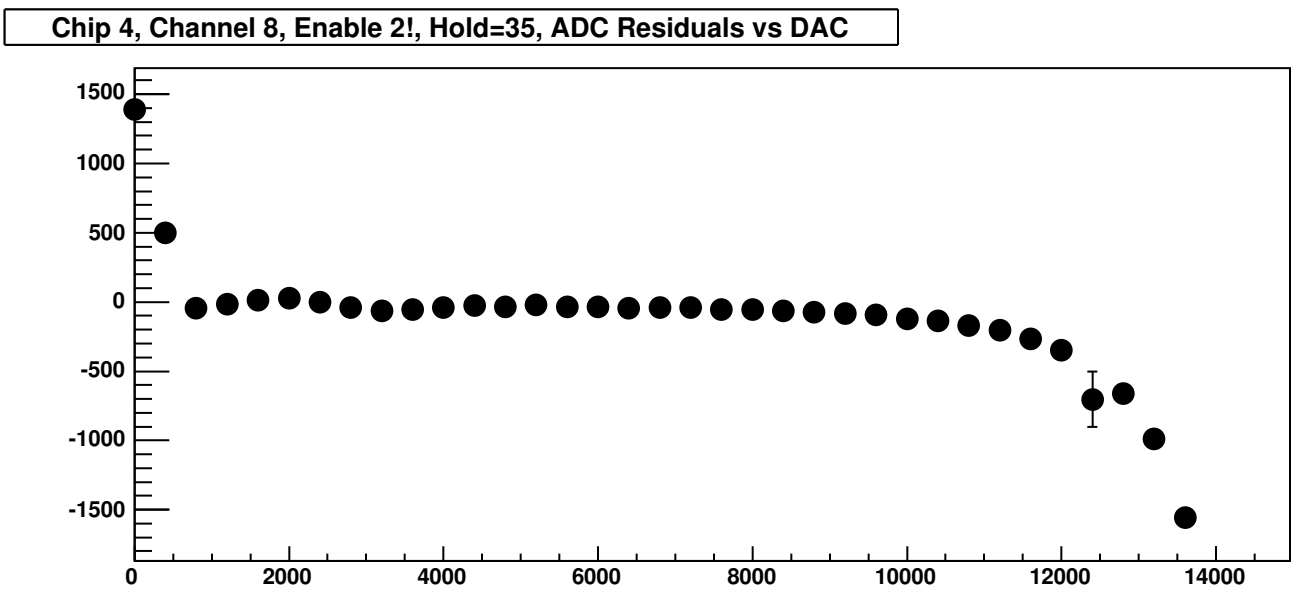

Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.27 / 23
p0 163 ± 0.7385
p1 0.02306 ± 0.000114
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

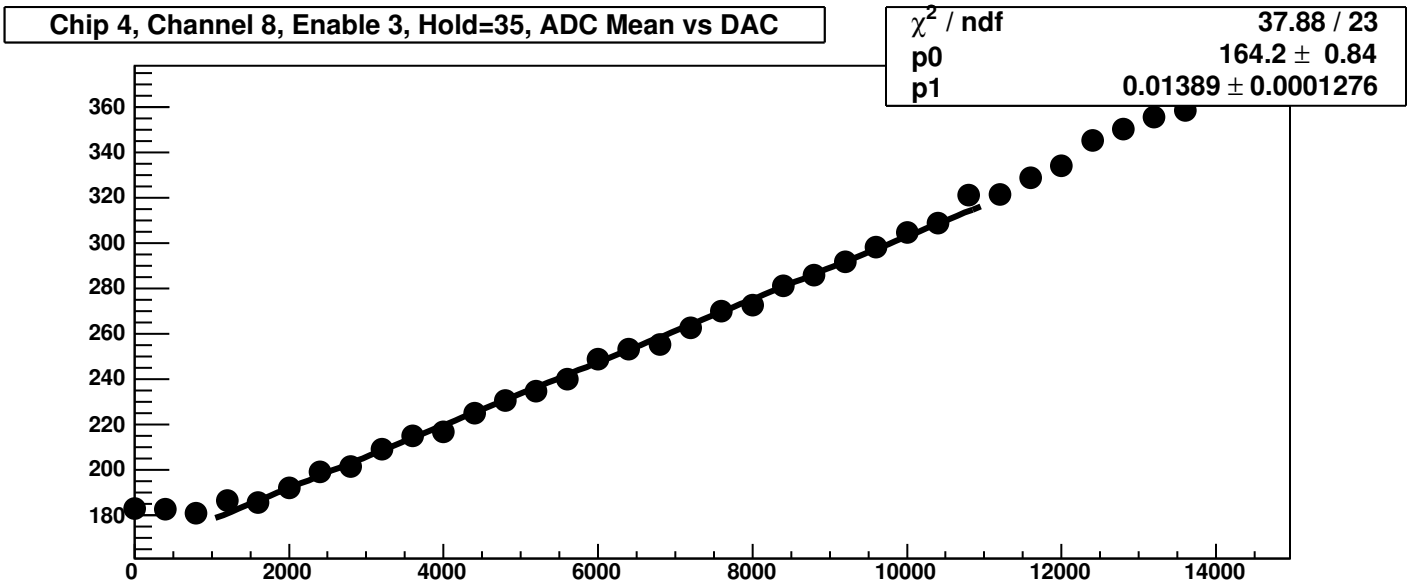

Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

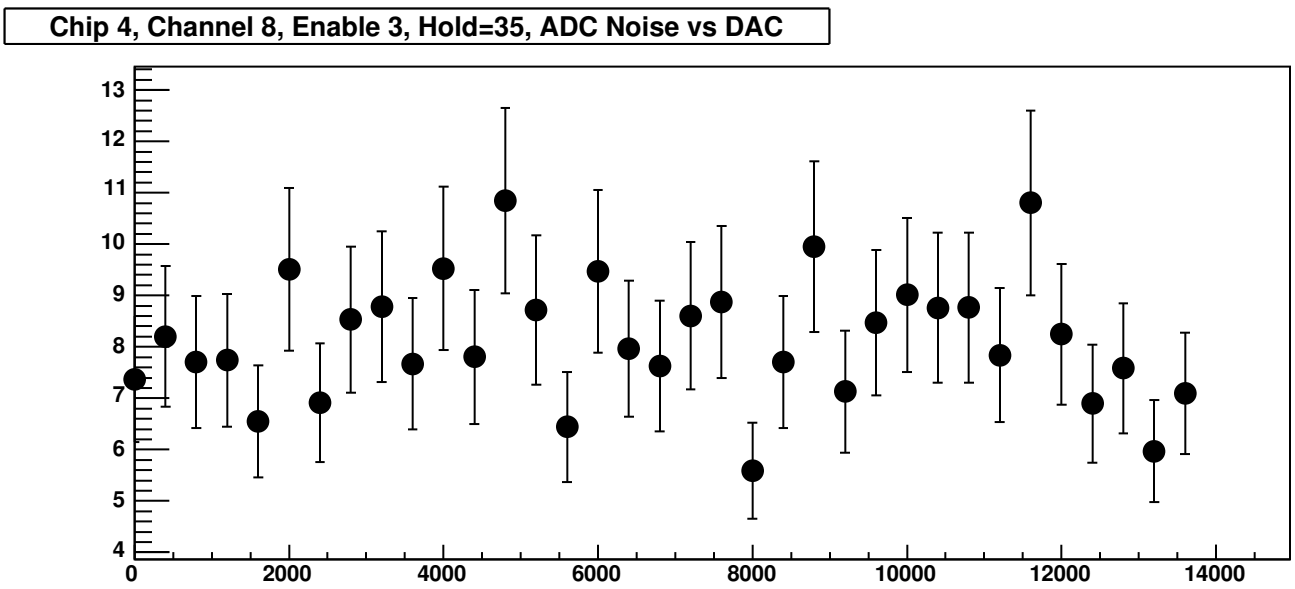

Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

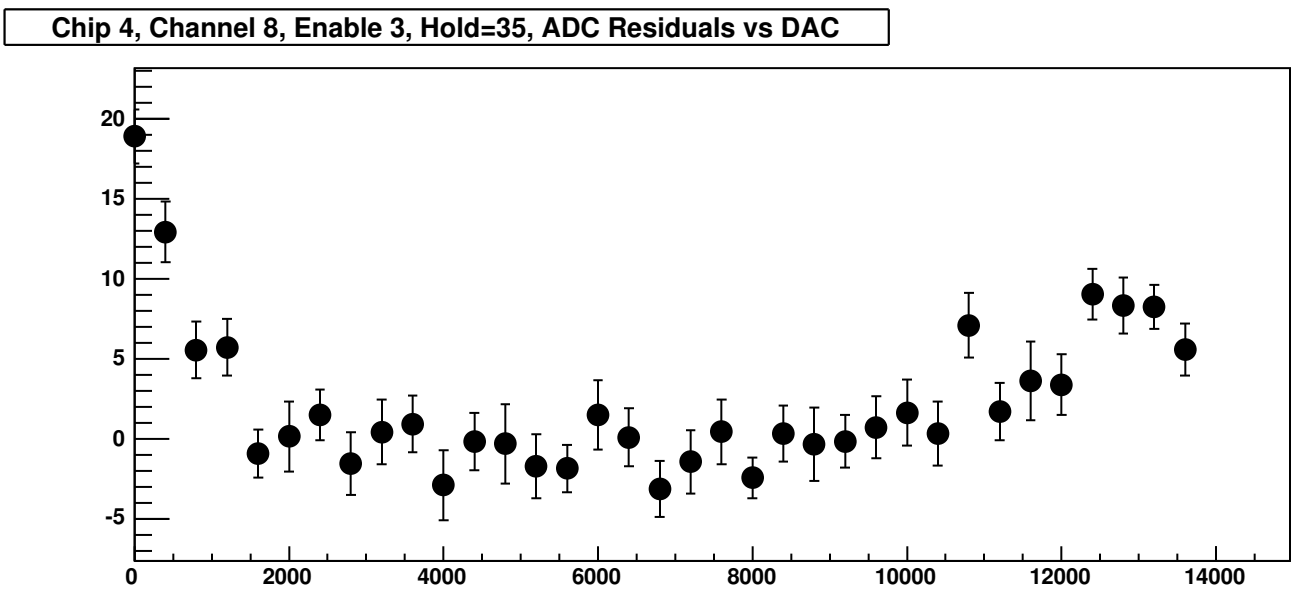

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 216.6 / 23
p0 282.4 ± 3.26
p1 2.259 ± 0.002061
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

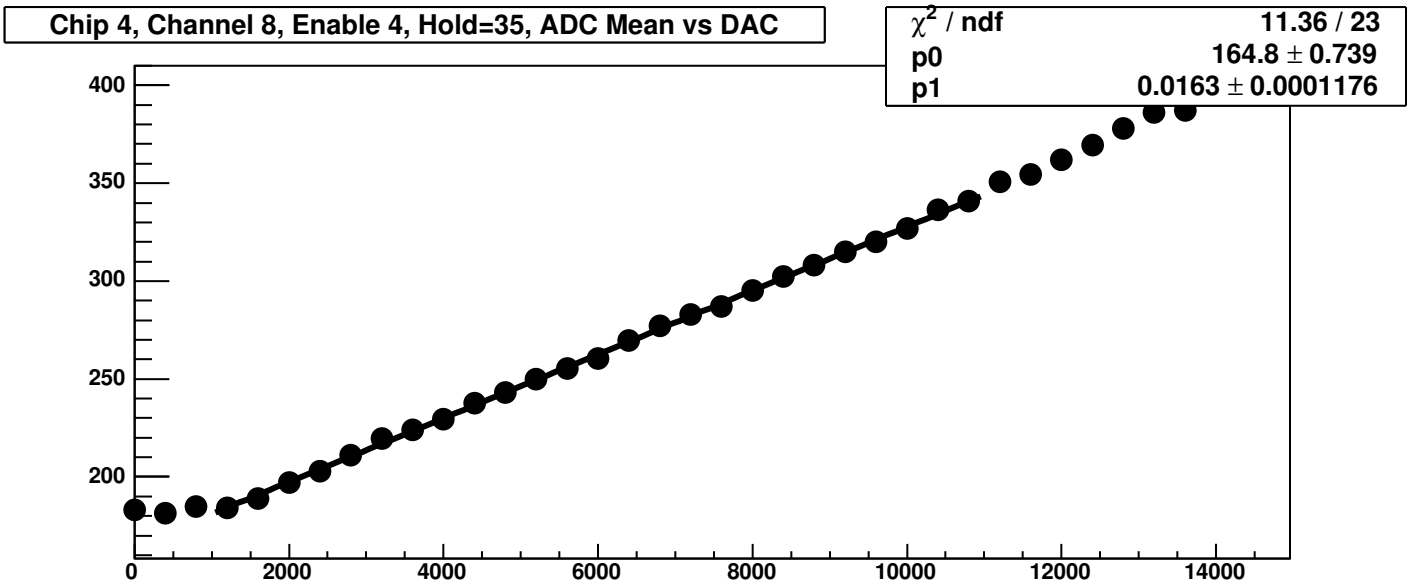

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

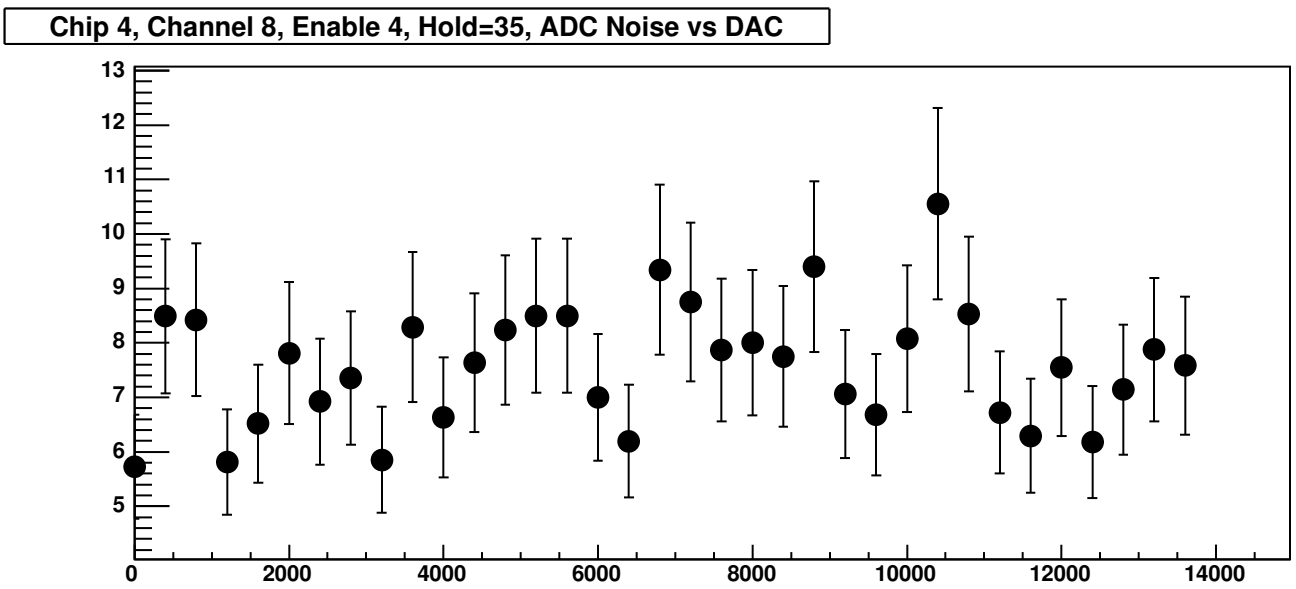

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

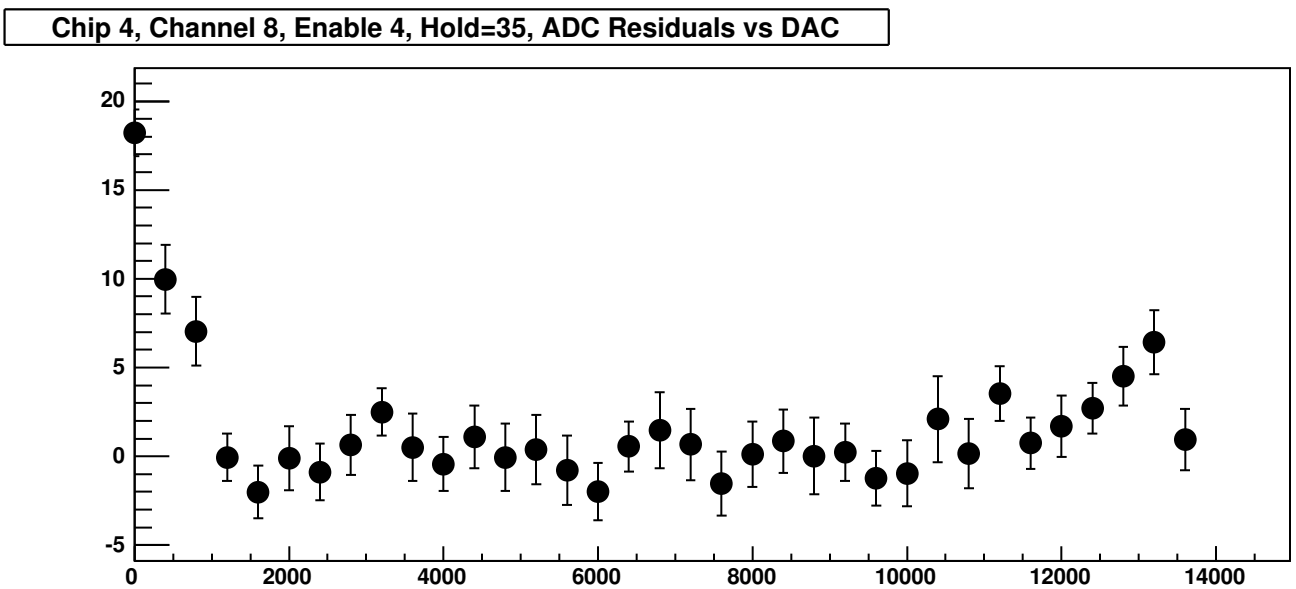

Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.88 / 23
p0 164.2 ± 0.84
p1 0.01389 ± 0.0001276
360
340
320
300
280
260
240
220
200
180
0
2000
4000
6000
8000
10000
12000
14000

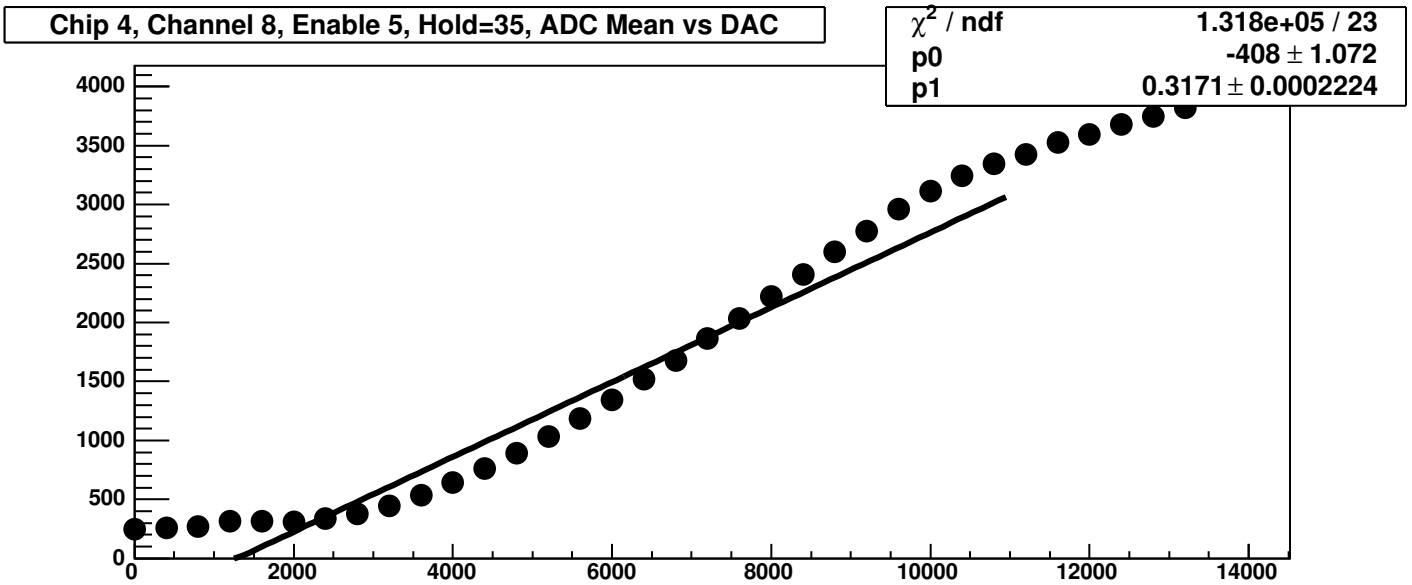

Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

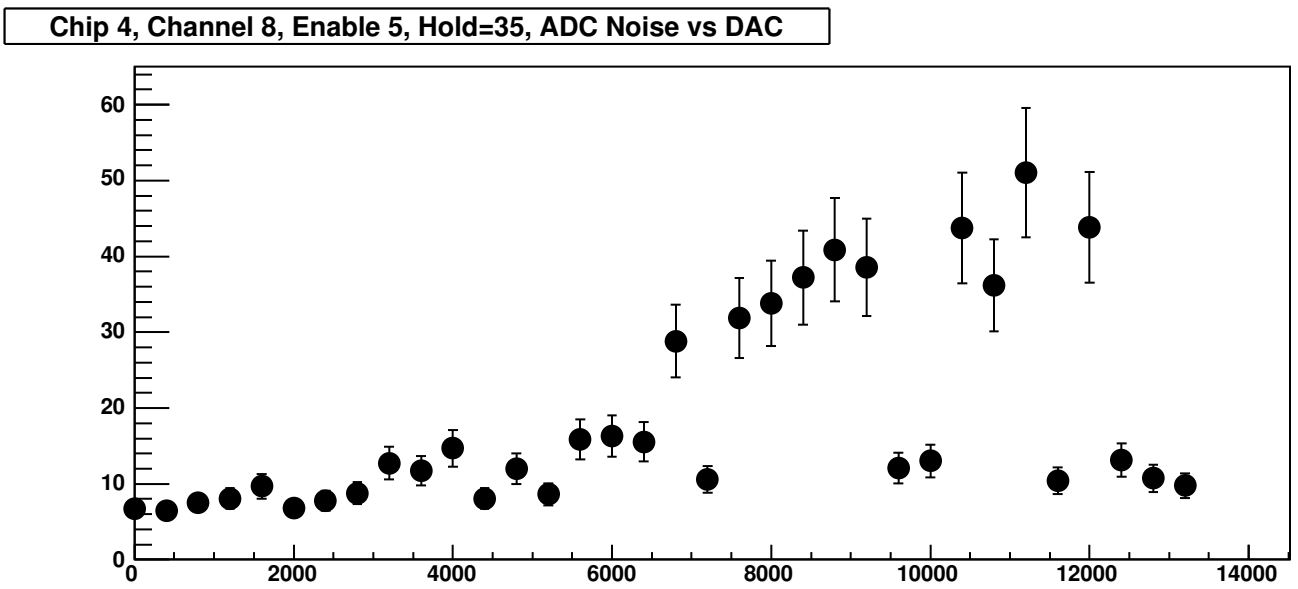

Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

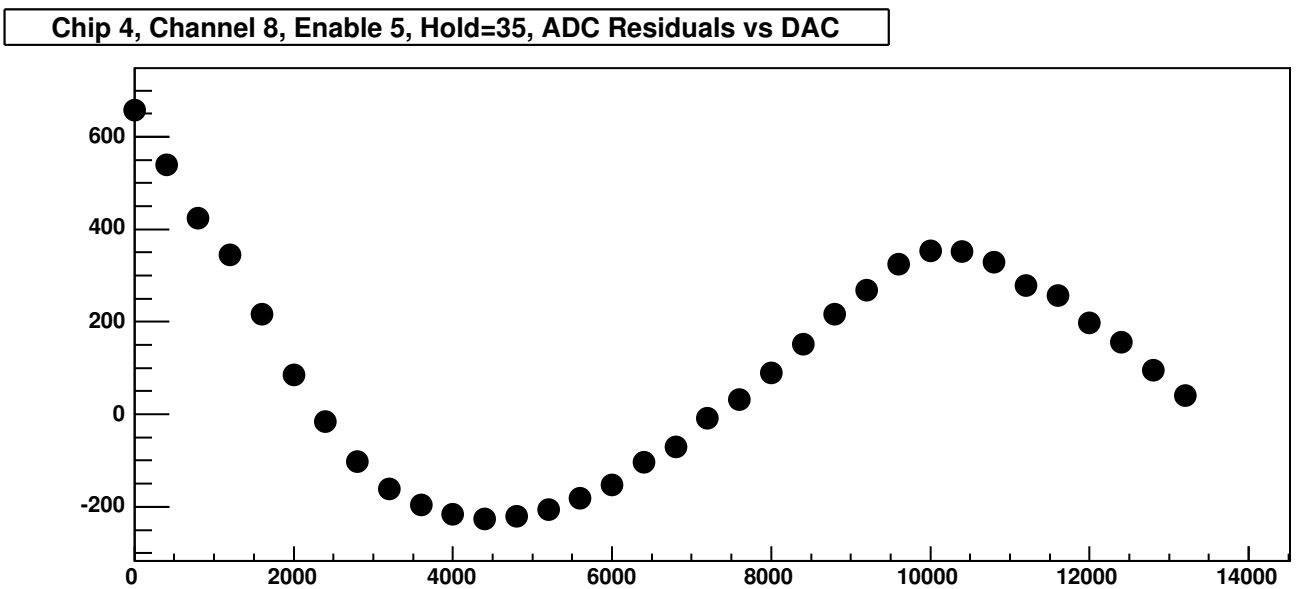

Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.36 / 23
p0 164.8 ± 0.739
p1 0.0163 ± 0.0001176
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

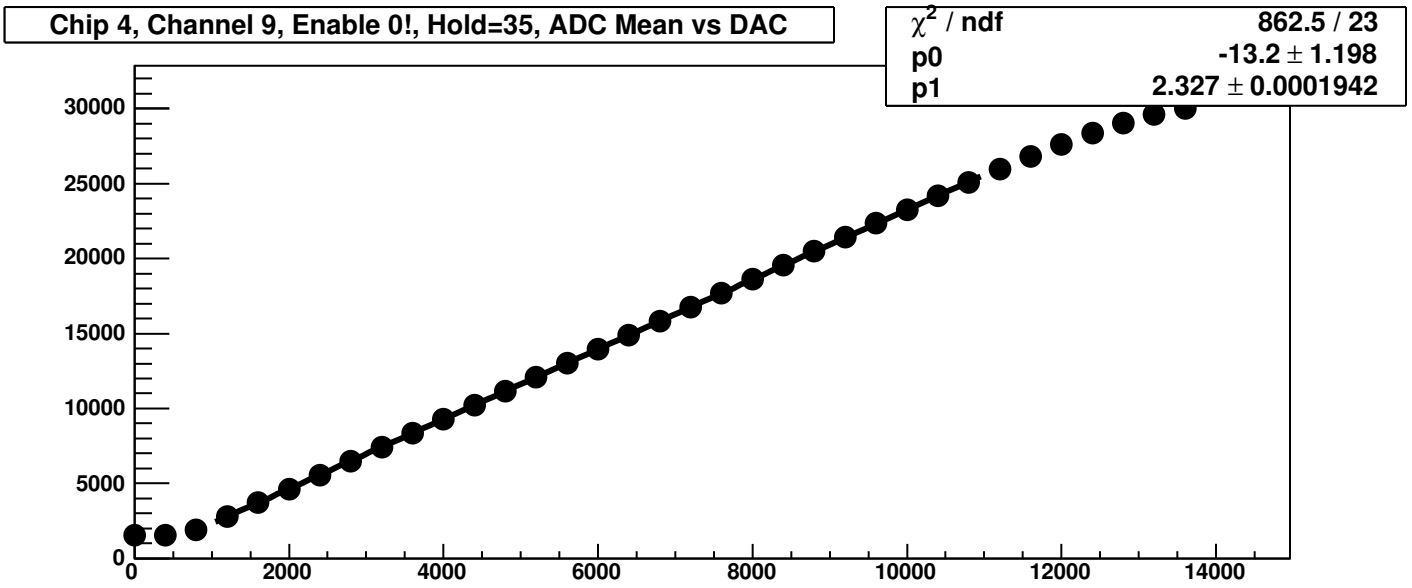

Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

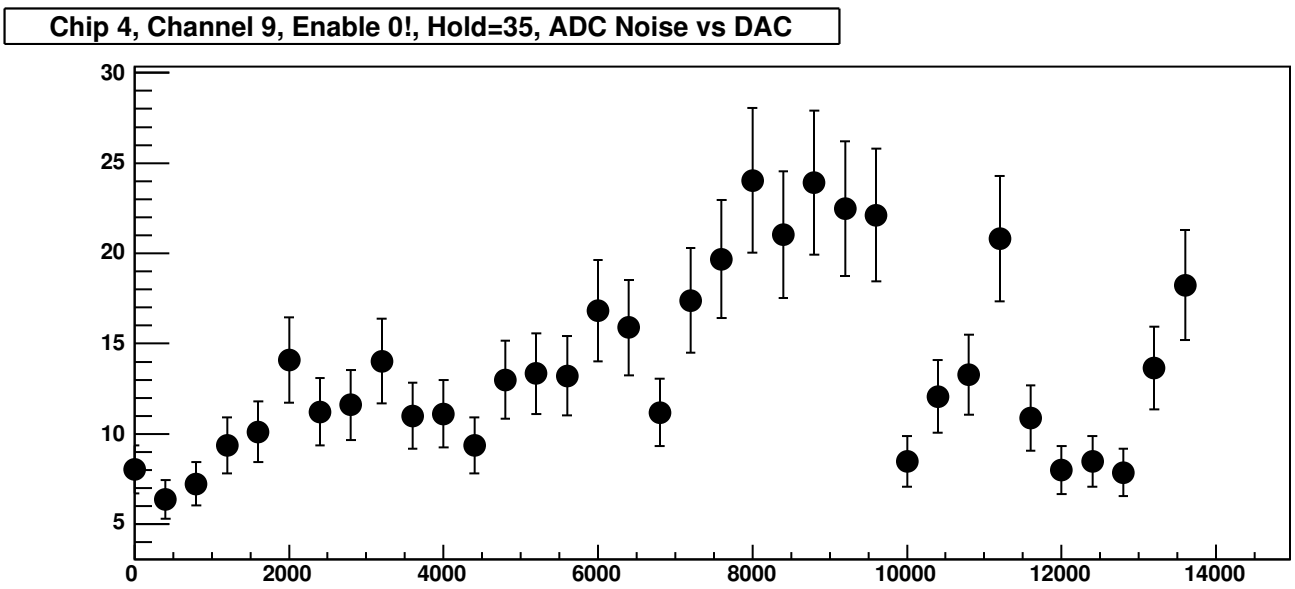

Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

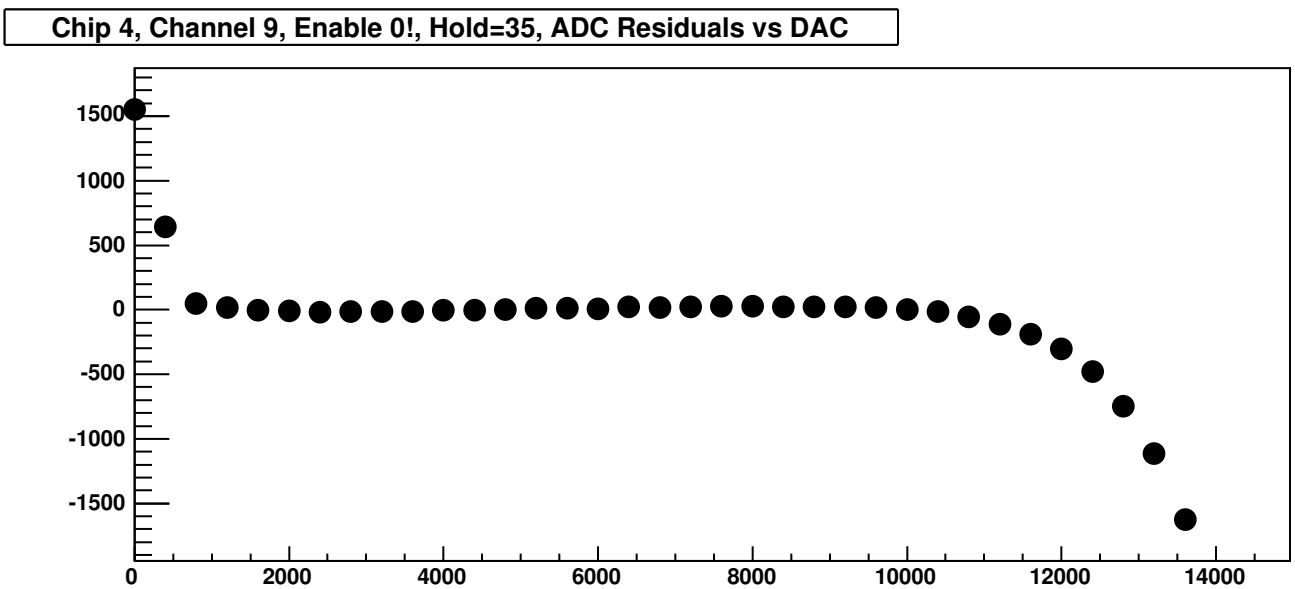

Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.318e+05 / 23
p0 -408 ± 1.072
p1 0.3171 ± 0.0002224
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

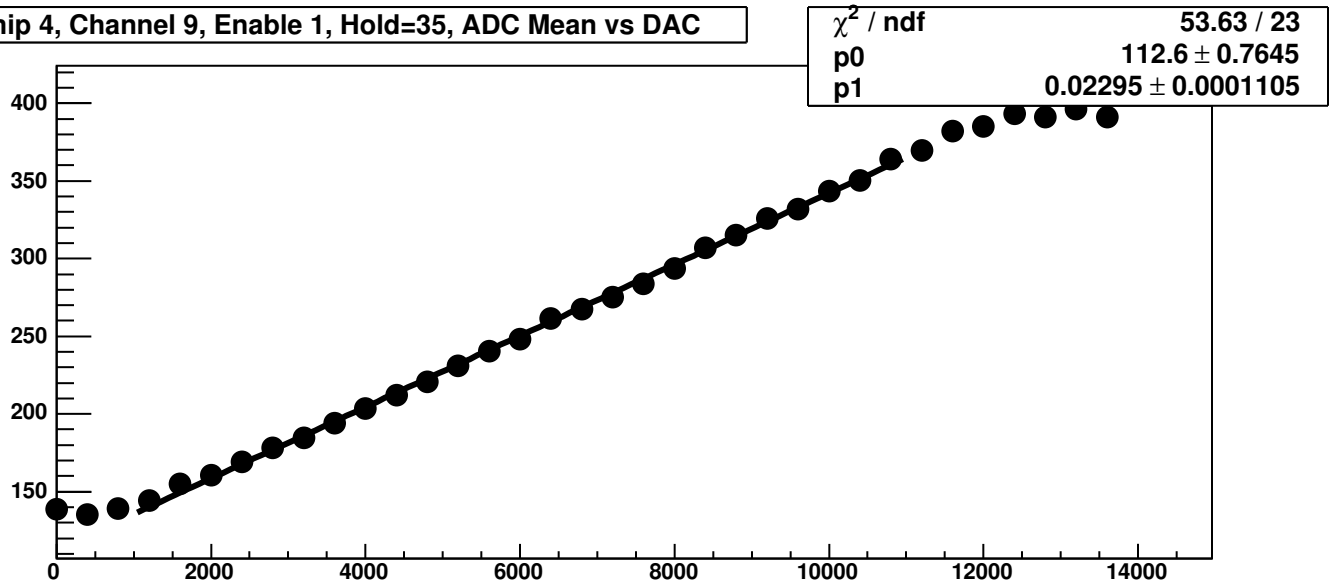

Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 862.5 / 23
p0 -13.2 ± 1.198
p1 2.327 ± 0.0001942
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

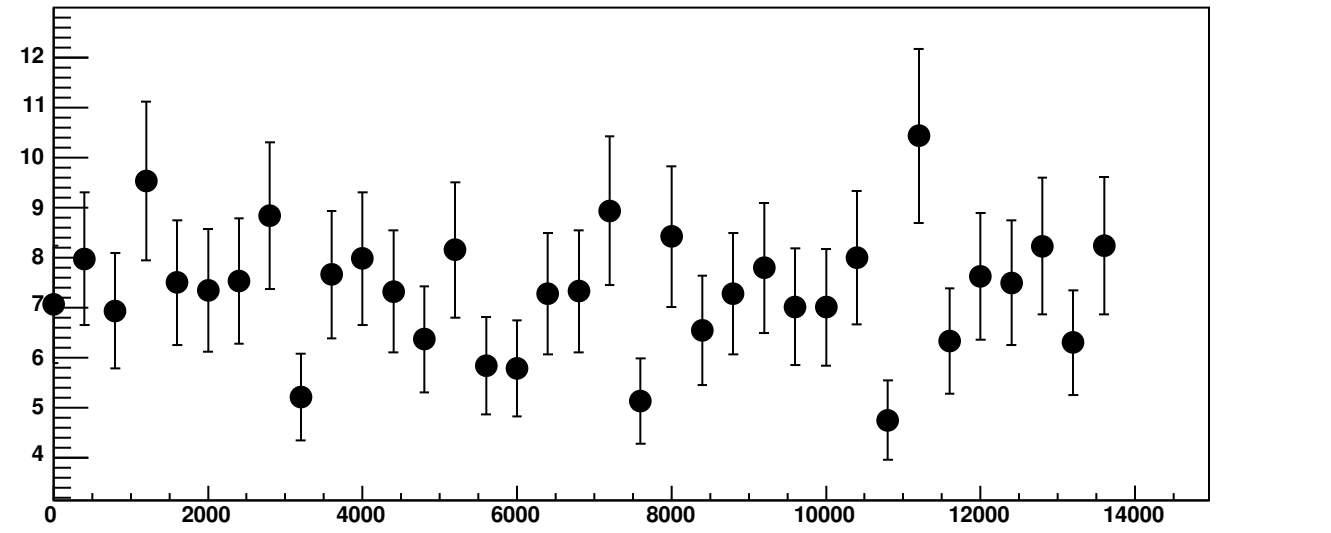

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

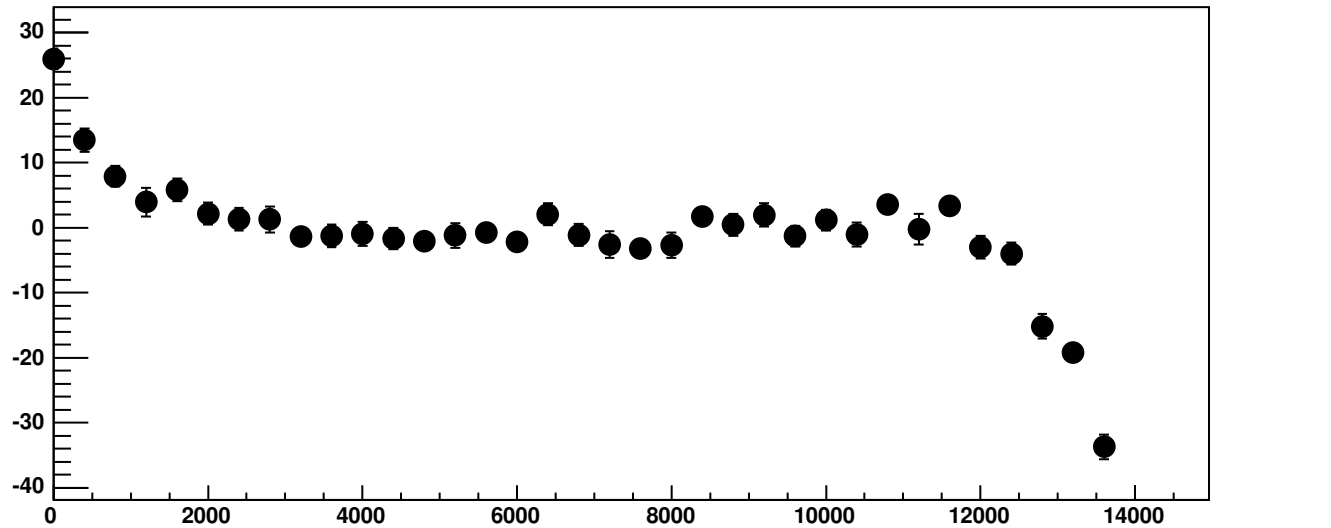
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

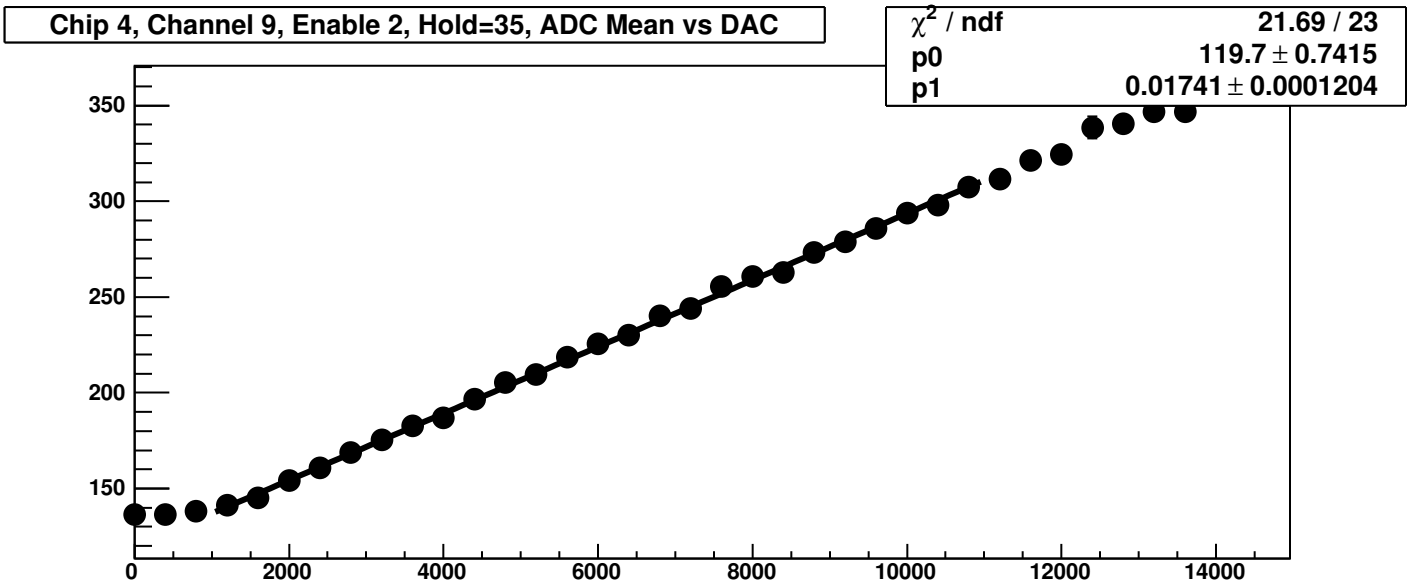


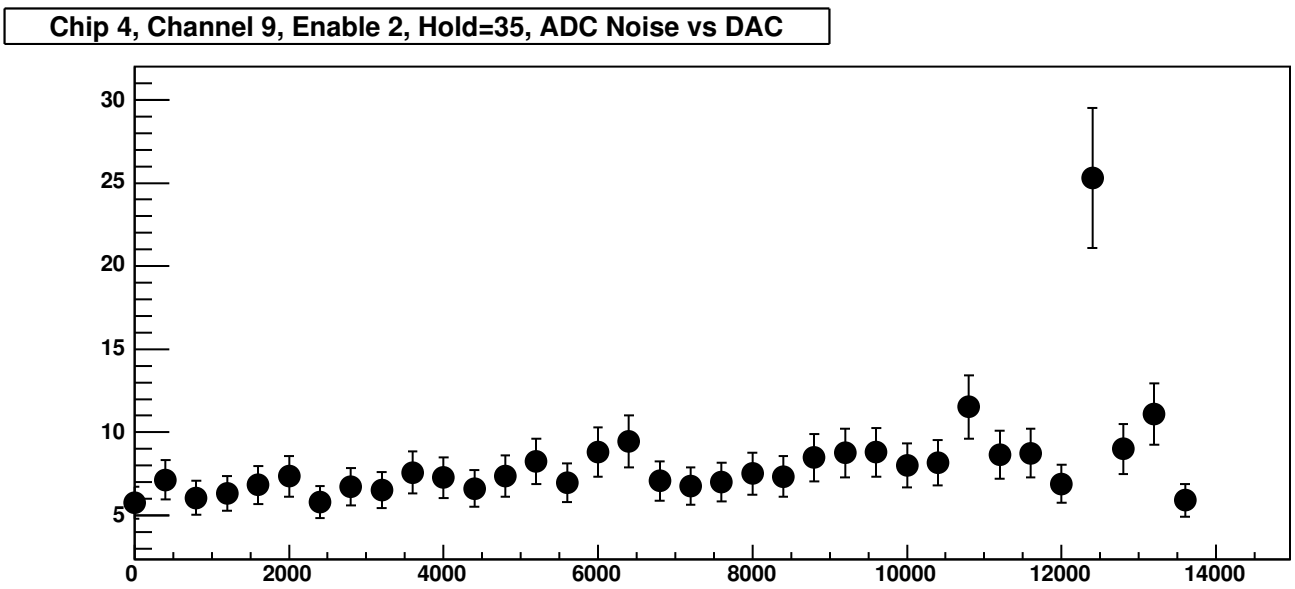
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

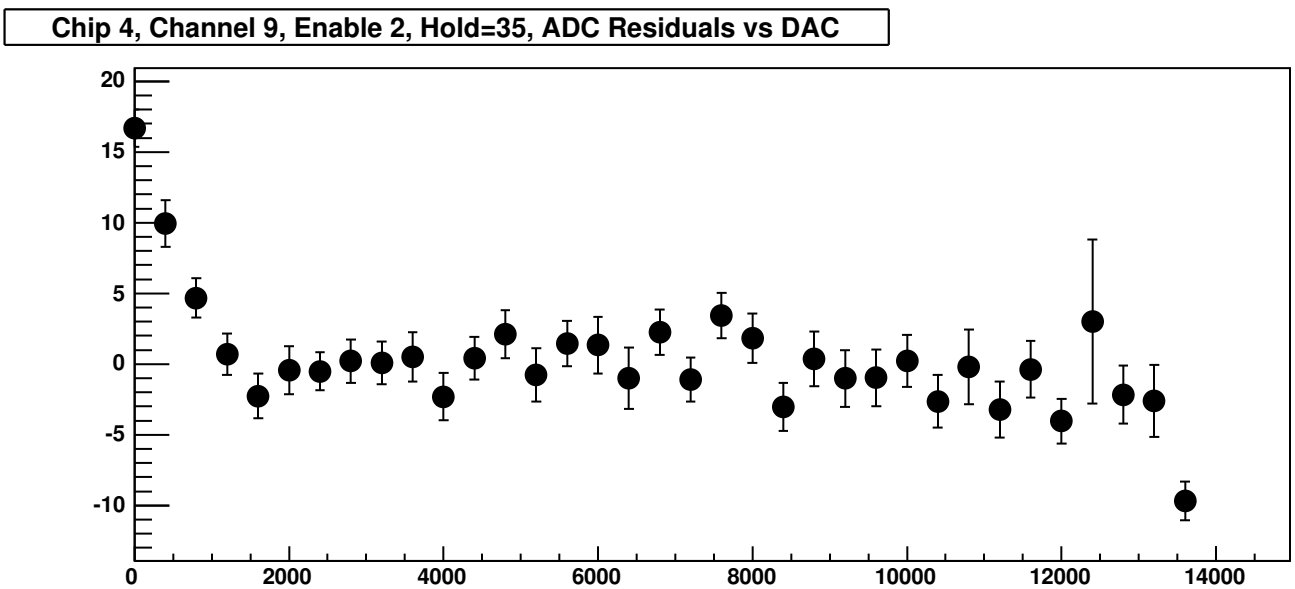


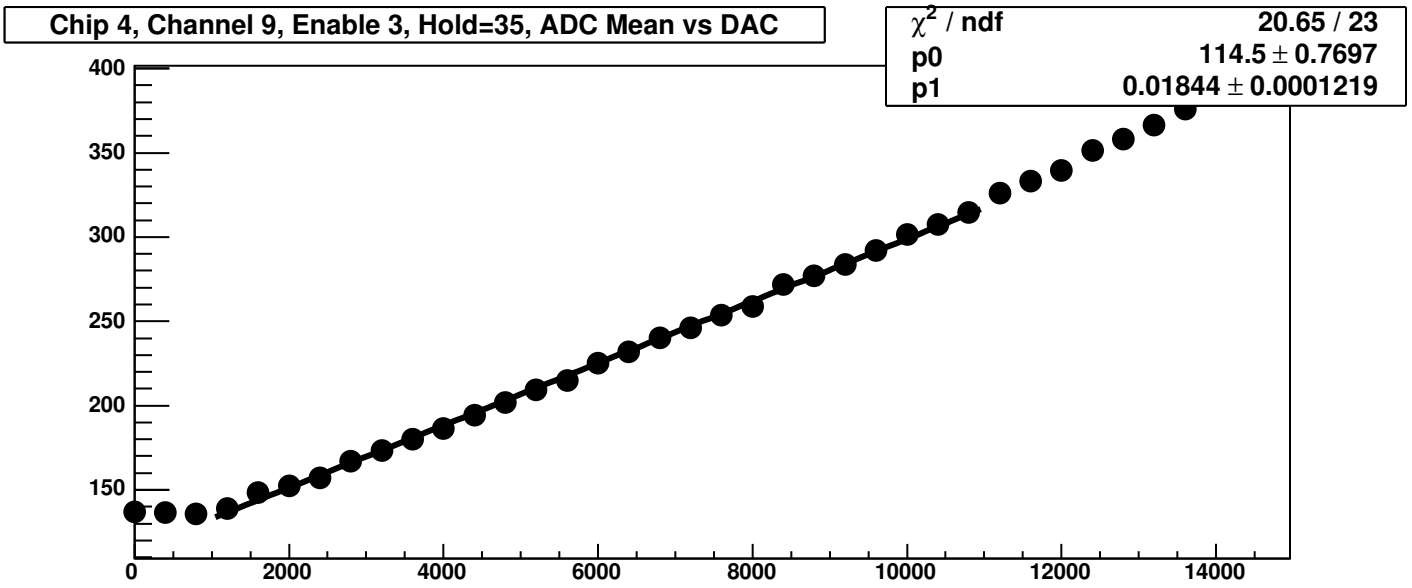
Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

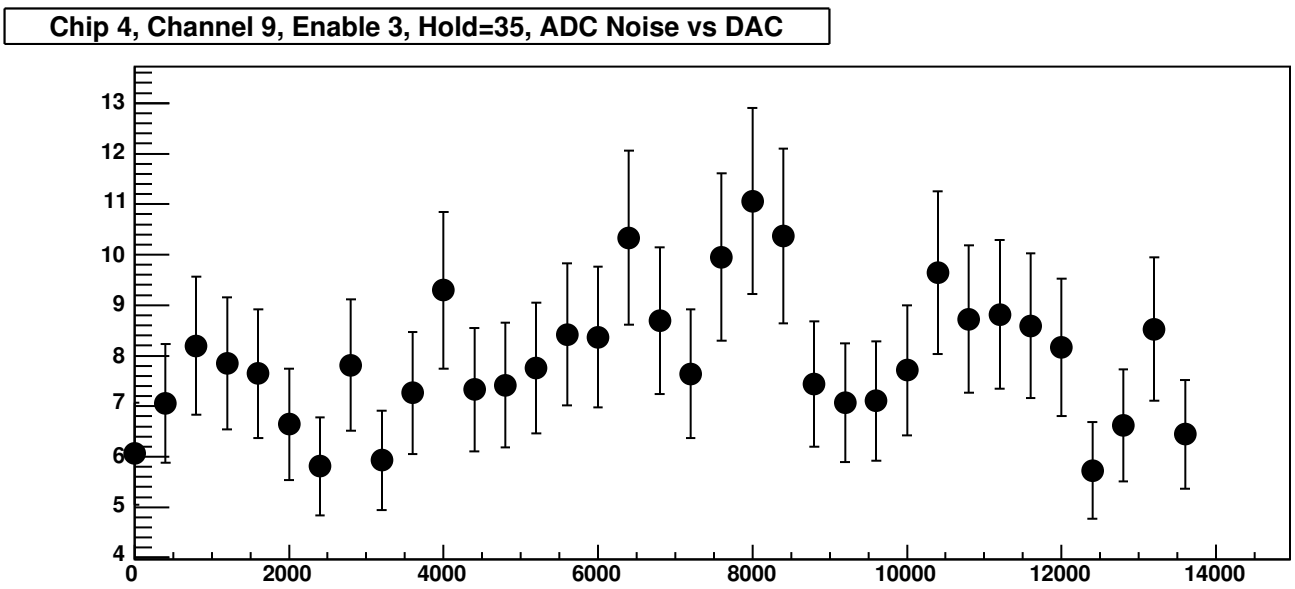


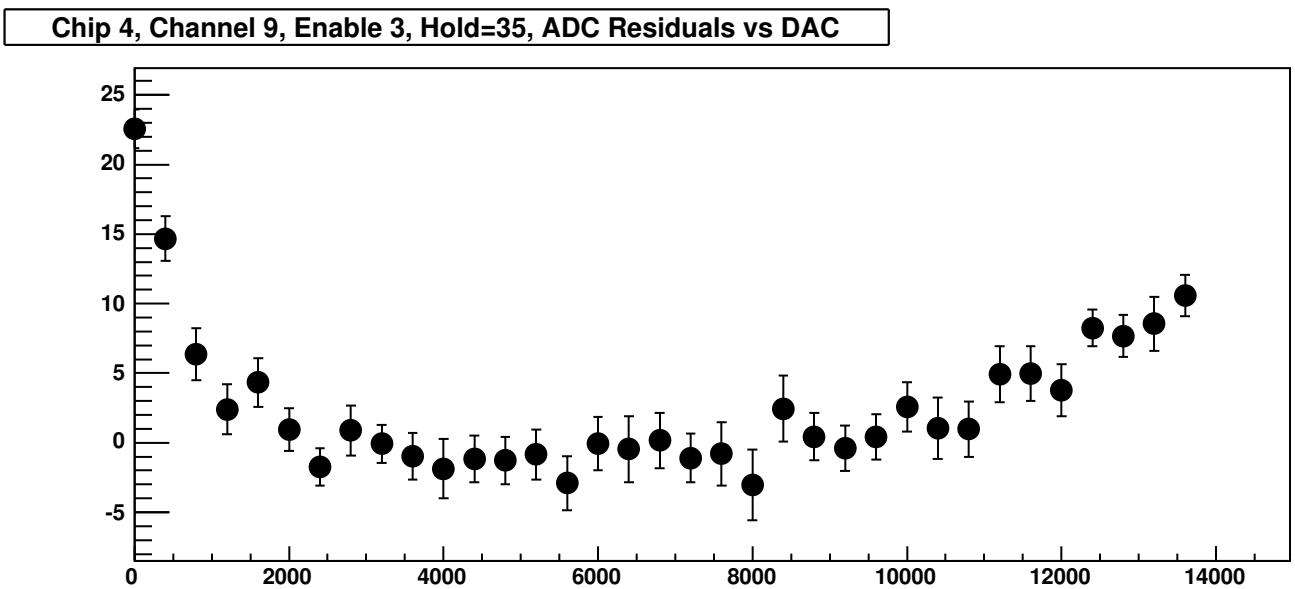

Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.69 / 23
p0 119.7 ± 0.7415
p1 0.01741 ± 0.0001204
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

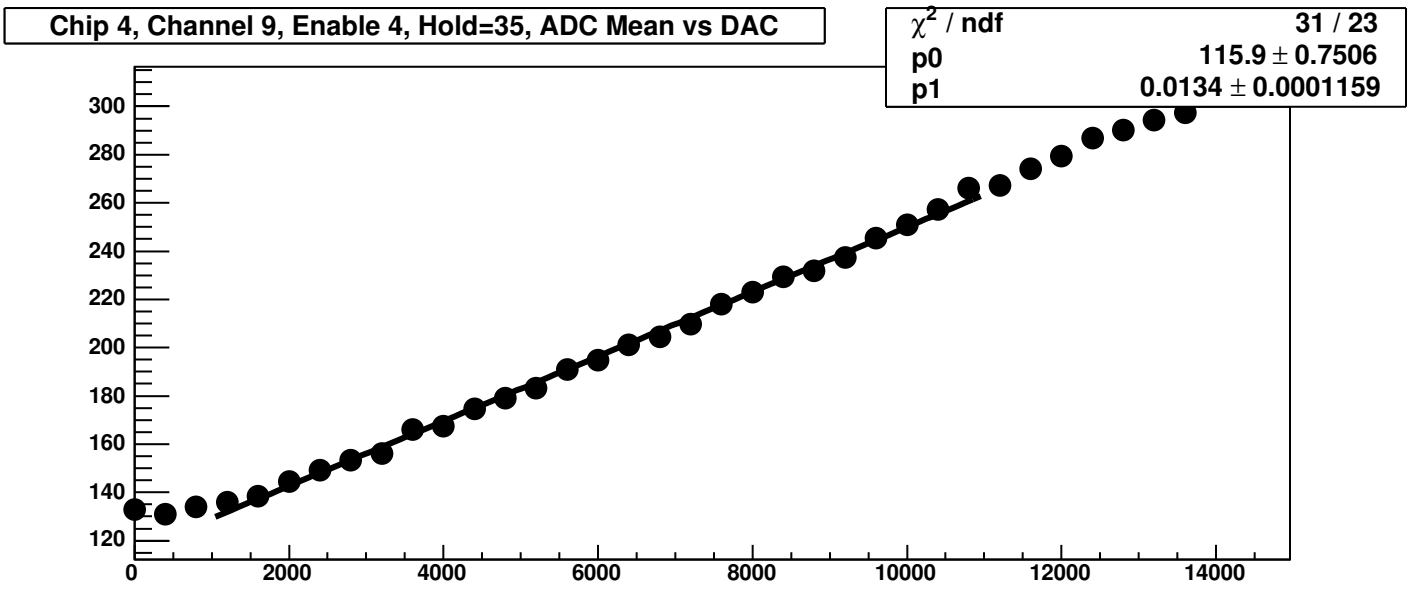

Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

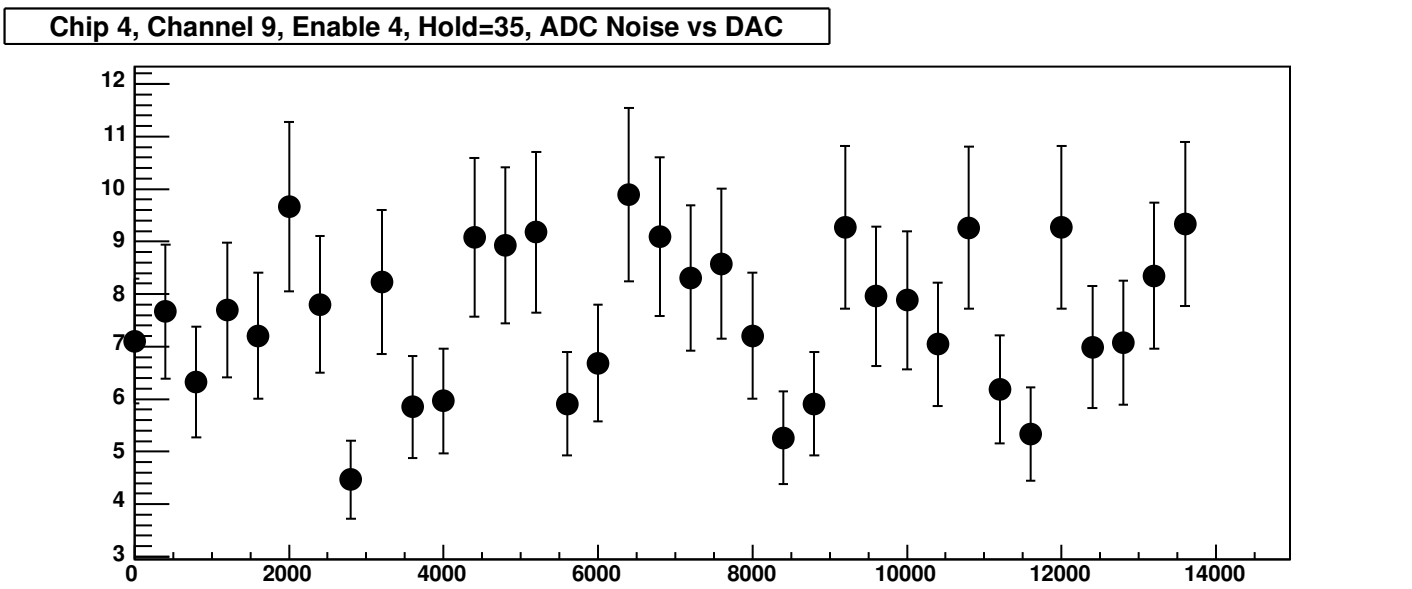

Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

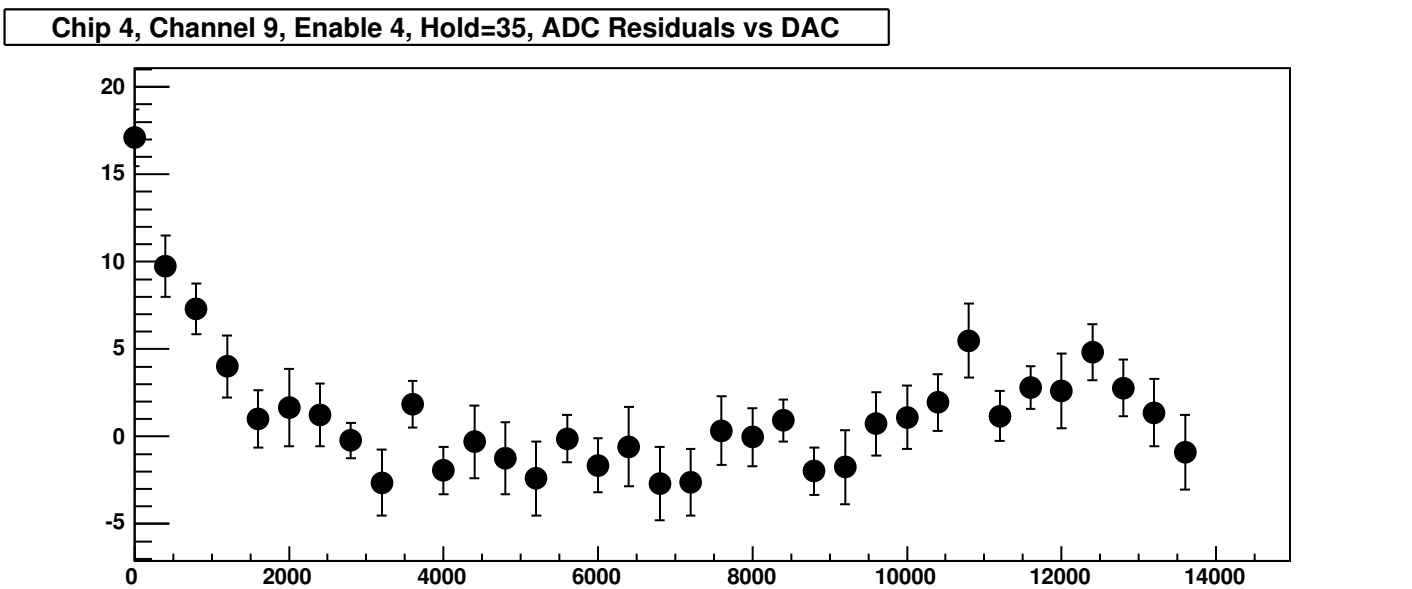

Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.65 / 23
p0 114.5 ± 0.7697
p1 0.01844 ± 0.0001219
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

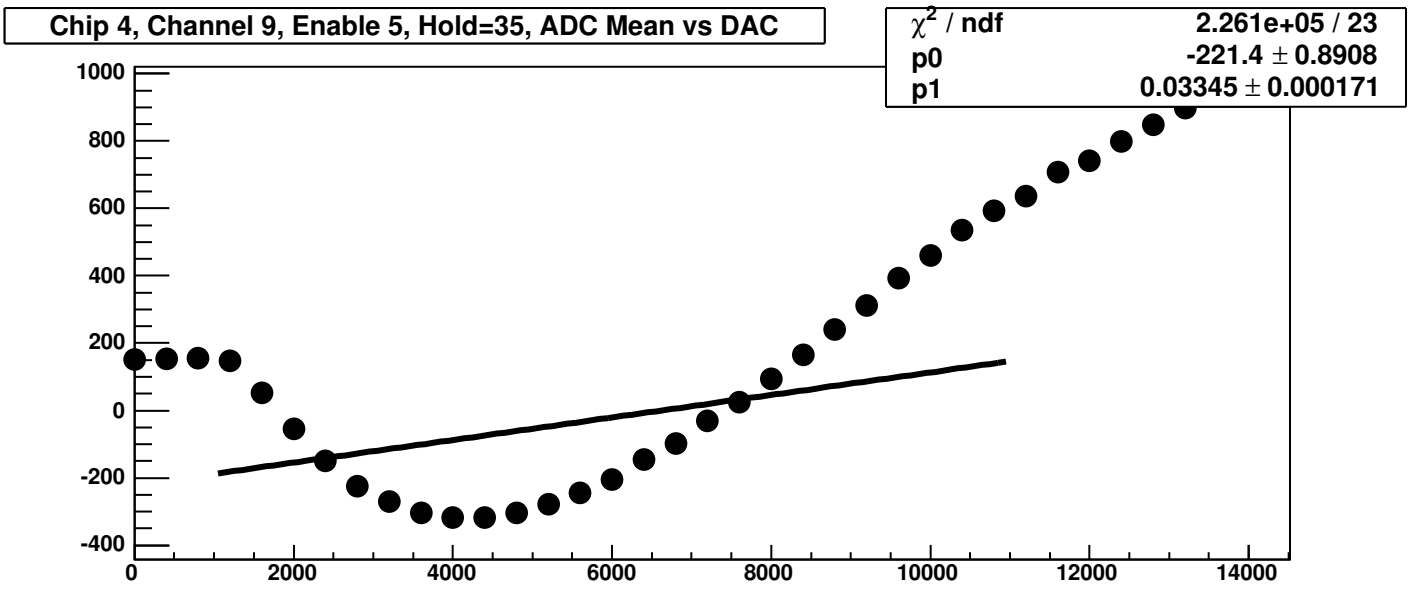

Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

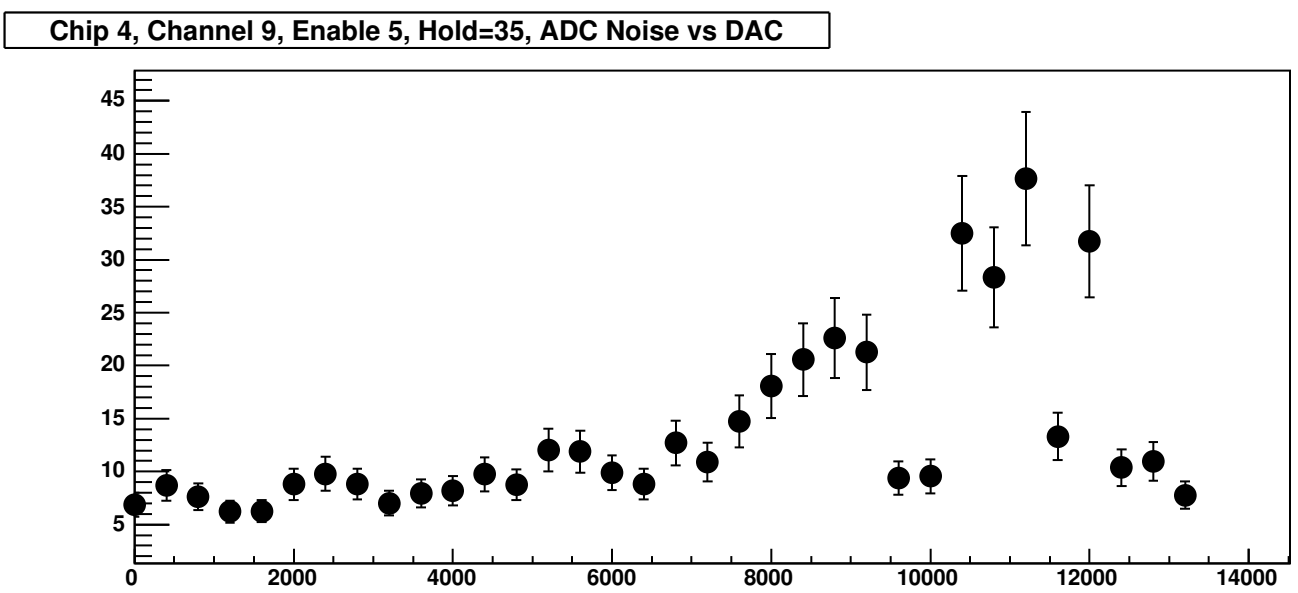

Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

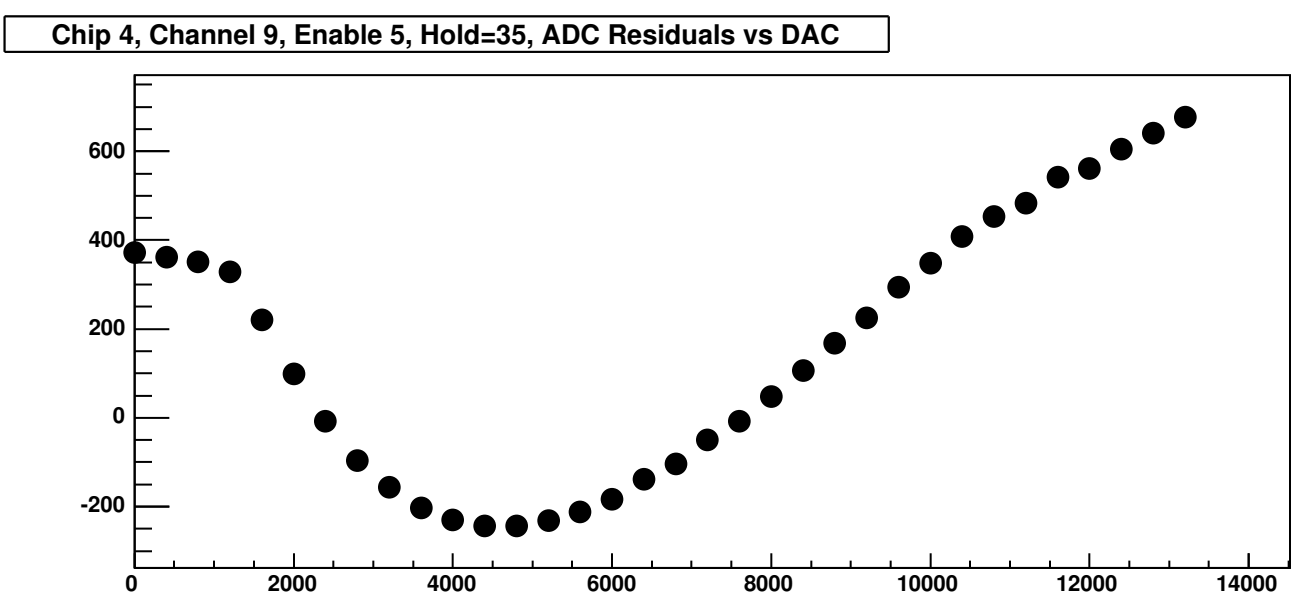

Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31 / 23
p0 115.9 ± 0.7506
p1 0.0134 ± 0.0001159
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

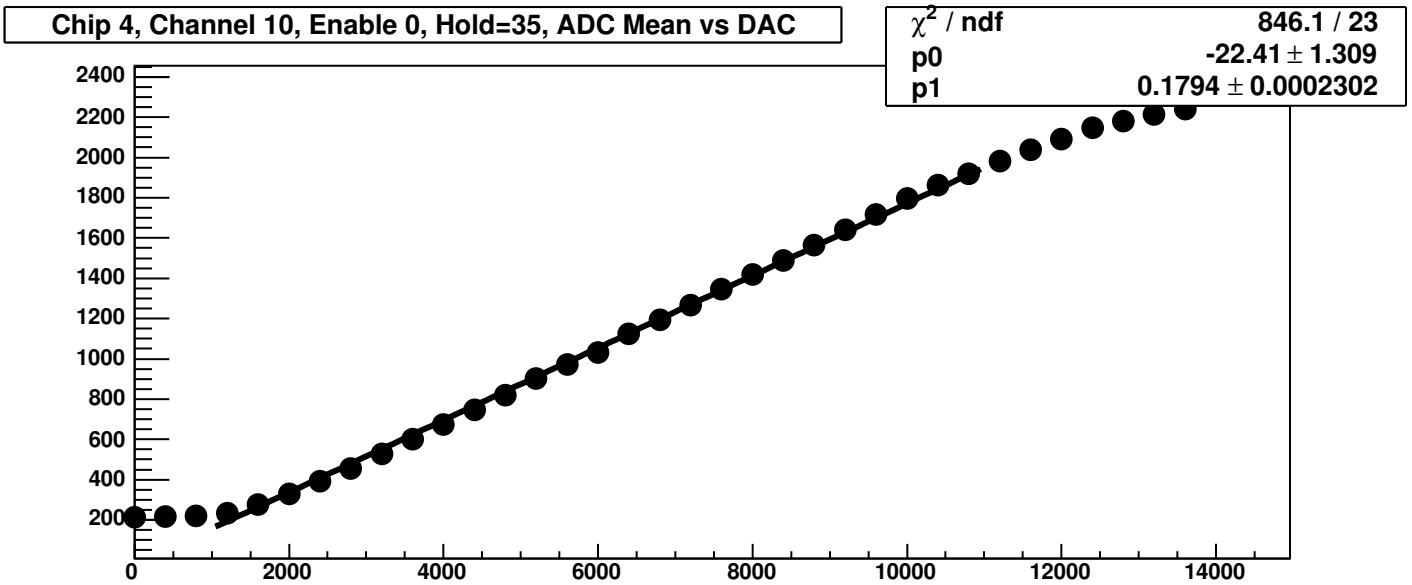

Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

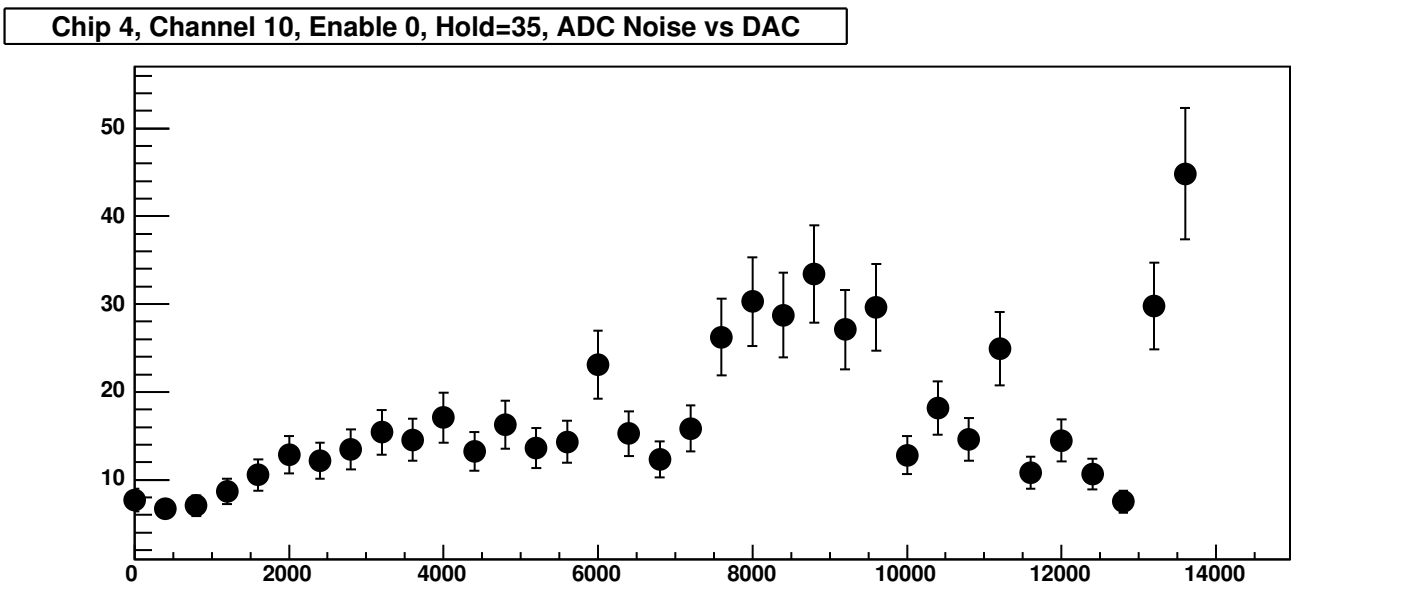

Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

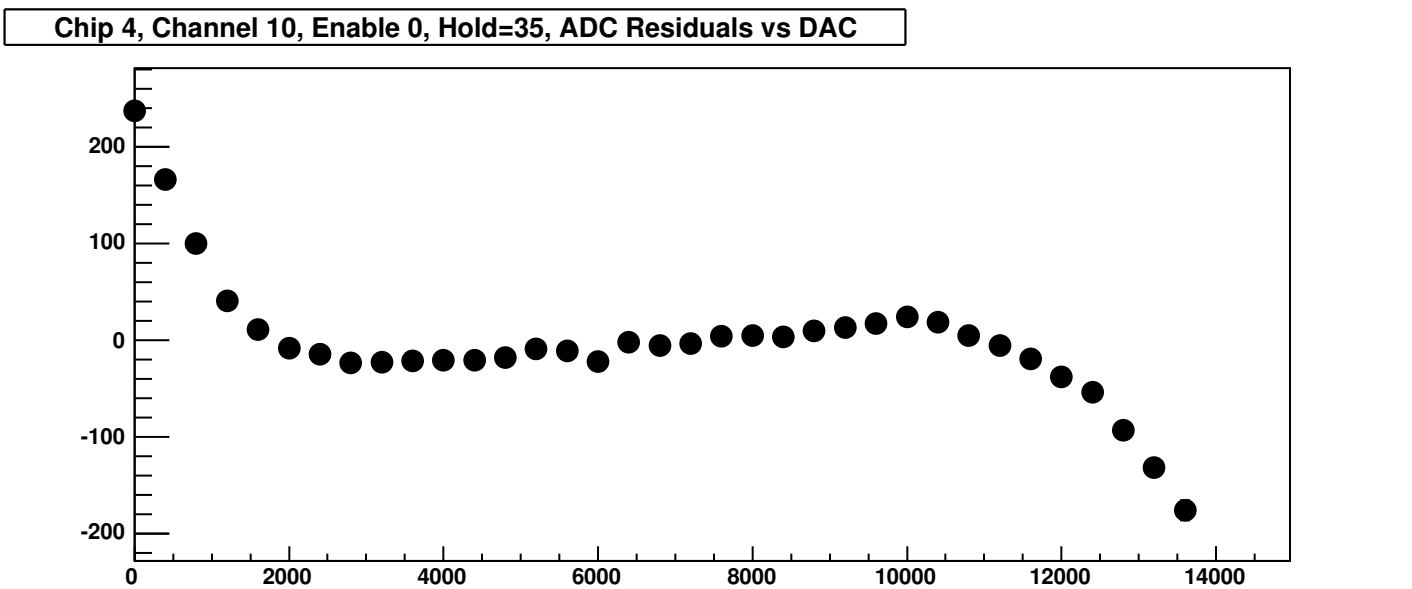

Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.261e+05 / 23
p0 -221.4 ± 0.8908
p1 0.03345 ± 0.000171
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

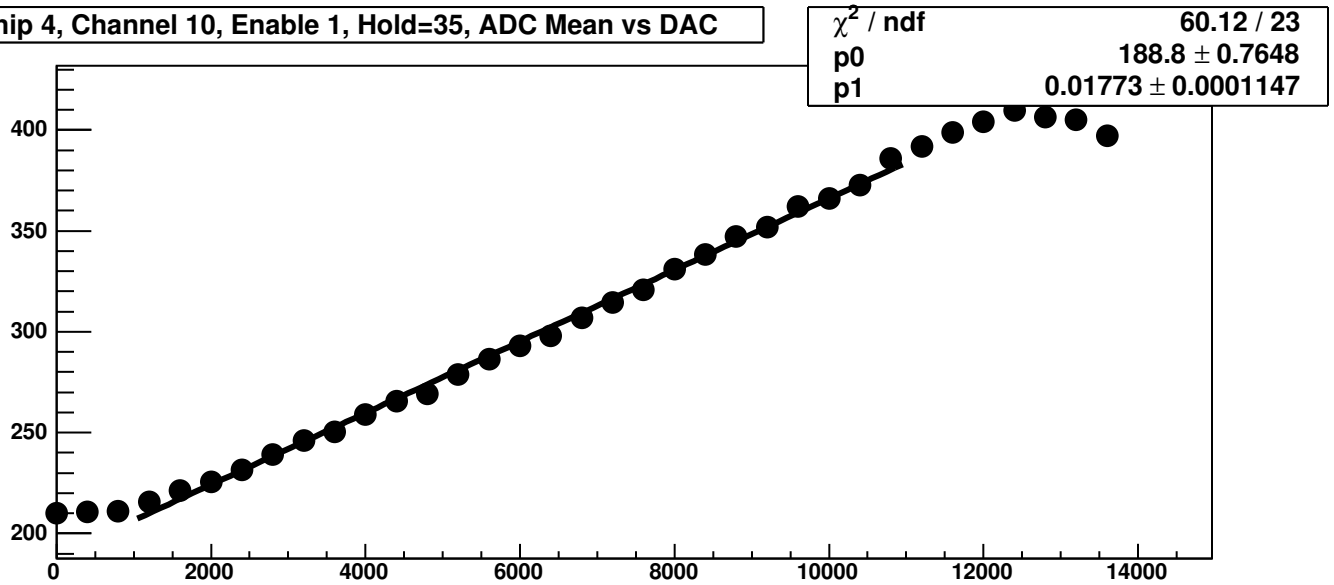

Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 846.1 / 23
p0 -22.41 ± 1.309
p1 0.1794 ± 0.0002302
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

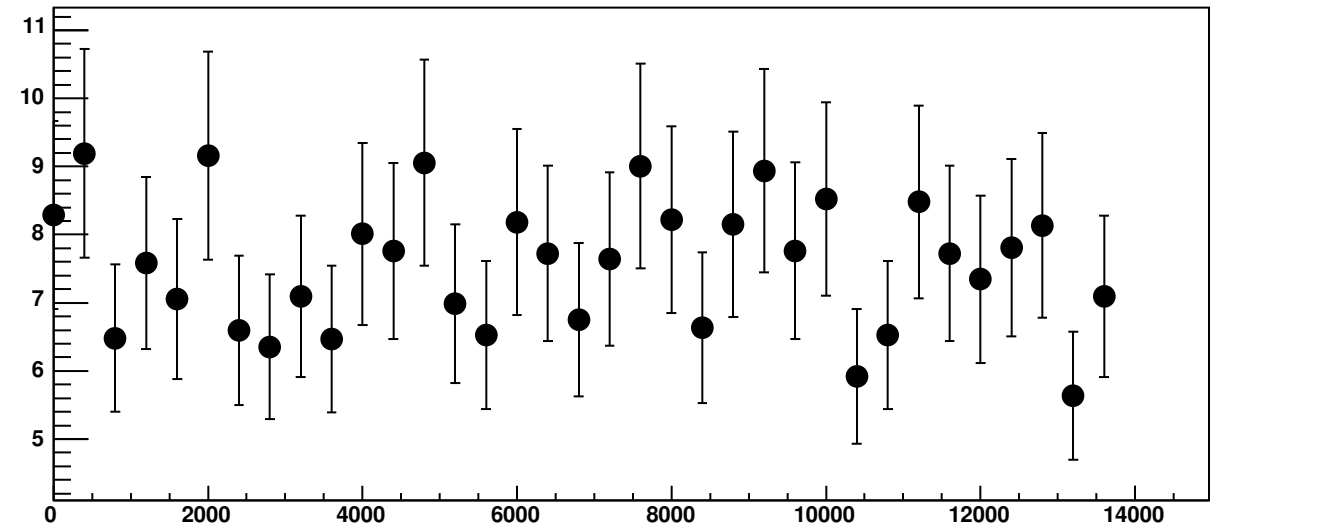

Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

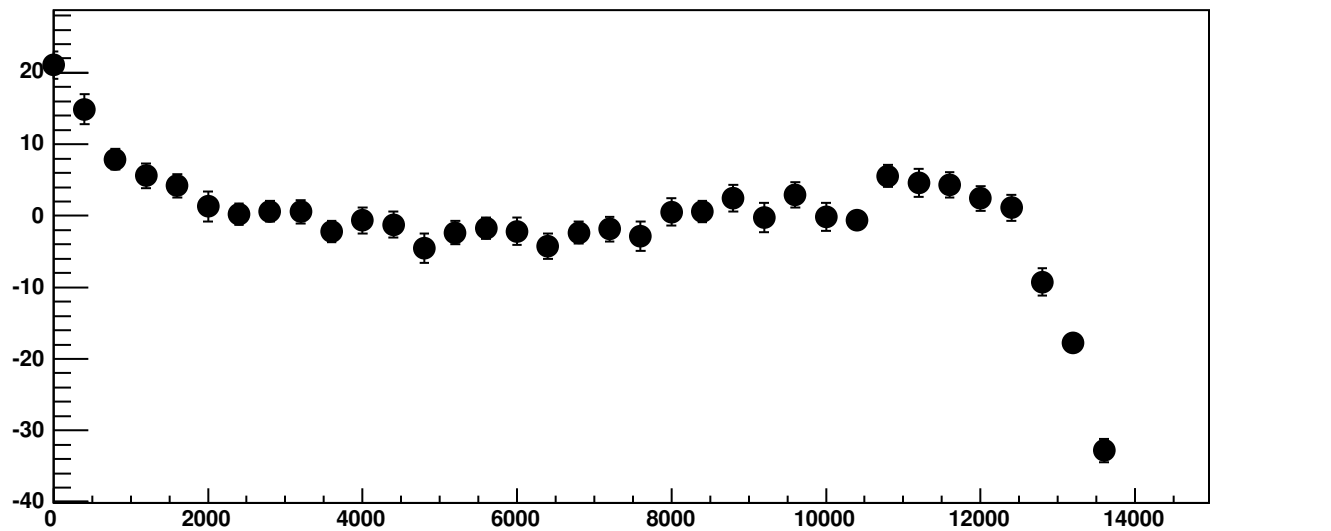
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



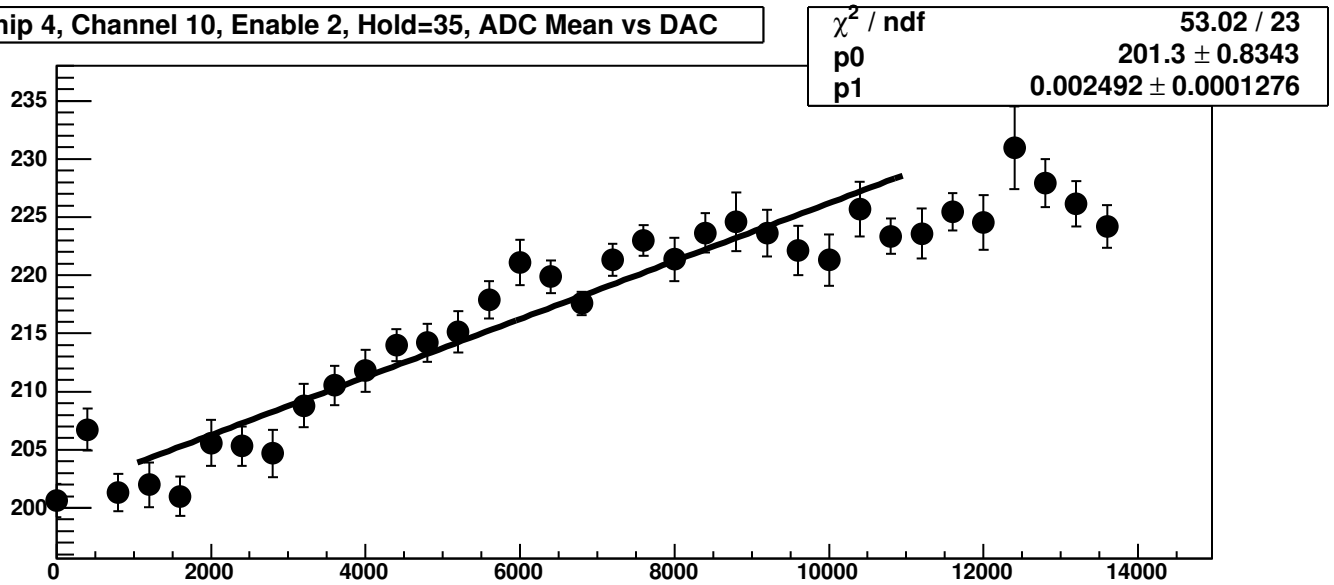
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC



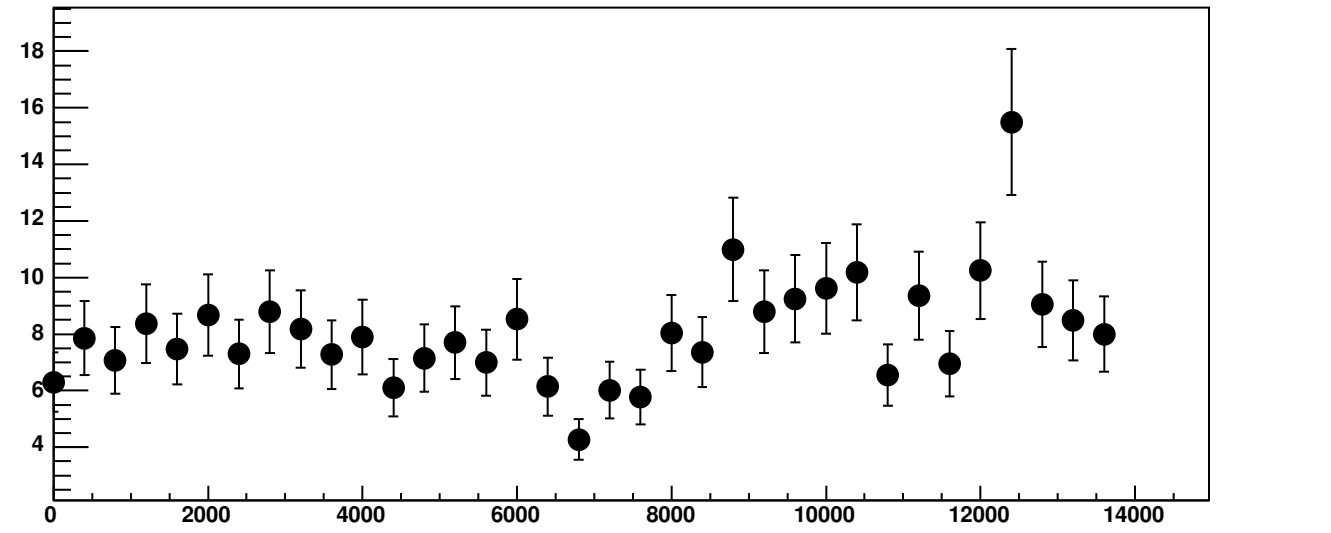
Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC



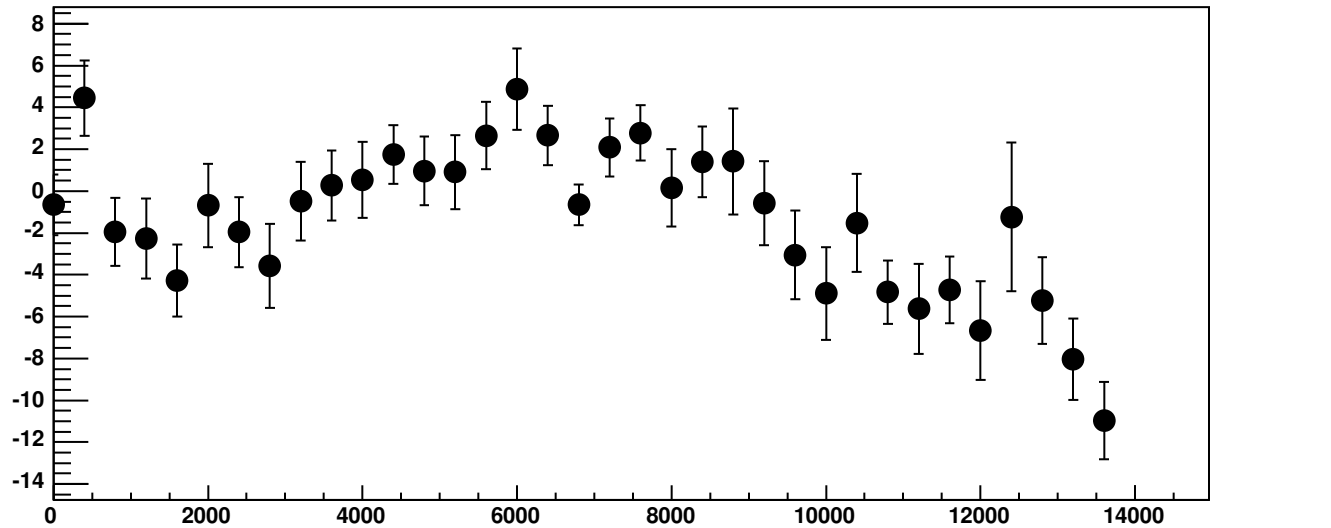
Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

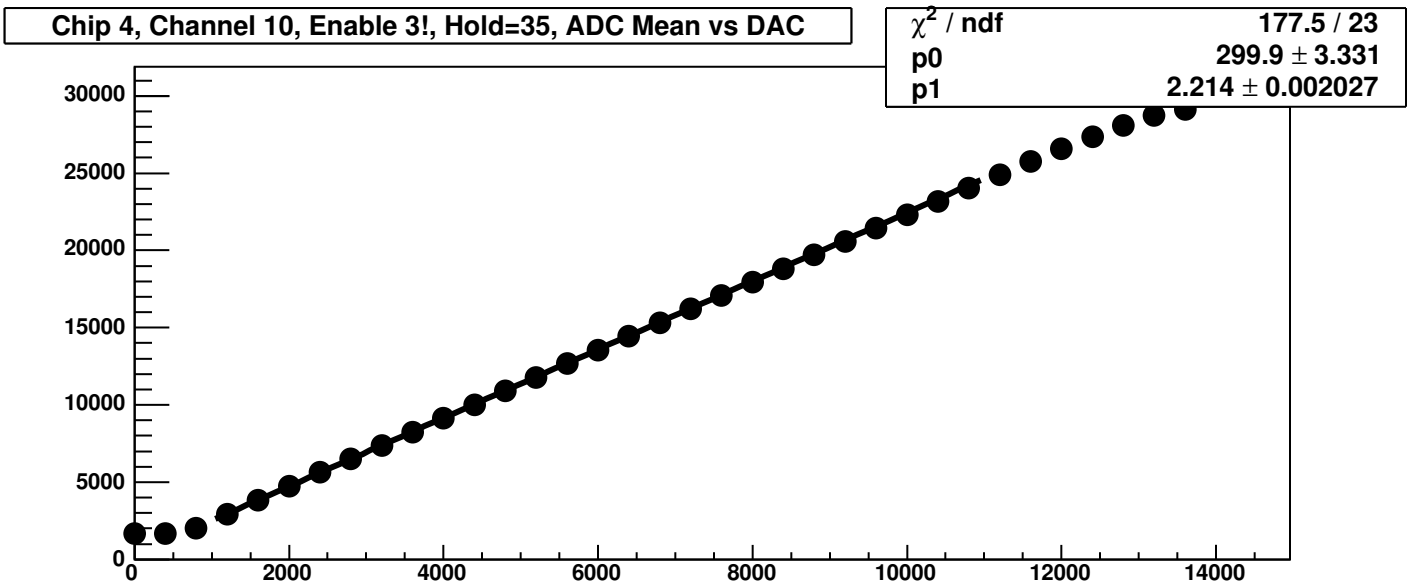


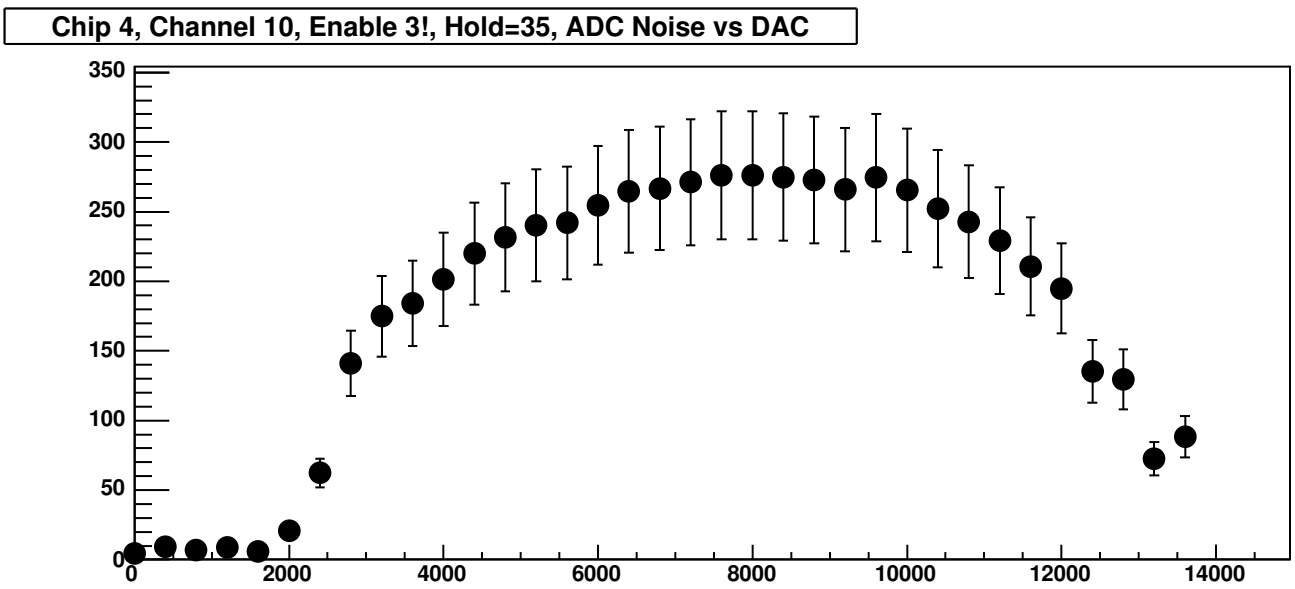
Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

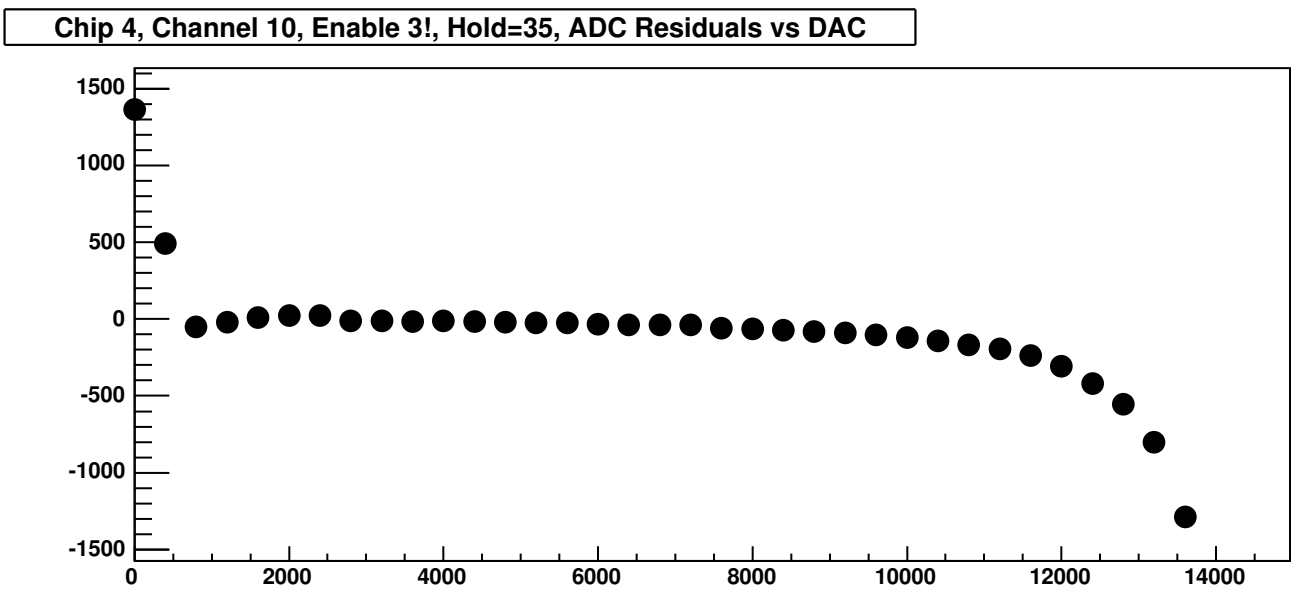


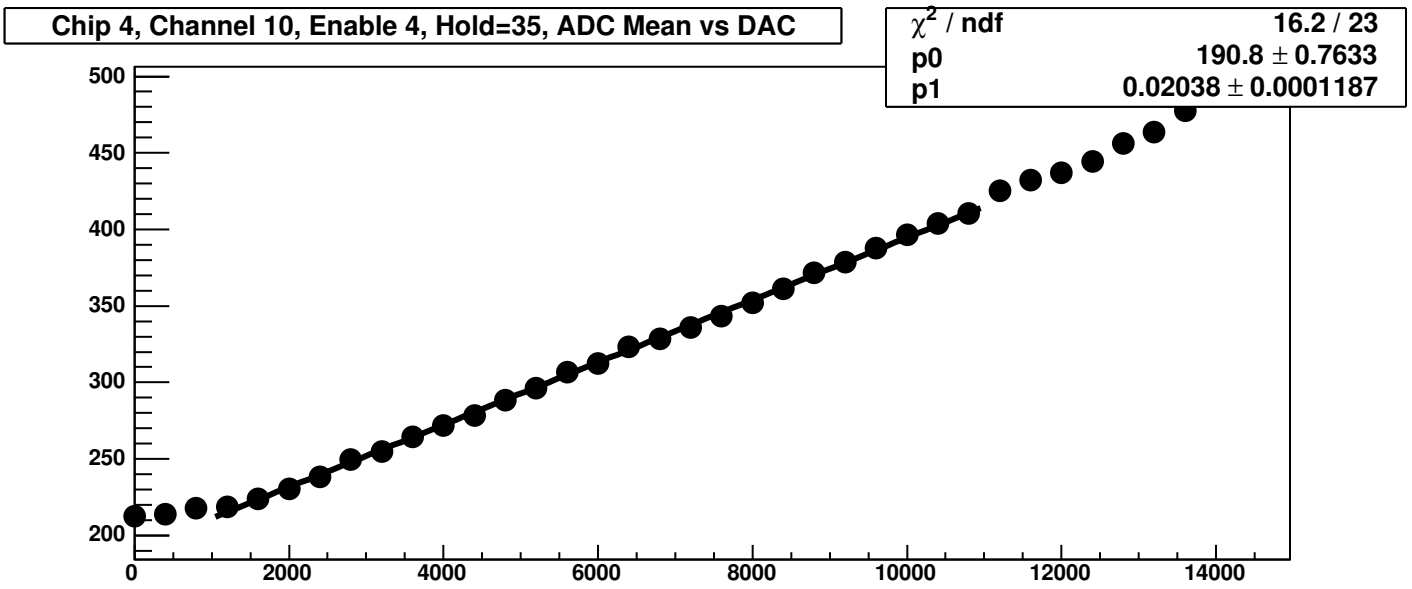
Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

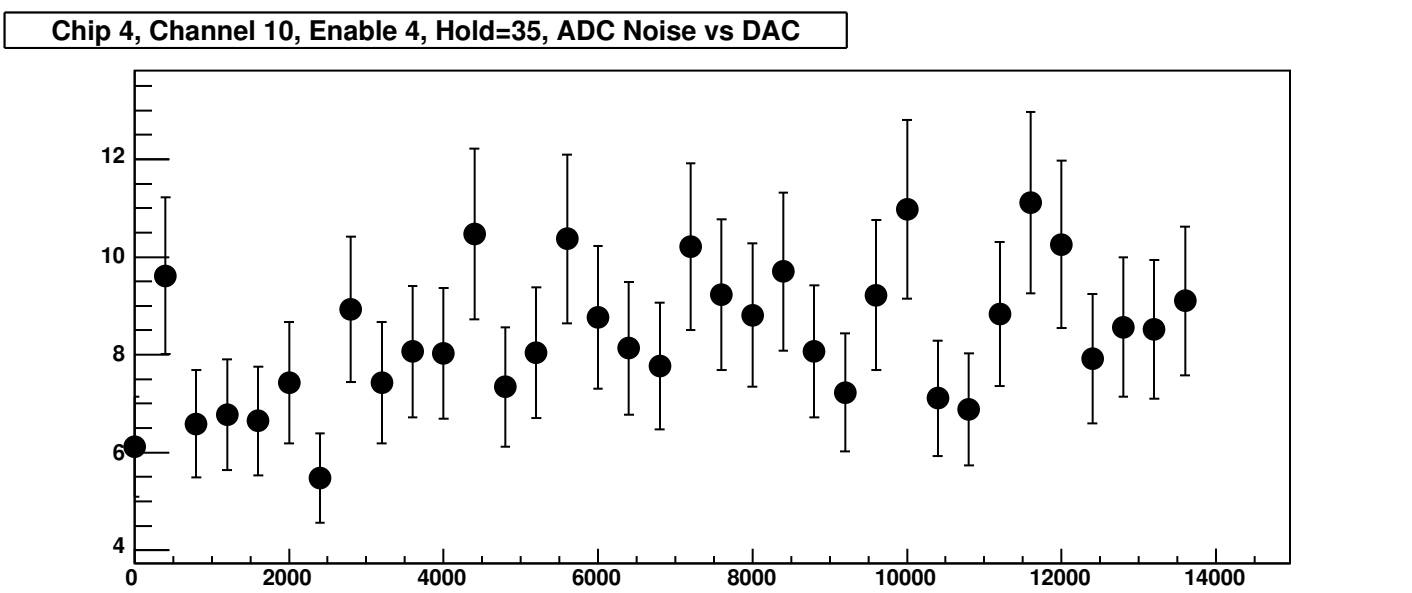


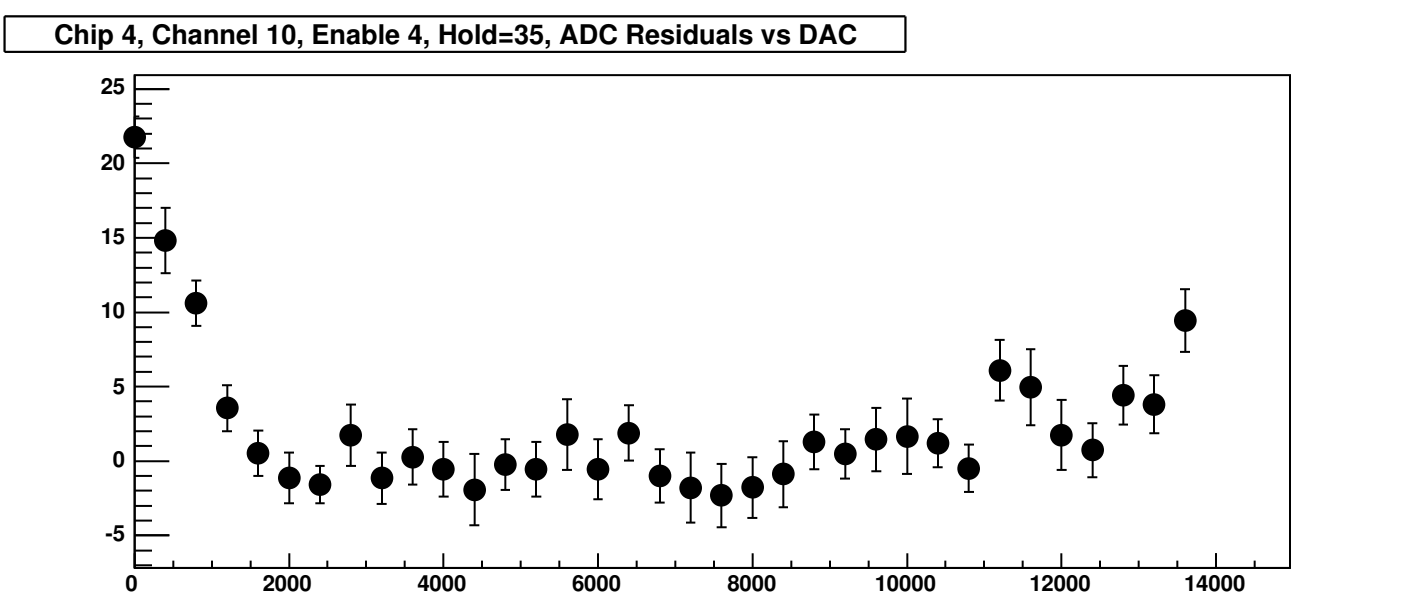

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 177.5 / 23
p0 299.9 ± 3.331
p1 2.214 ± 0.002027
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

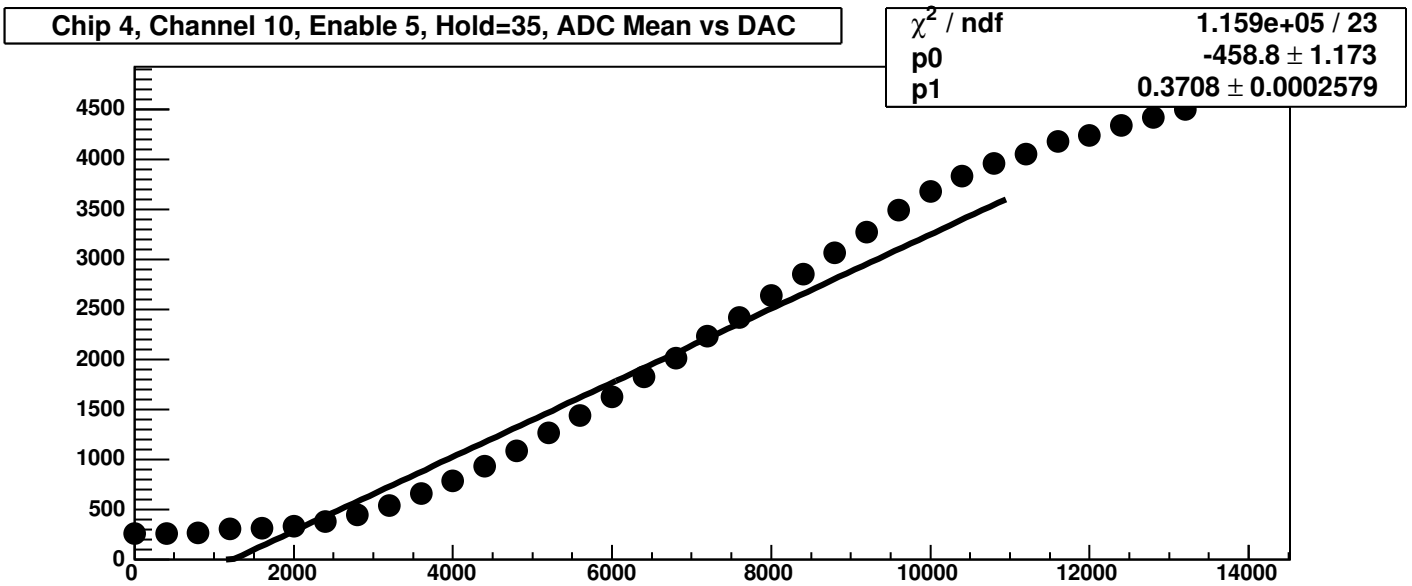

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

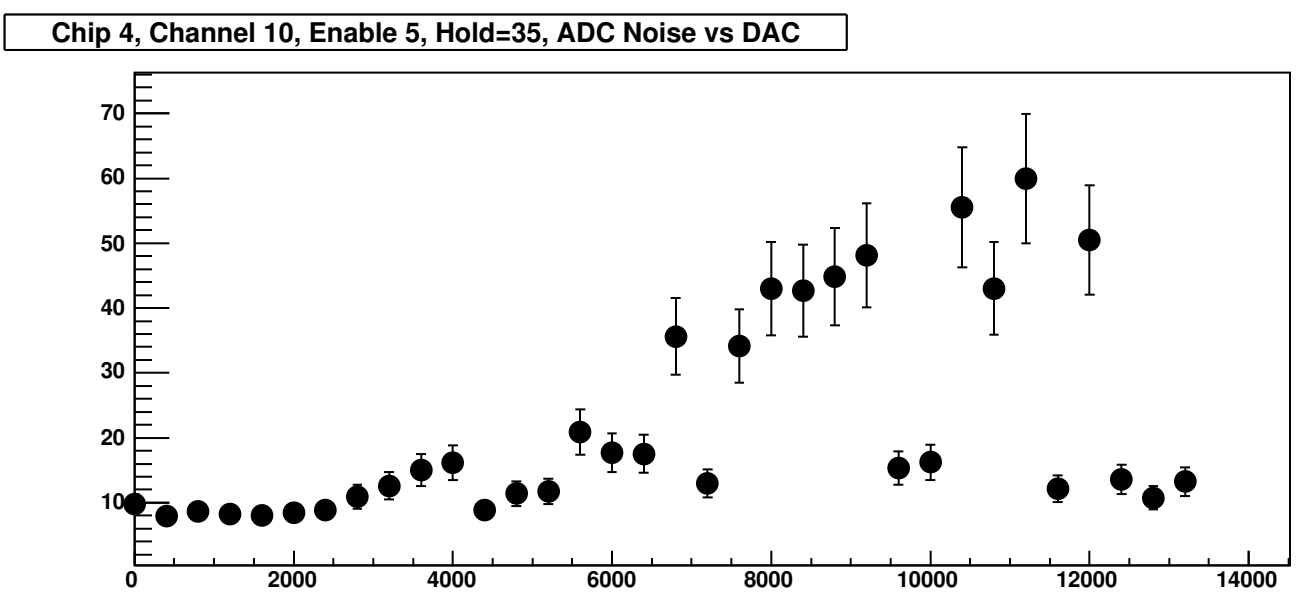

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

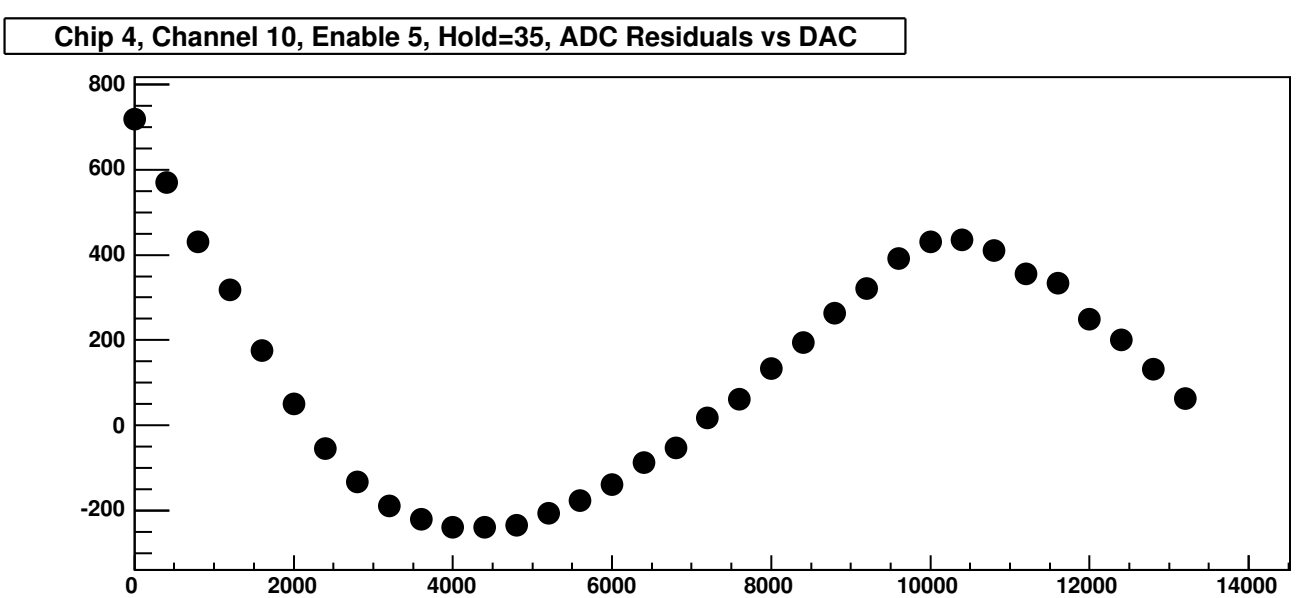

Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.2 / 23
p0 190.8 ± 0.7633
p1 0.02038 ± 0.0001187
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

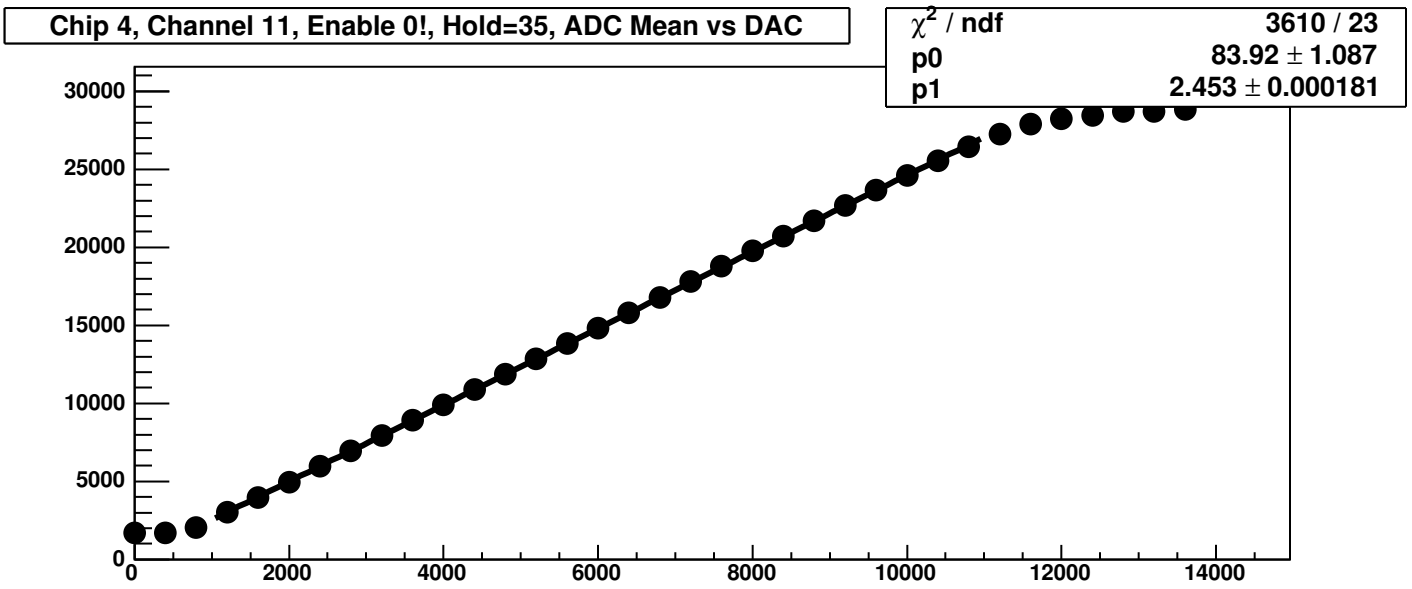

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

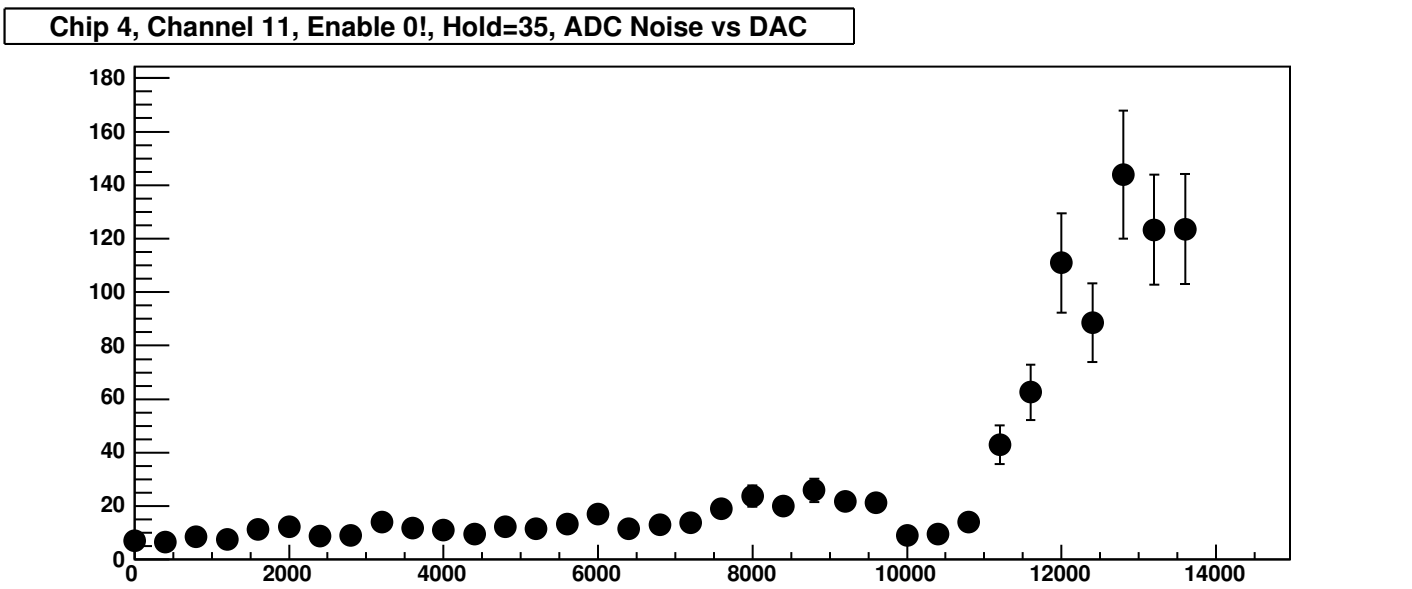

Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

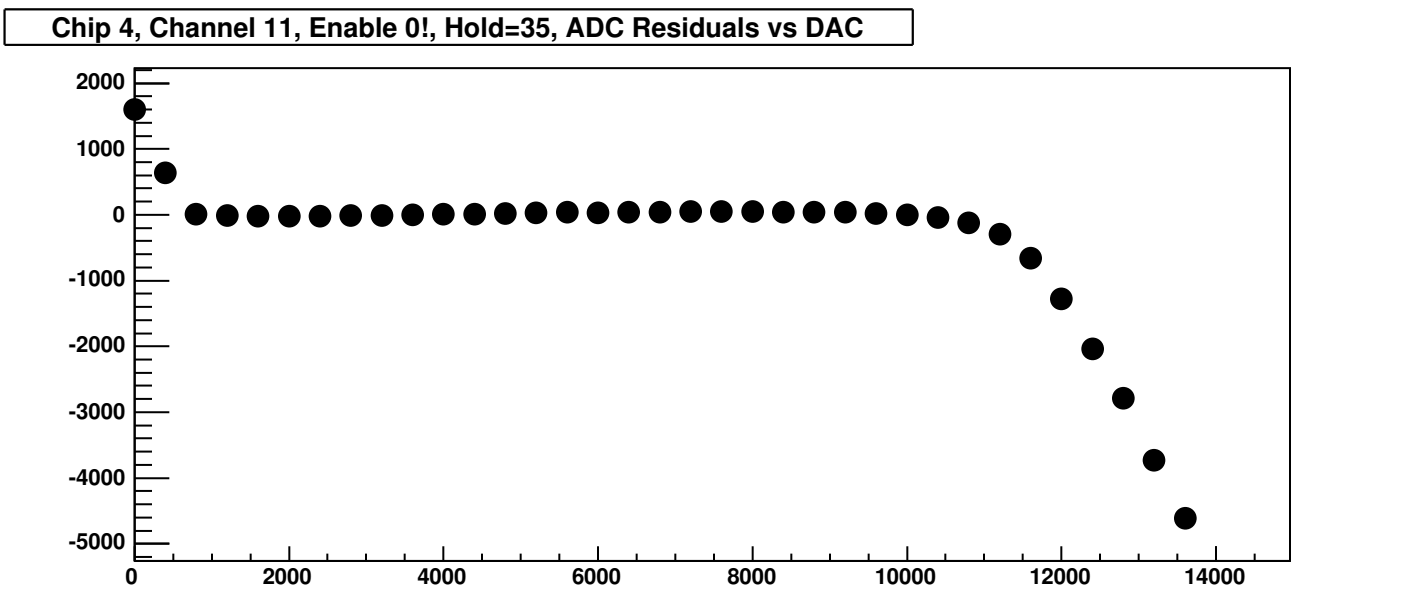

Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.159e+05 / 23
p0 -458.8 ± 1.173
p1 0.3708 ± 0.0002579
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

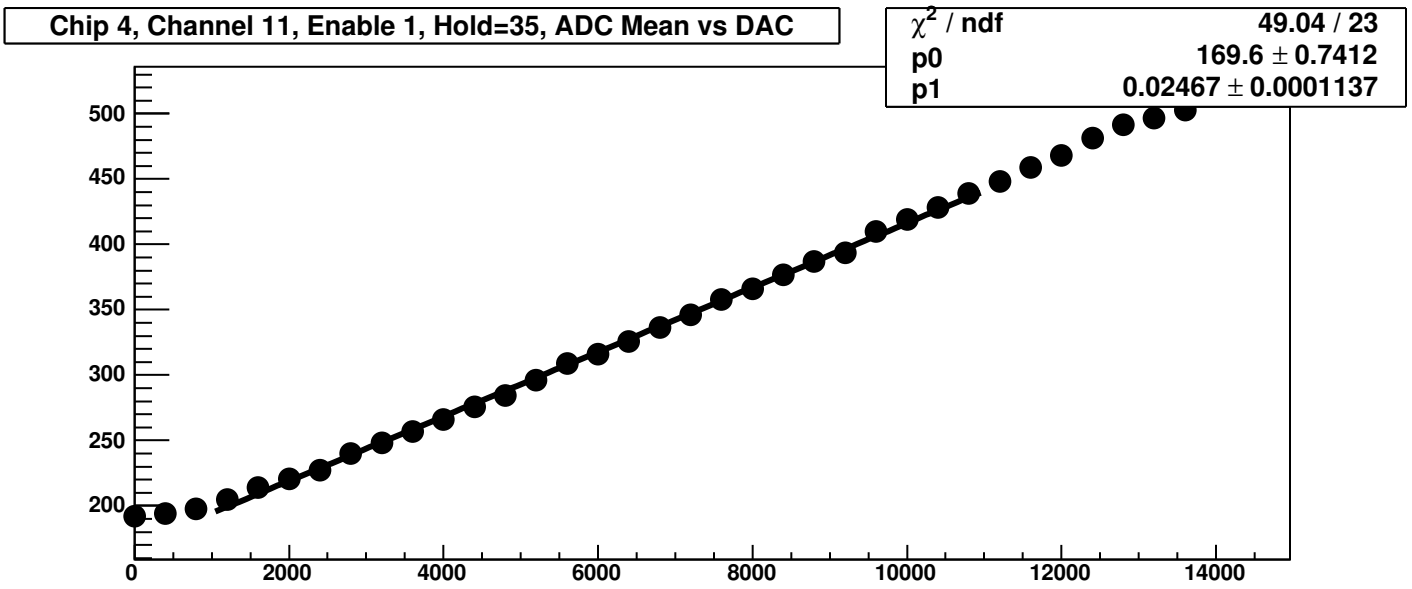

Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

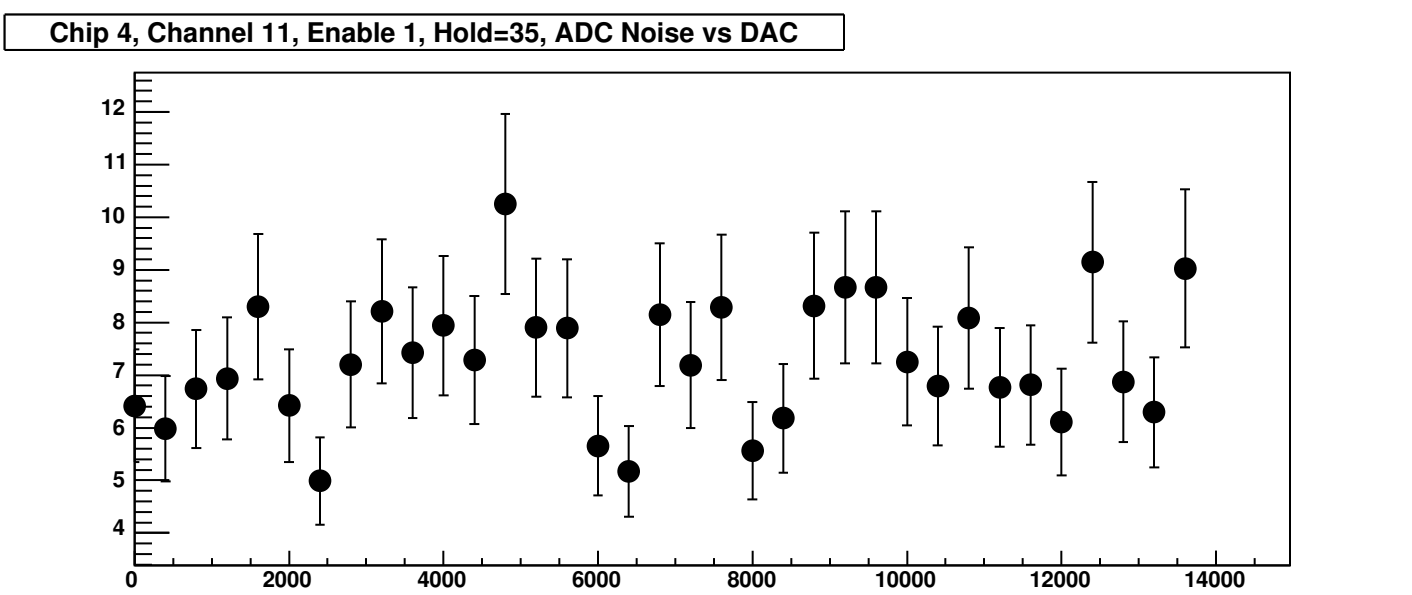

Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

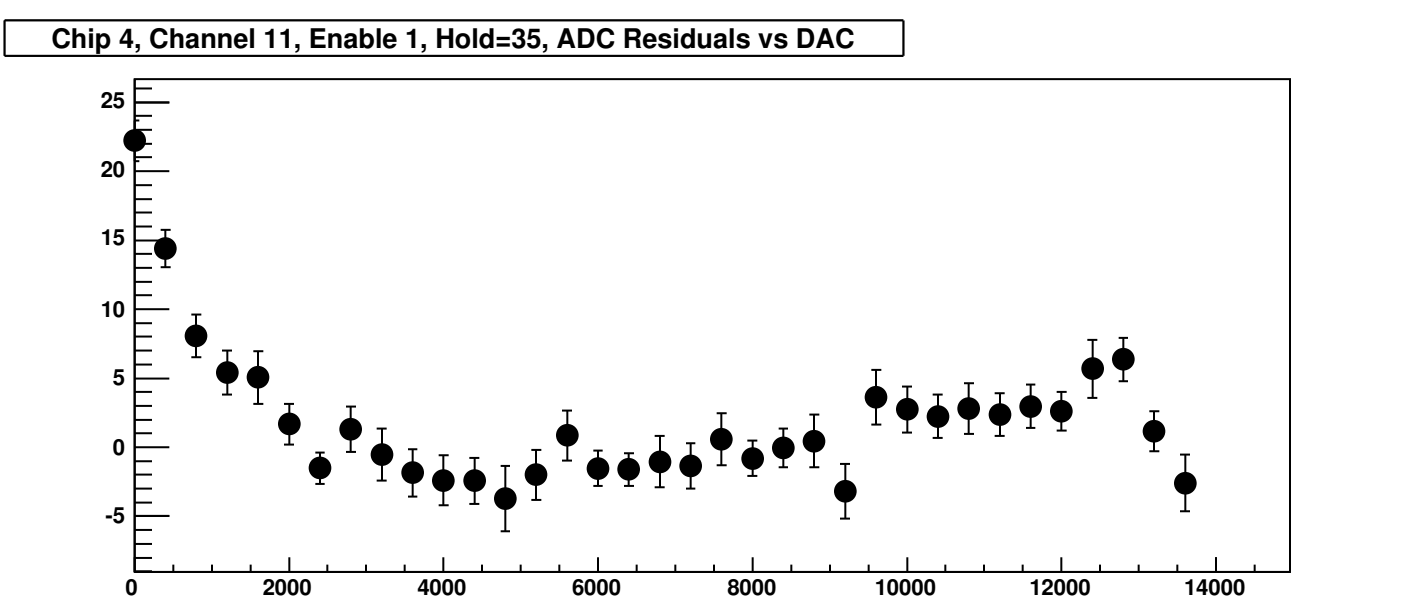

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 3610 / 23
p0 83.92 ± 1.087
p1 2.453 ± 0.000181
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

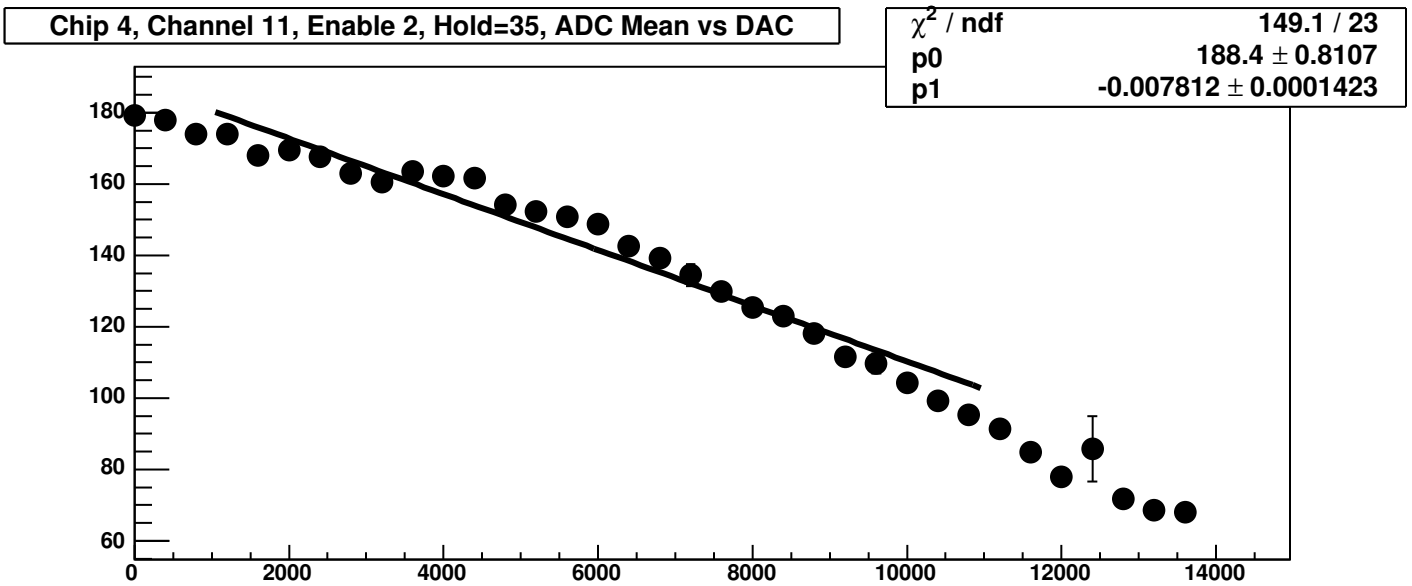

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

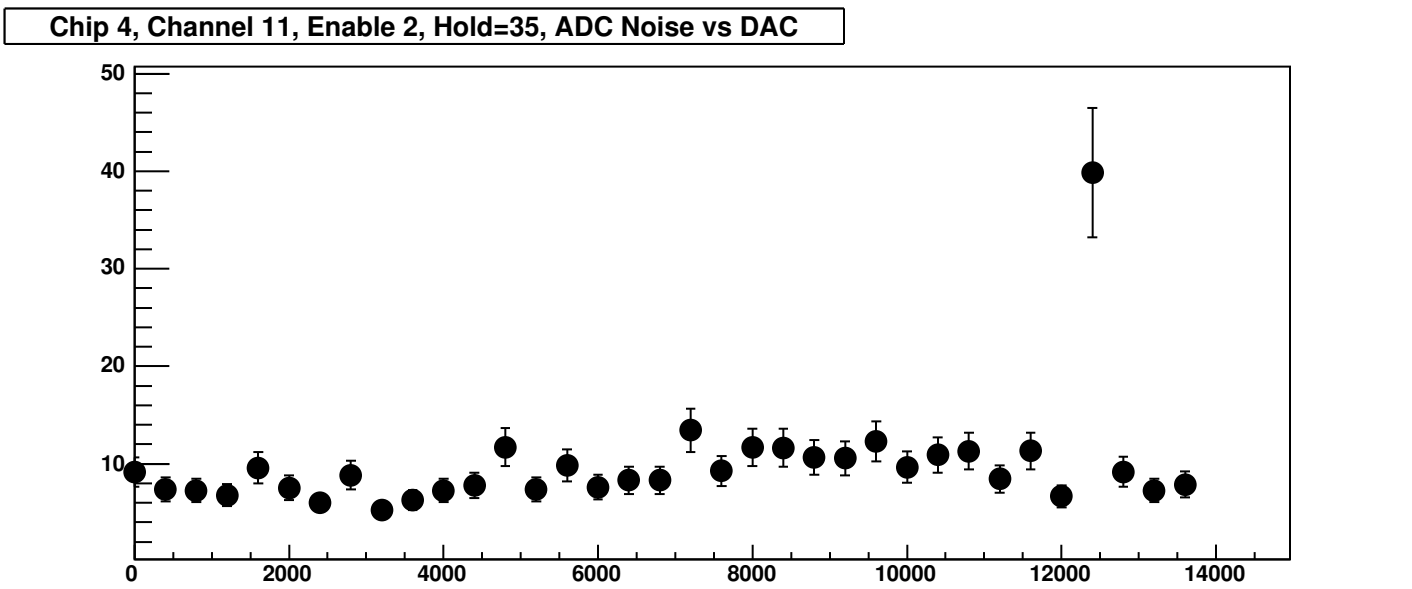

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

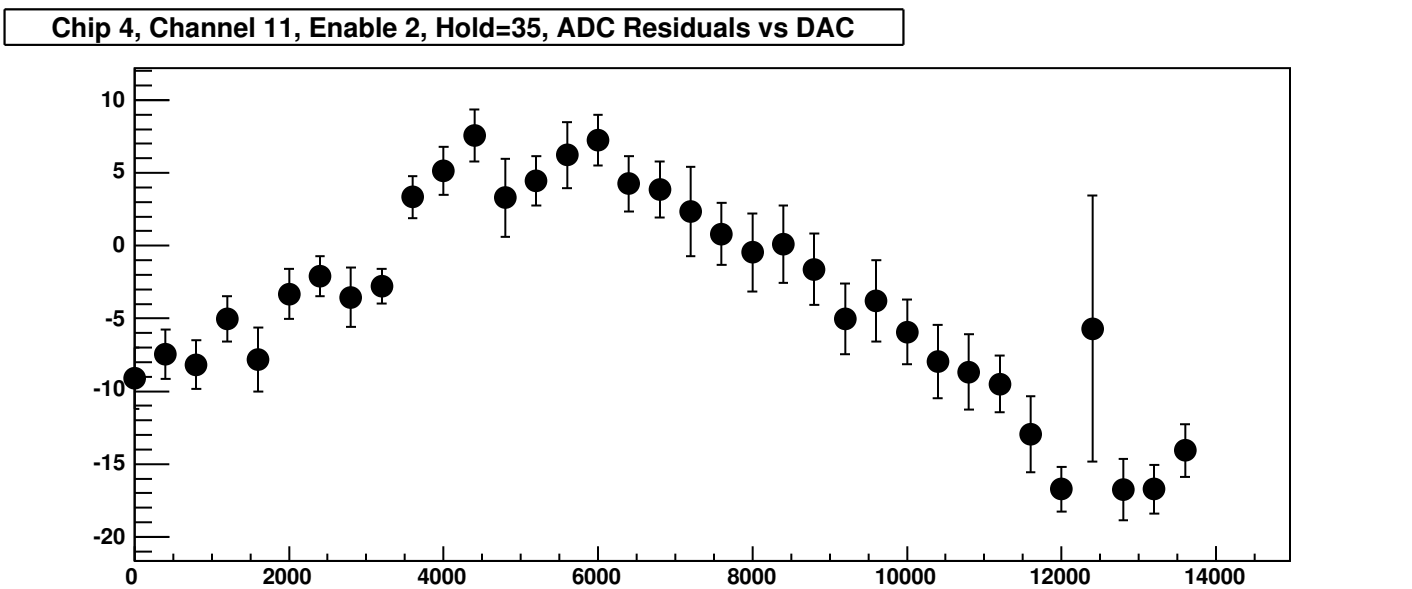

Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.04 / 23
p0 169.6 ± 0.7412
p1 0.02467 ± 0.0001137
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

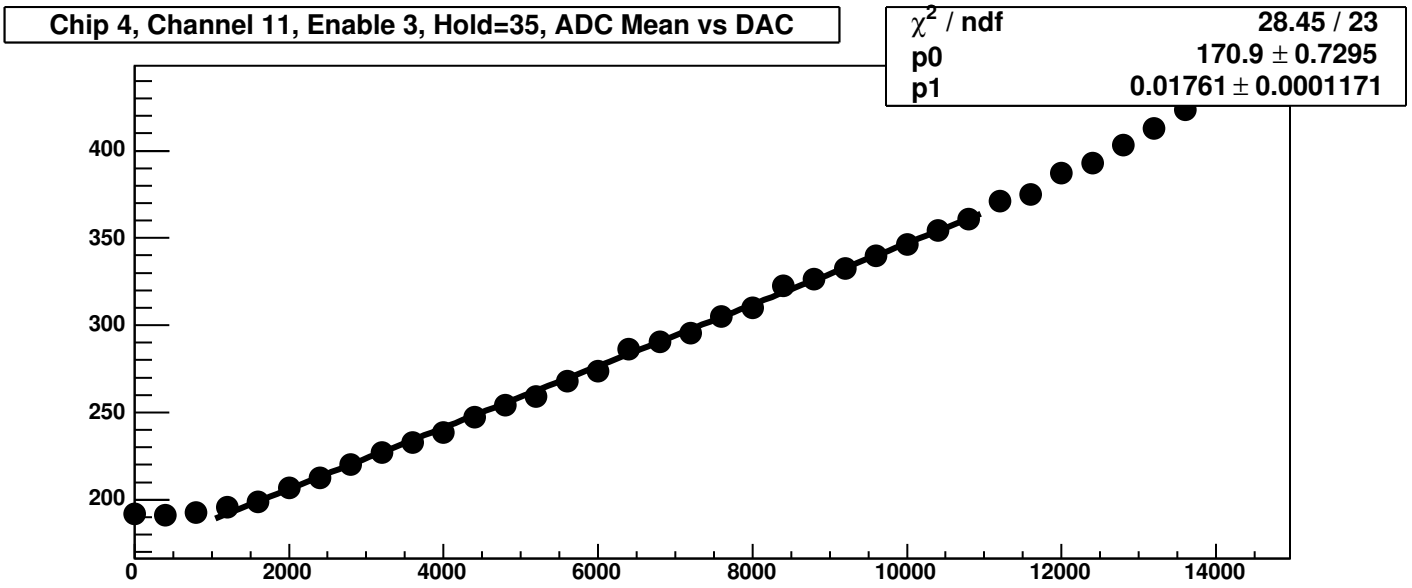

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

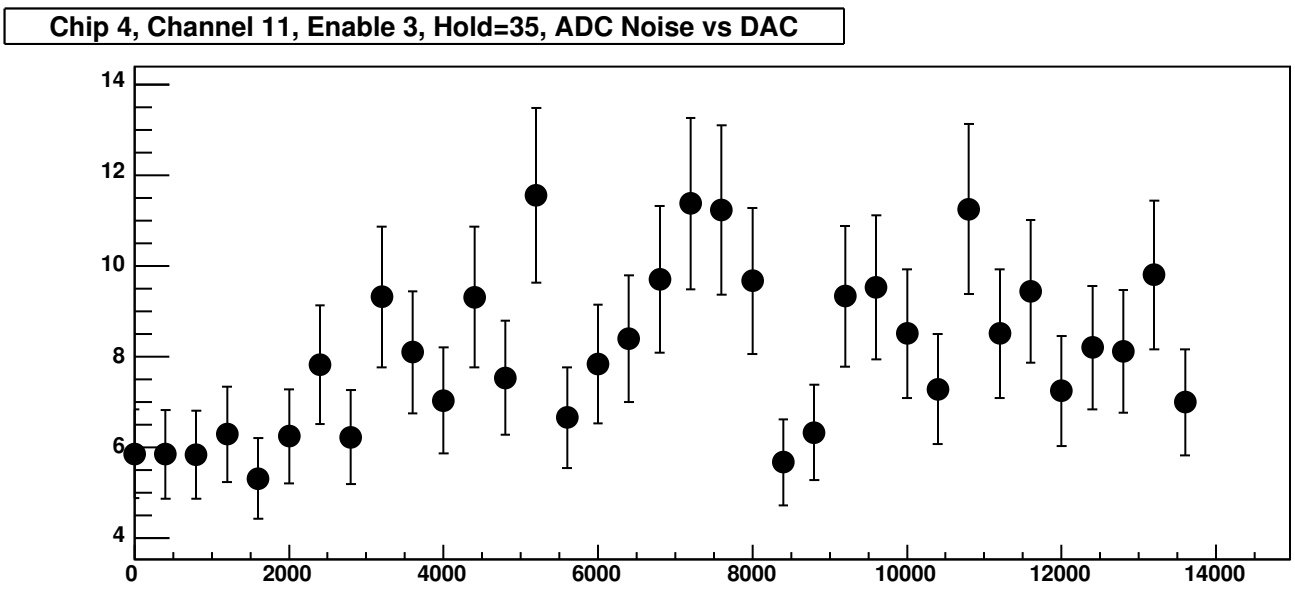

Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

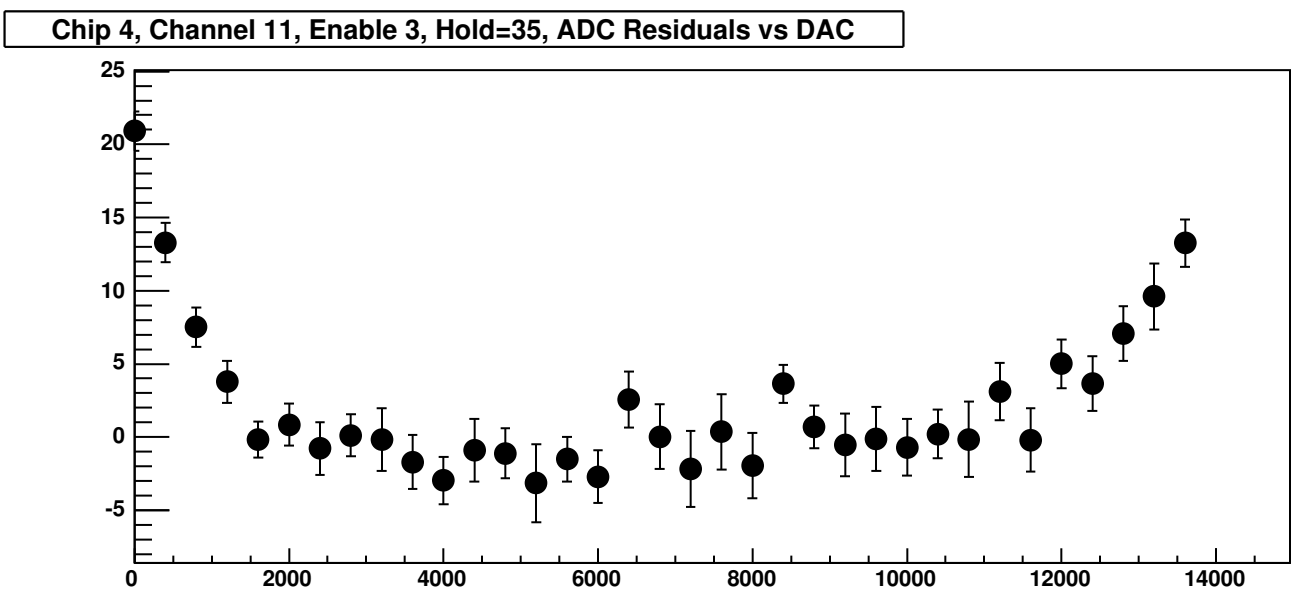

Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 149.1 / 23
p0 188.4 ± 0.8107
p1 -0.007812 ± 0.0001423
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

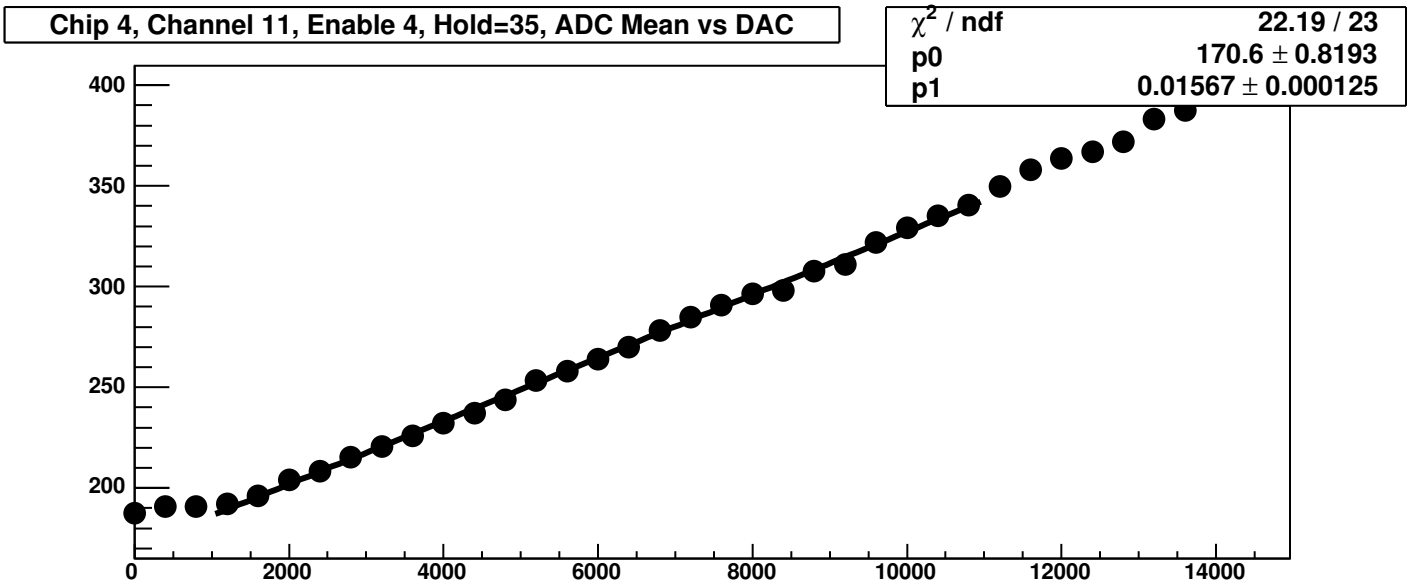

Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

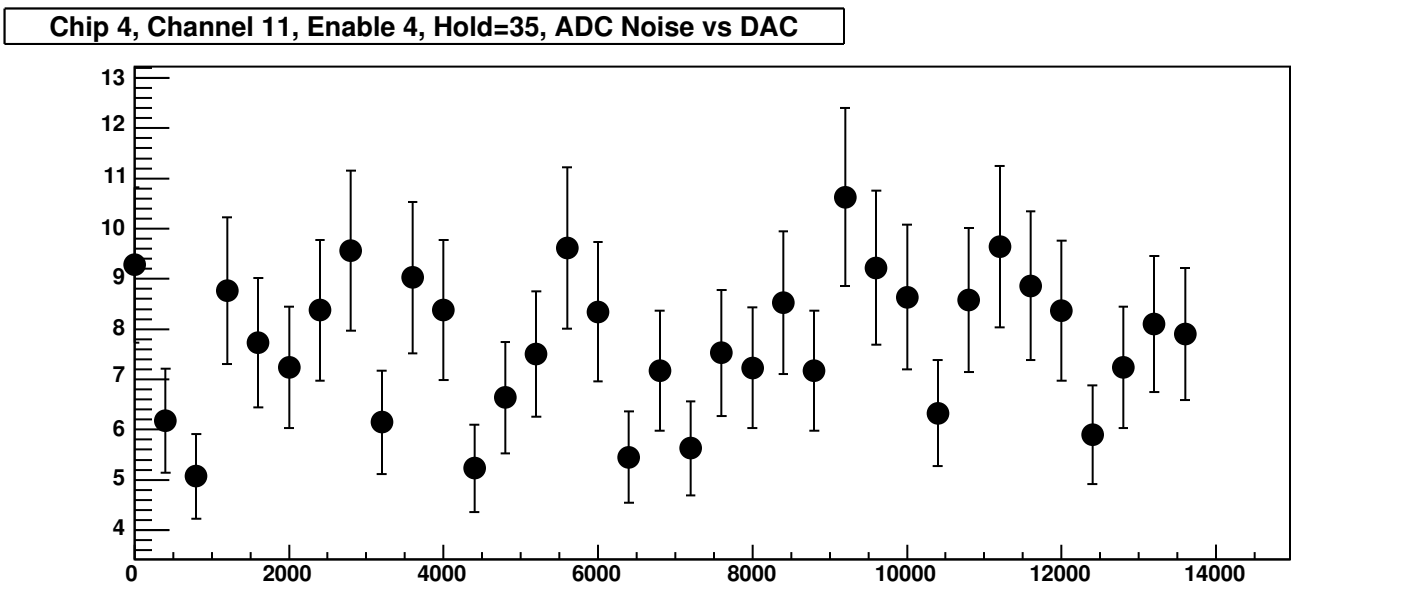

Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

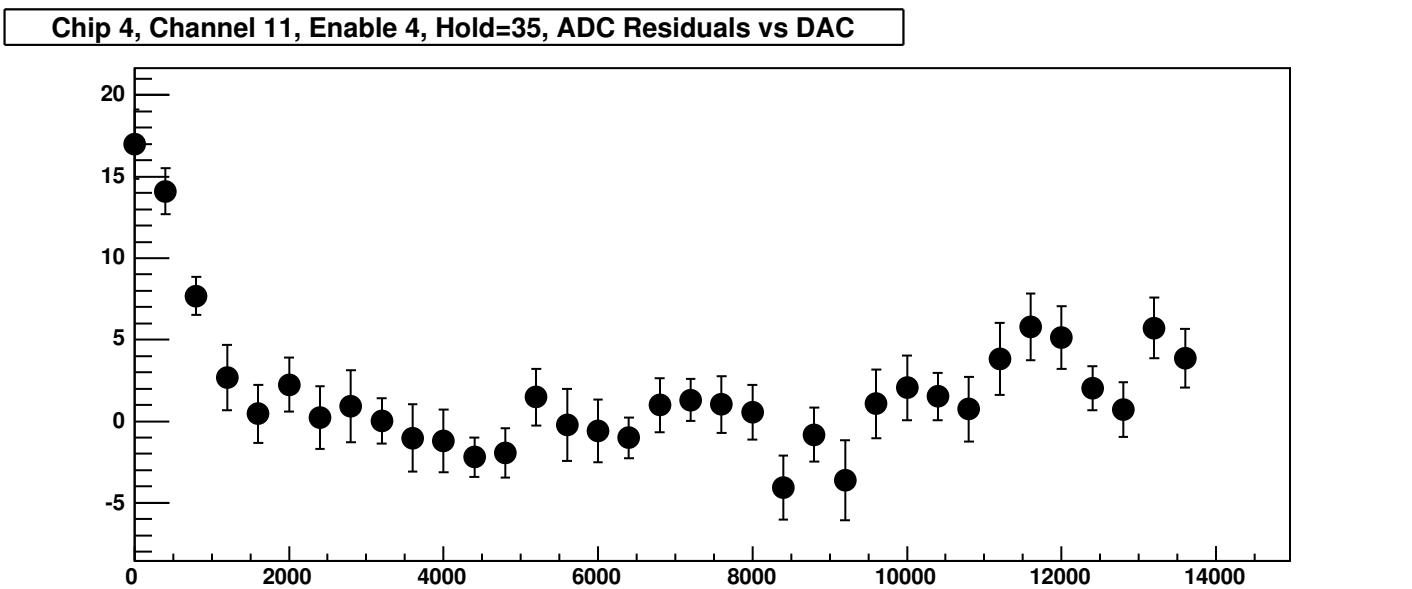

Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.45 / 23
p0 170.9 ± 0.7295
p1 0.01761 ± 0.0001171
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

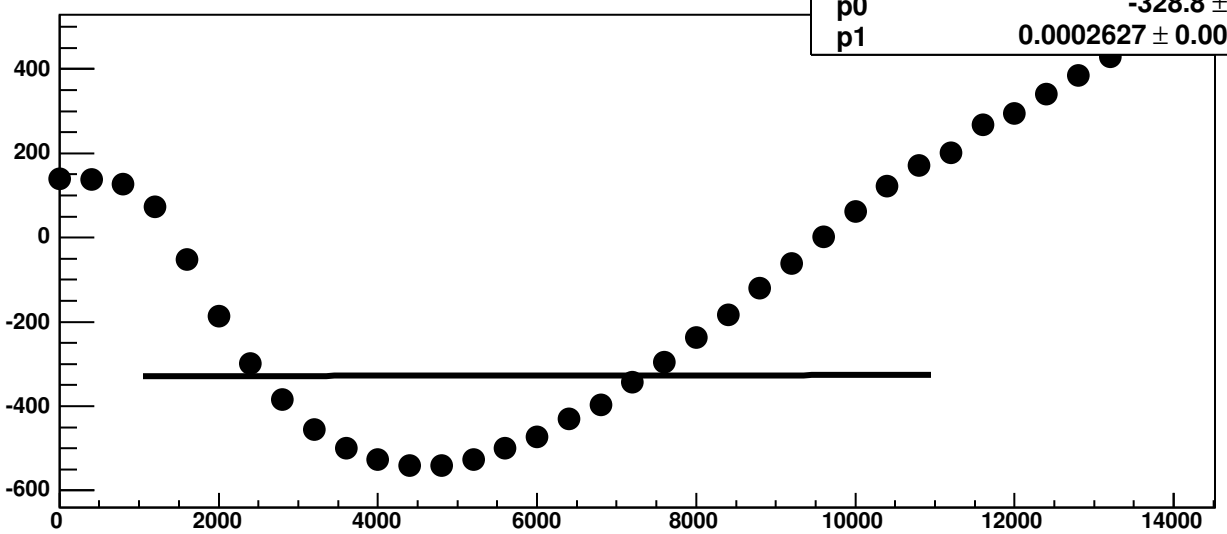

Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.19 / 23
p0 170.6 ± 0.8193
p1 0.01567 ± 0.000125
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

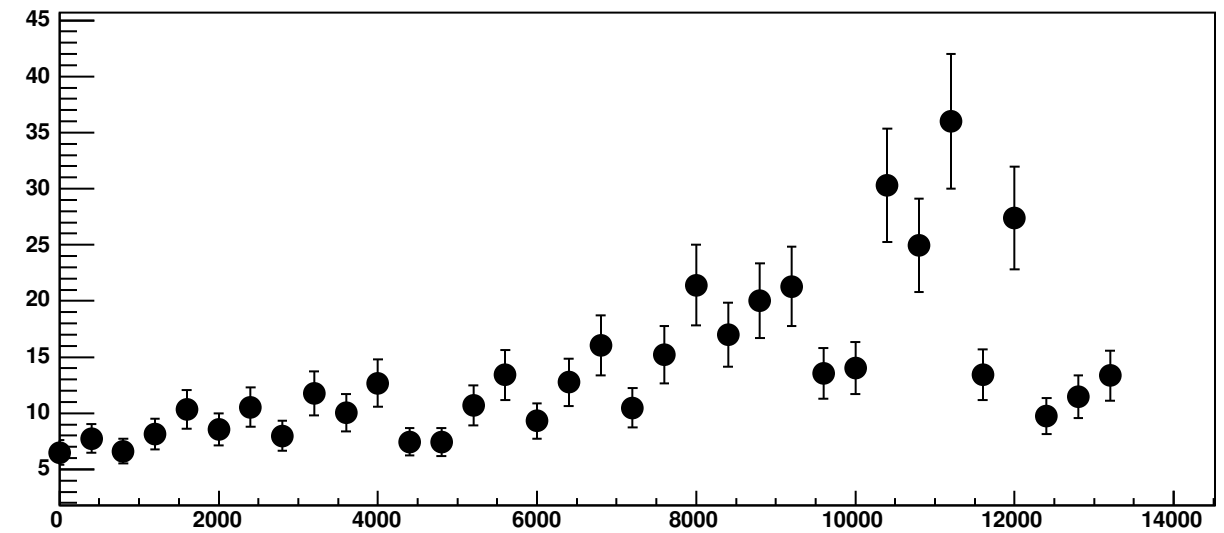

Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

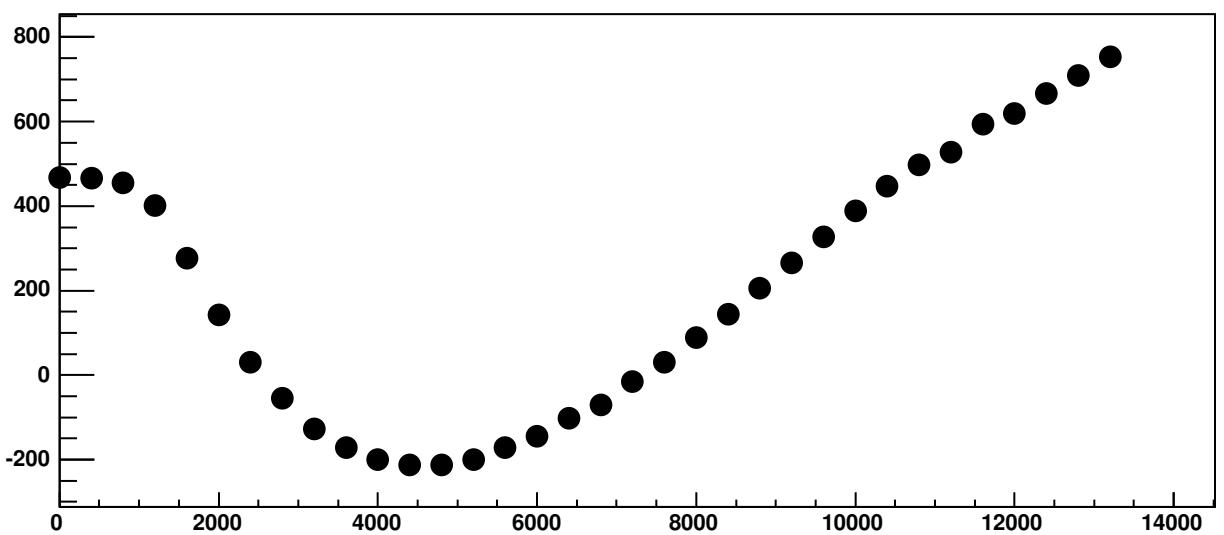
χ^2 / ndf	1.696e+05 / 23
p0	-328.8 ± 1.106
p1	0.0002627 ± 0.0002102



Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

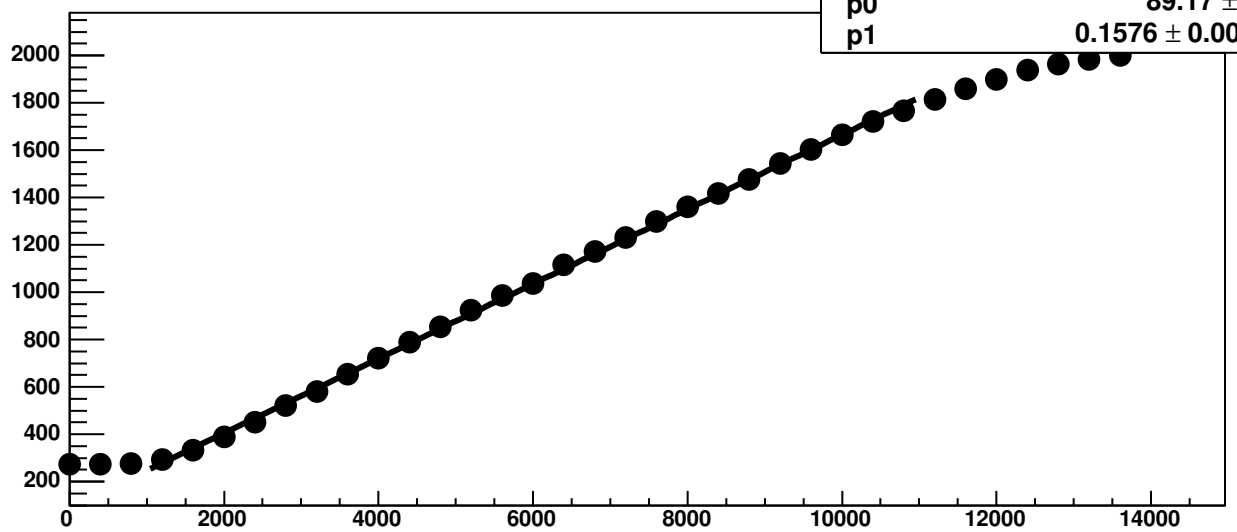


Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

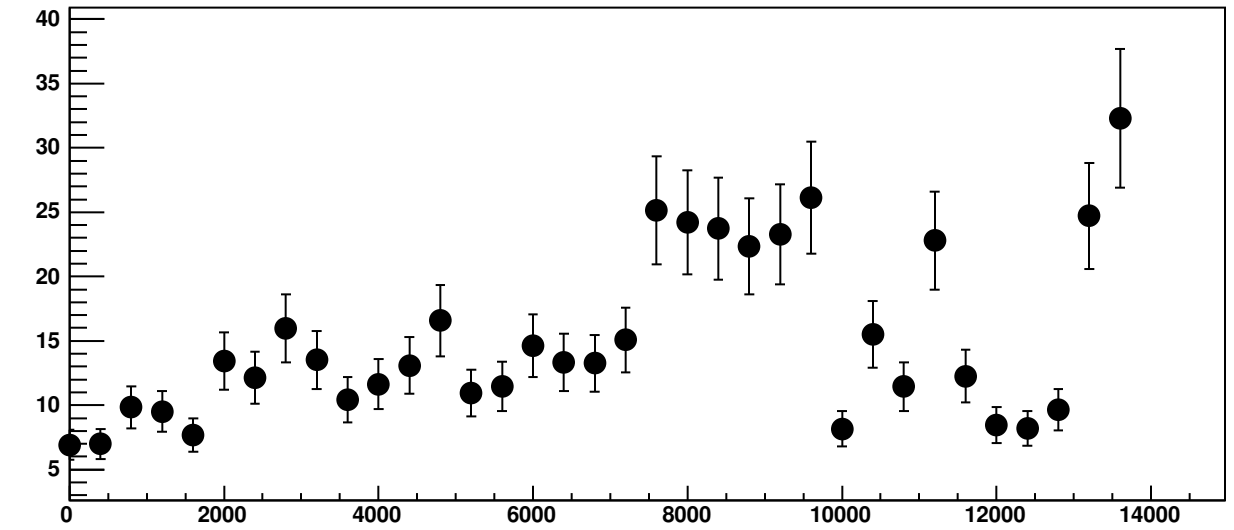


Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

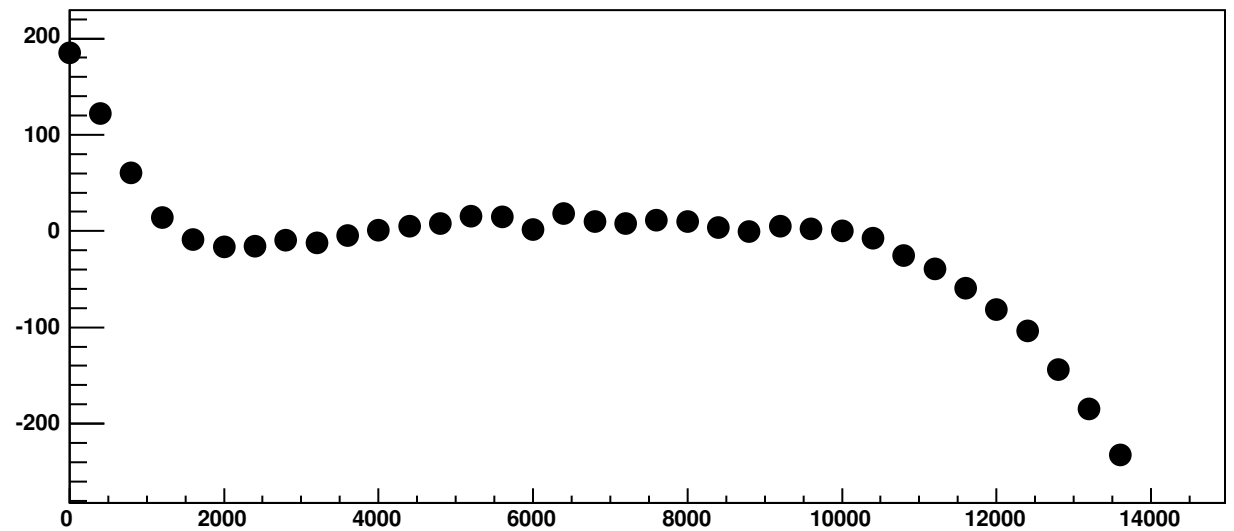
χ^2 / ndf	381.5 / 23
p0	89.17 ± 1.165
p1	0.1576 ± 0.0001901

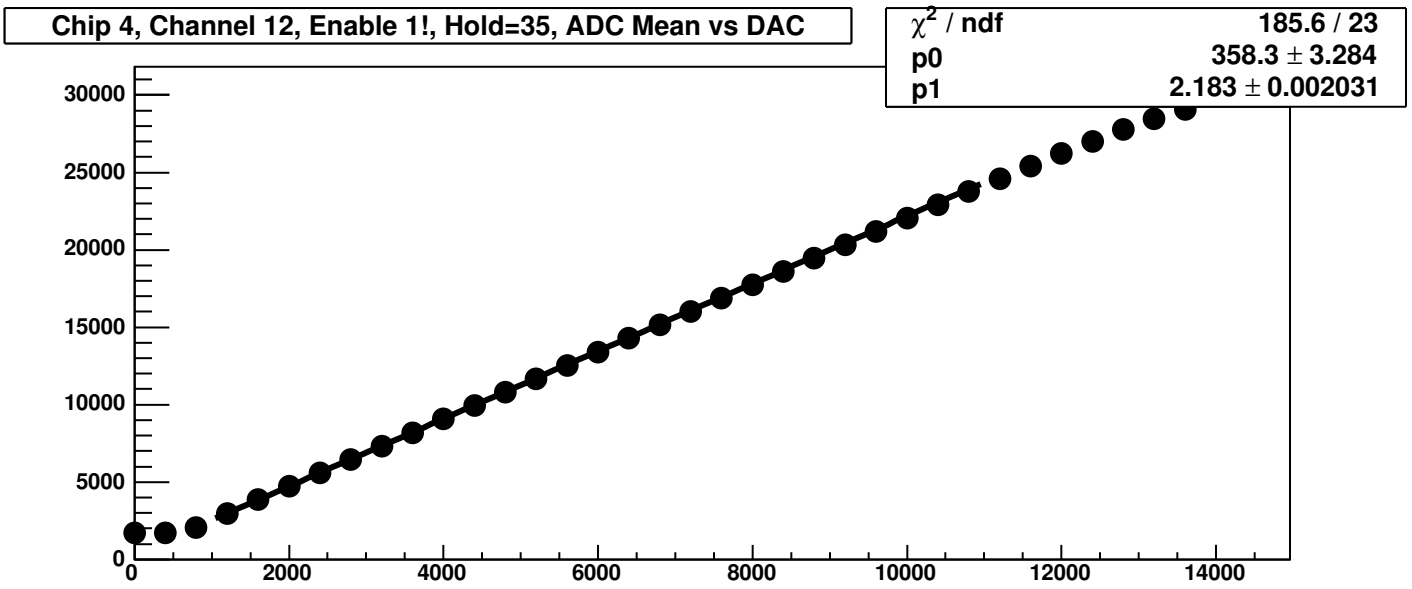


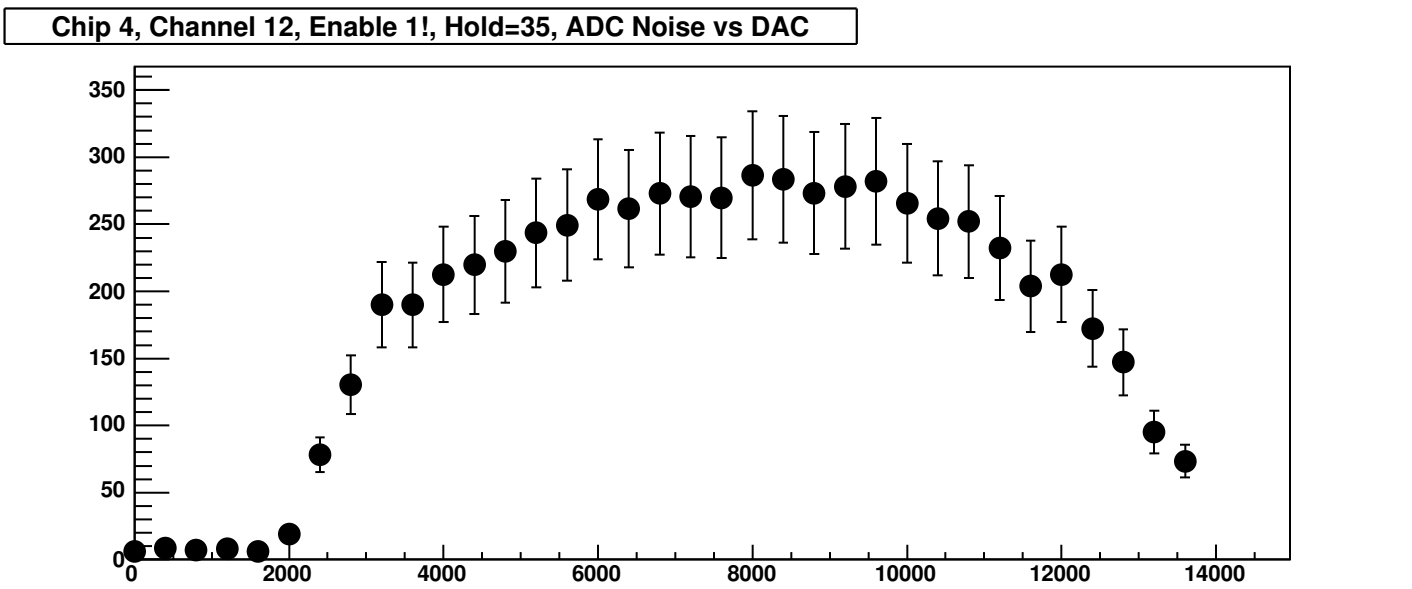
Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

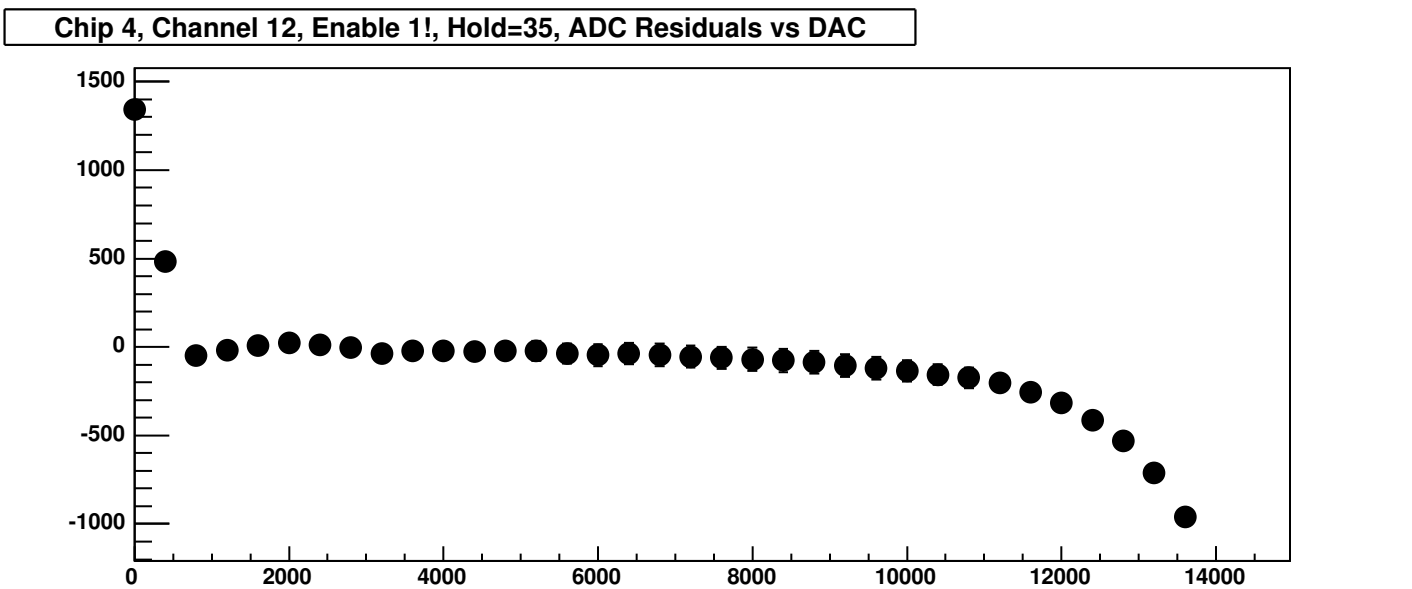


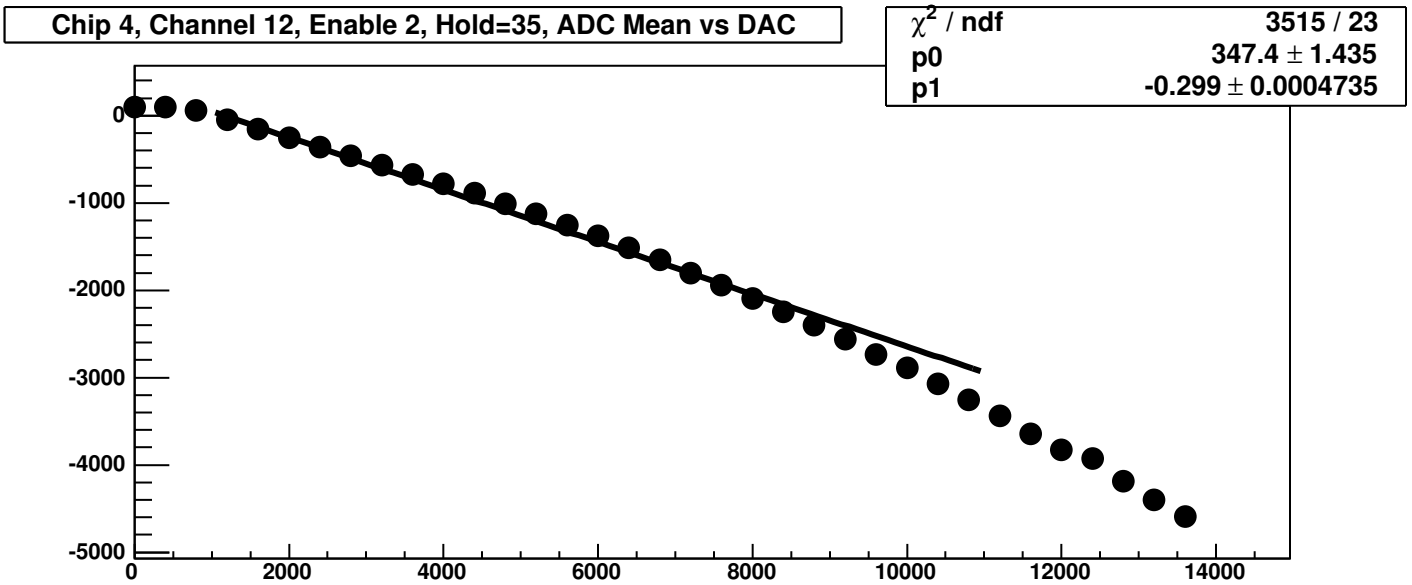
Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

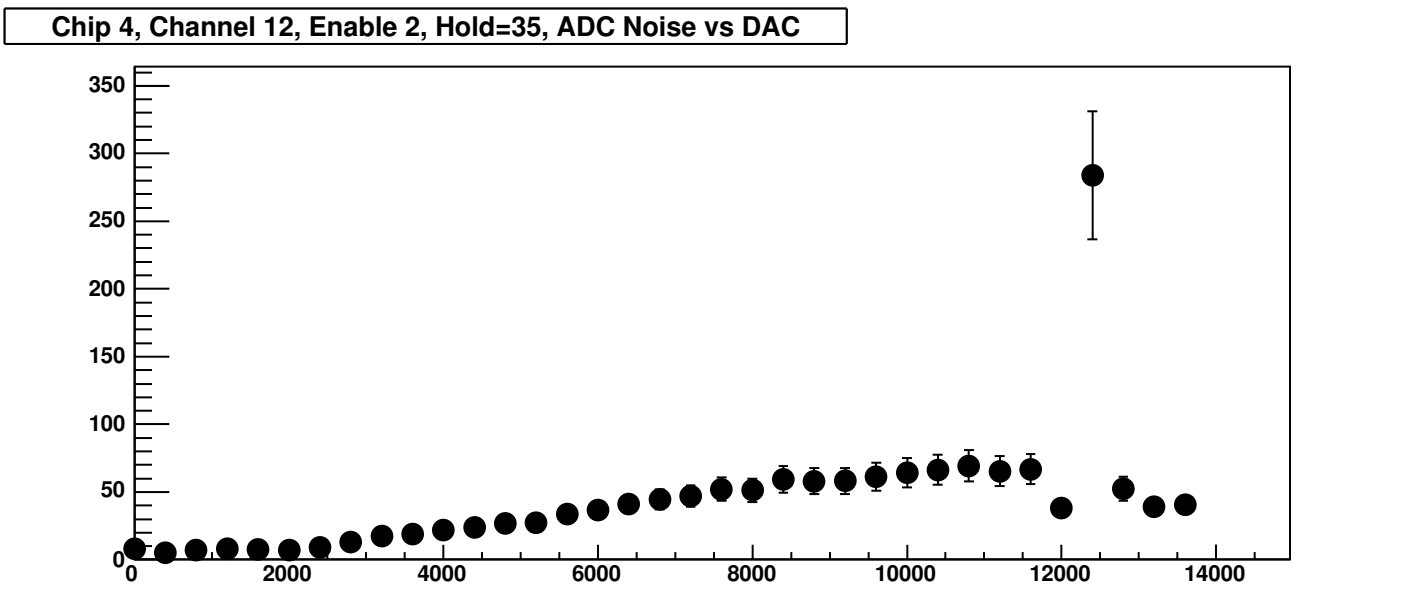


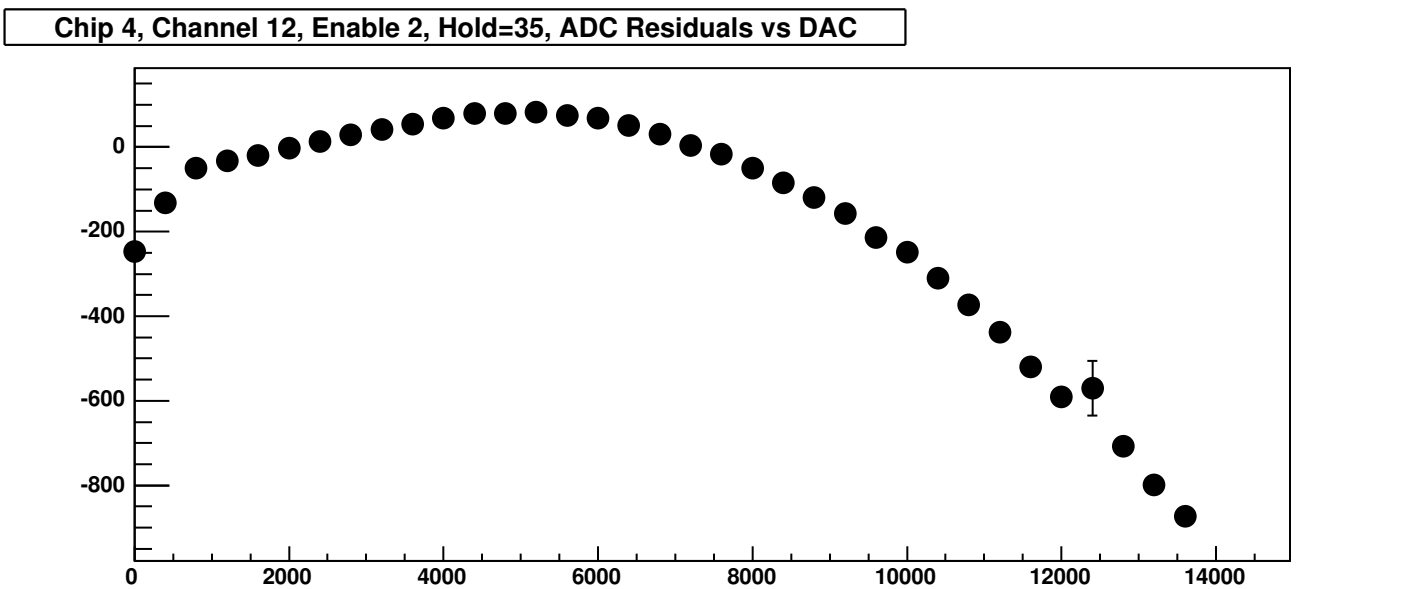

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 185.6 / 23
p0 358.3 ± 3.284
p1 2.183 ± 0.002031
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

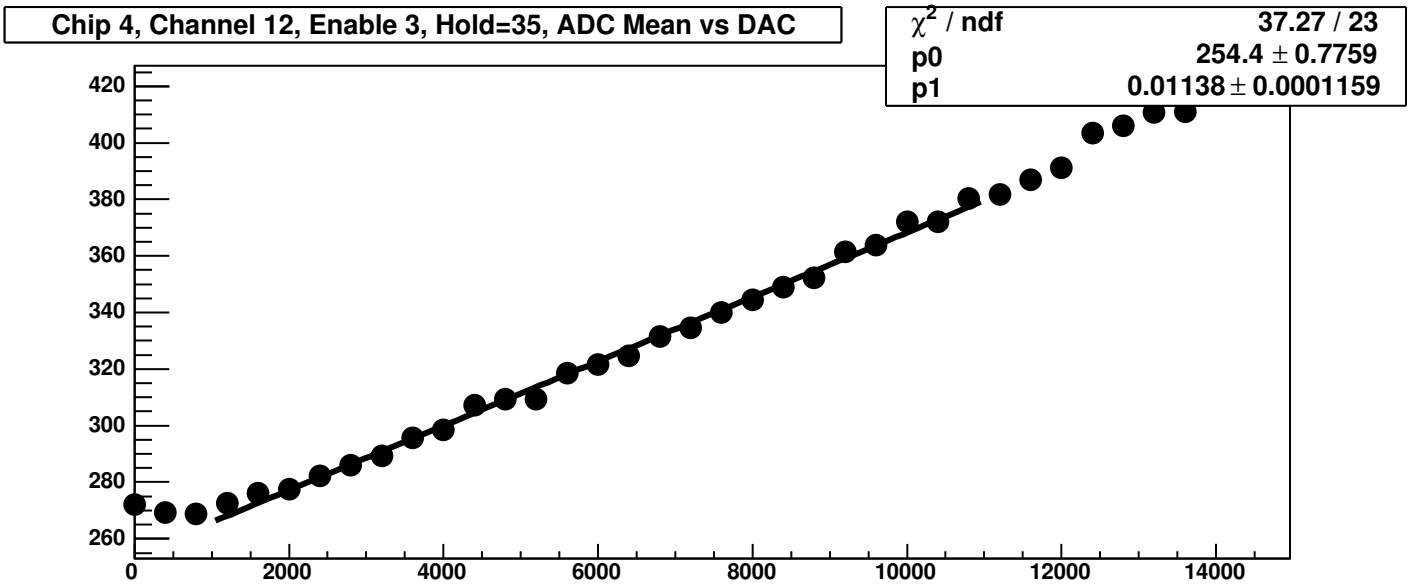

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

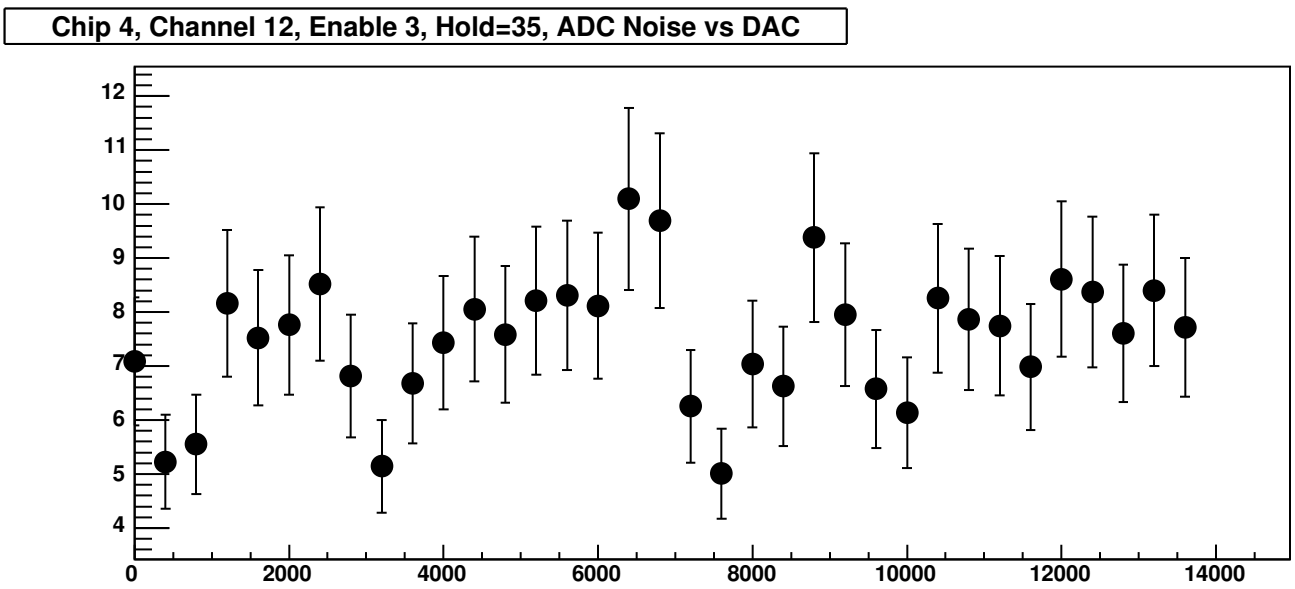

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

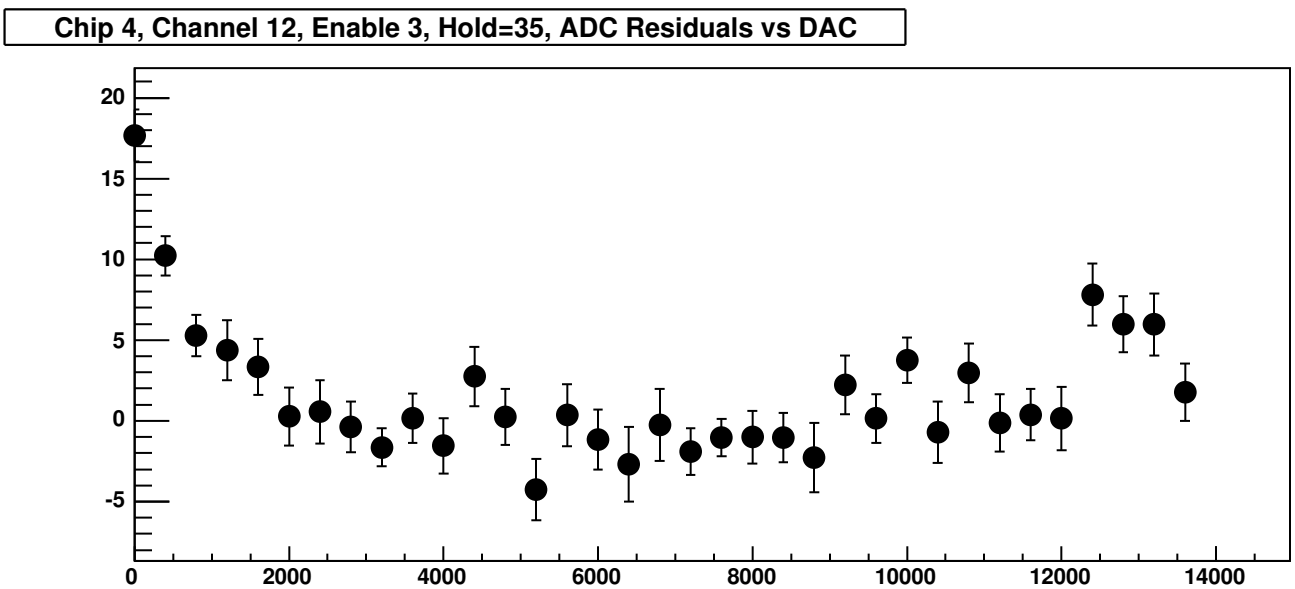

Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3515 / 23
p0 347.4 ± 1.435
p1 -0.299 ± 0.0004735
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

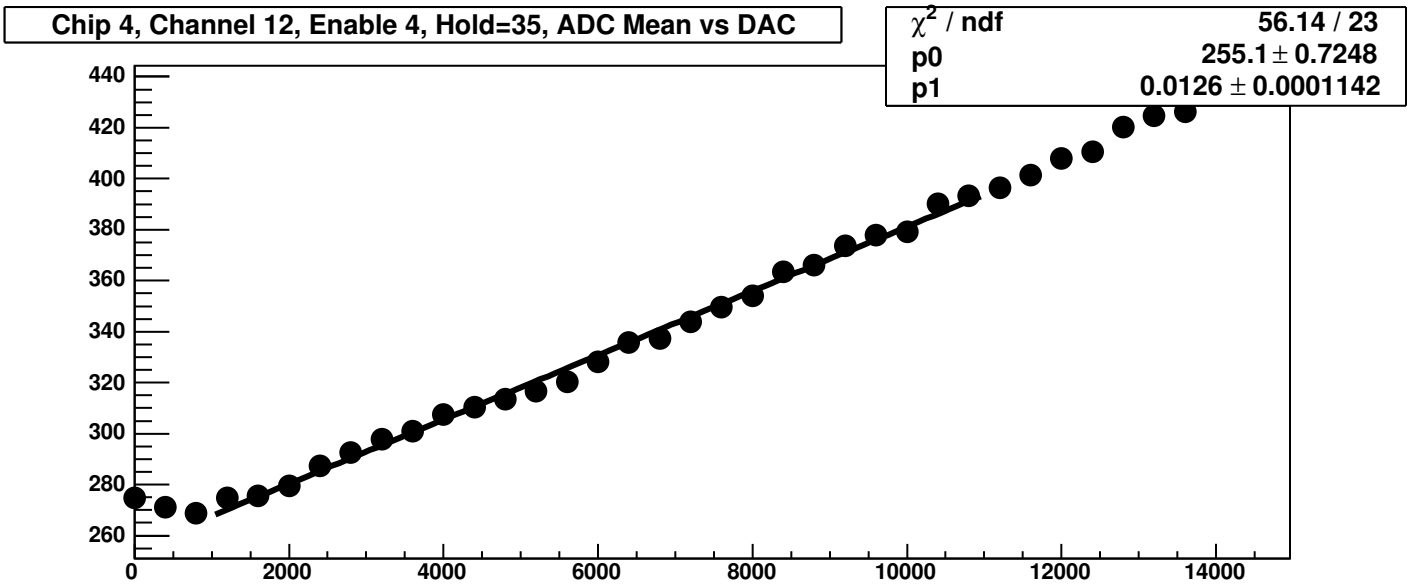

Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

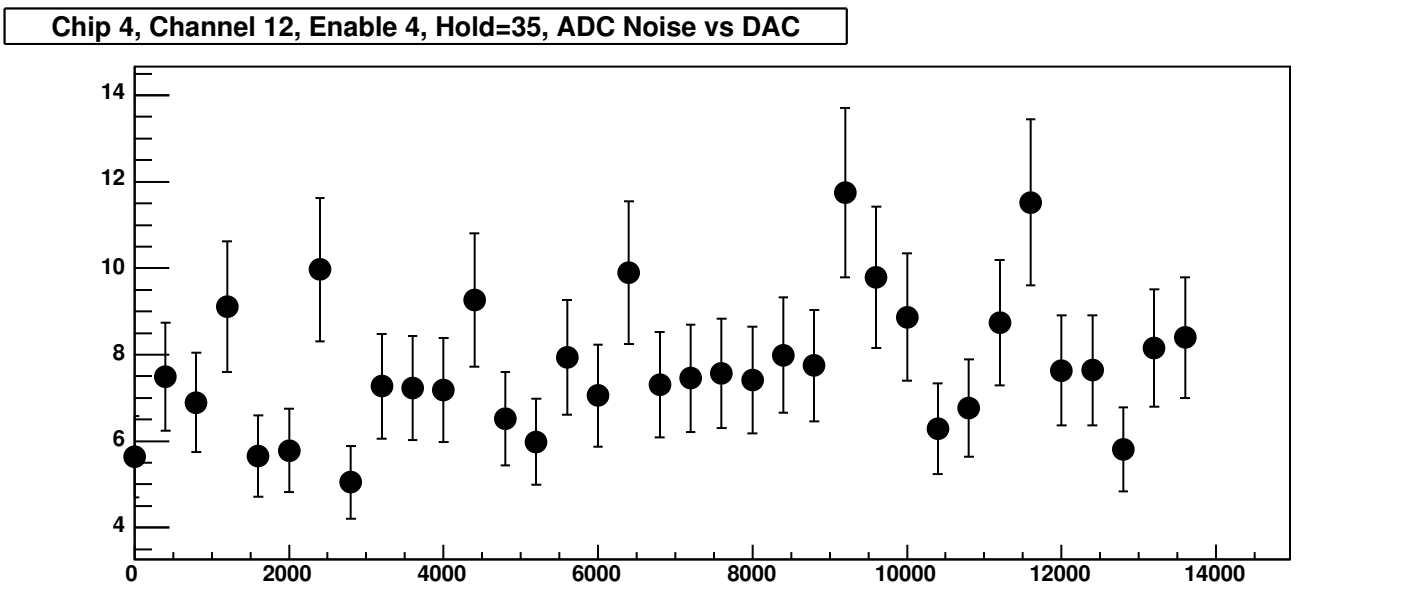

Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

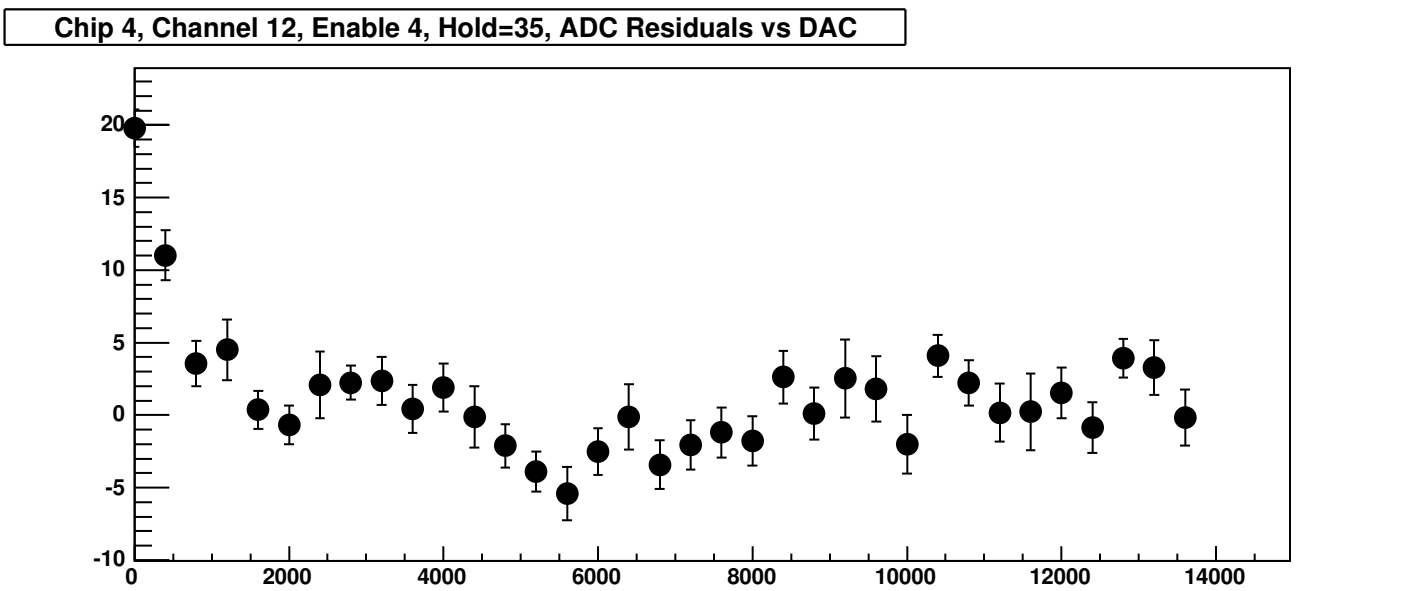

Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.27 / 23
p0 254.4 ± 0.7759
p1 0.01138 ± 0.0001159
420
400
380
360
340
320
300
280
260
0
2000
4000
6000
8000
10000
12000
14000

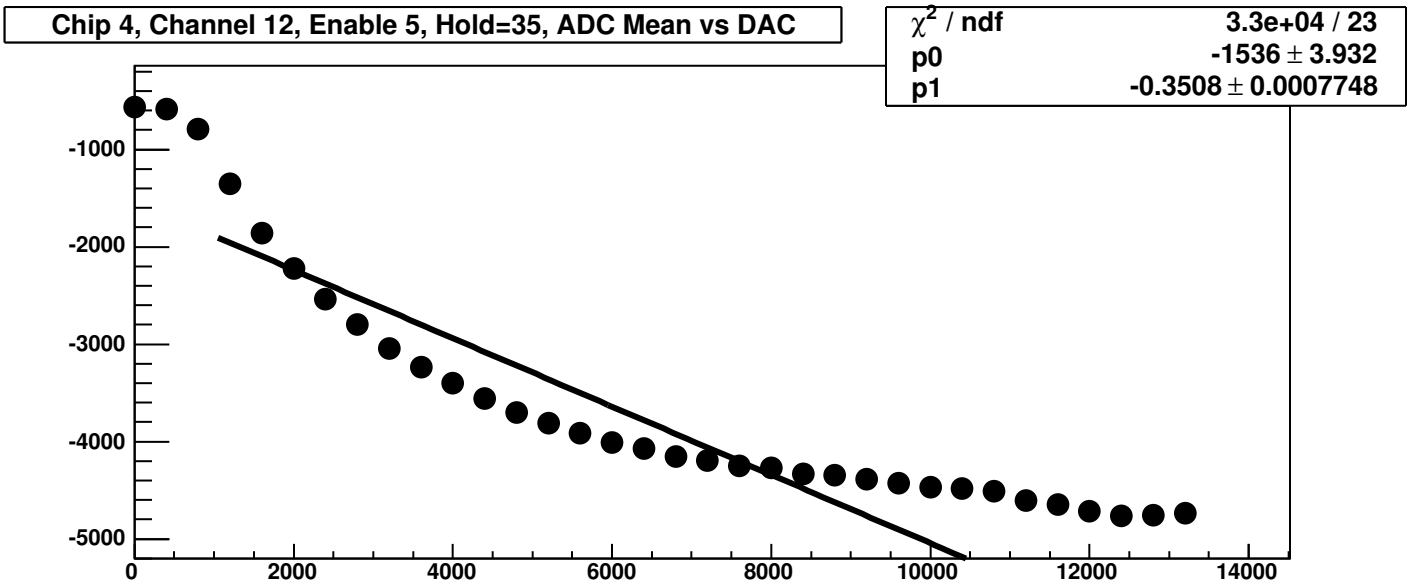

Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

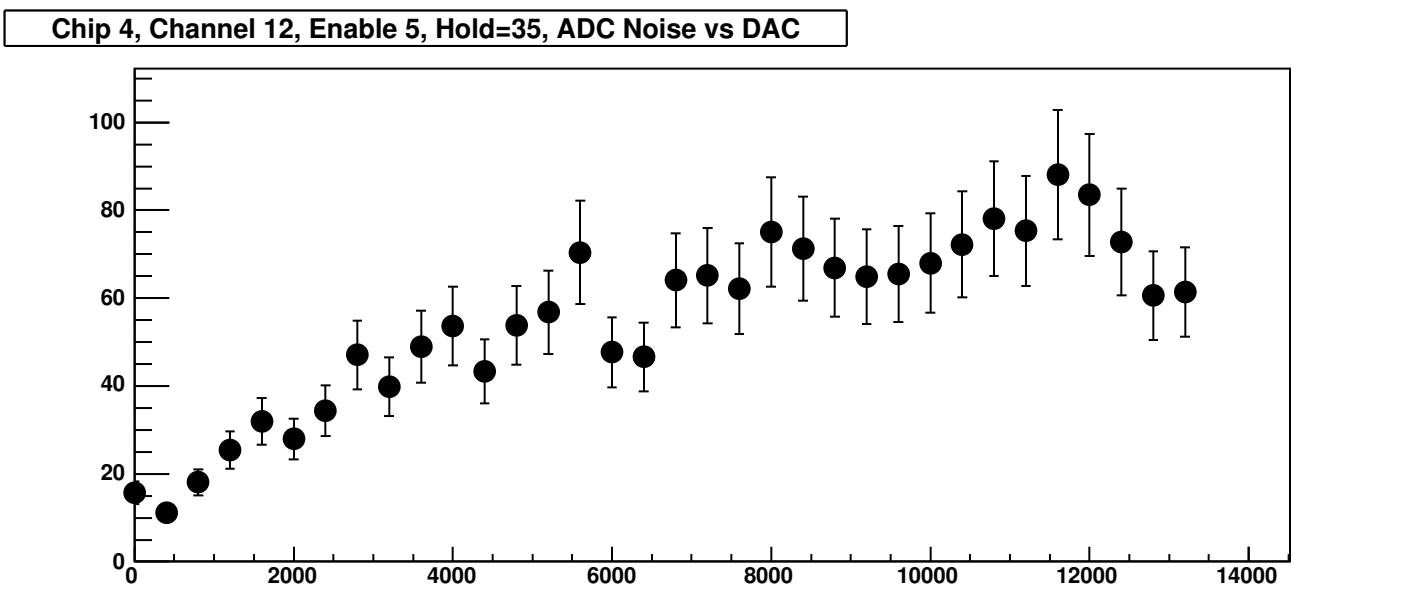

Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

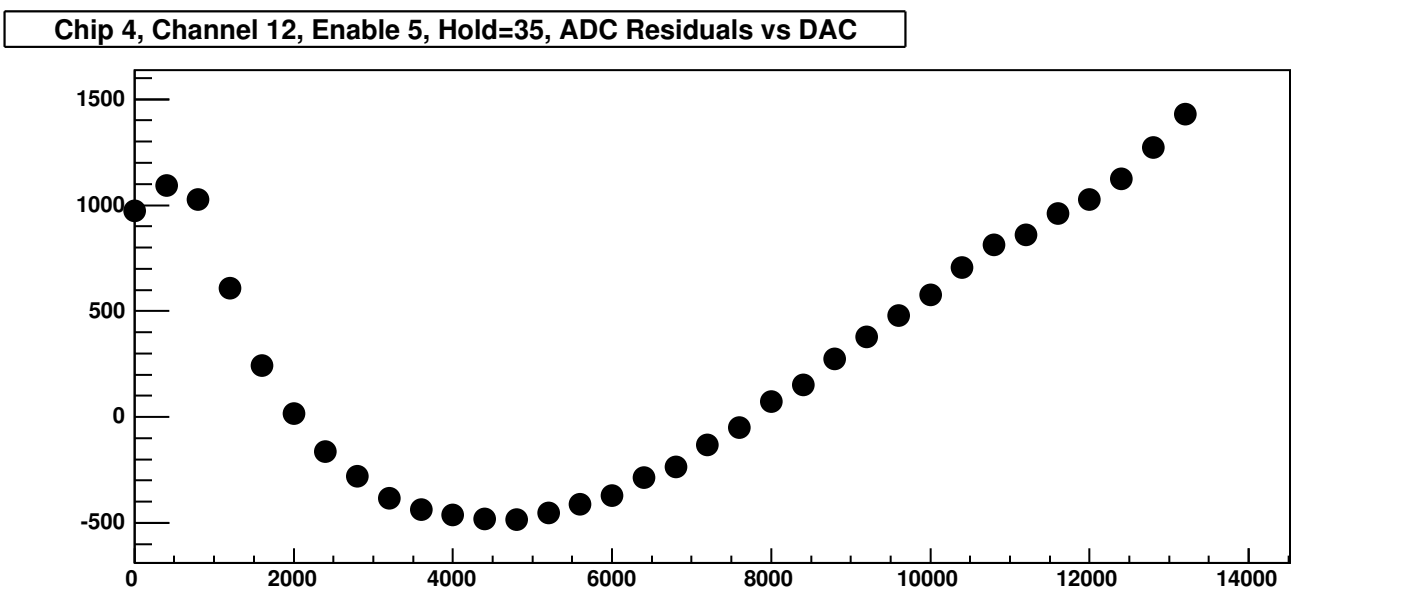

Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 56.14 / 23
p0 255.1 ± 0.7248
p1 0.0126 ± 0.0001142
440
420
400
380
360
340
320
300
280
260
0
2000
4000
6000
8000
10000
12000
14000

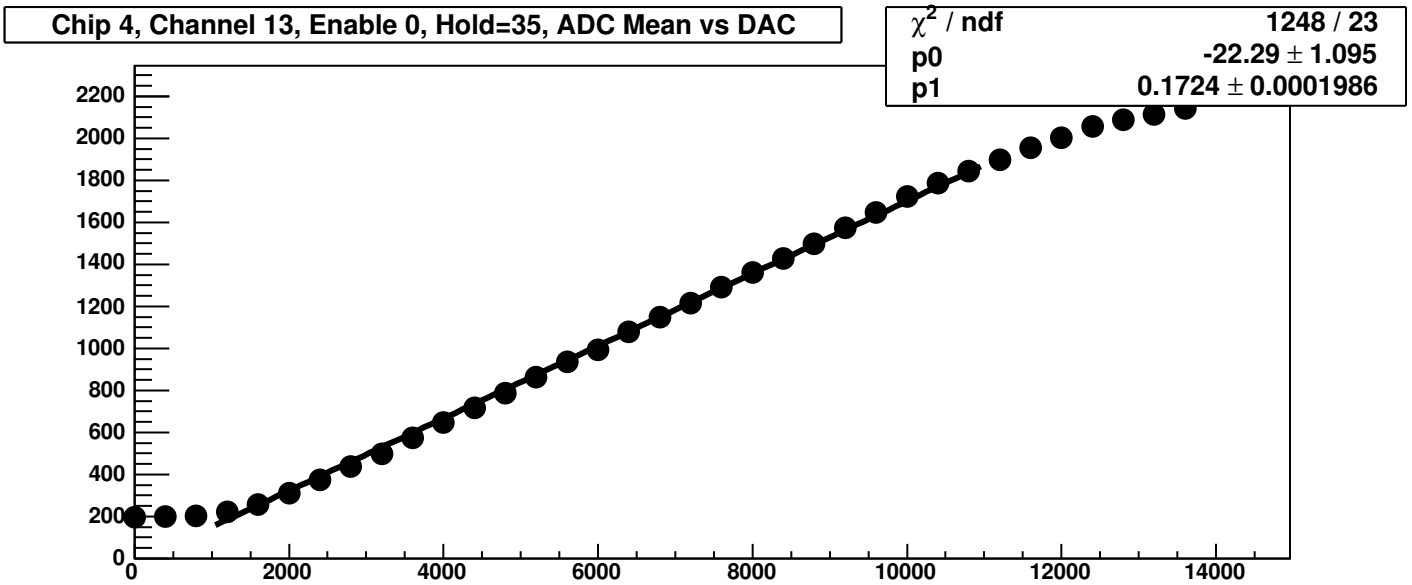

Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

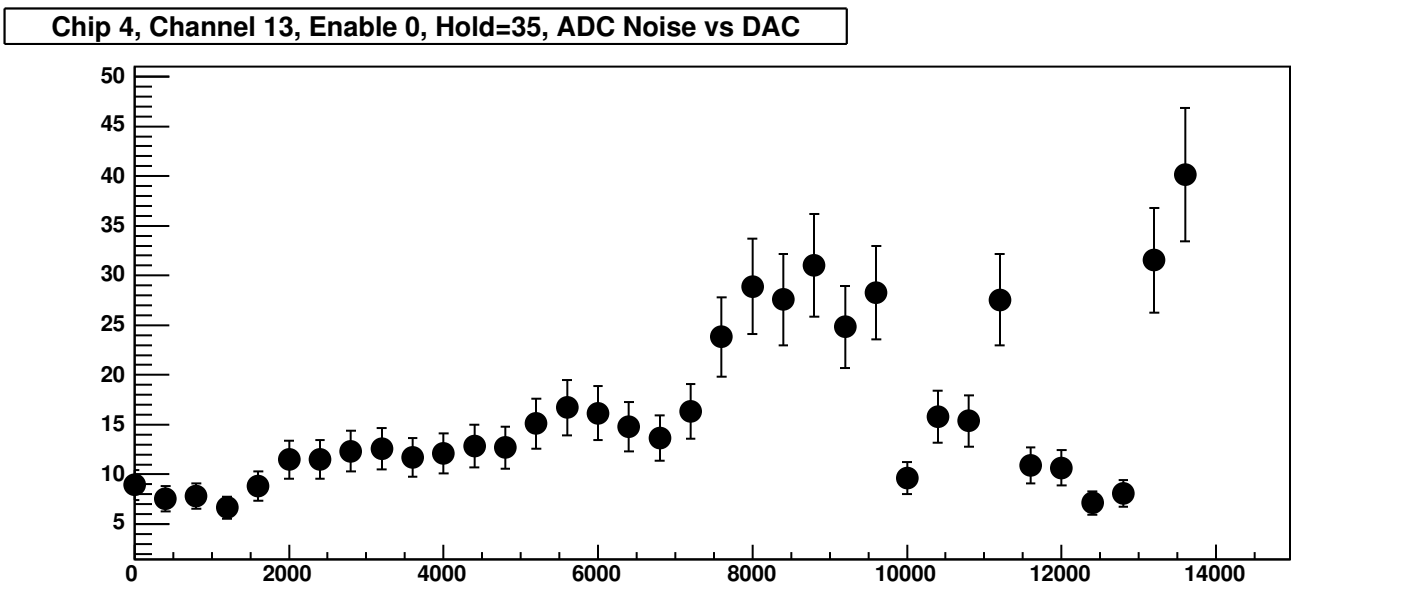

Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

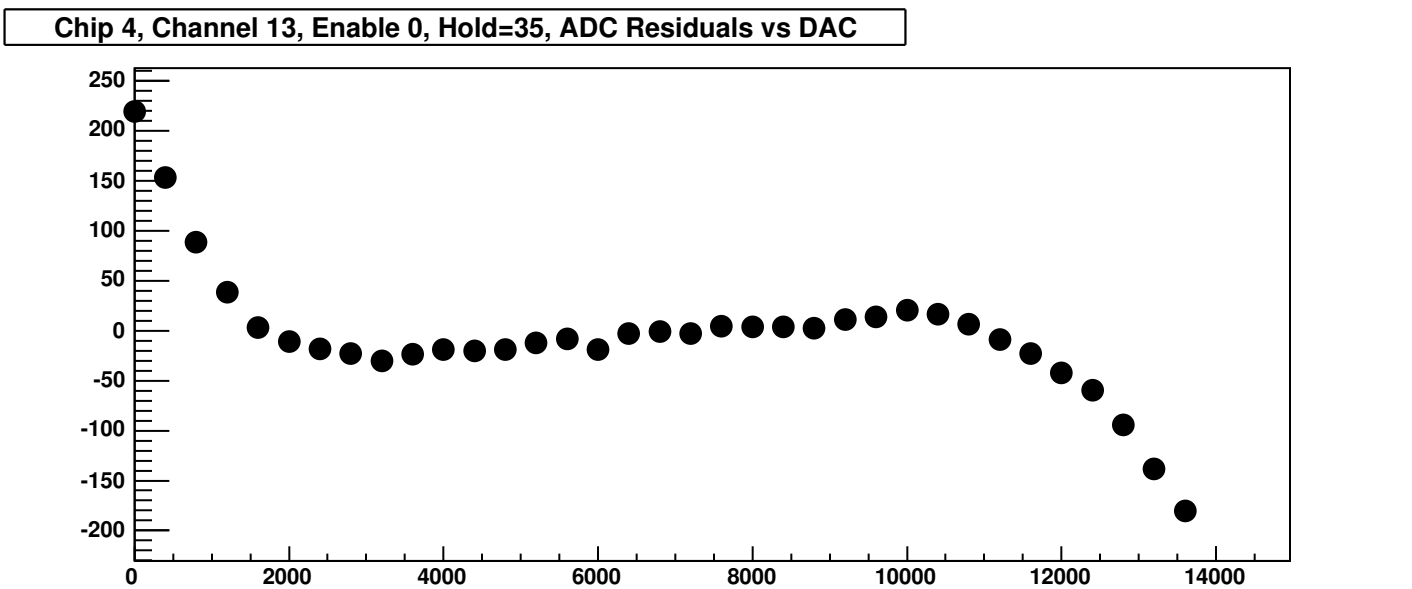

Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.3e+04 / 23
p0 -1536 ± 3.932
p1 -0.3508 ± 0.0007748
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

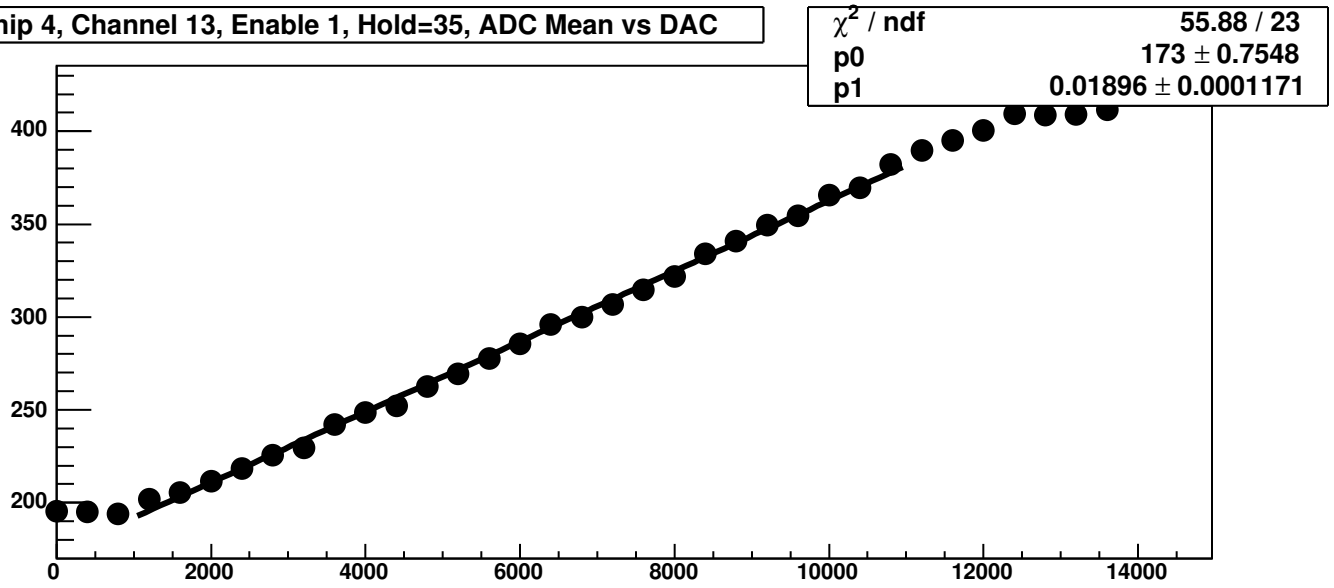

Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1248 / 23
p0 -22.29 ± 1.095
p1 0.1724 ± 0.0001986
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

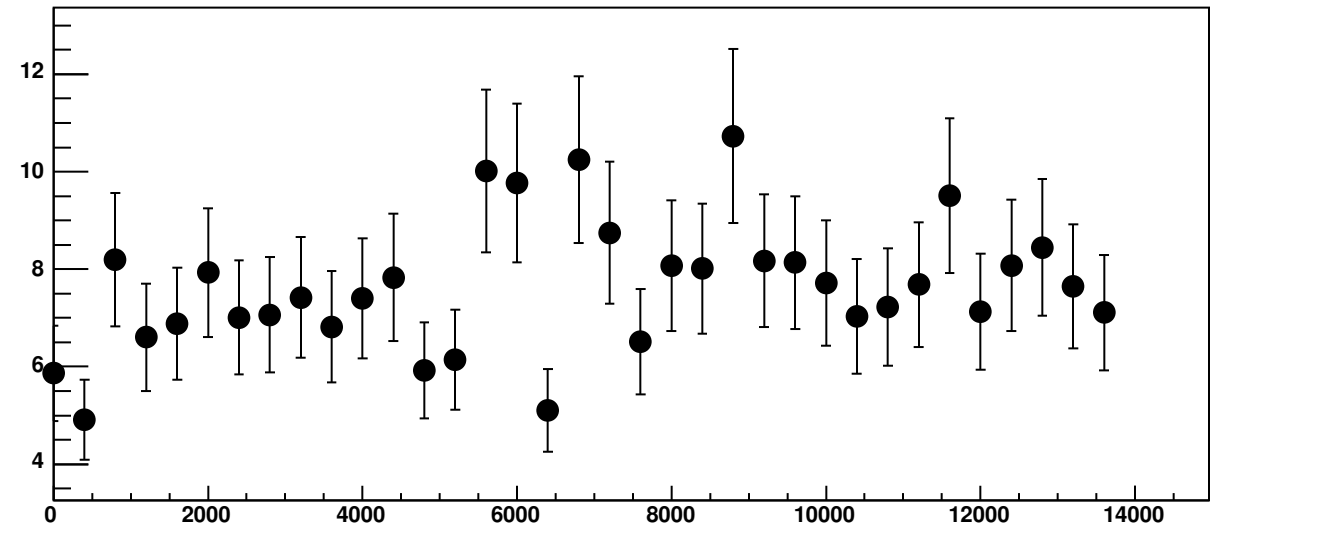

Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

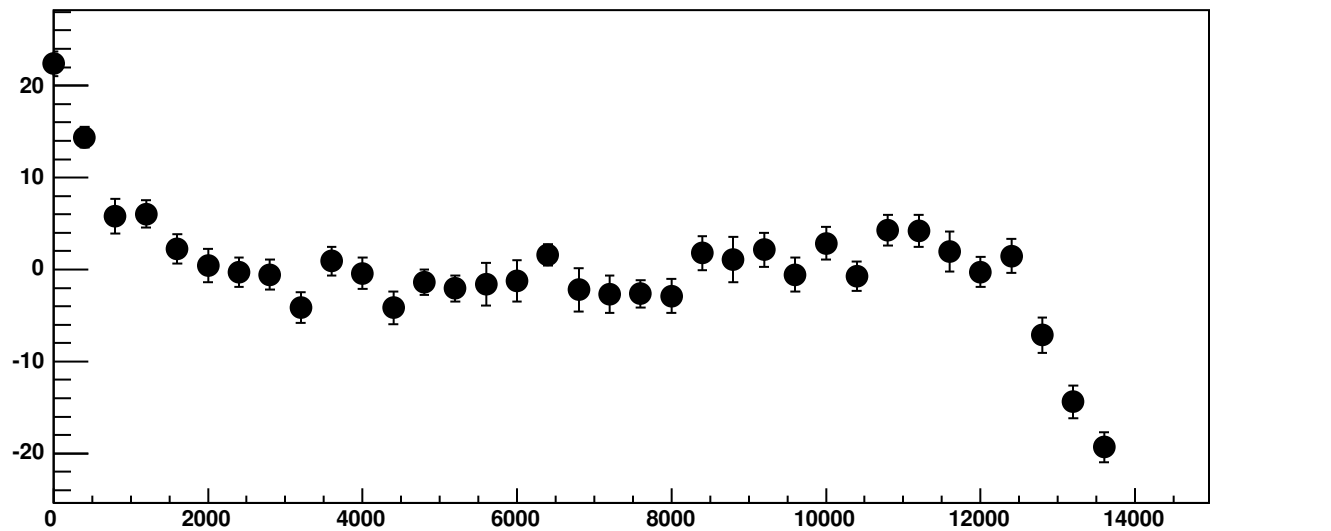
Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

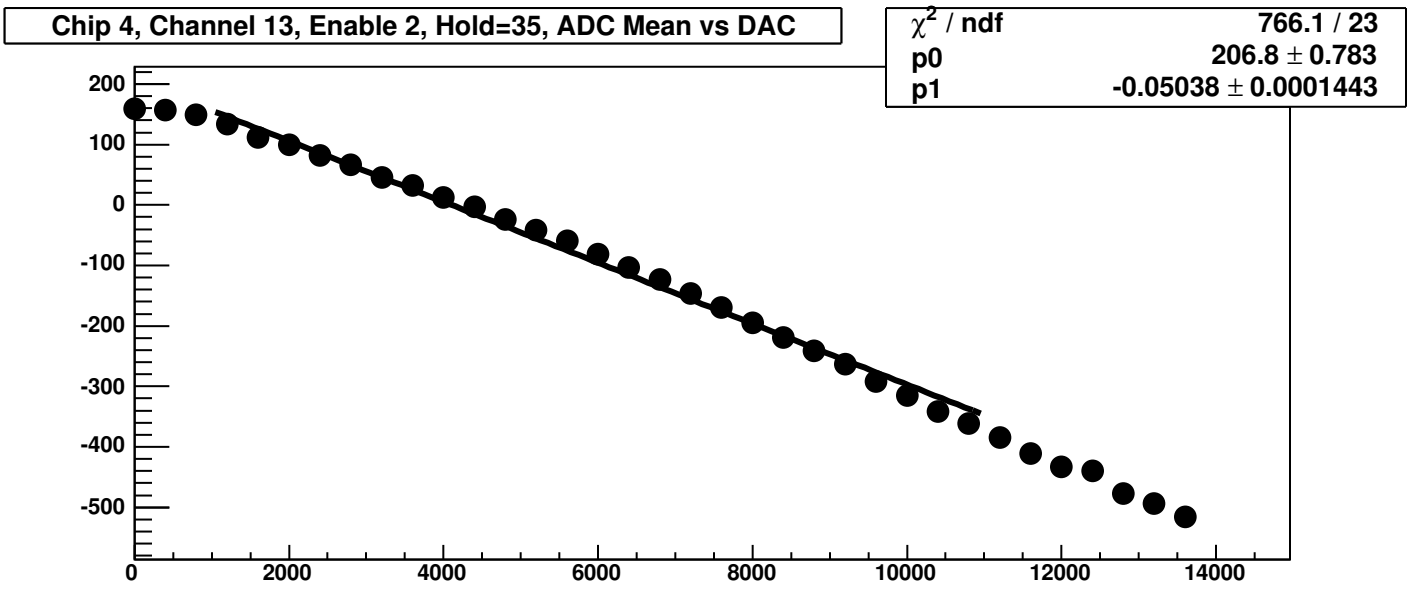


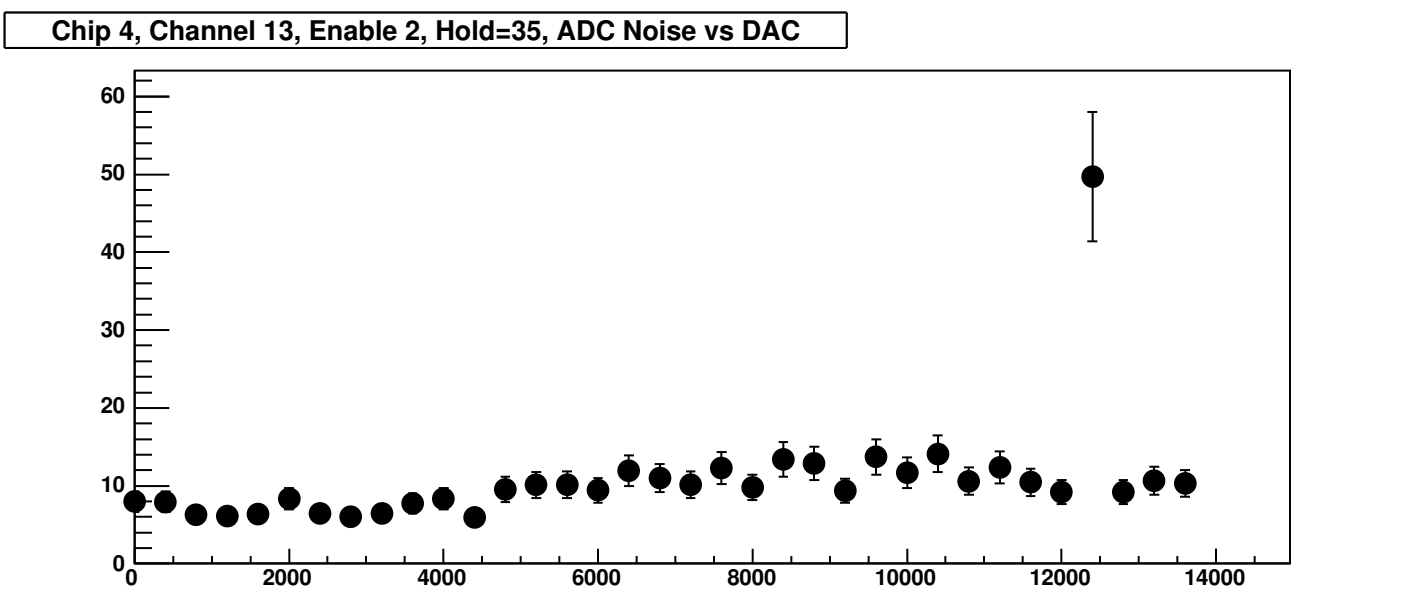
Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

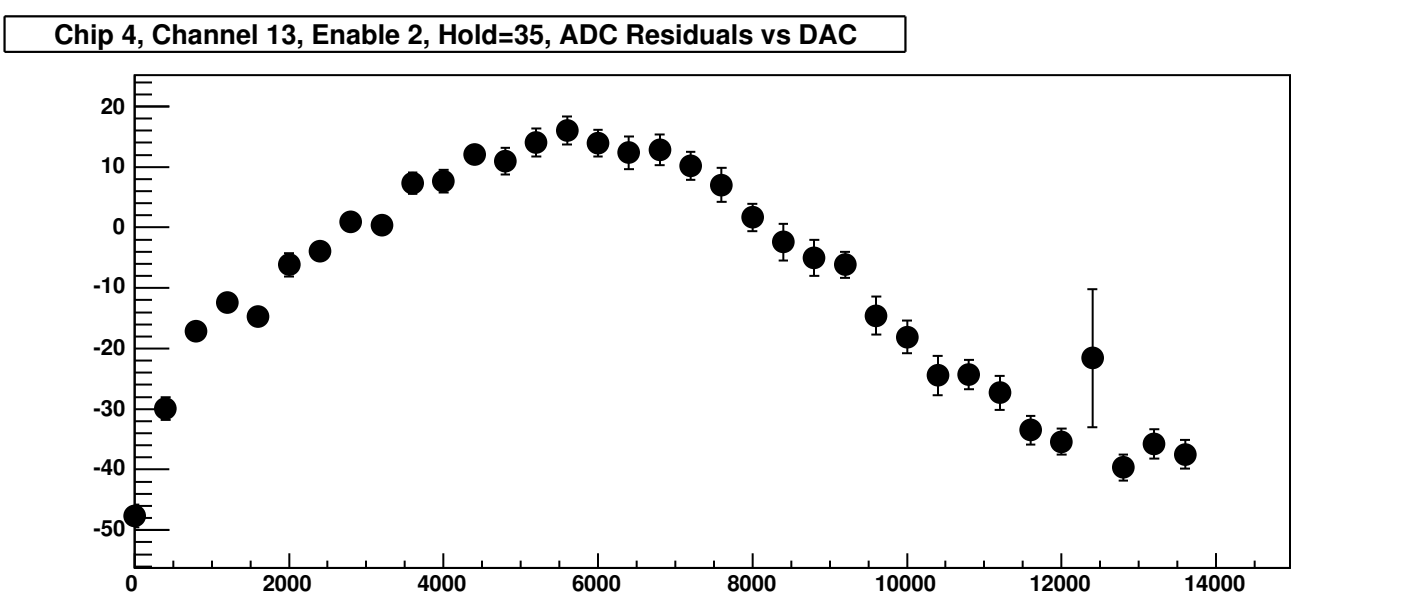


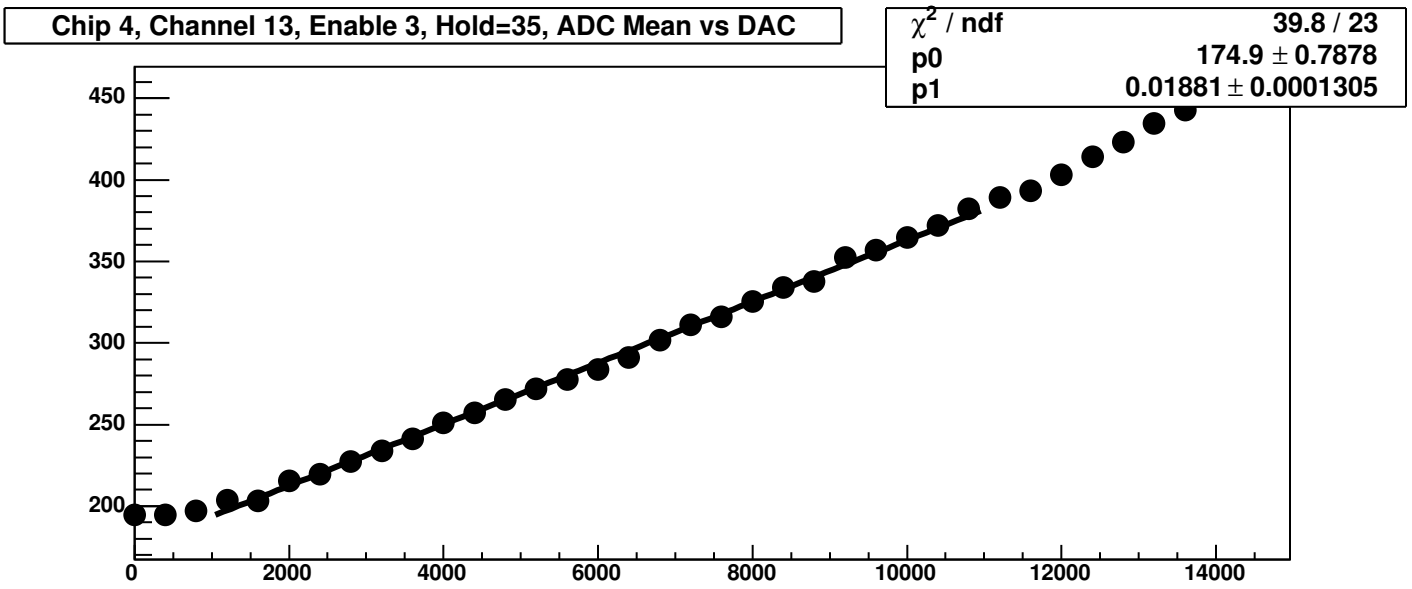
Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

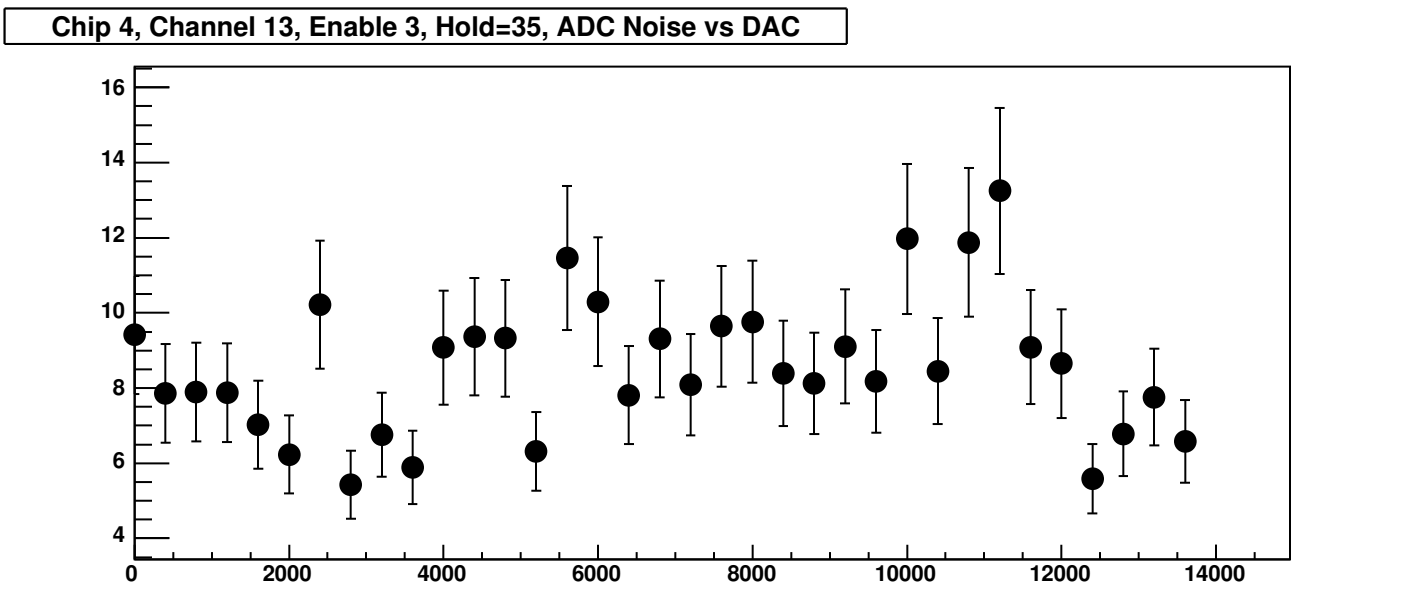


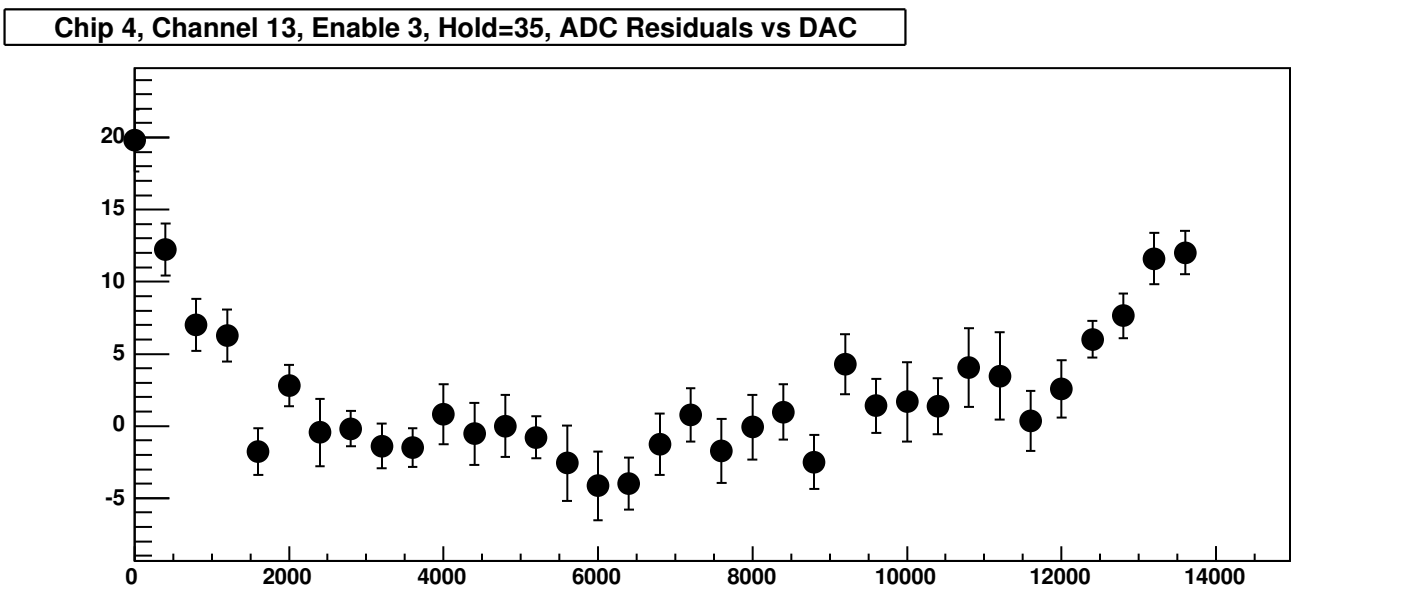

Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 766.1 / 23
p0 206.8 ± 0.783
p1 -0.05038 ± 0.0001443
200
100
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

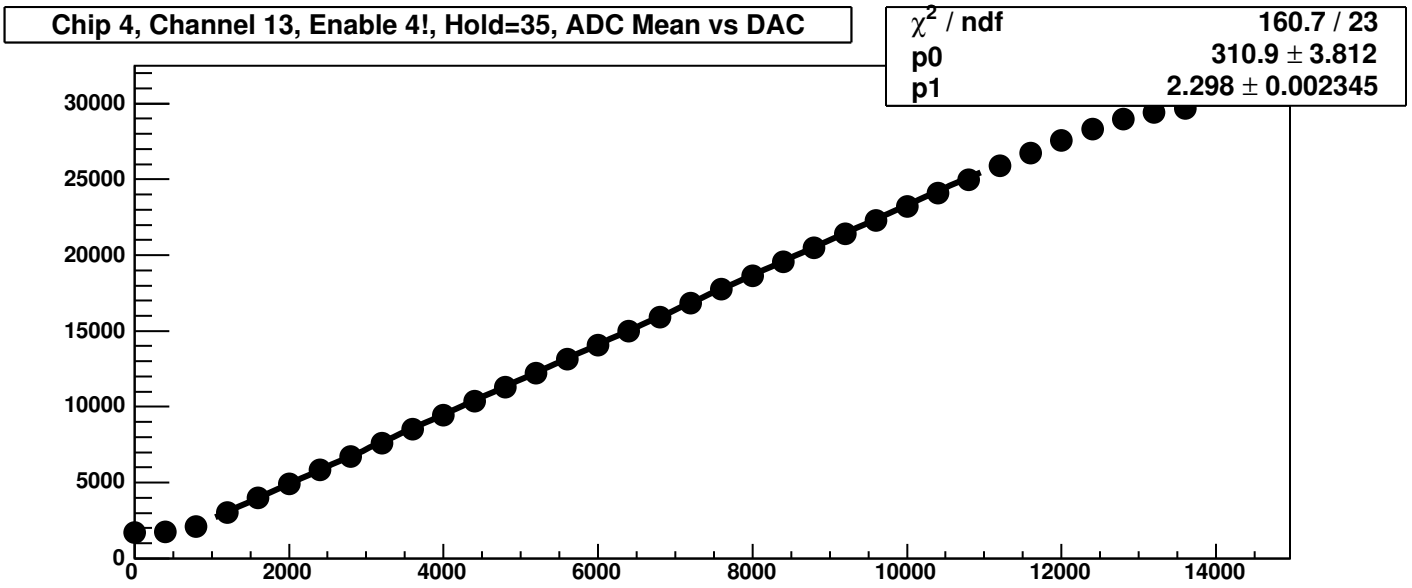

Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

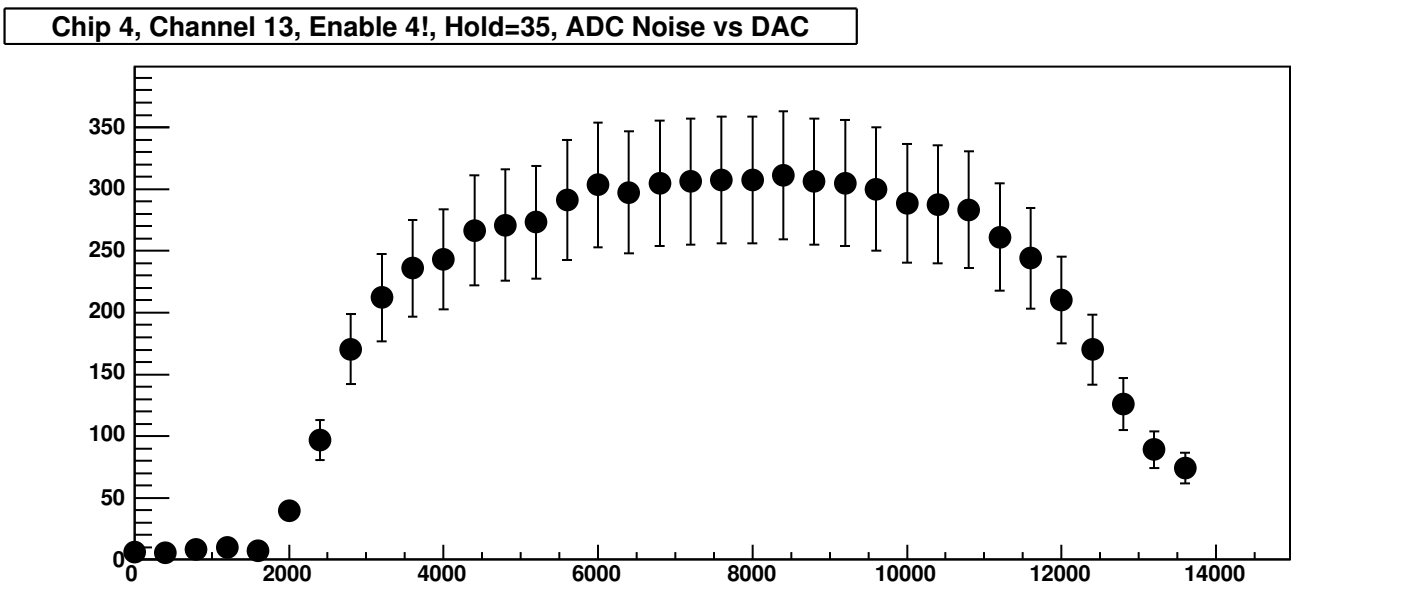

Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

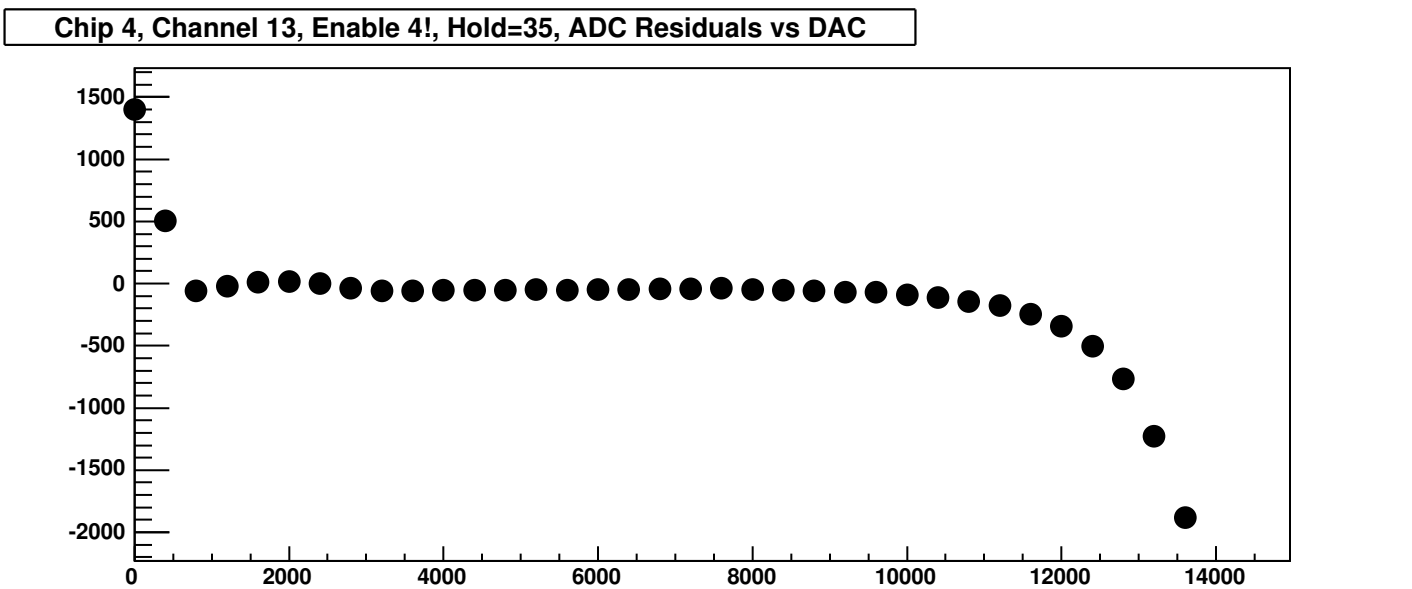

Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.8 / 23
p0 174.9 ± 0.7878
p1 0.01881 ± 0.0001305
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

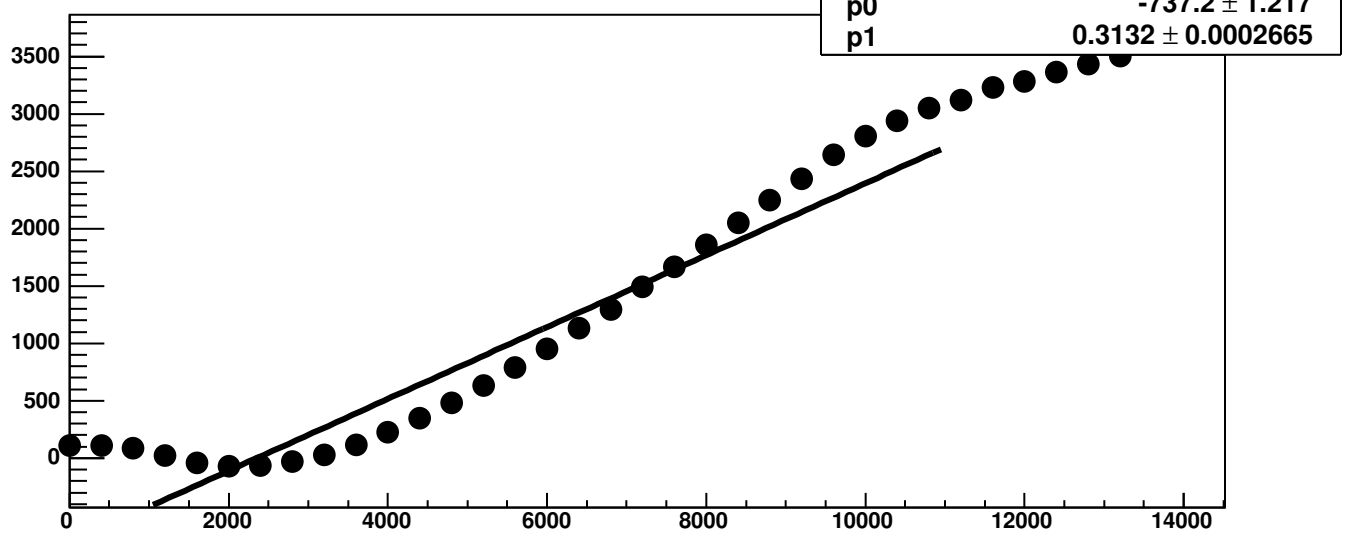

Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 160.7 / 23
p0 310.9 ± 3.812
p1 2.298 ± 0.002345
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

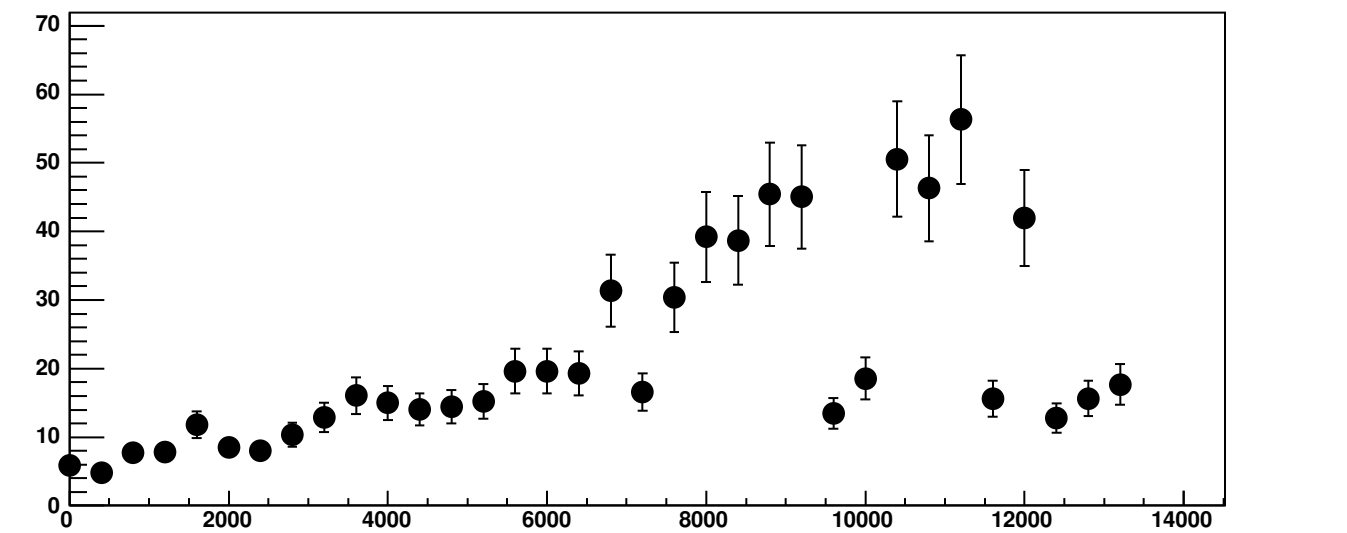

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

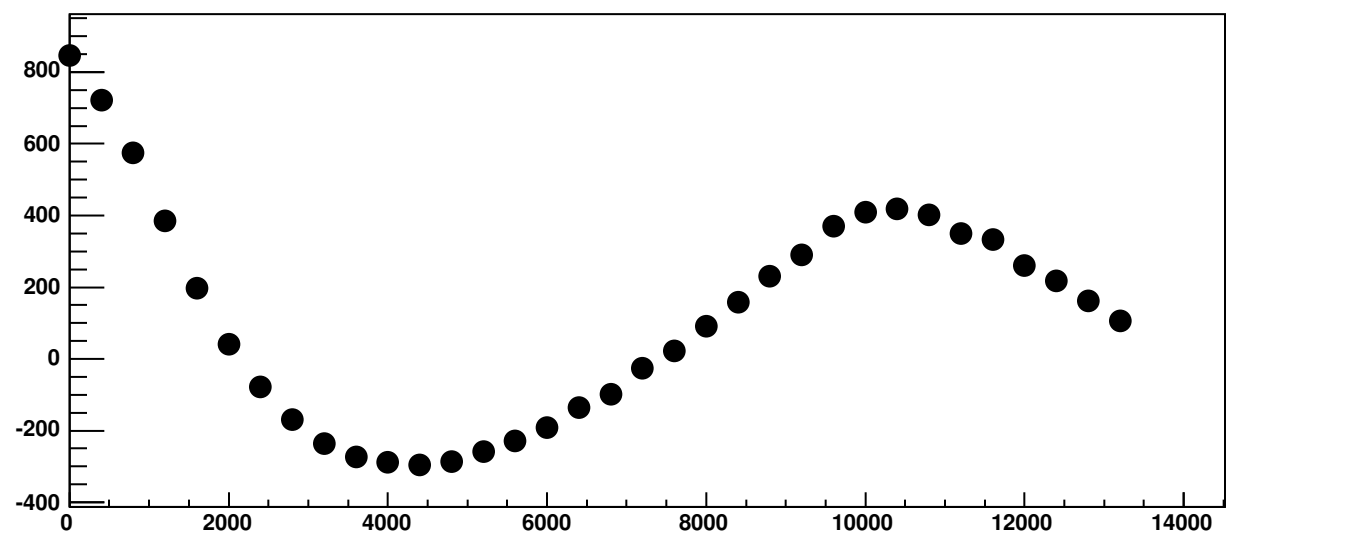
Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

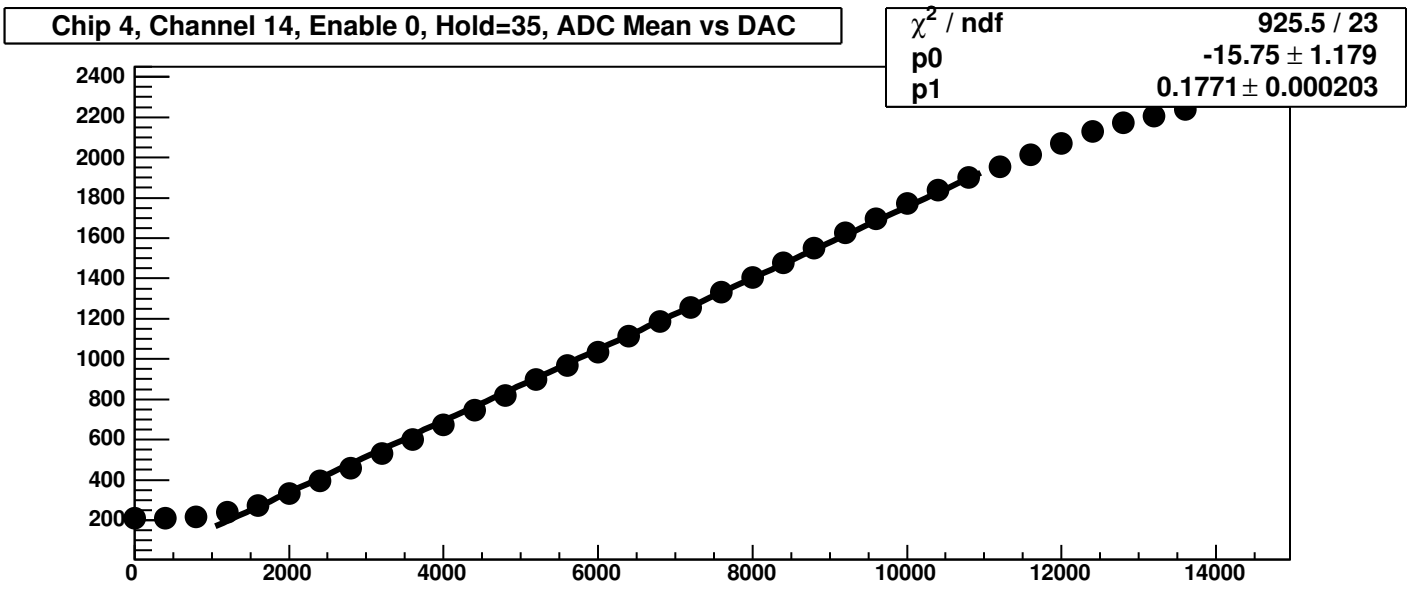


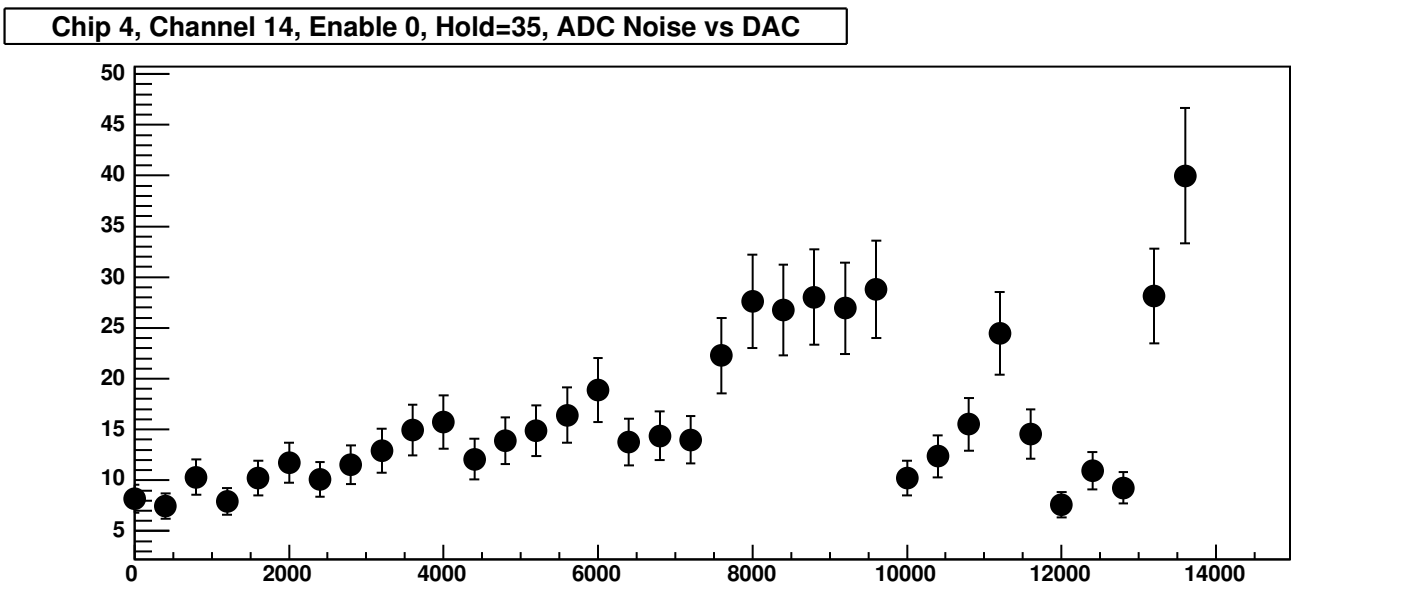
Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

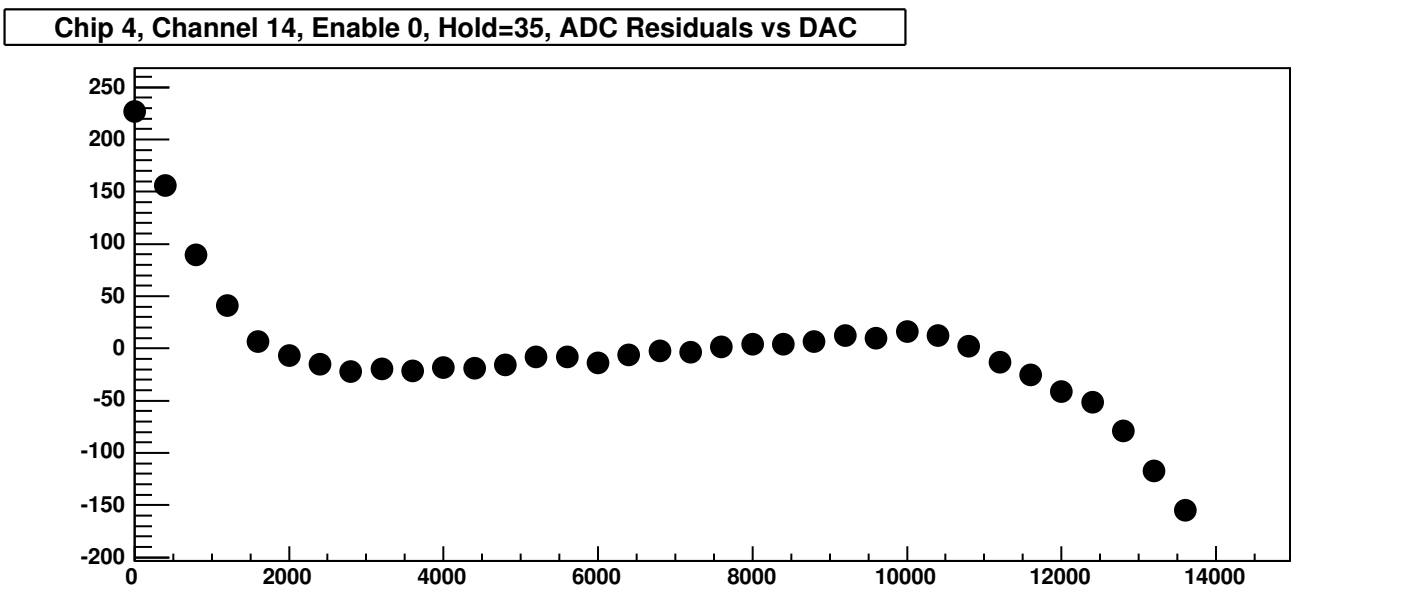


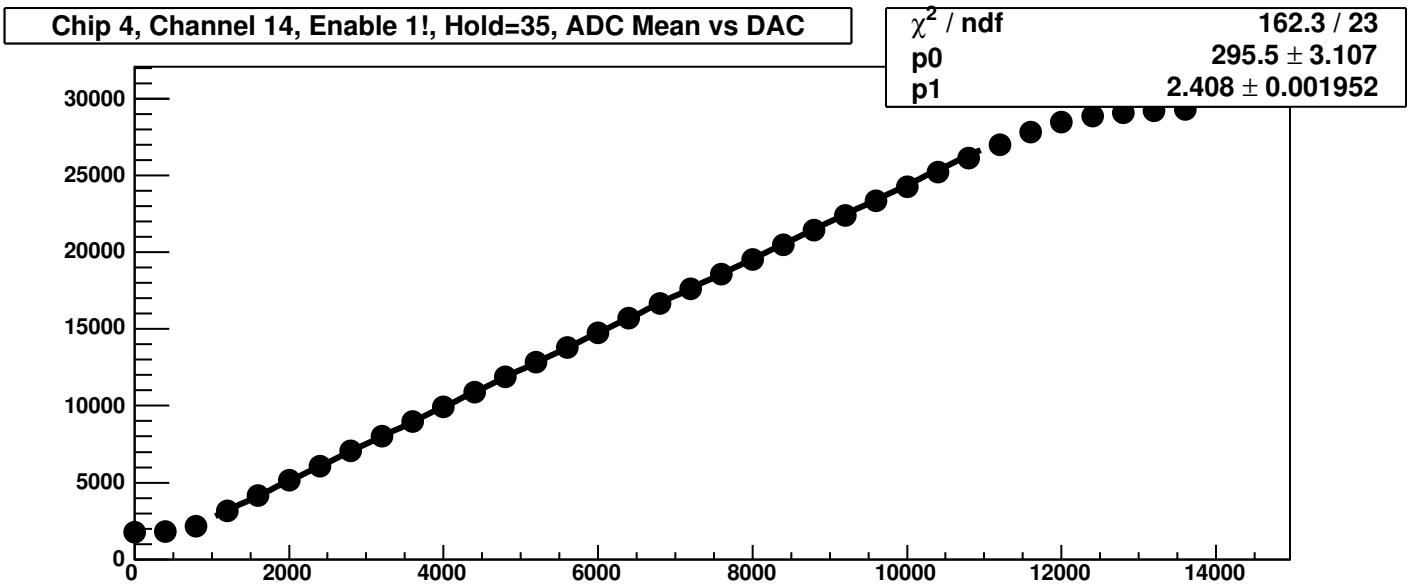
Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

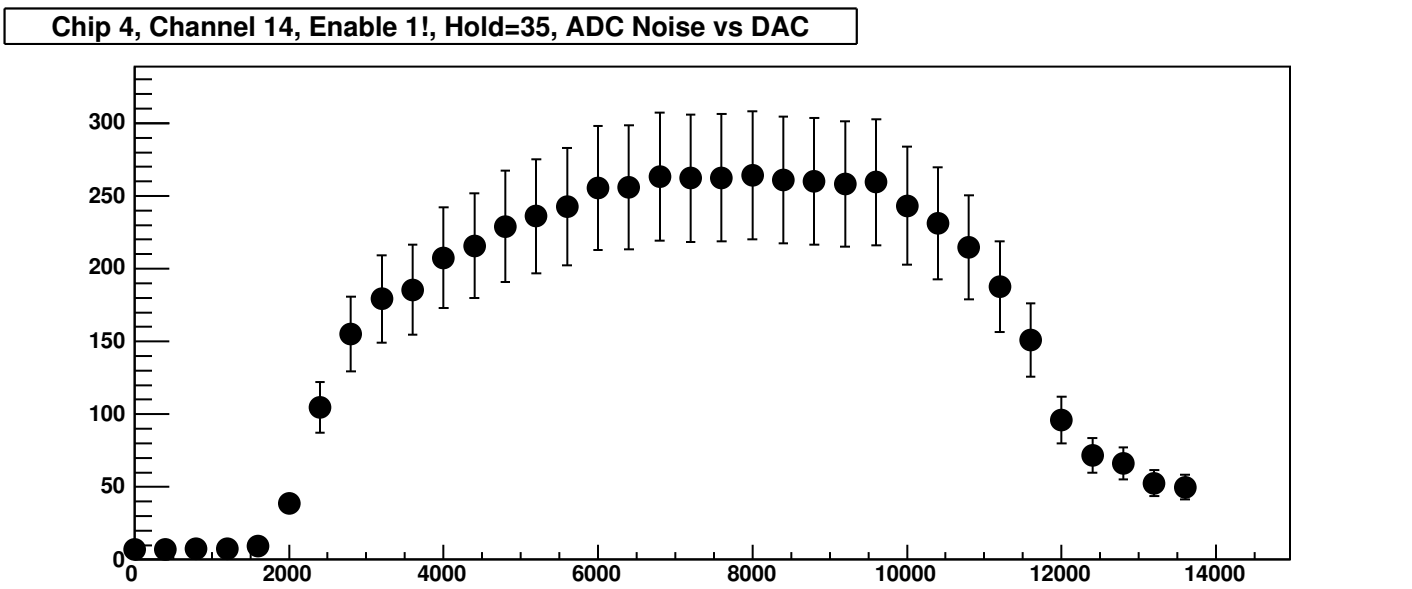


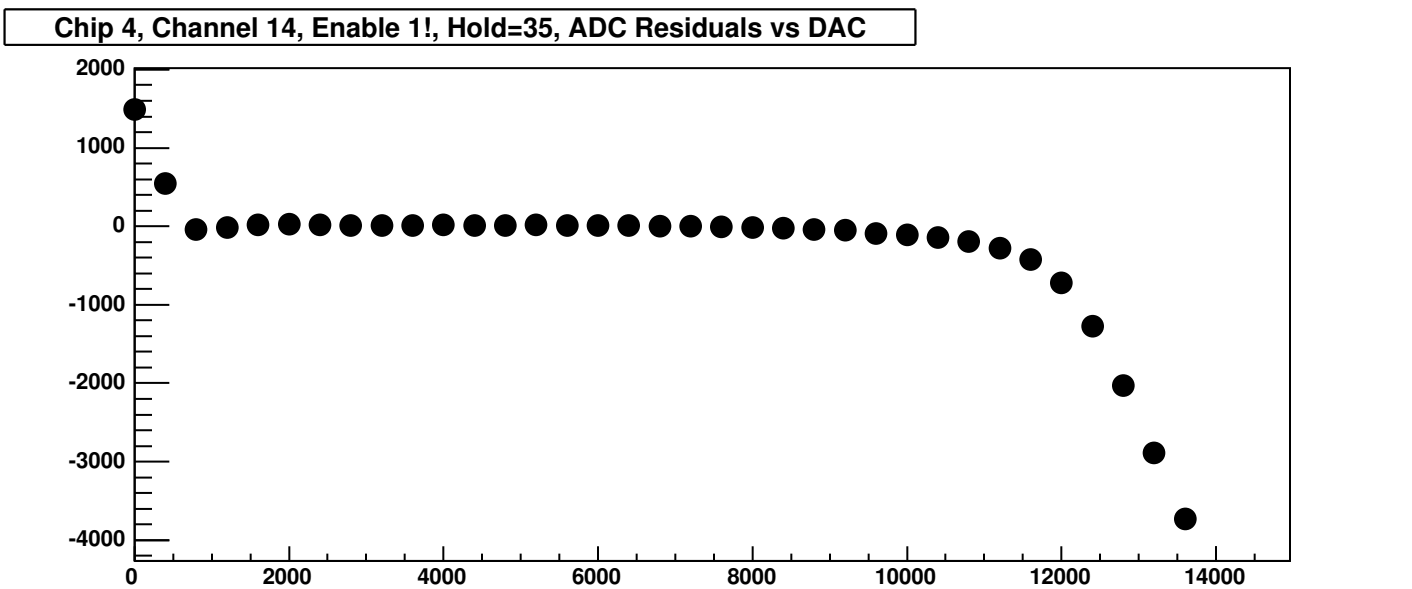

Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 925.5 / 23
p0 -15.75 ± 1.179
p1 0.1771 ± 0.000203
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

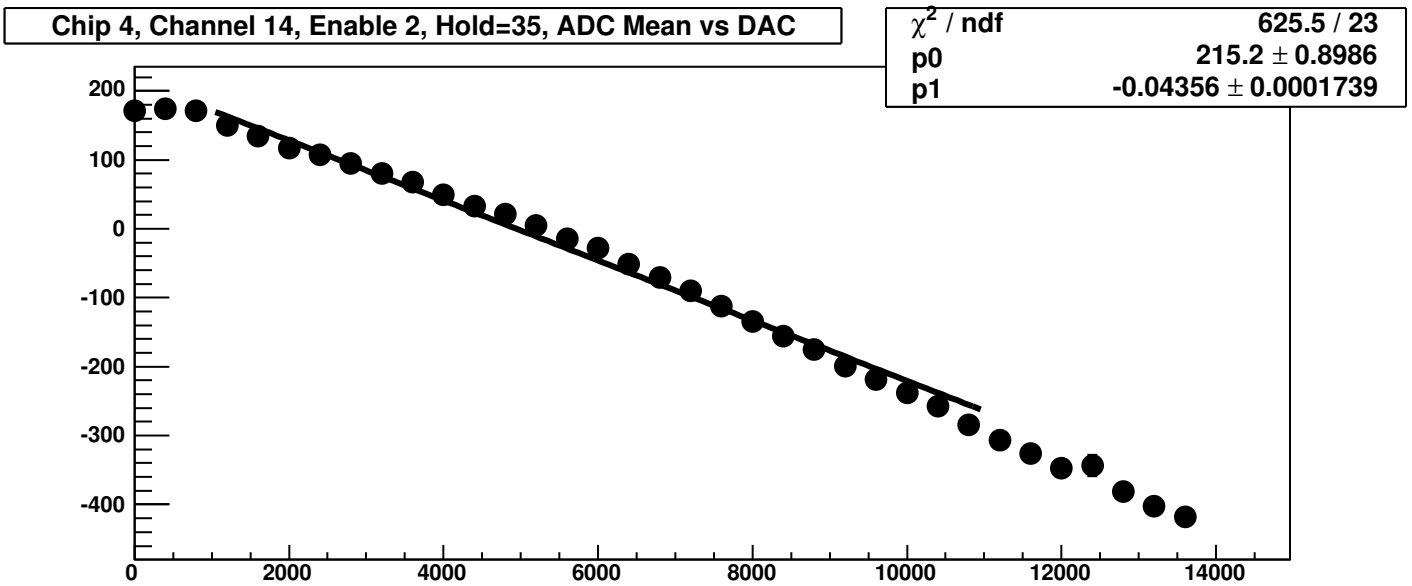

Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

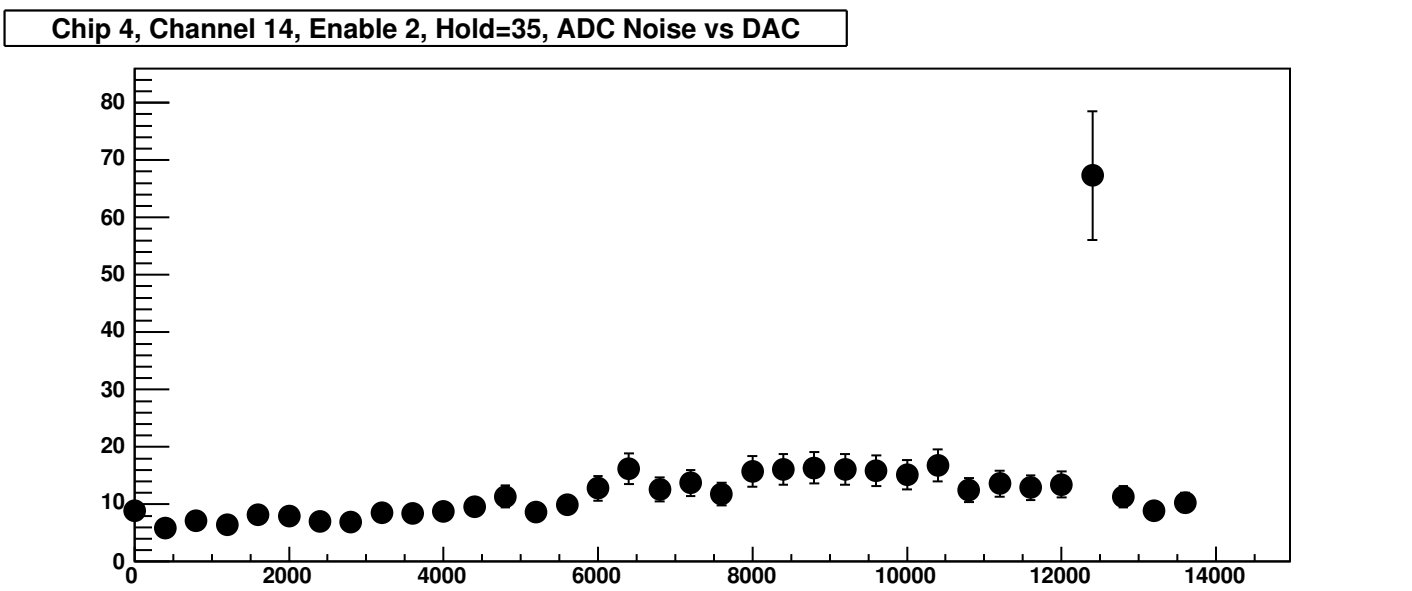

Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

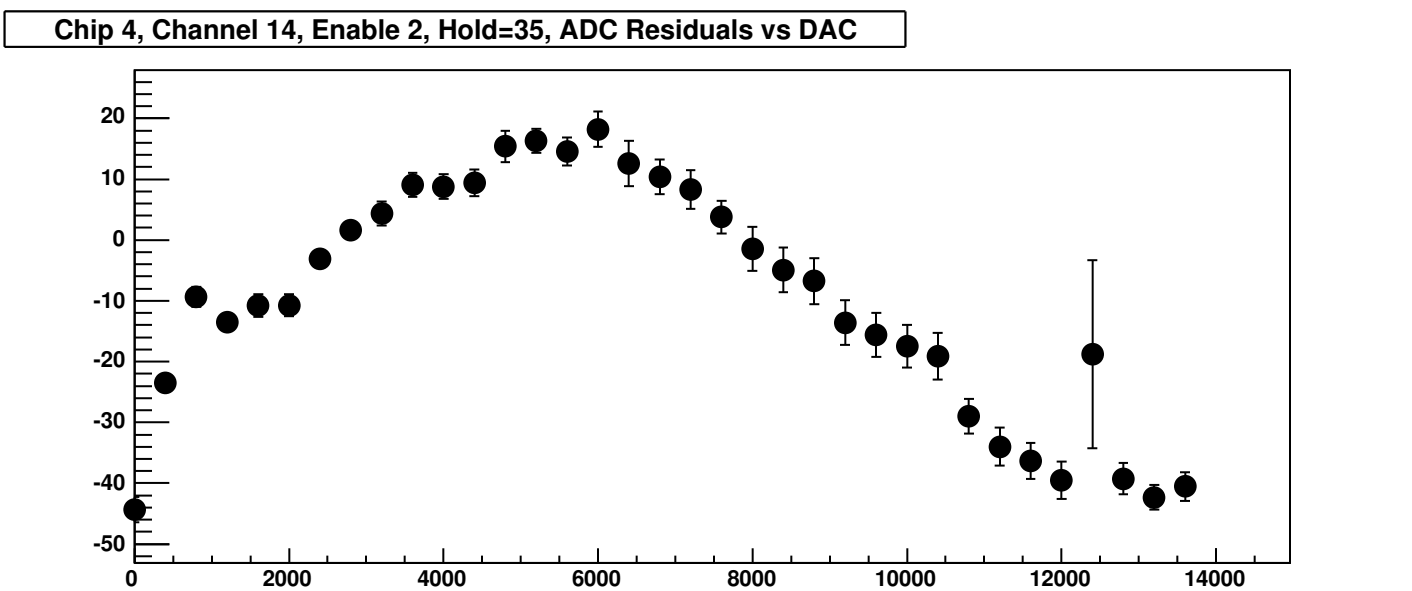

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 162.3 / 23
p0 295.5 ± 3.107
p1 2.408 ± 0.001952
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

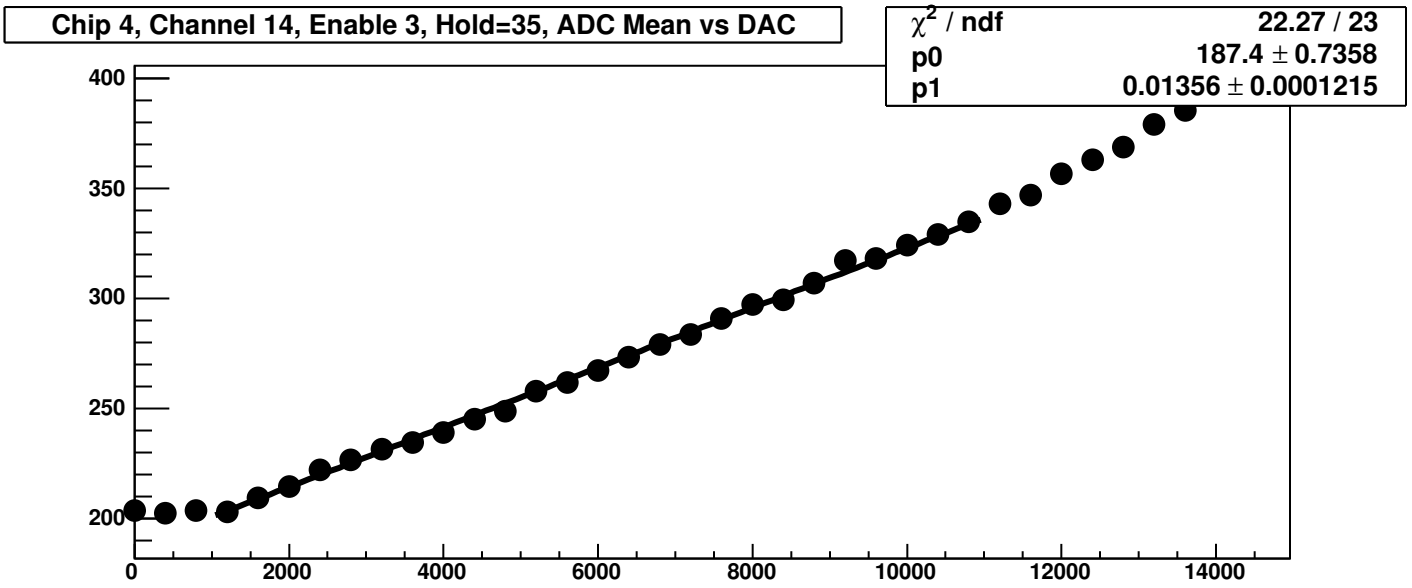

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

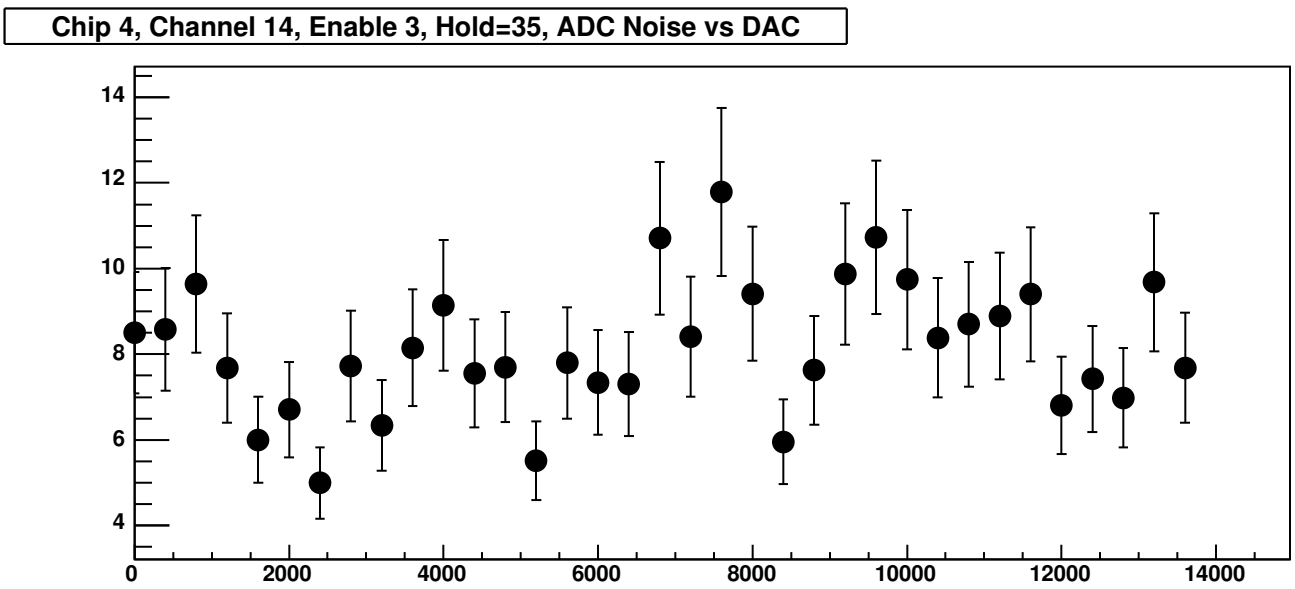

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

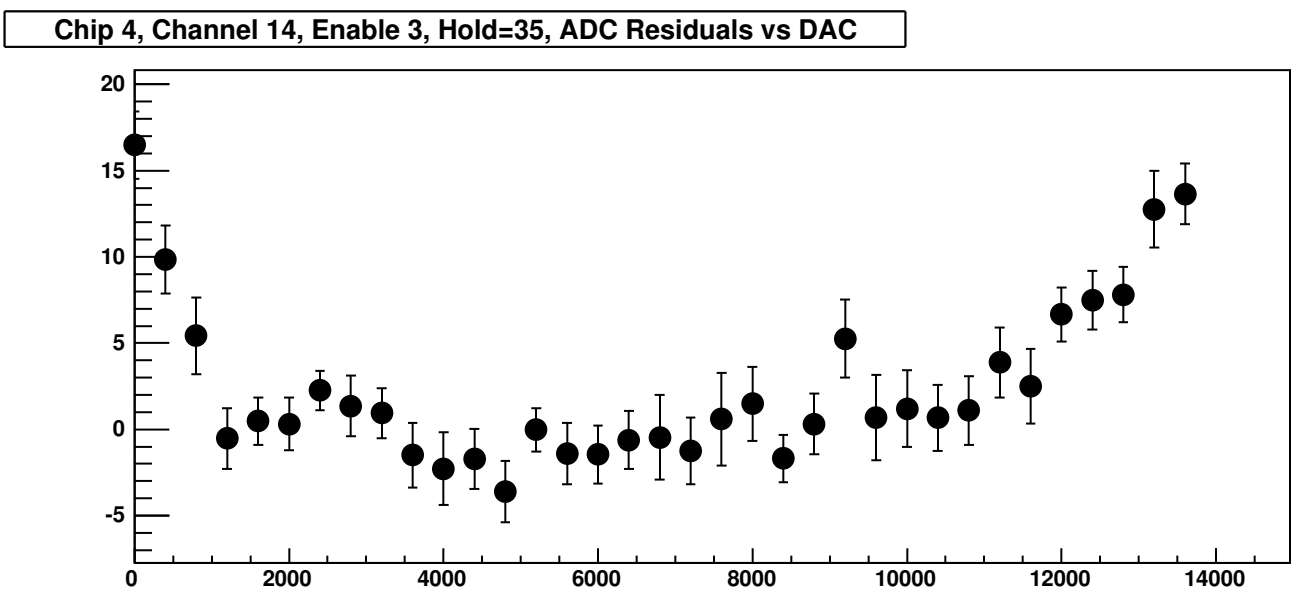

Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 625.5 / 23
p0 215.2 ± 0.8986
p1 -0.04356 ± 0.0001739
200
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

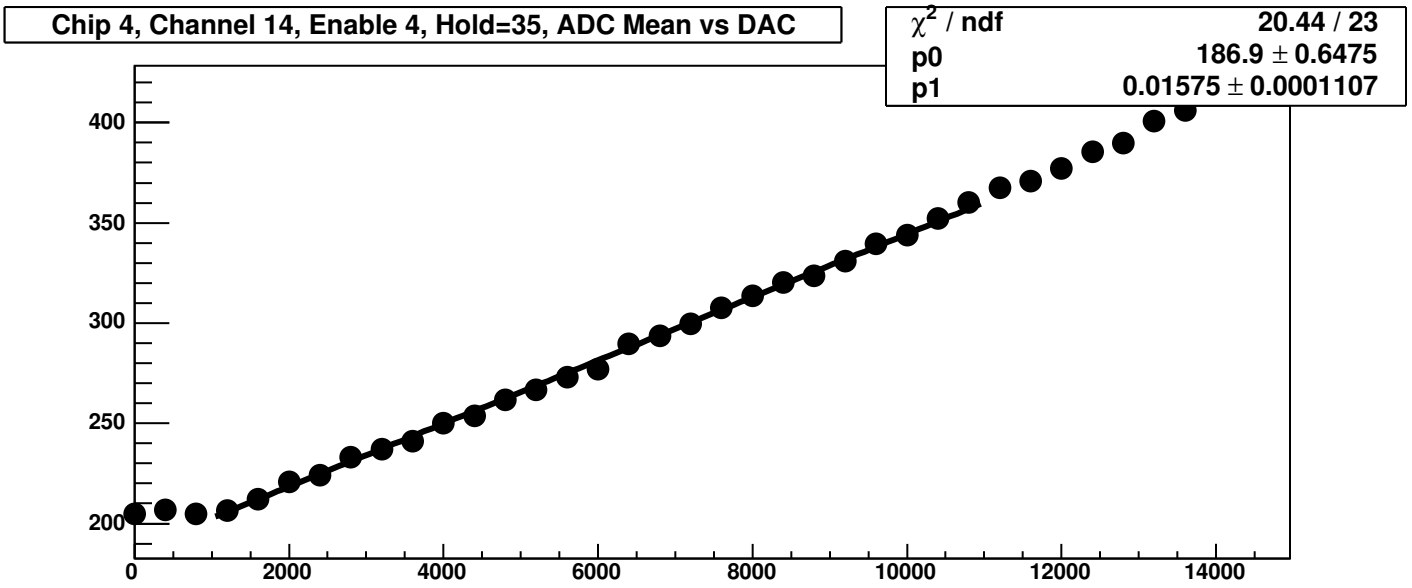

Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

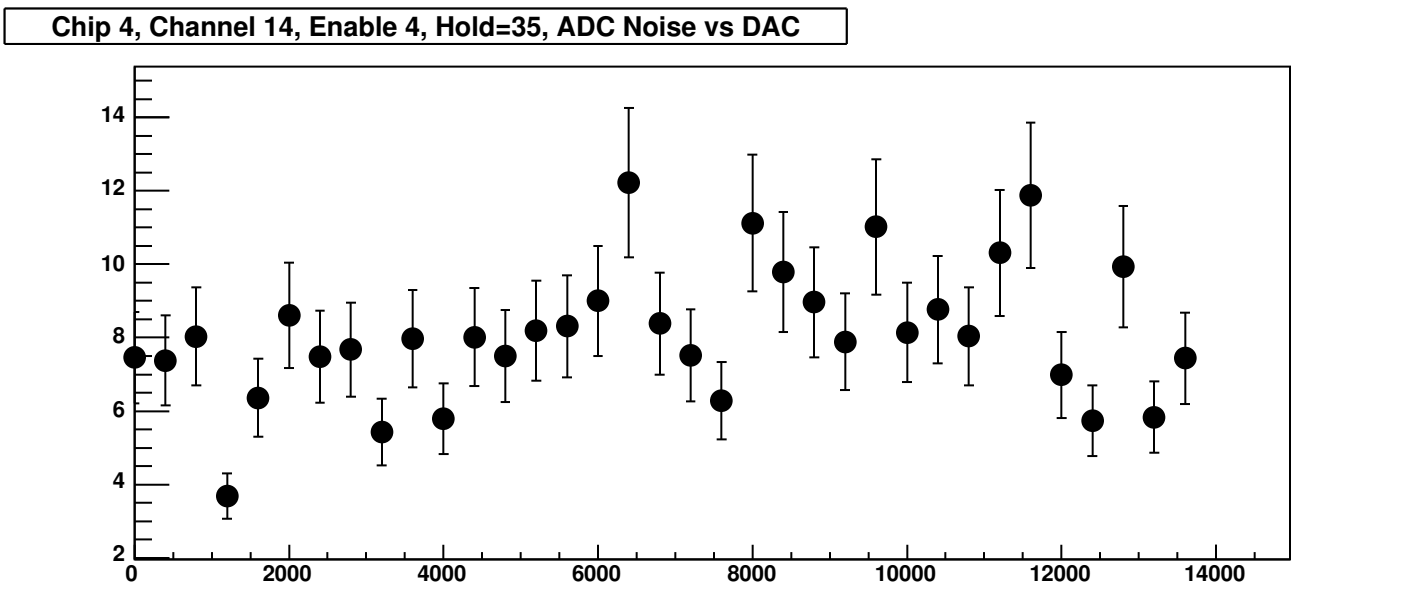

Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

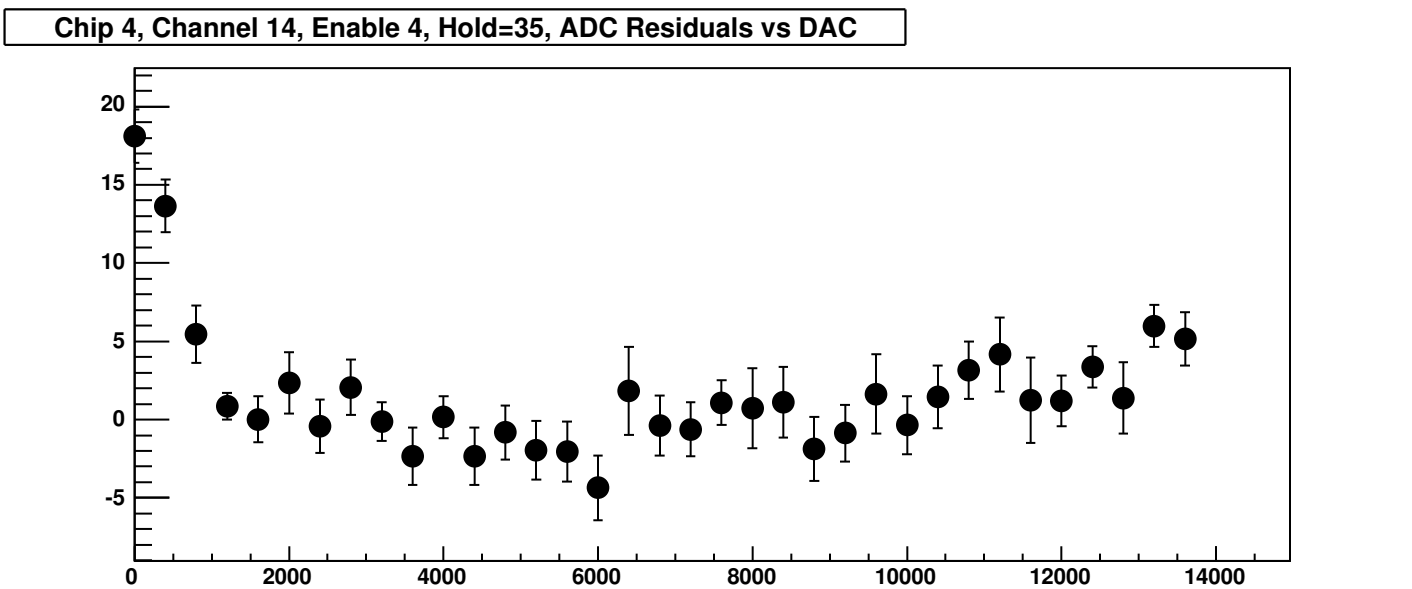

Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.27 / 23
p0 187.4 ± 0.7358
p1 0.01356 ± 0.0001215
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

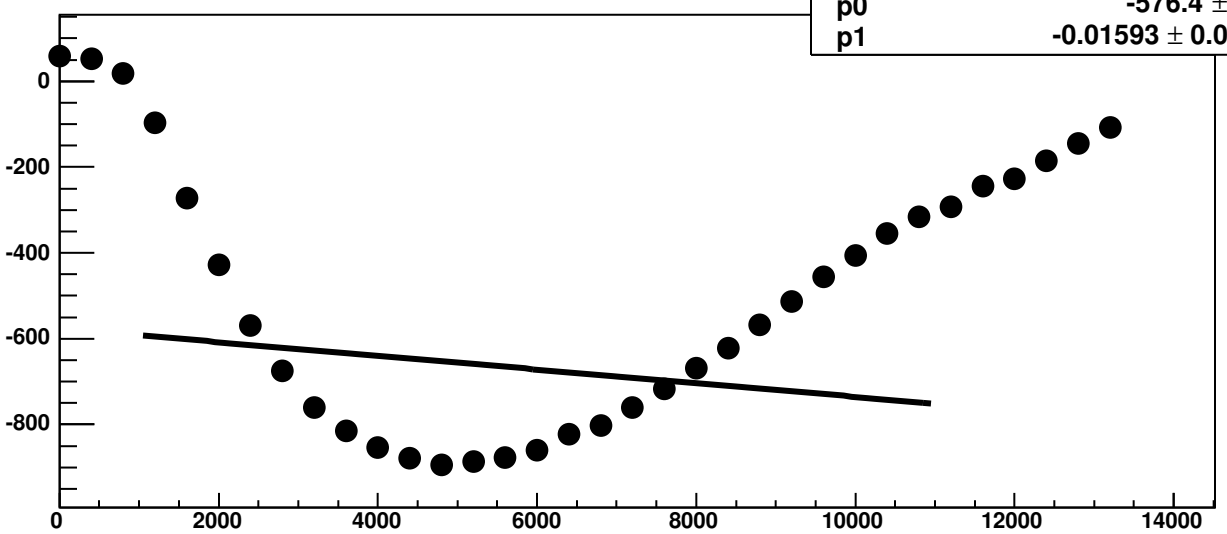

Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.44 / 23
p0 186.9 ± 0.6475
p1 0.01575 ± 0.0001107
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

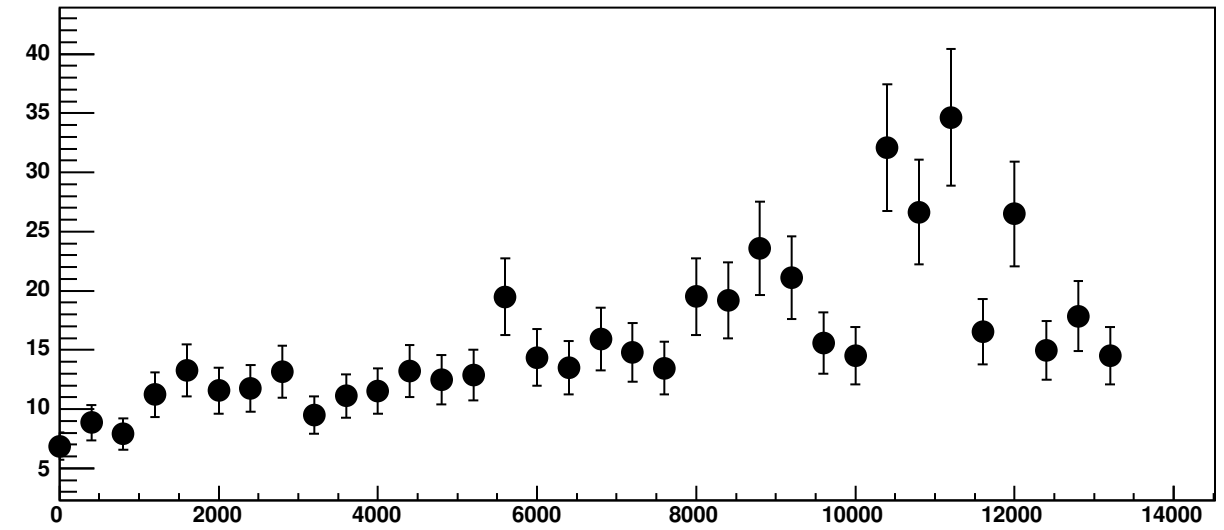

Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

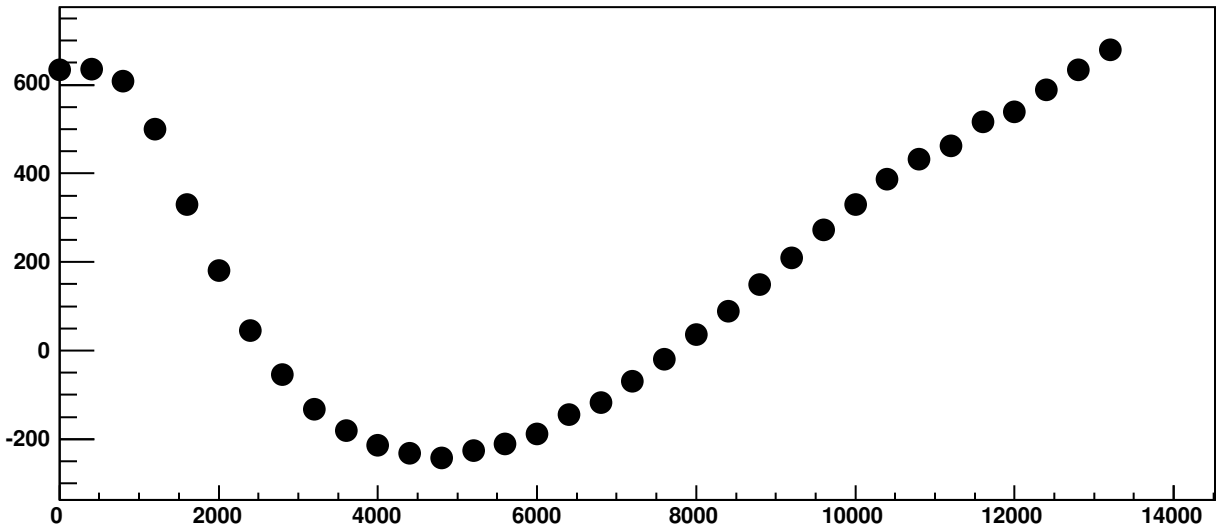
χ^2 / ndf	1.244e+05 / 23
p0	-576.4 ± 1.366
p1	-0.01593 ± 0.000245

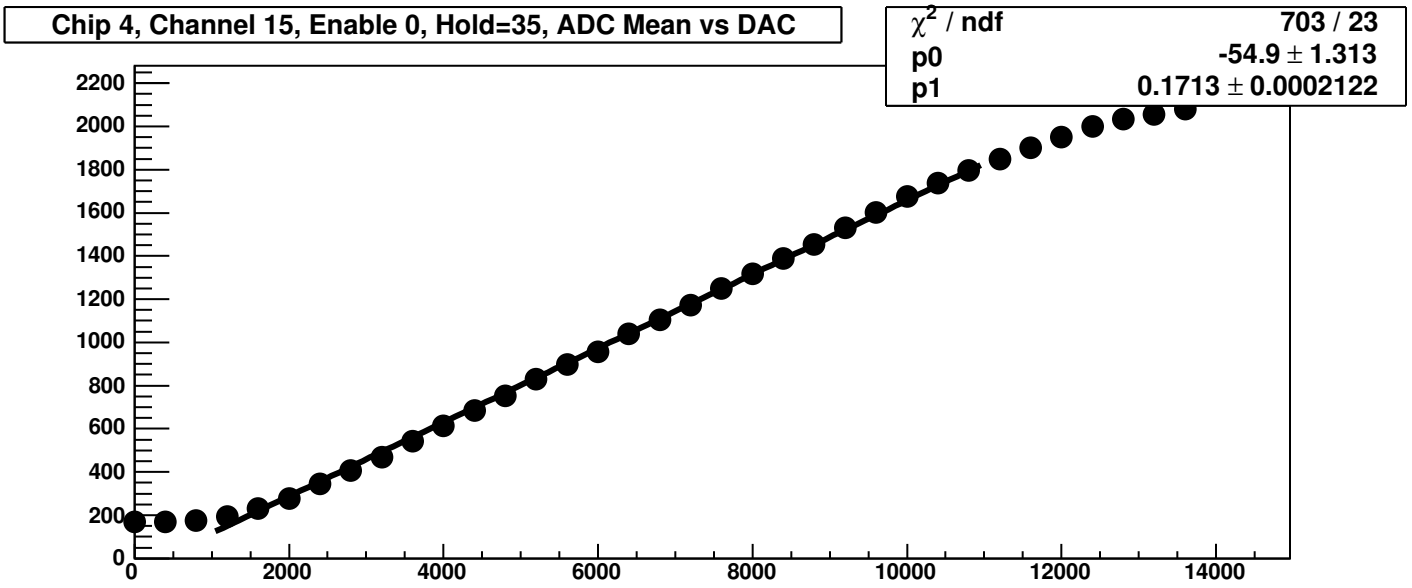


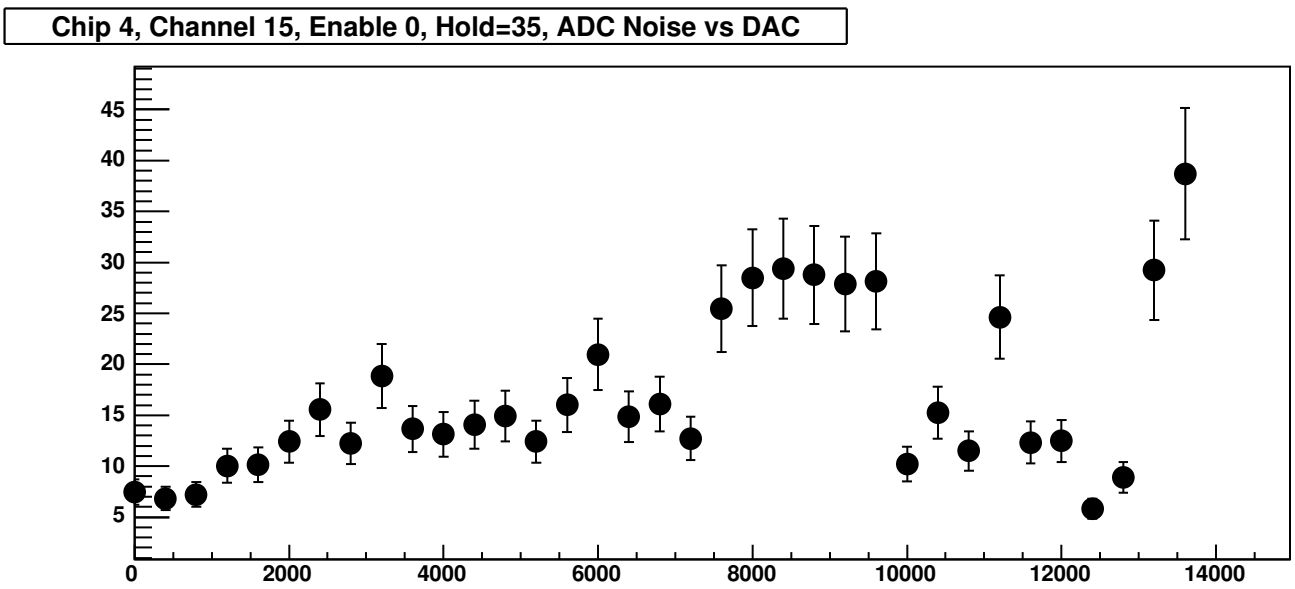
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

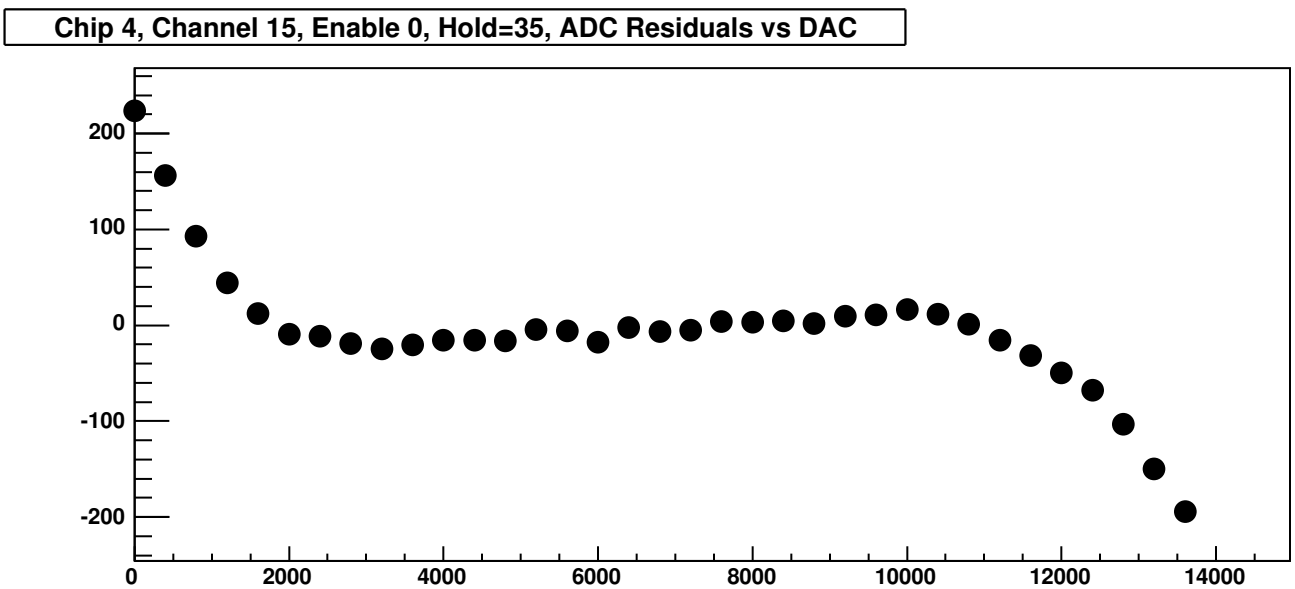


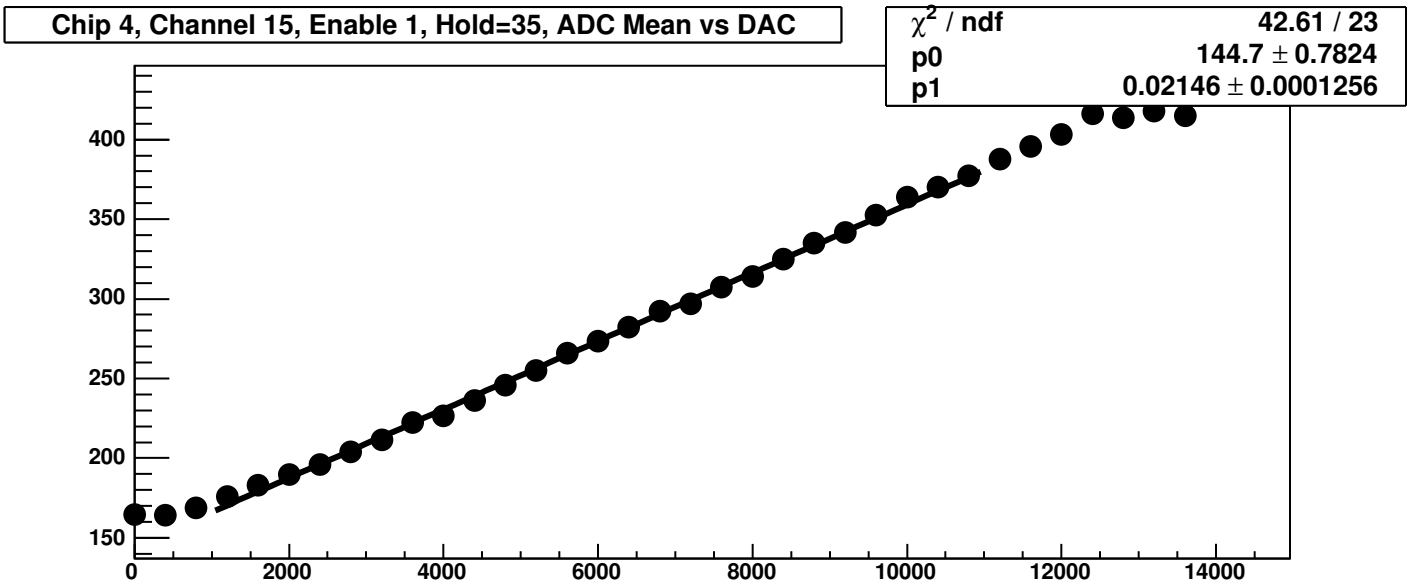
Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

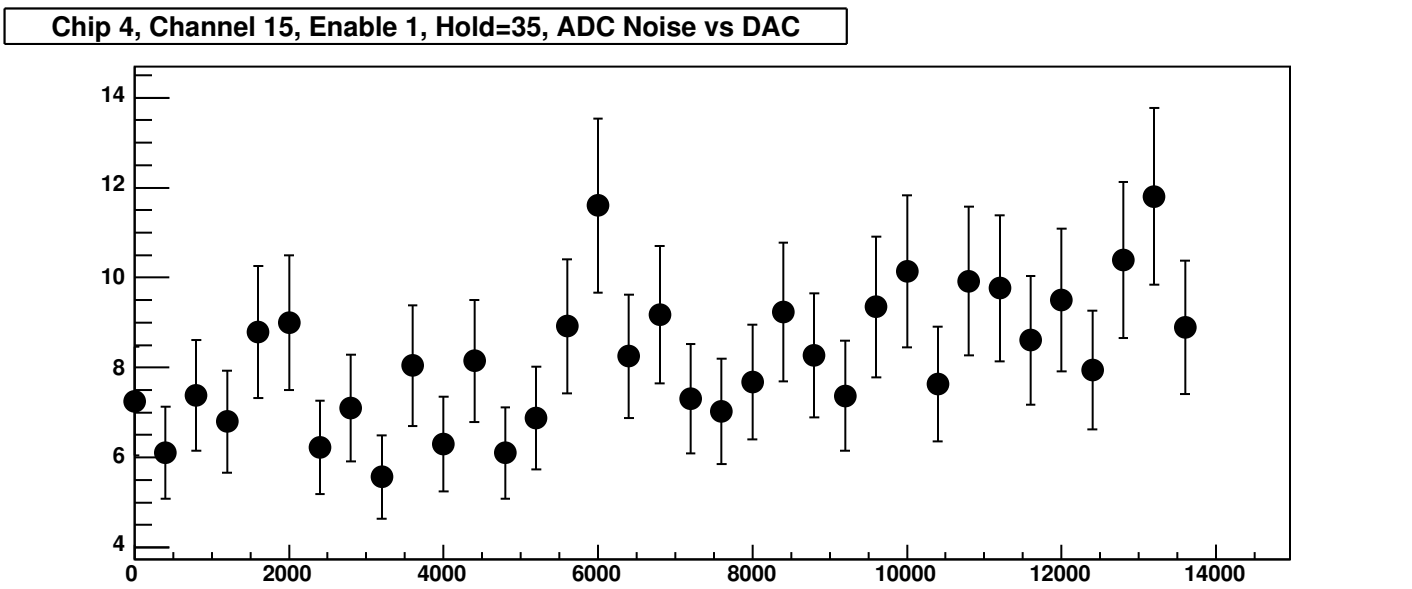


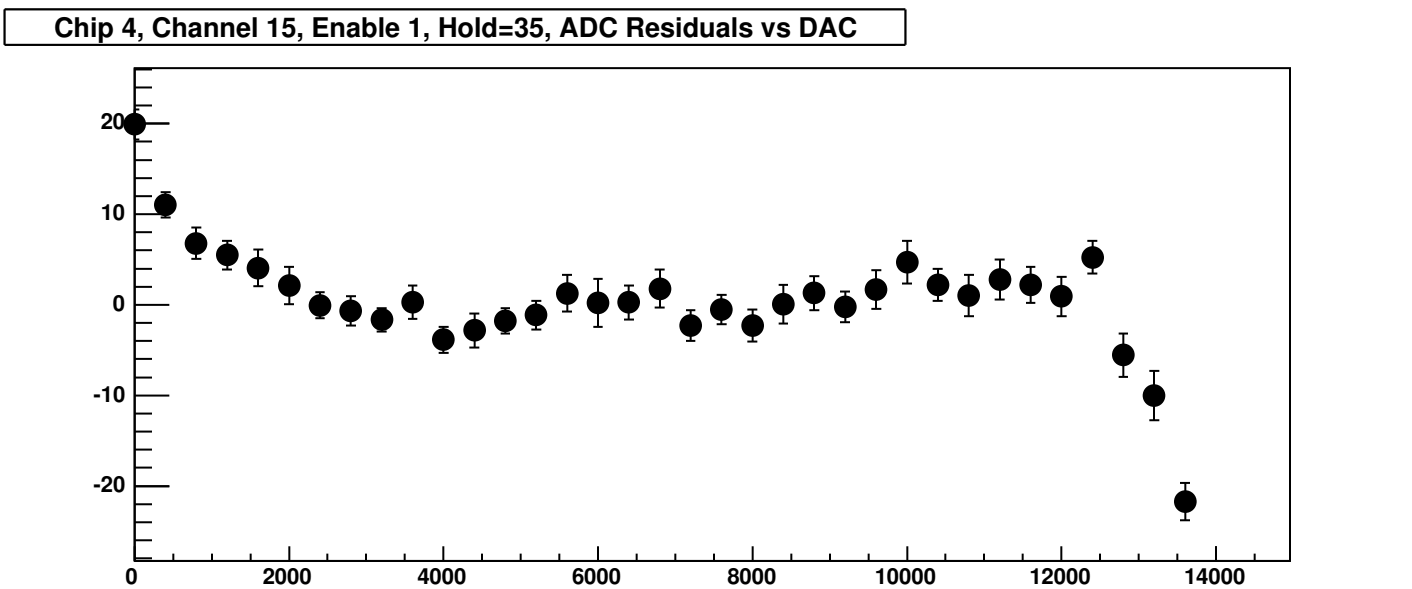

Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 703 / 23
p0 -54.9 ± 1.313
p1 0.1713 ± 0.0002122
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

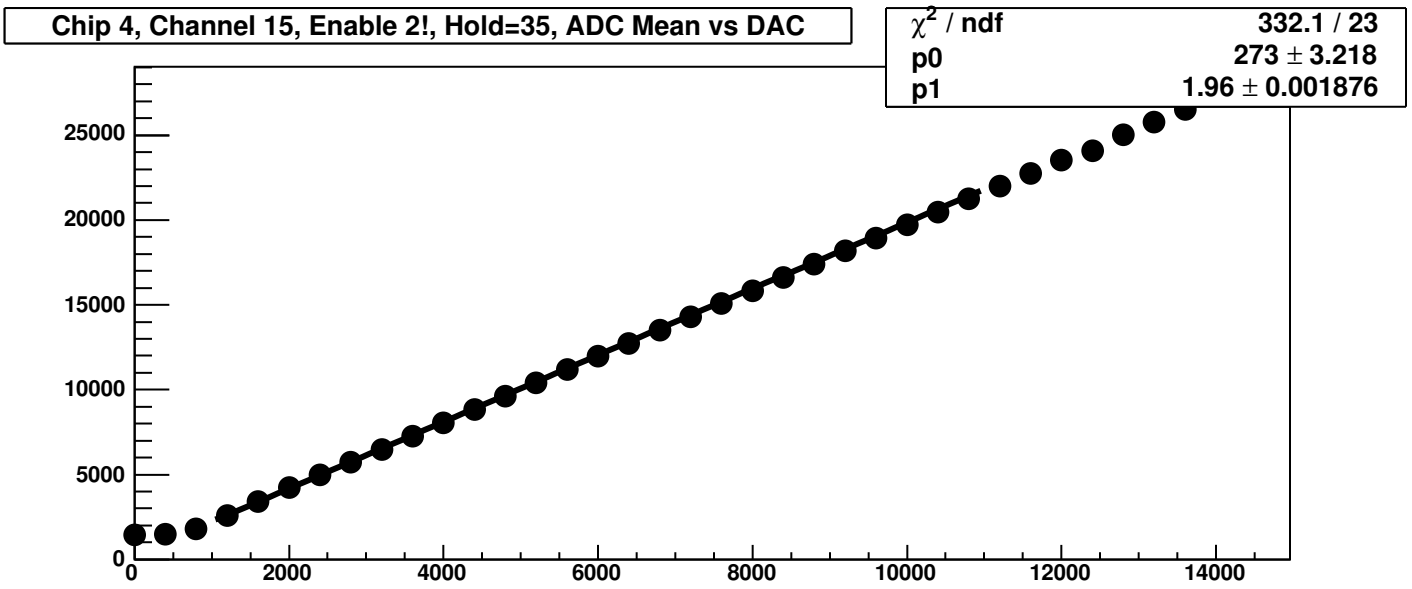

Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

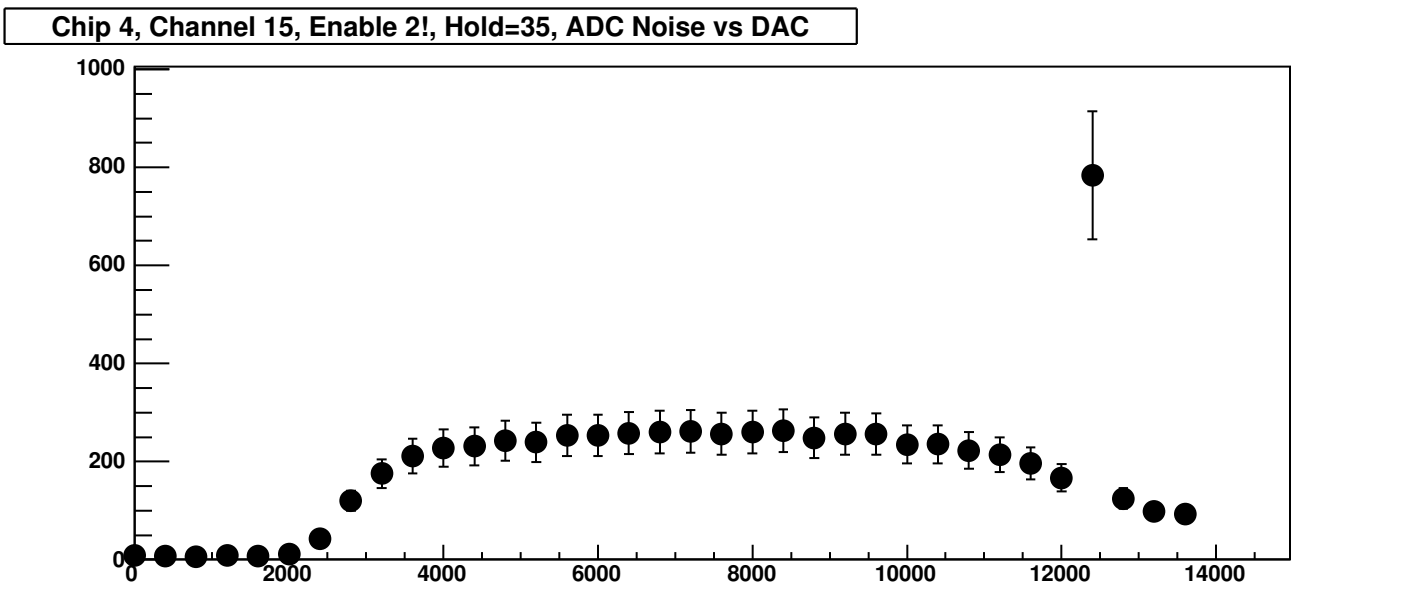

Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

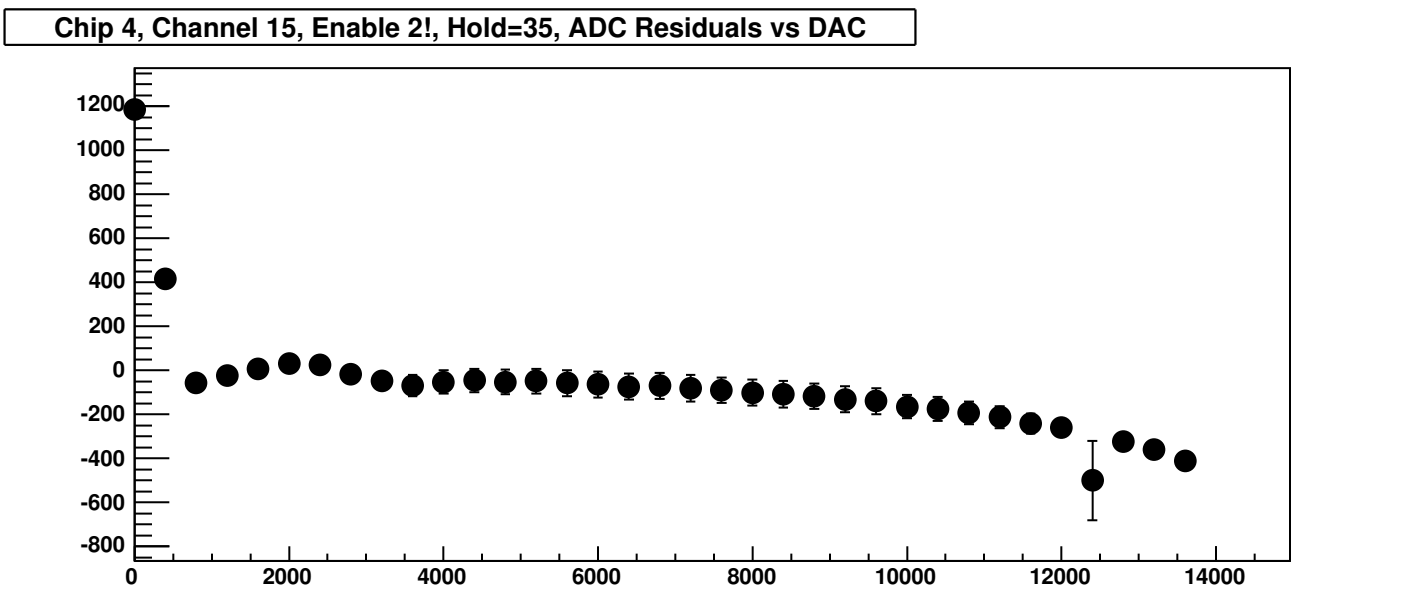

Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.61 / 23
p0 144.7 ± 0.7824
p1 0.02146 ± 0.0001256
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

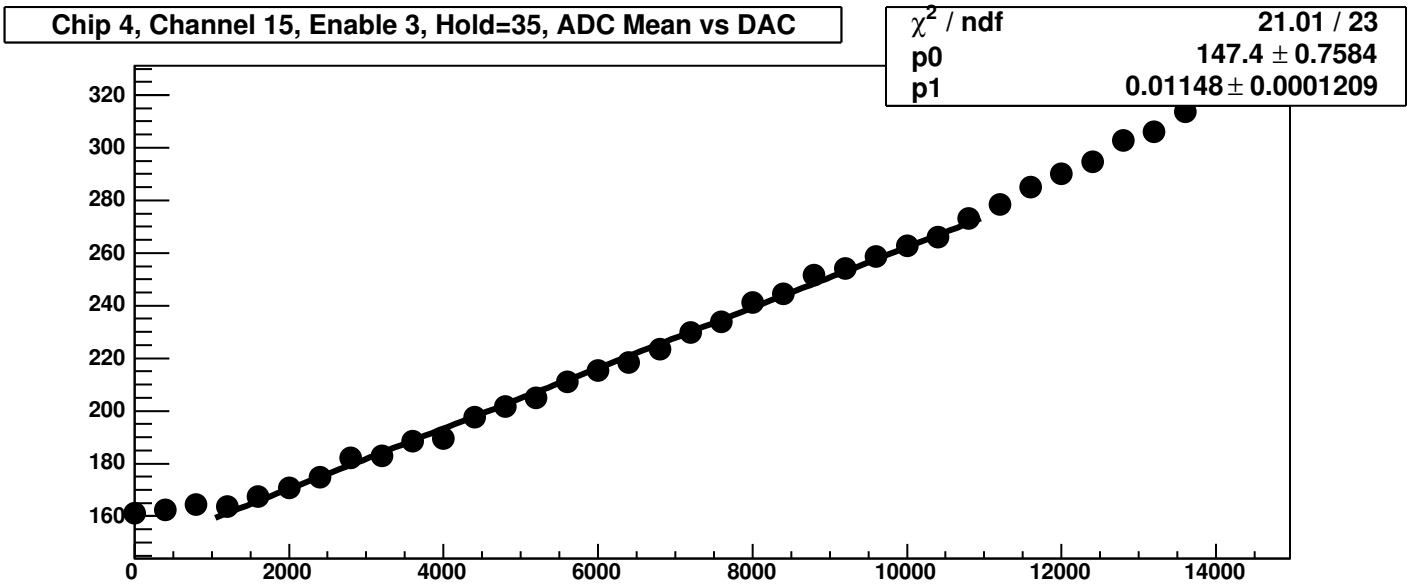

Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

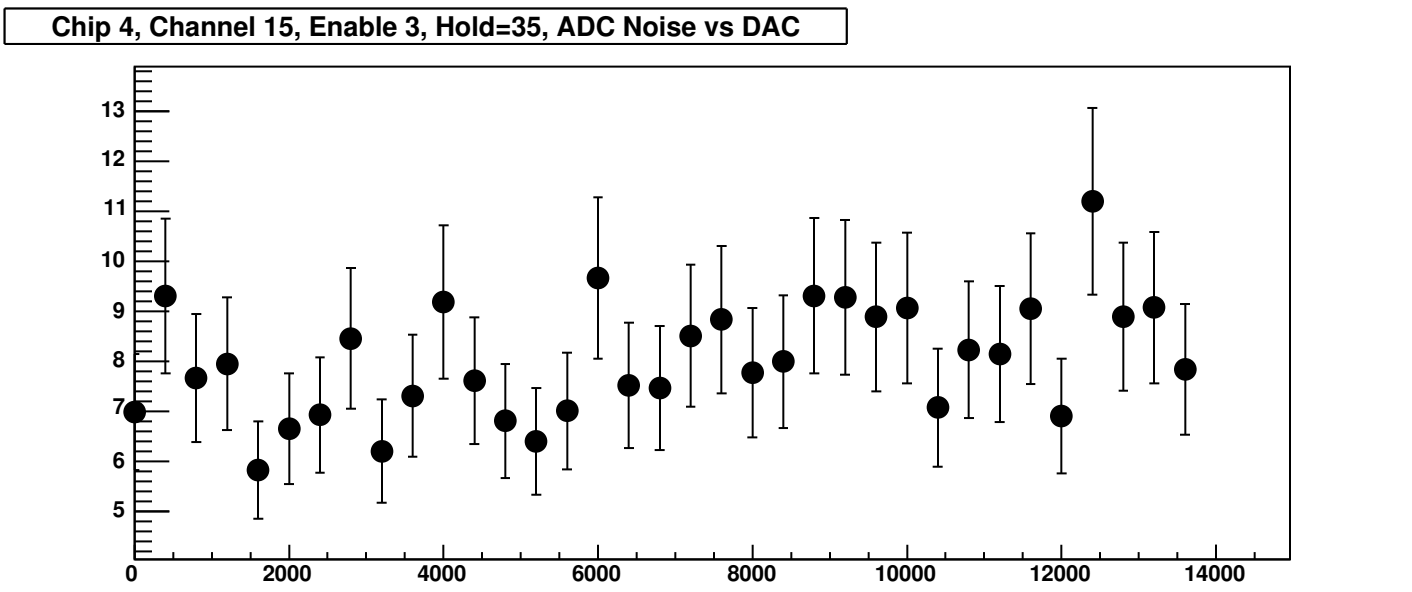

Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

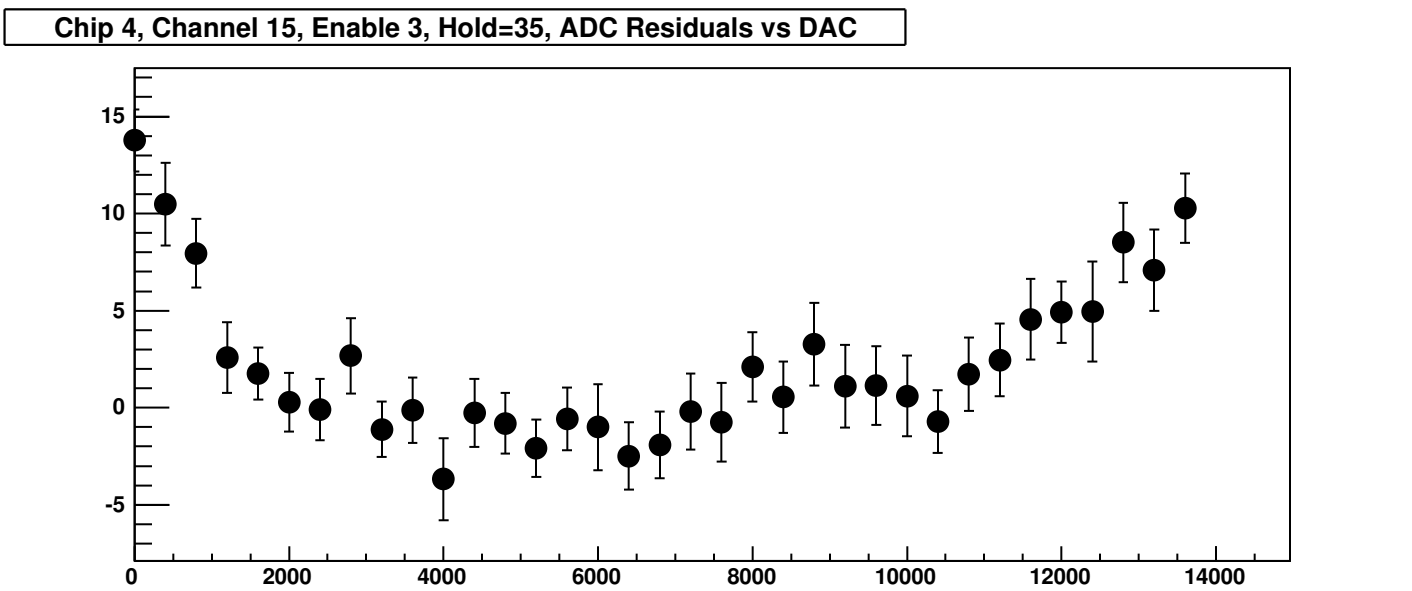

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 332.1 / 23
p0 273 ± 3.218
p1 1.96 ± 0.001876
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

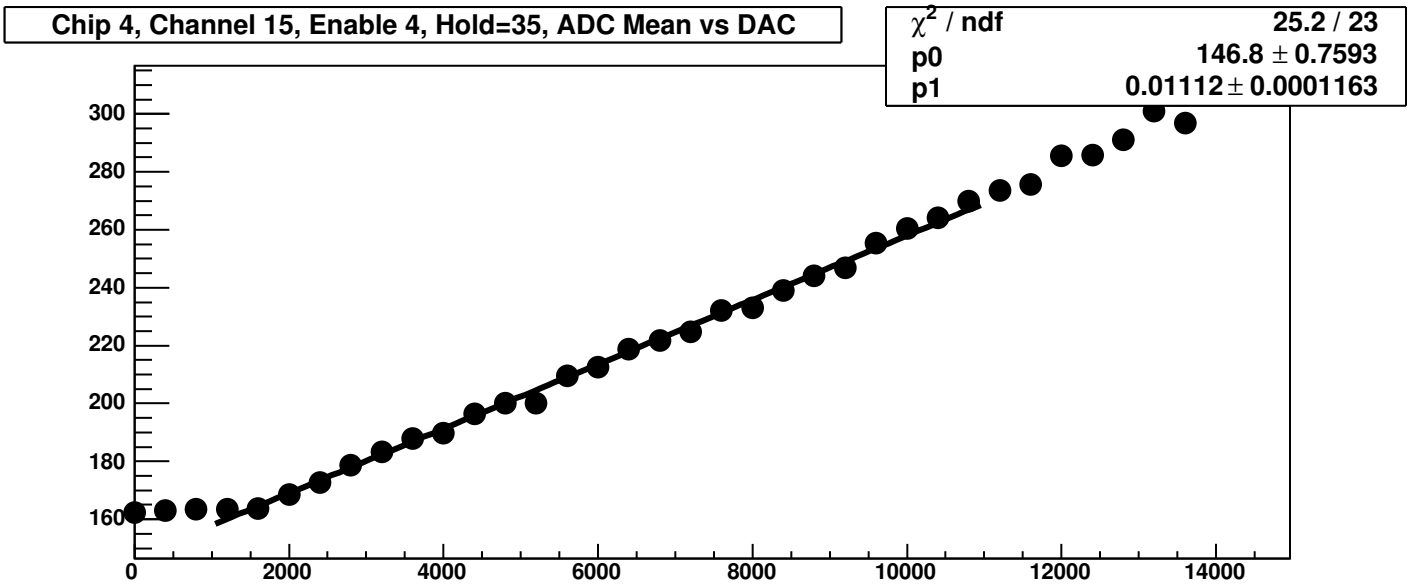

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

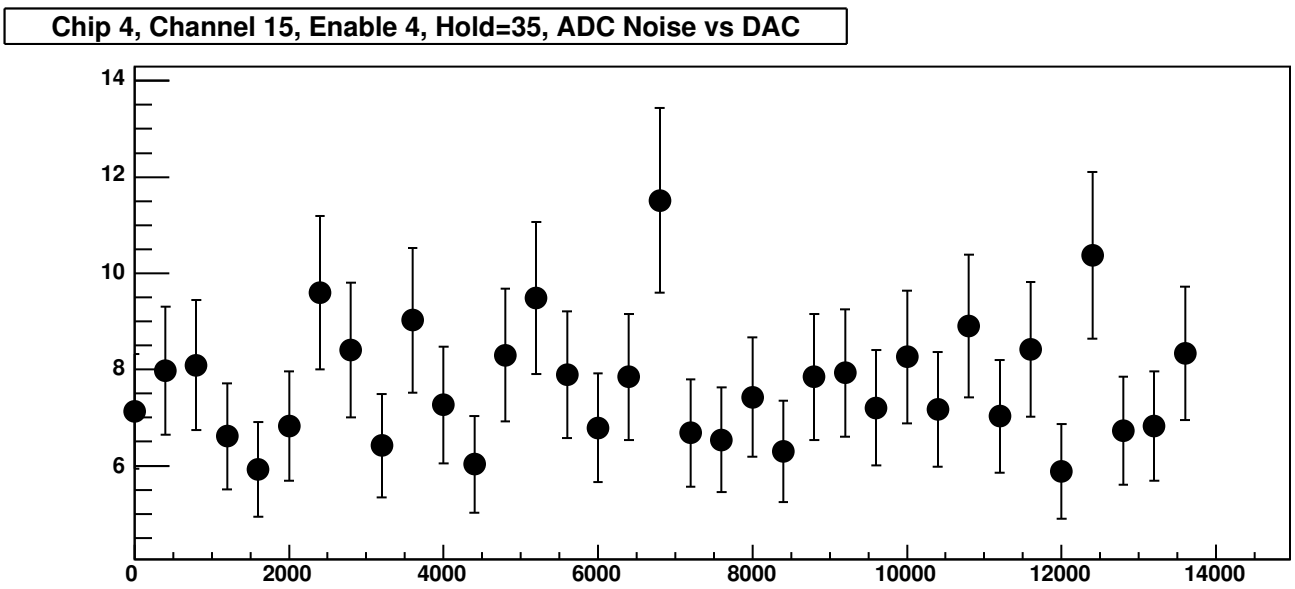

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

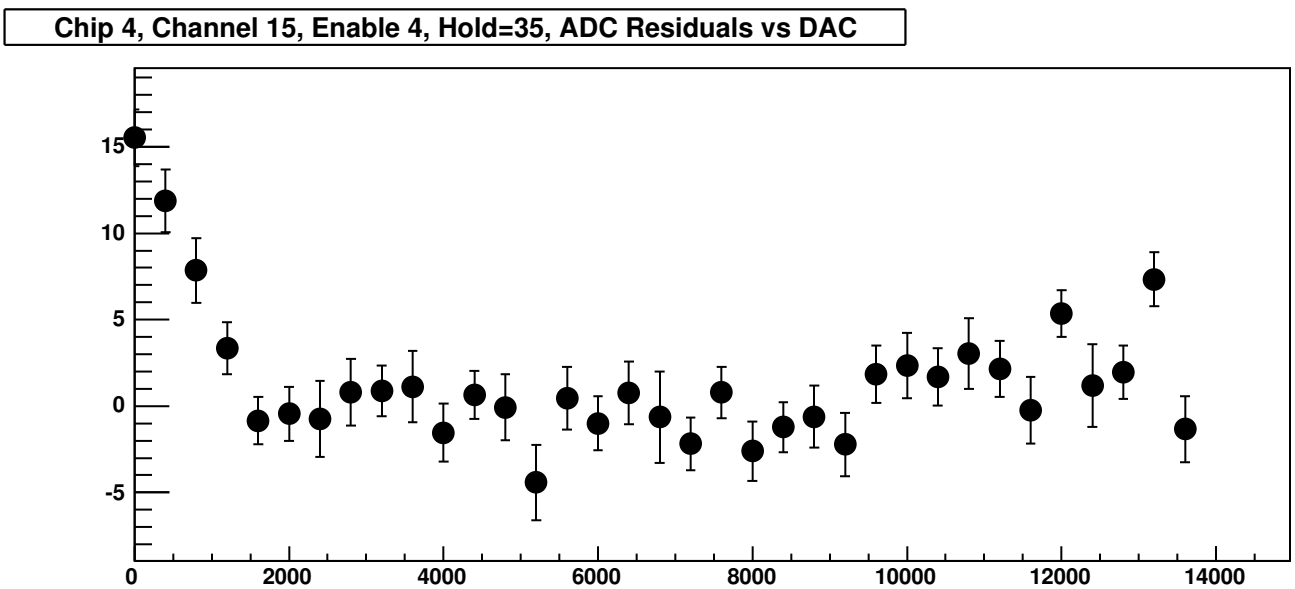

Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.01 / 23
p0 147.4 ± 0.7584
p1 0.01148 ± 0.0001209
320
300
280
260
240
220
200
180
160
0
2000
4000
6000
8000
10000
12000
14000

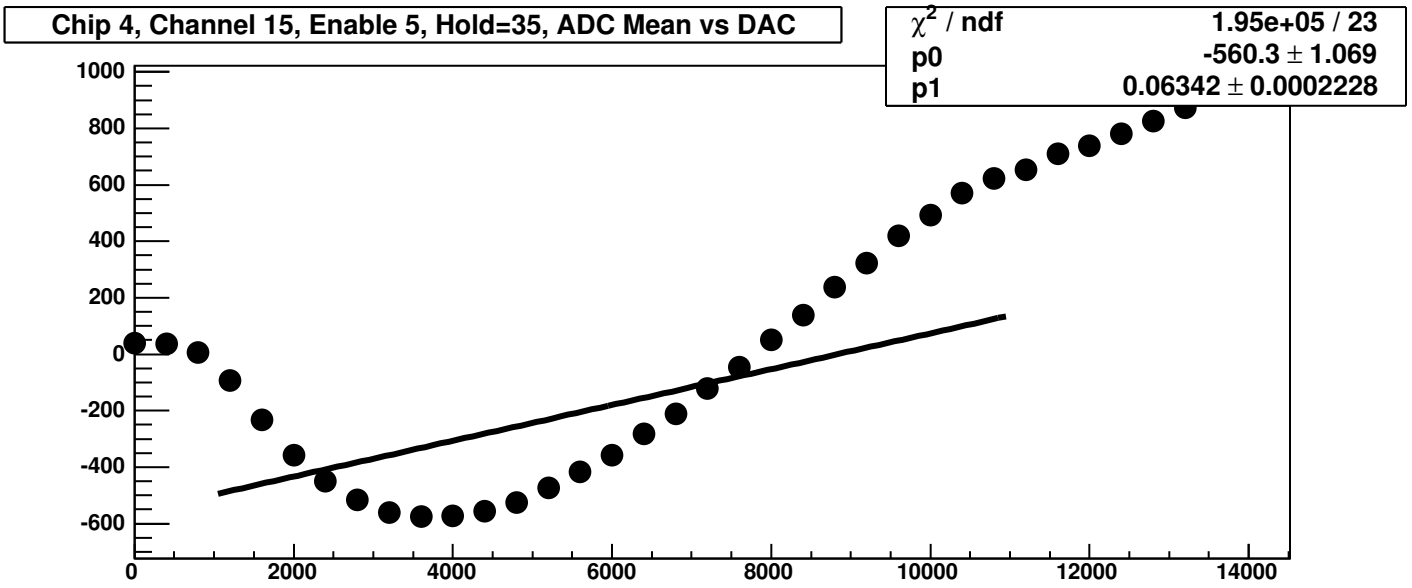

Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

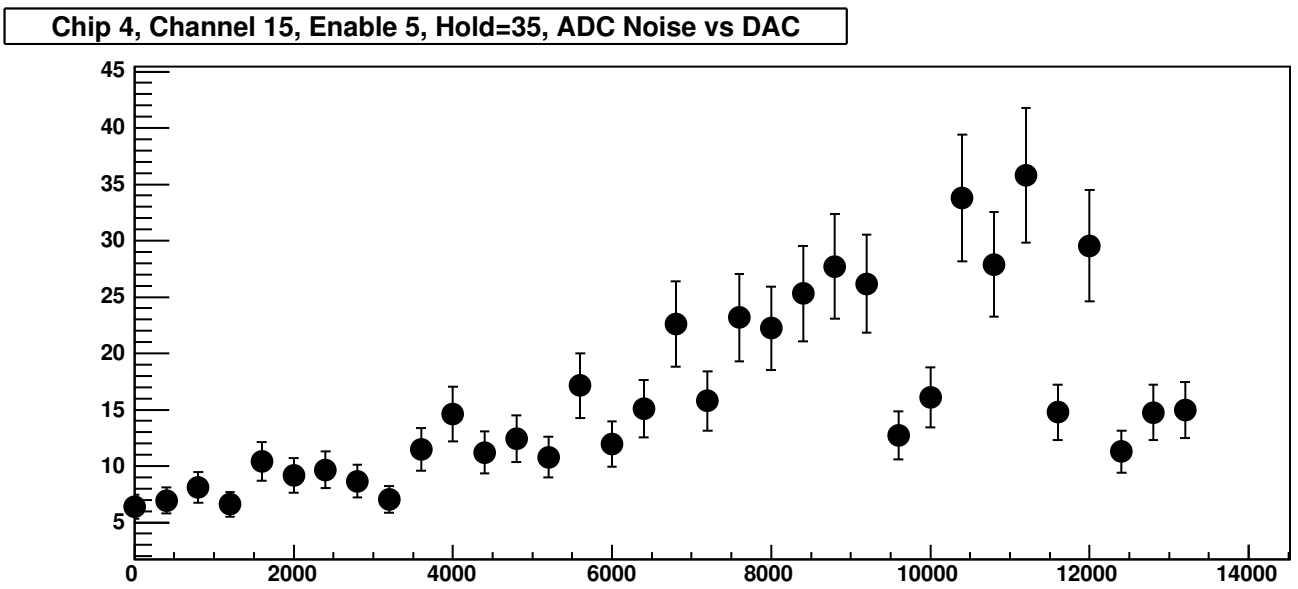

Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

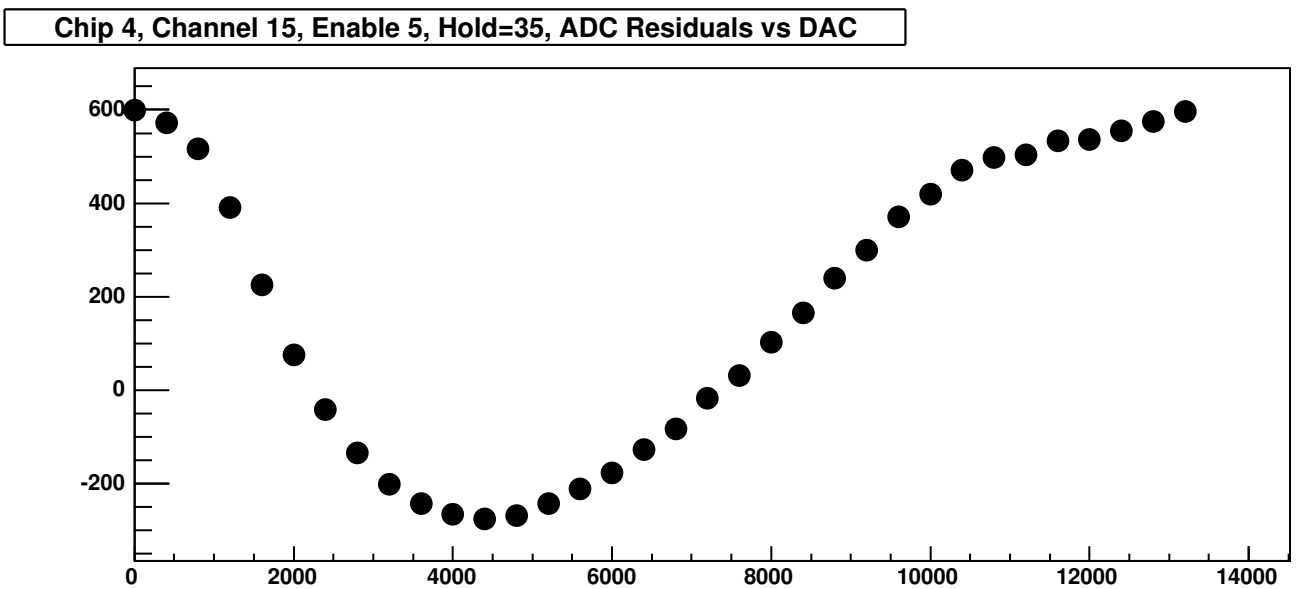

Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.2 / 23
p0 146.8 ± 0.7593
p1 0.01112 ± 0.0001163
300
280
260
240
220
200
180
160
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

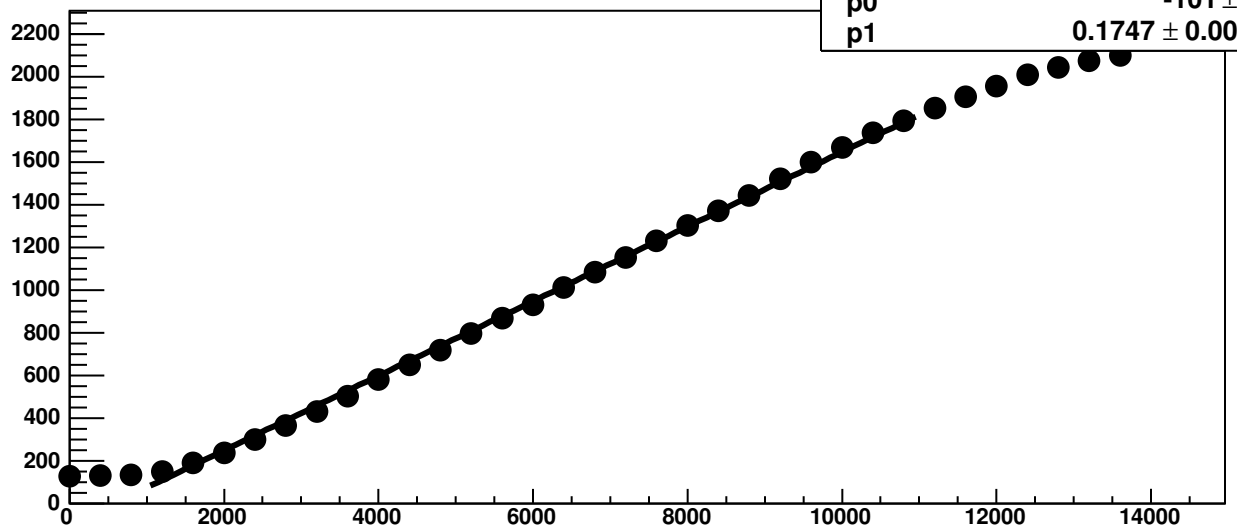

Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.95e+05 / 23
p0 -560.3 ± 1.069
p1 0.06342 ± 0.0002228
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

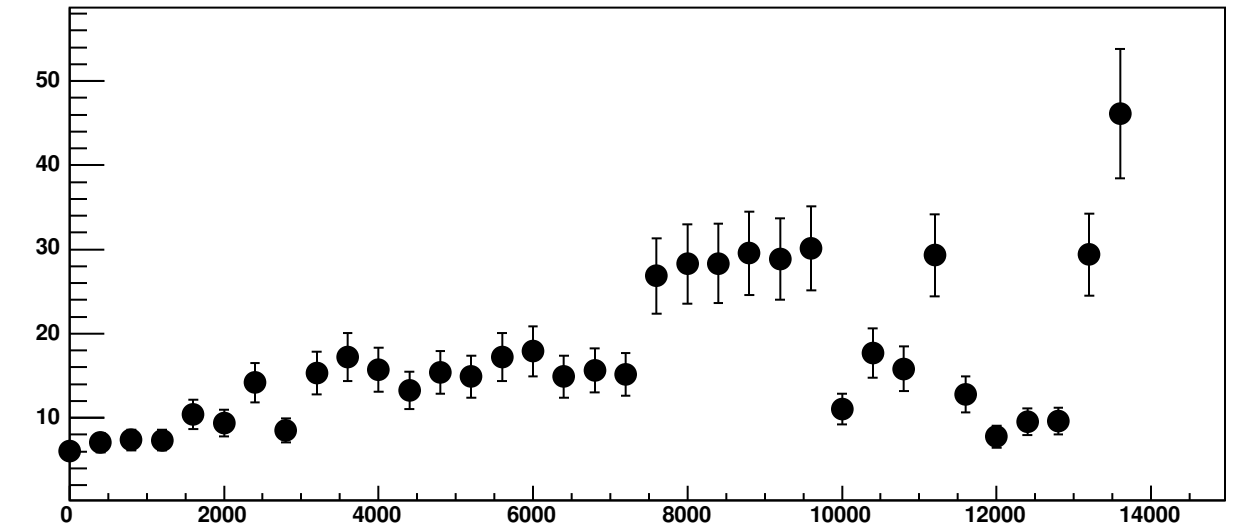

Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

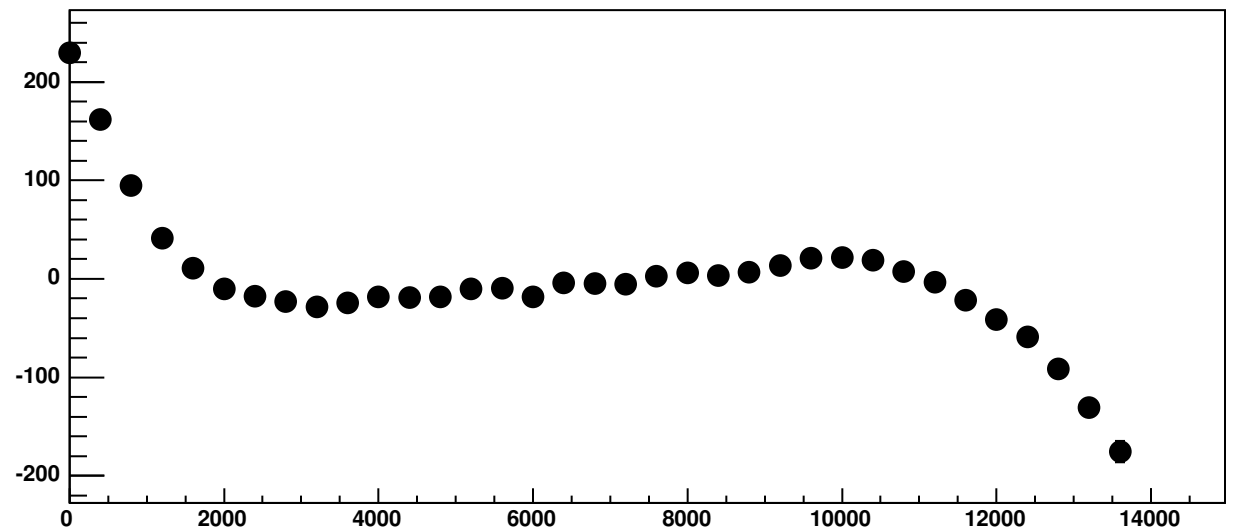
χ^2 / ndf	1157 / 23
p0	-101 ± 1.159
p1	0.1747 ± 0.0002139

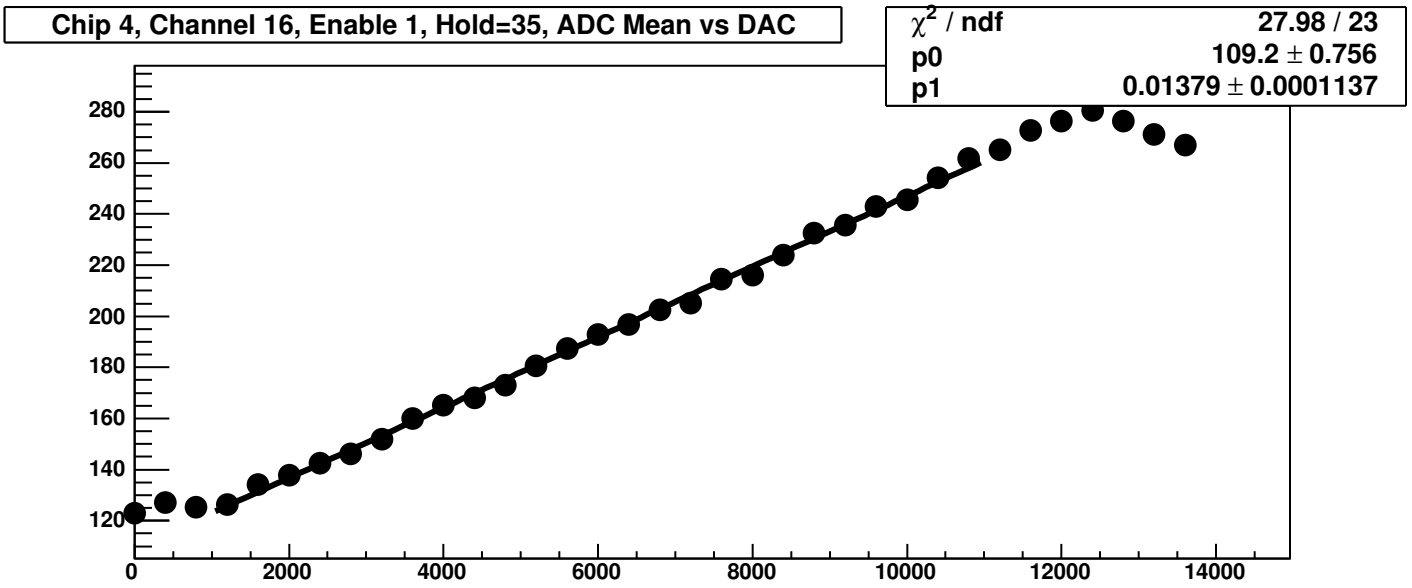


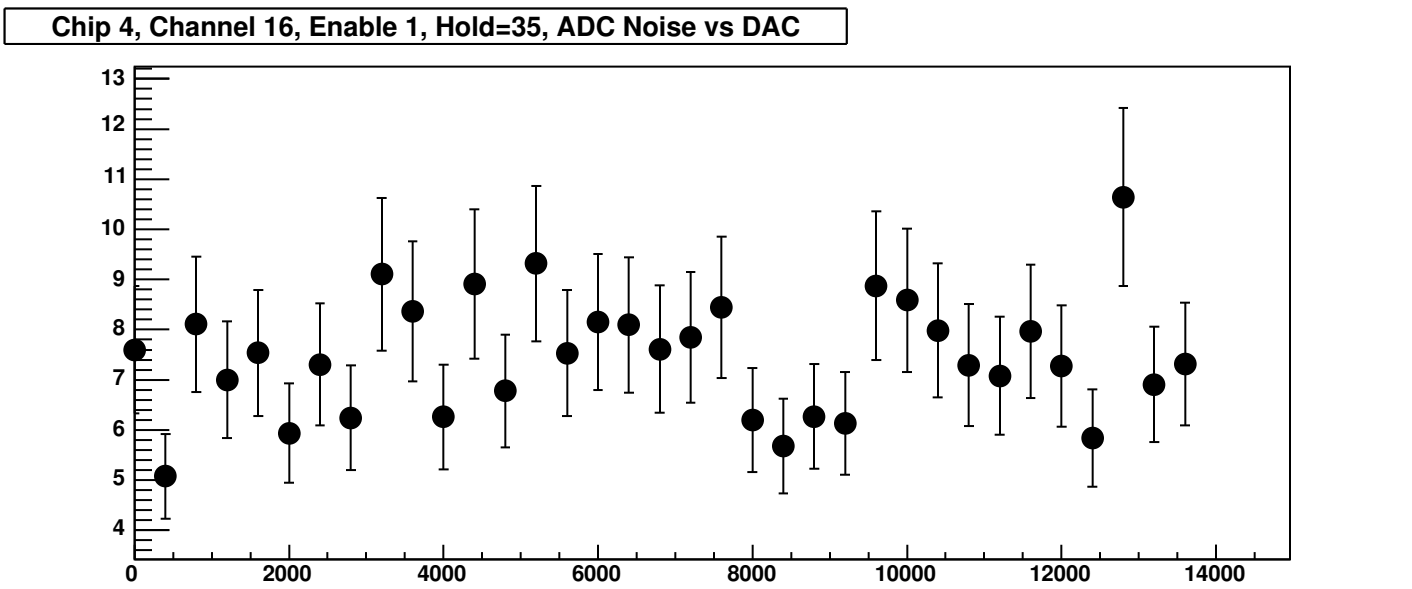
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

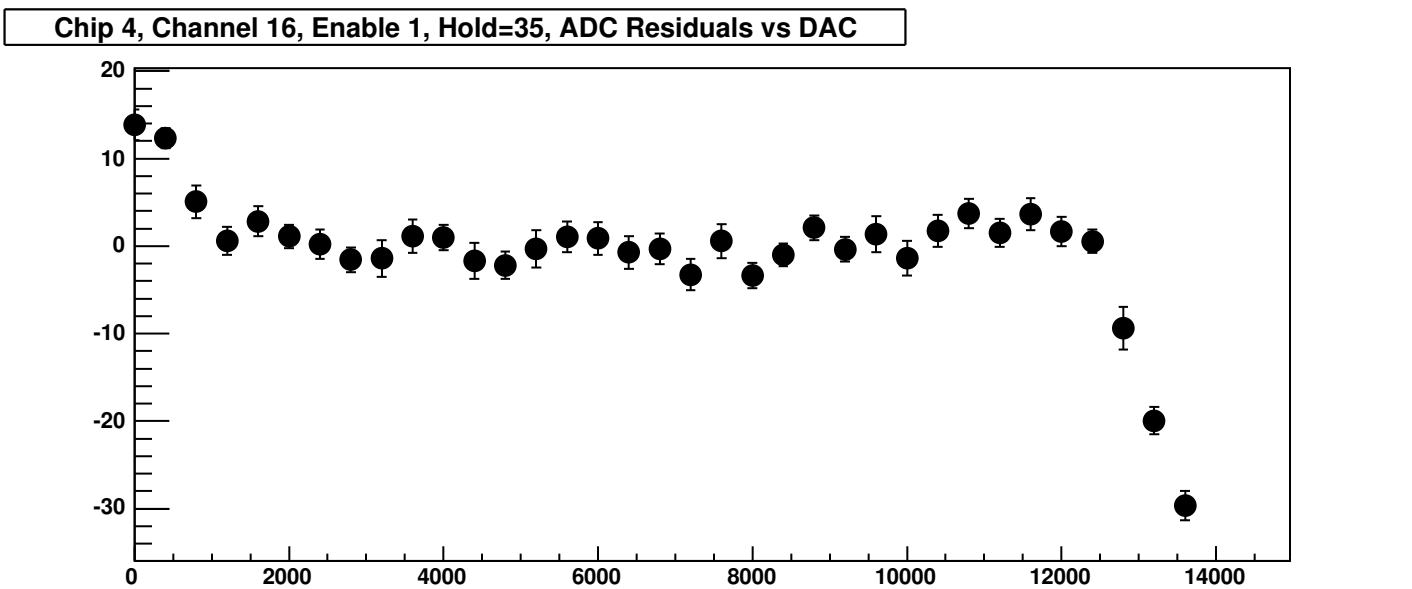


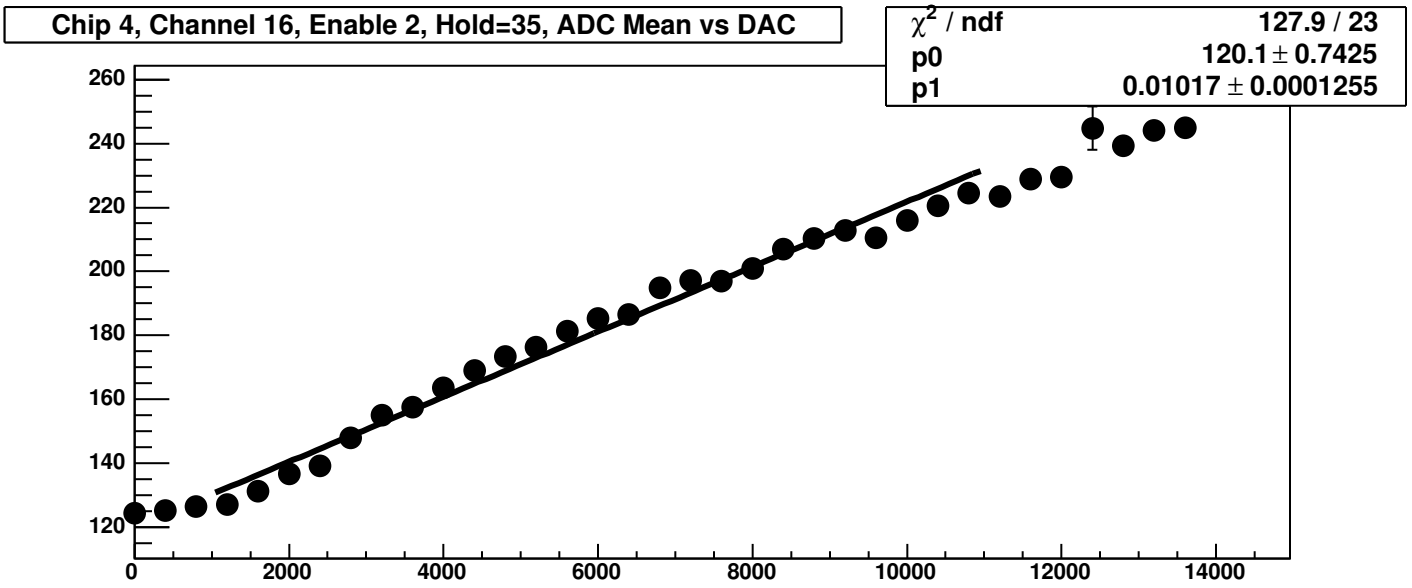
Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

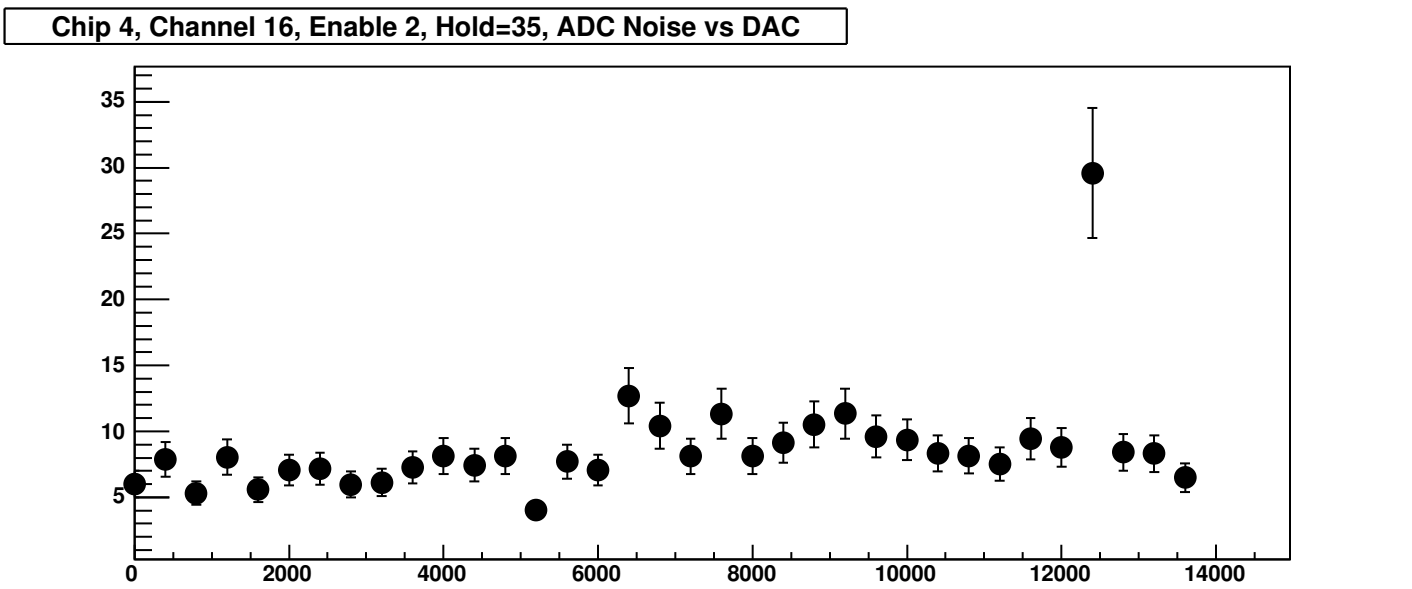


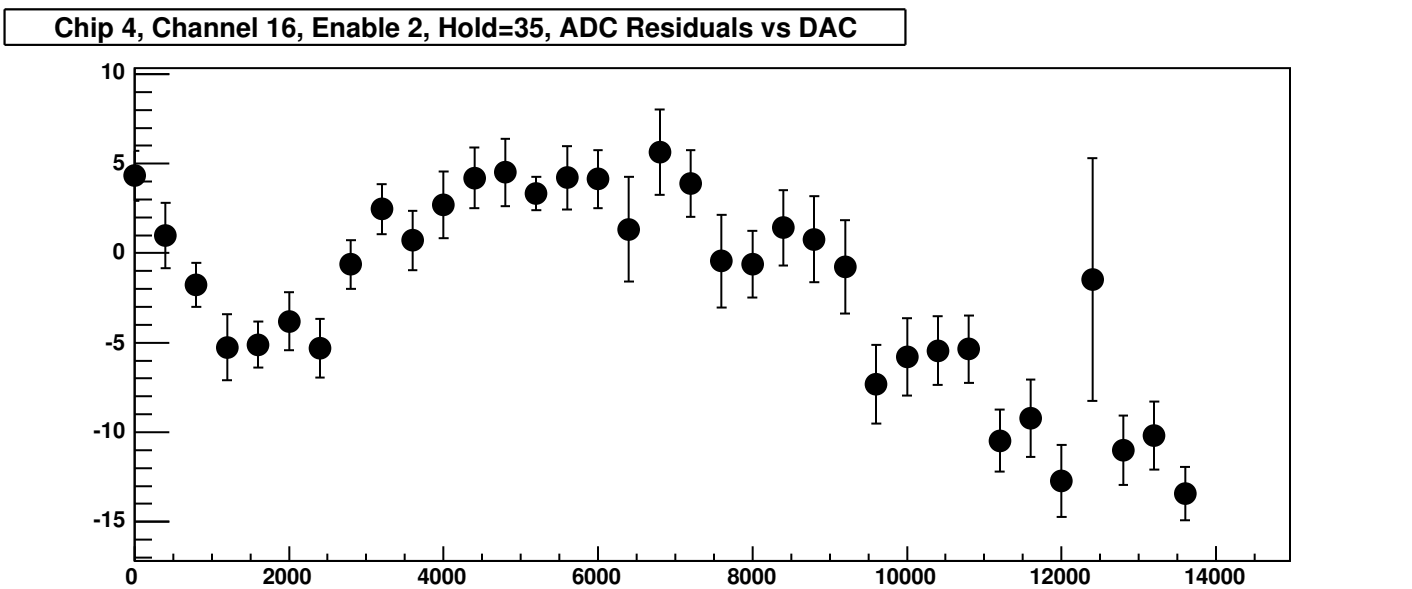

Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.98 / 23
p0 109.2 ± 0.756
p1 0.01379 ± 0.0001137
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

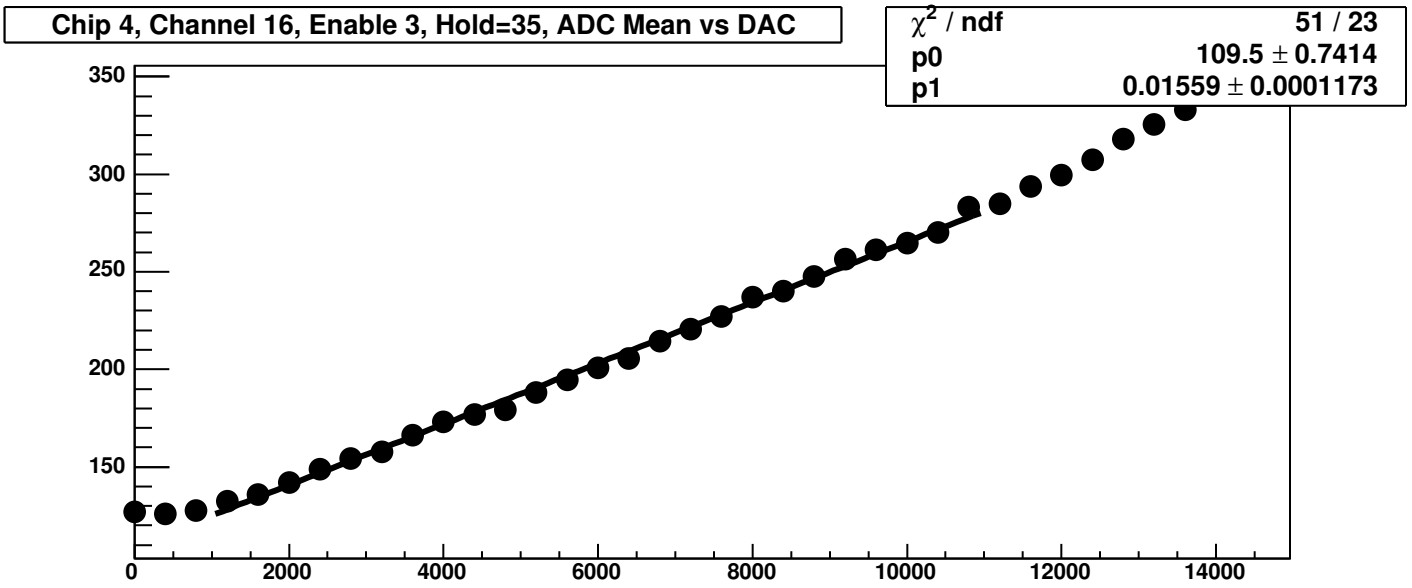

Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

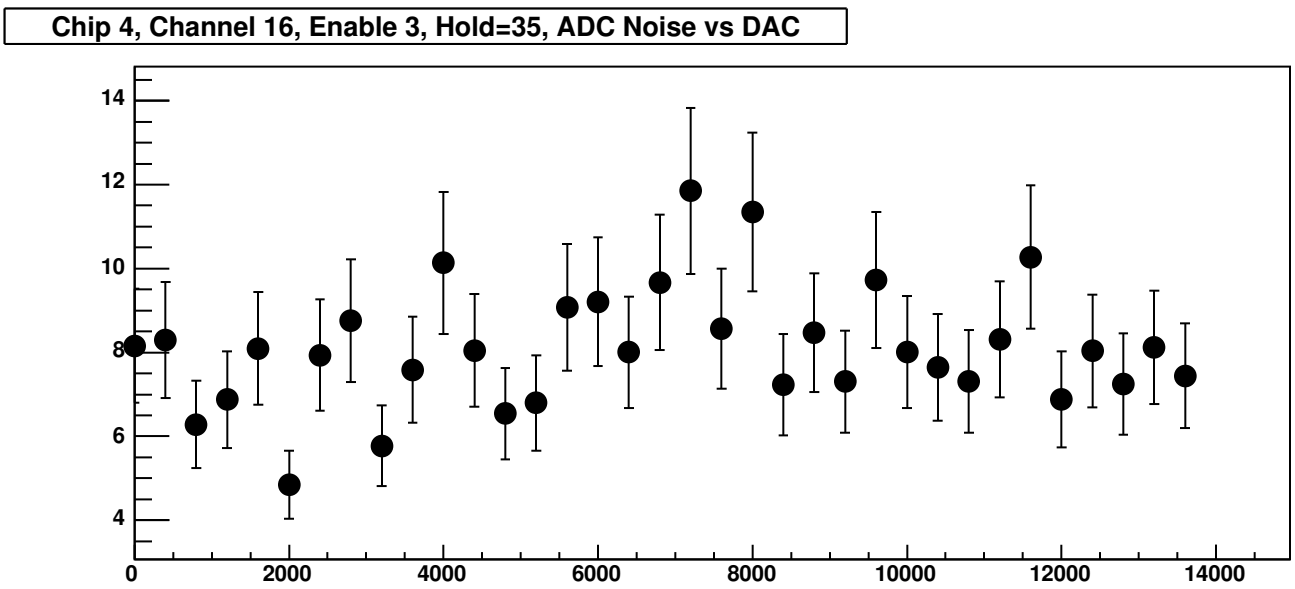

Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

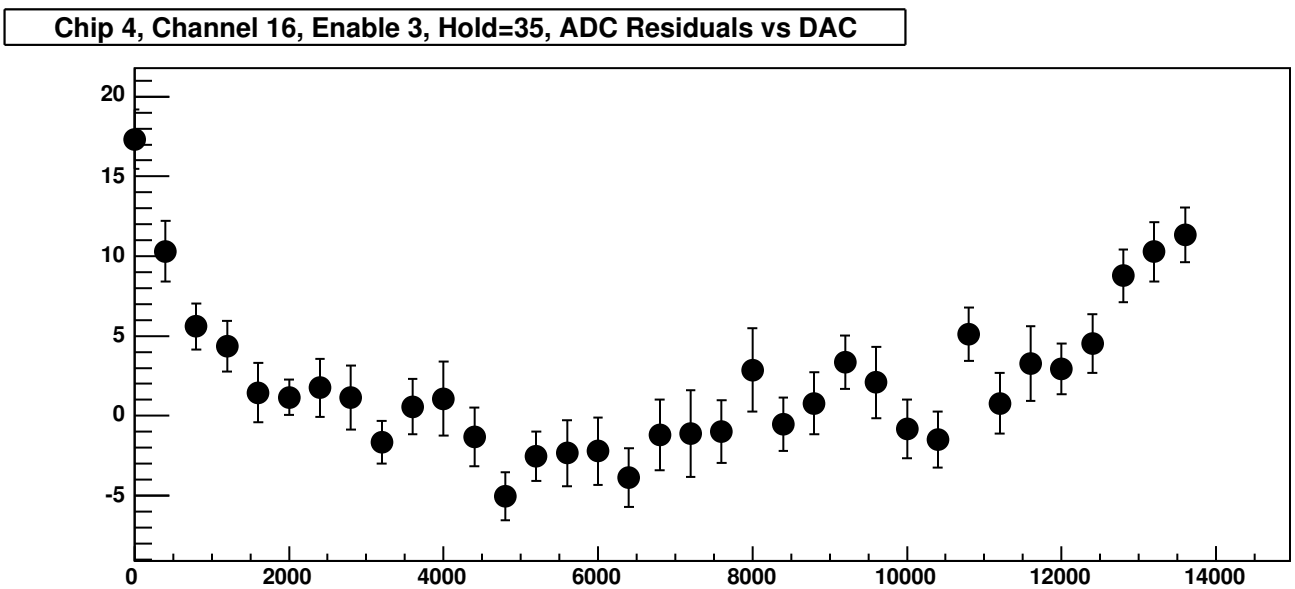

Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 127.9 / 23
p0 120.1 ± 0.7425
p1 0.01017 ± 0.0001255
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

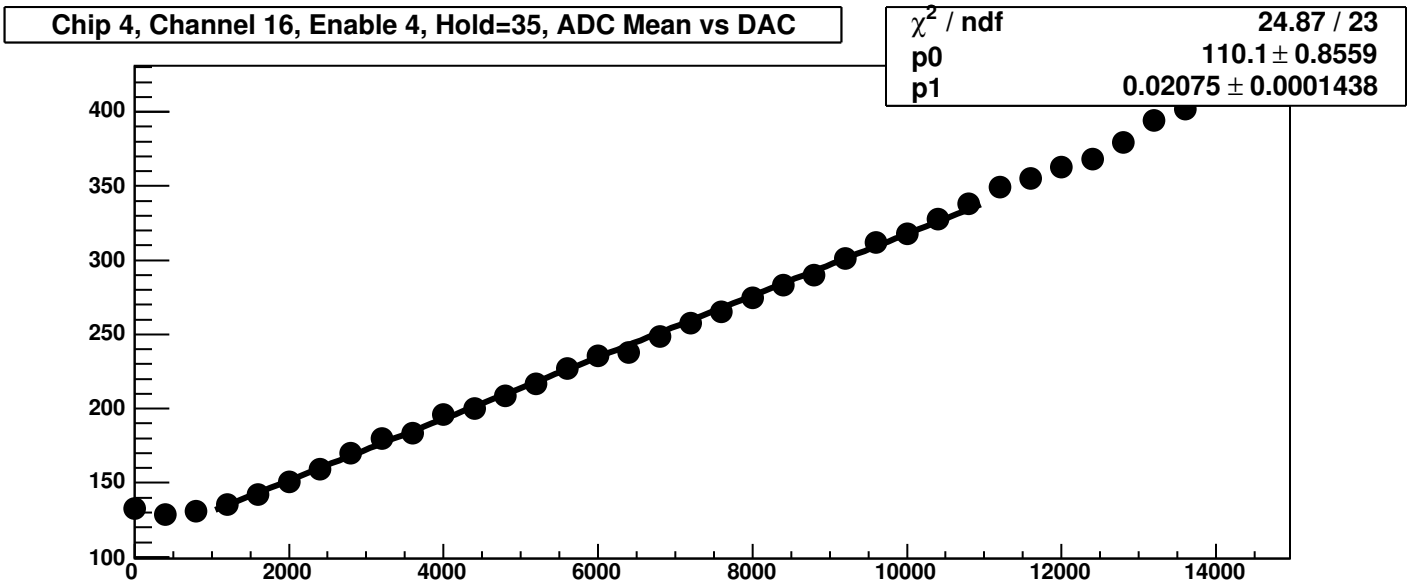

Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

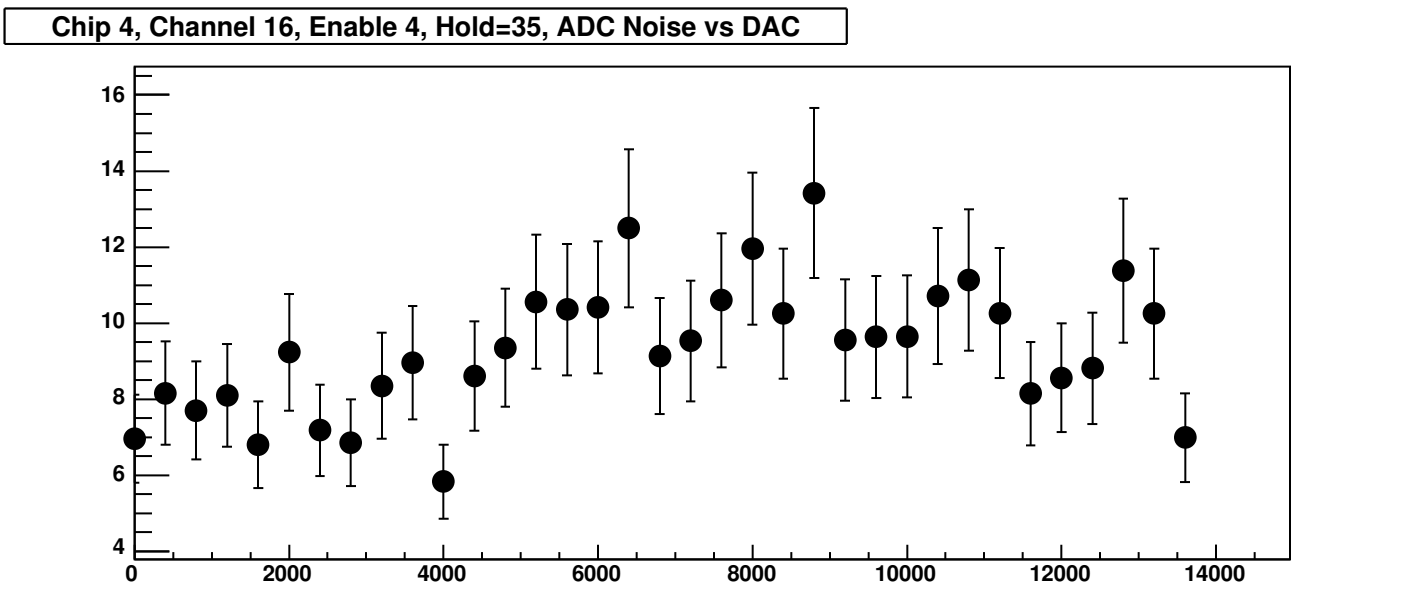

Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

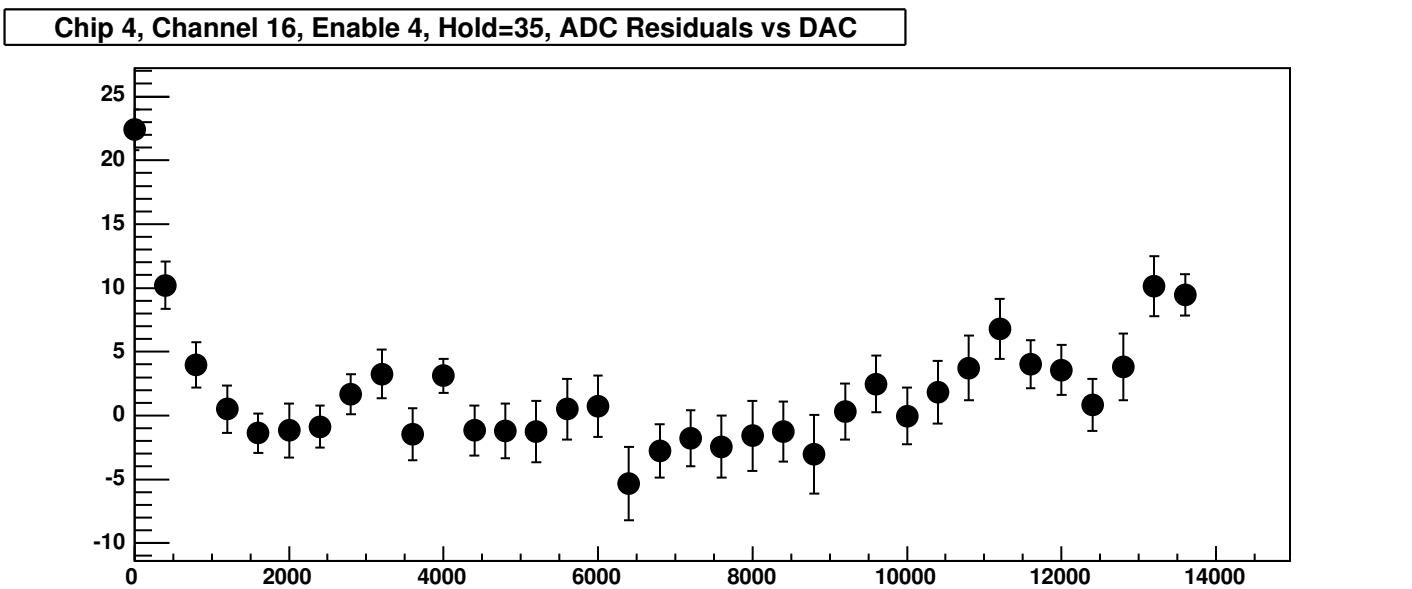

Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51 / 23
p0 109.5 ± 0.7414
p1 0.01559 ± 0.0001173
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

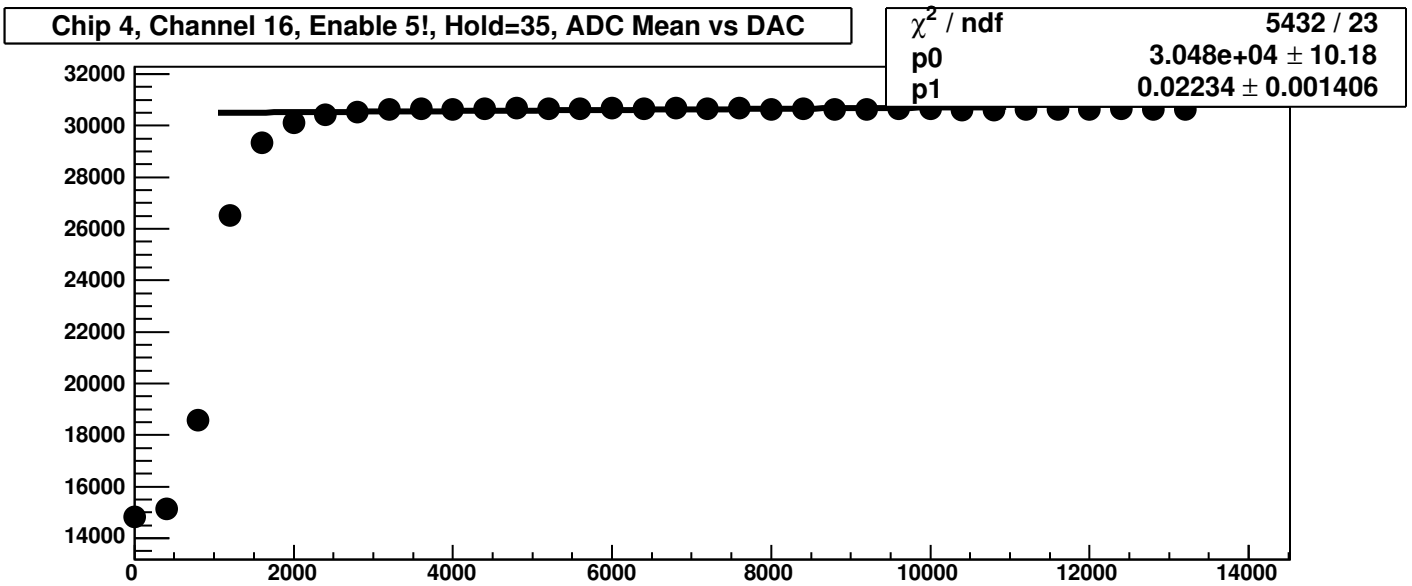

Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

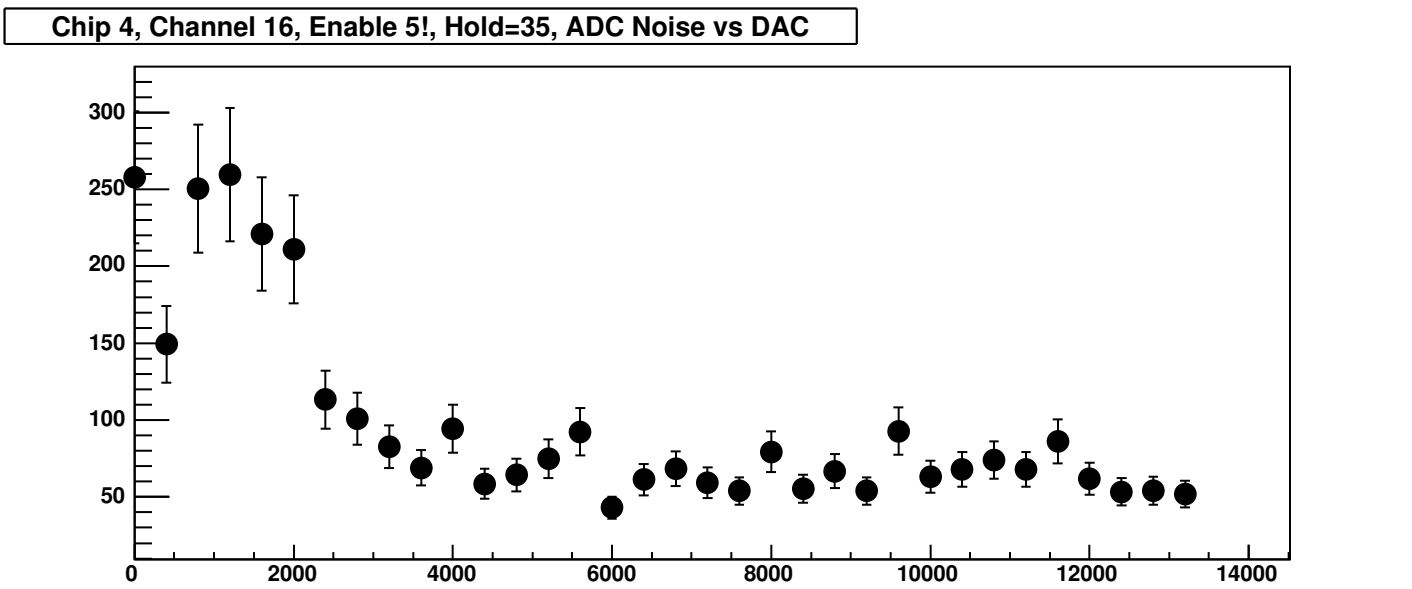

Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

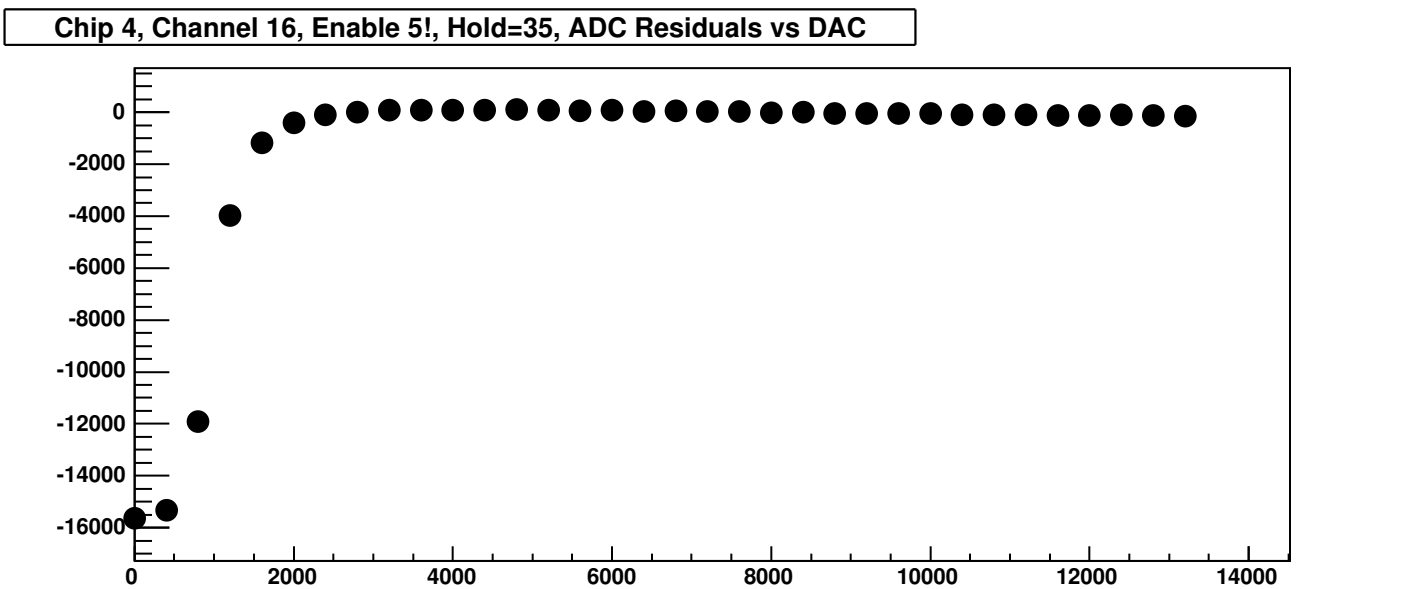

Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.87 / 23
p0 110.1 ± 0.8559
p1 0.02075 ± 0.0001438
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

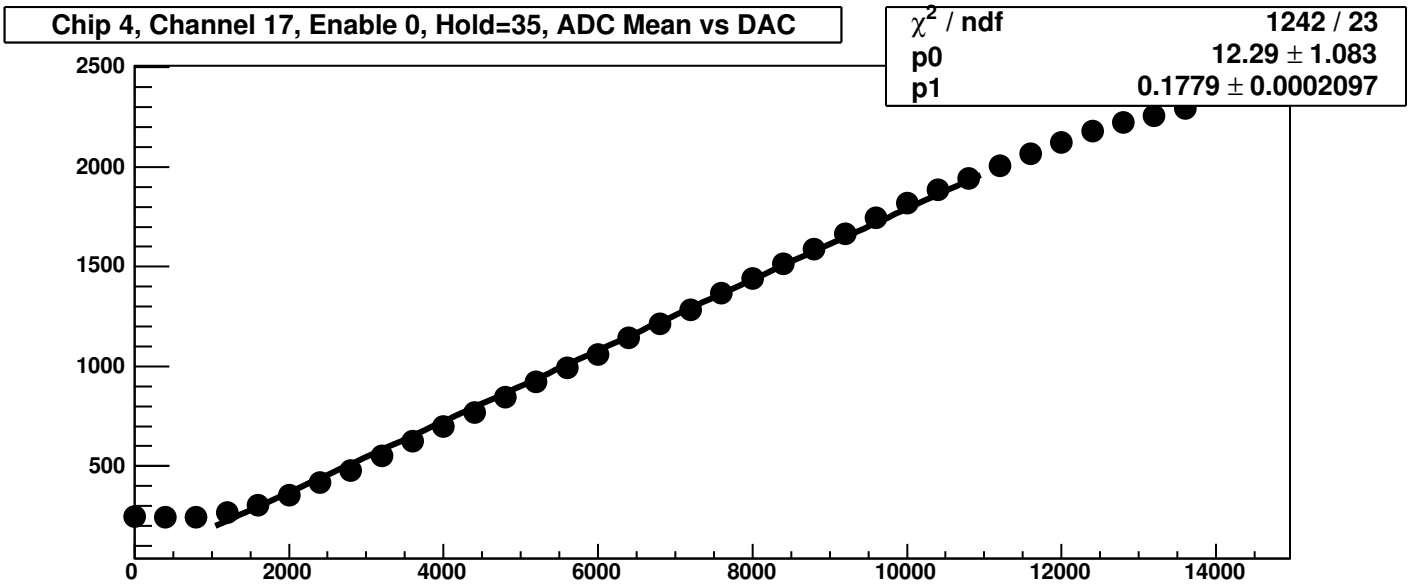

Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

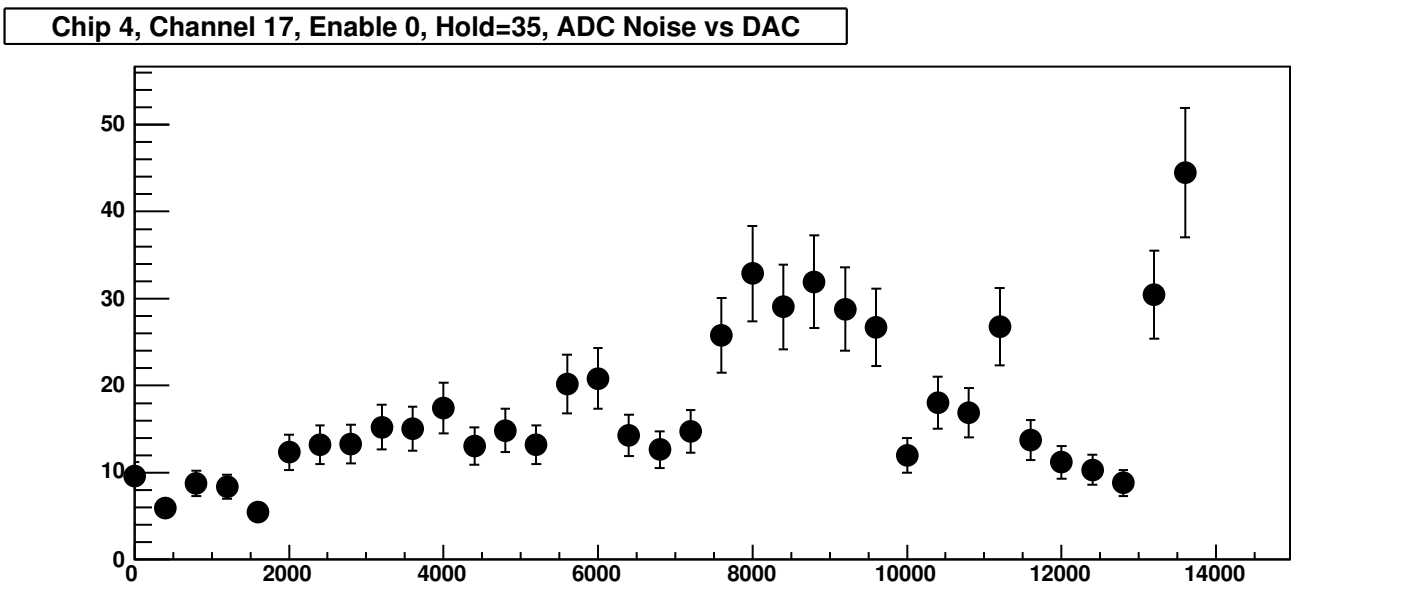

Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

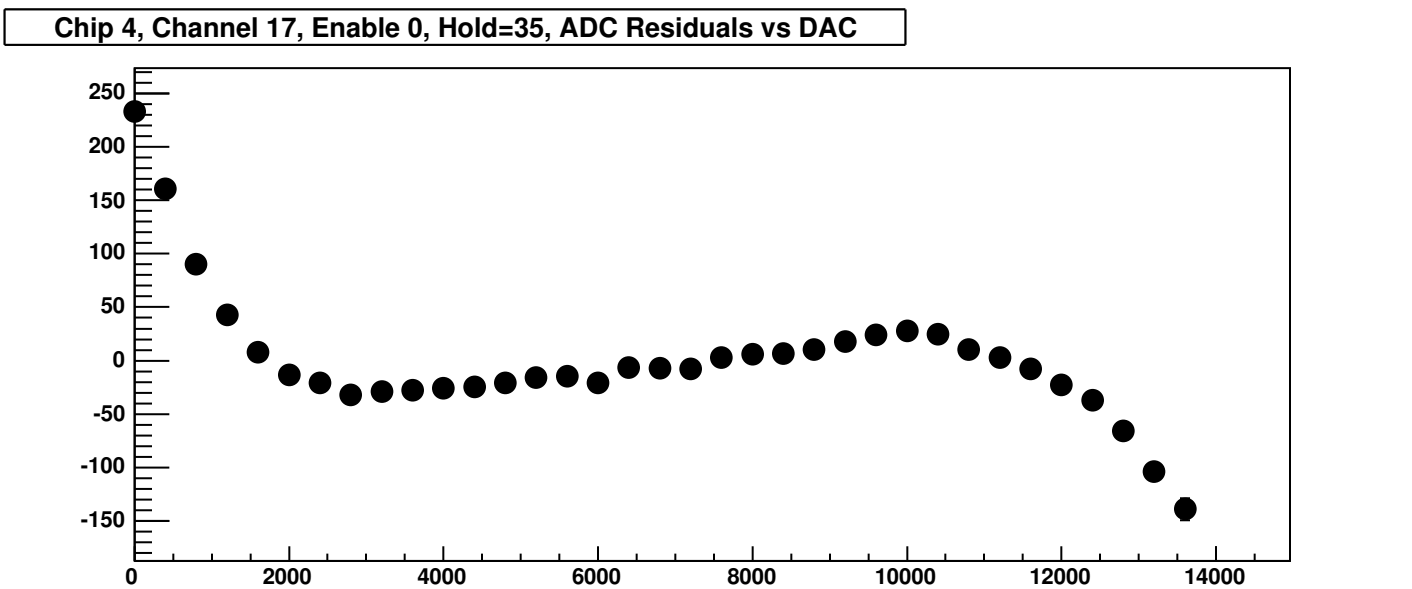

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5432 / 23
p0 3.048e+04 ± 10.18
p1 0.02234 ± 0.001406
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

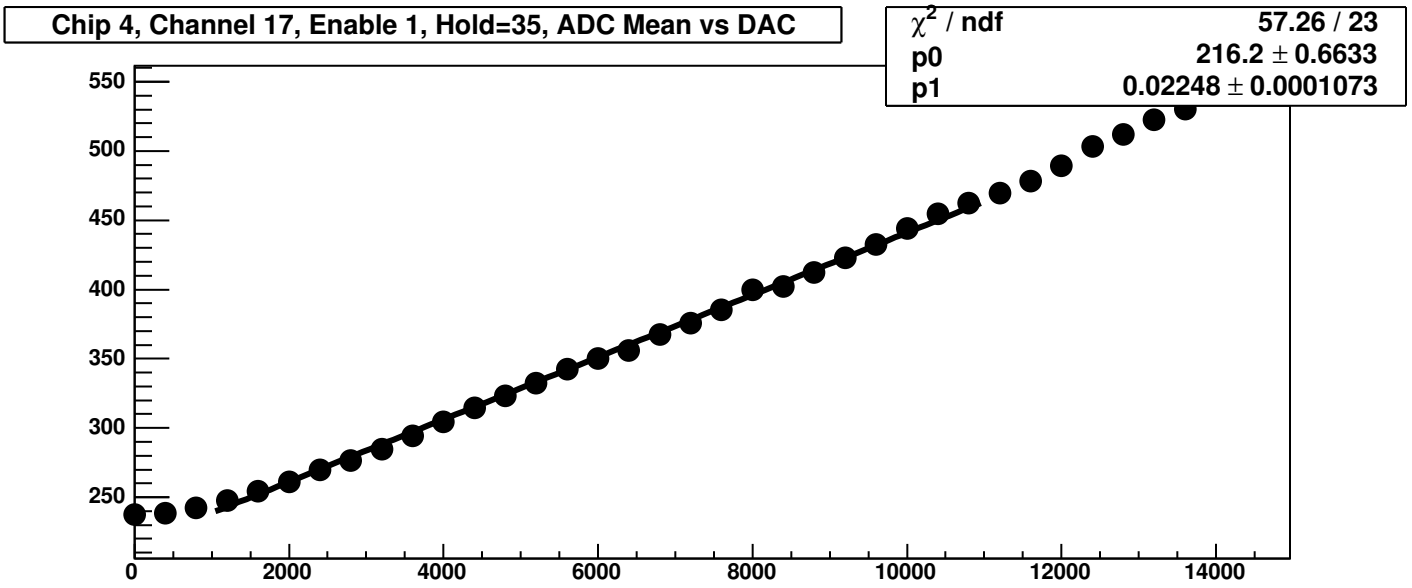

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

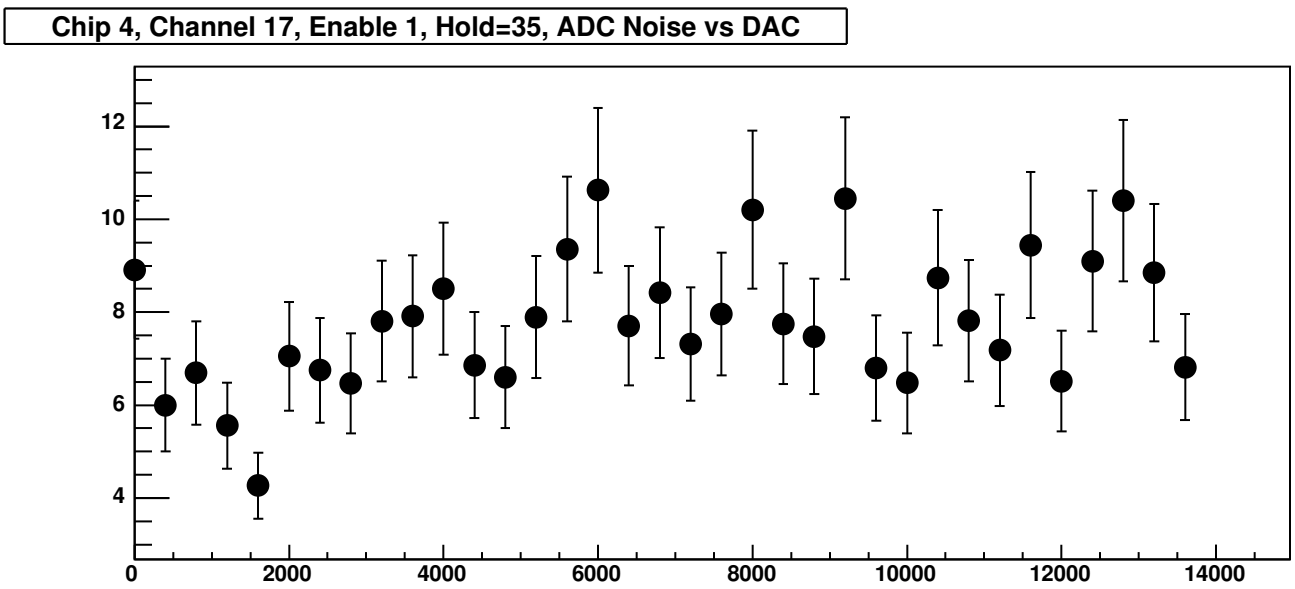

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

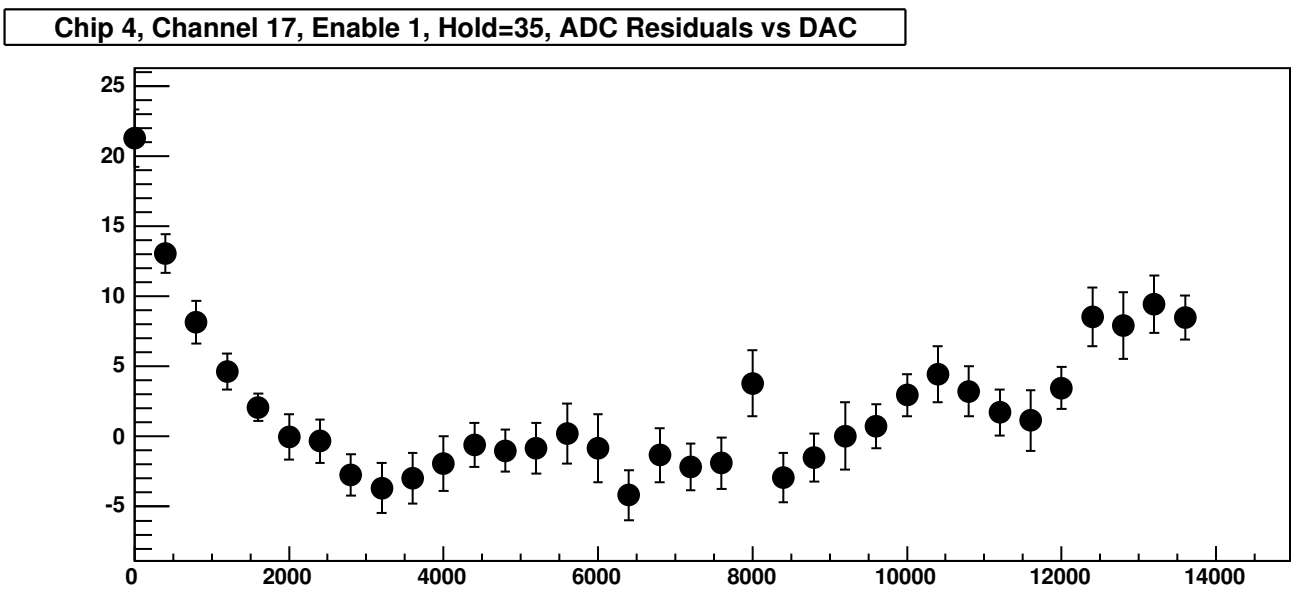

Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1242 / 23
p0 12.29 ± 1.083
p1 0.1779 ± 0.0002097
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

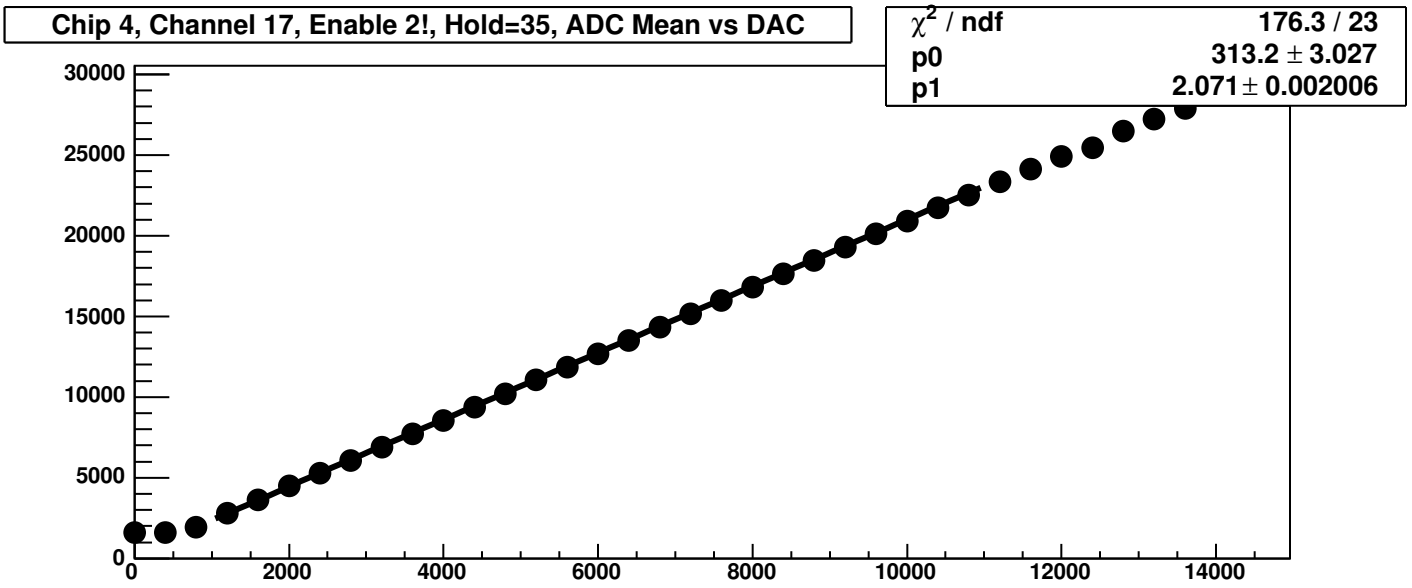

Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

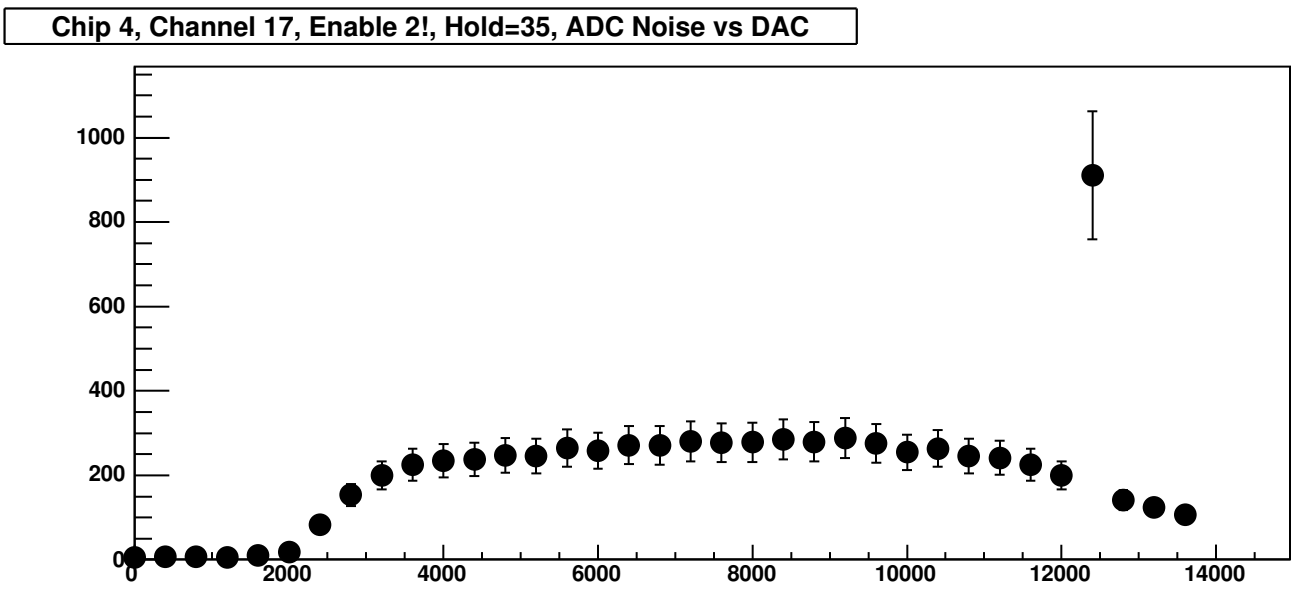

Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

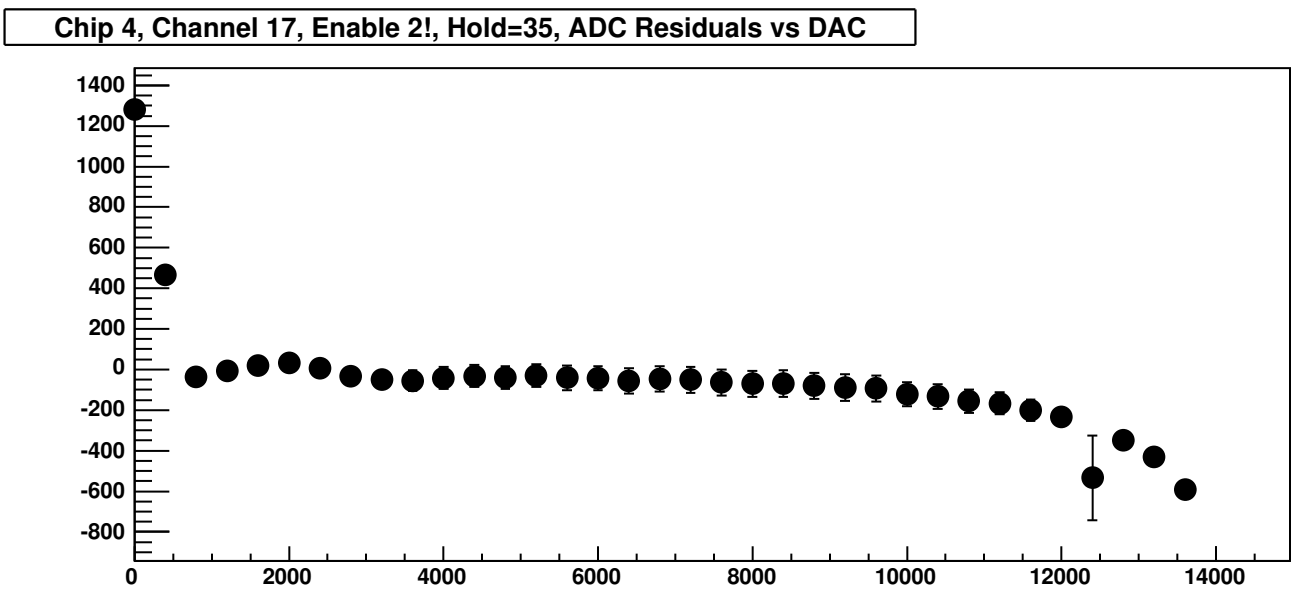

Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 57.26 / 23
p0 216.2 ± 0.6633
p1 0.02248 ± 0.0001073
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

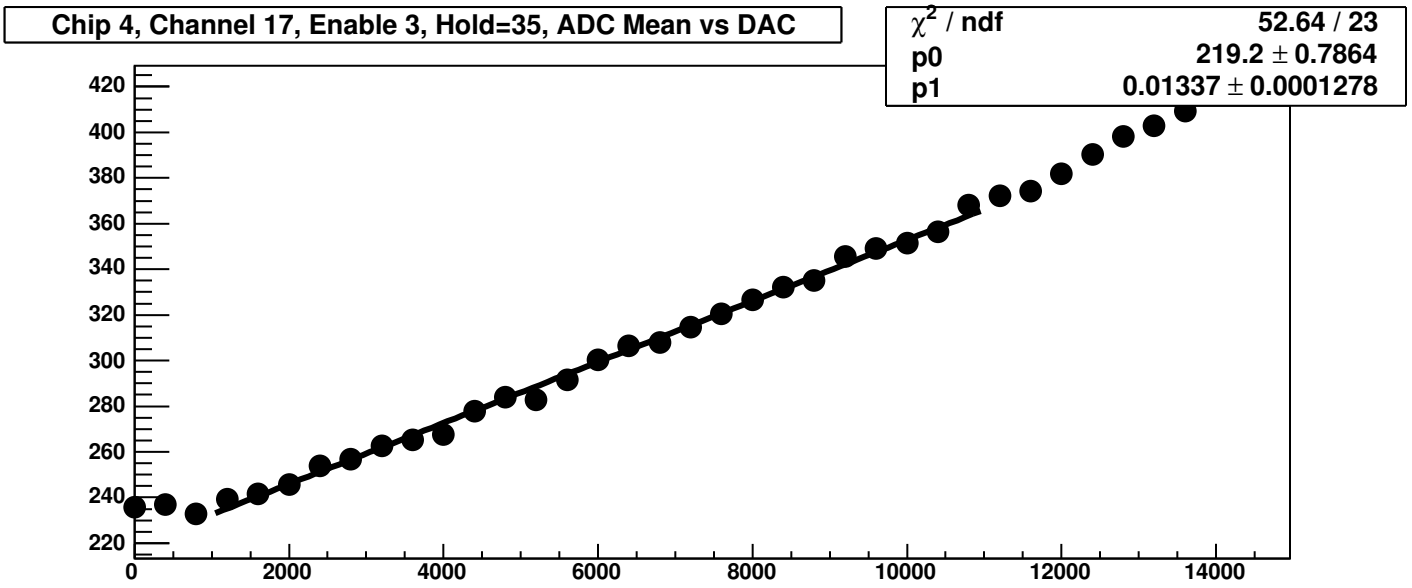

Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

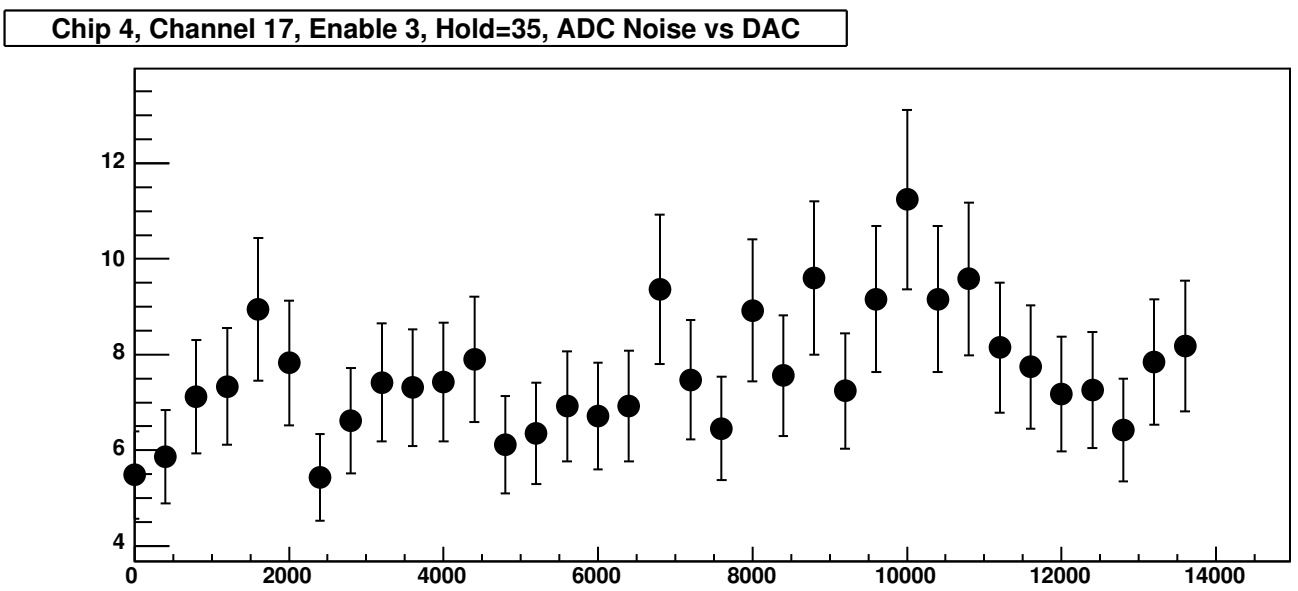

Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

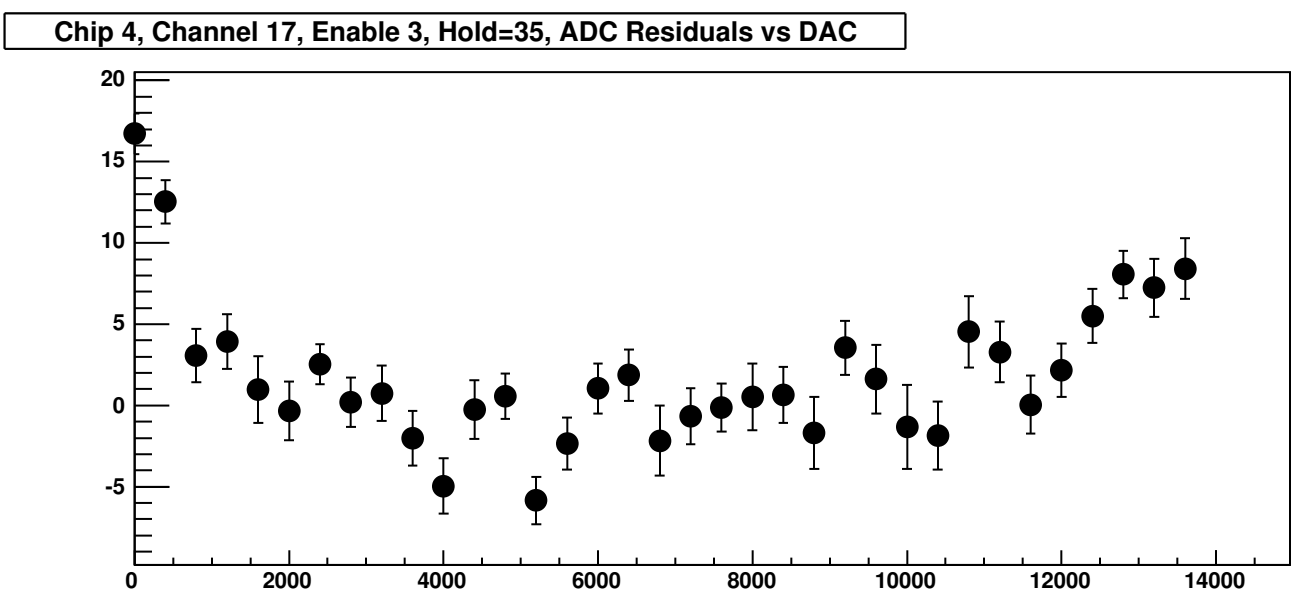

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 176.3 / 23
p0 313.2 ± 3.027
p1 2.071 ± 0.002006
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

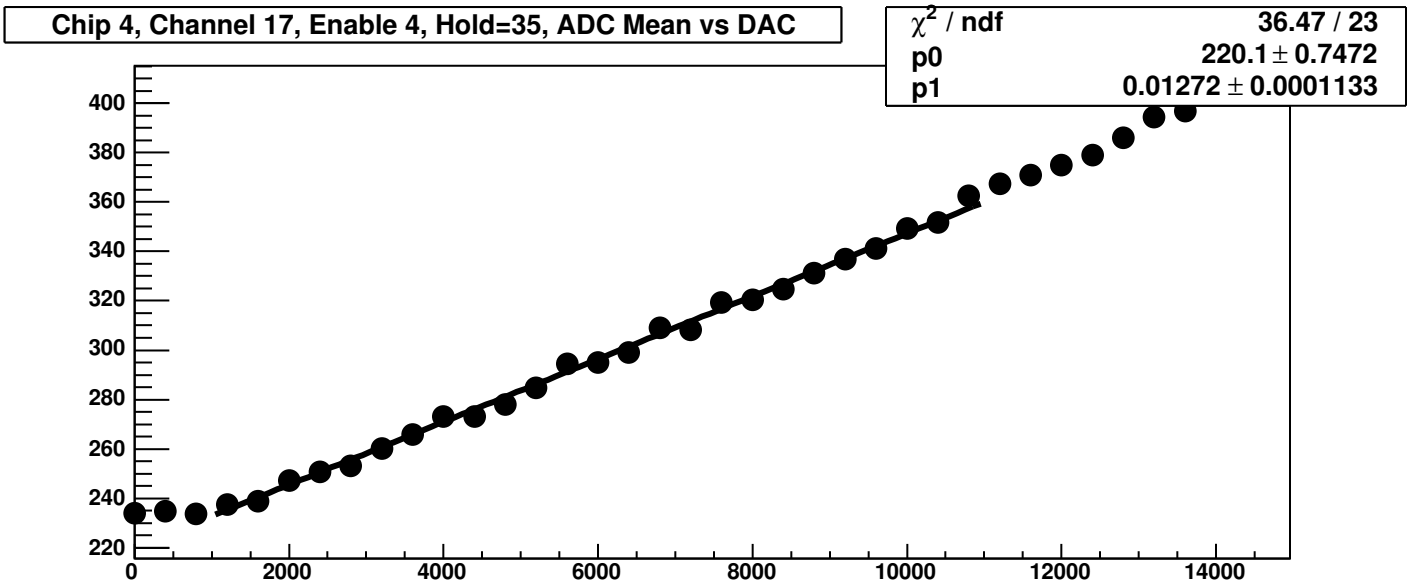

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

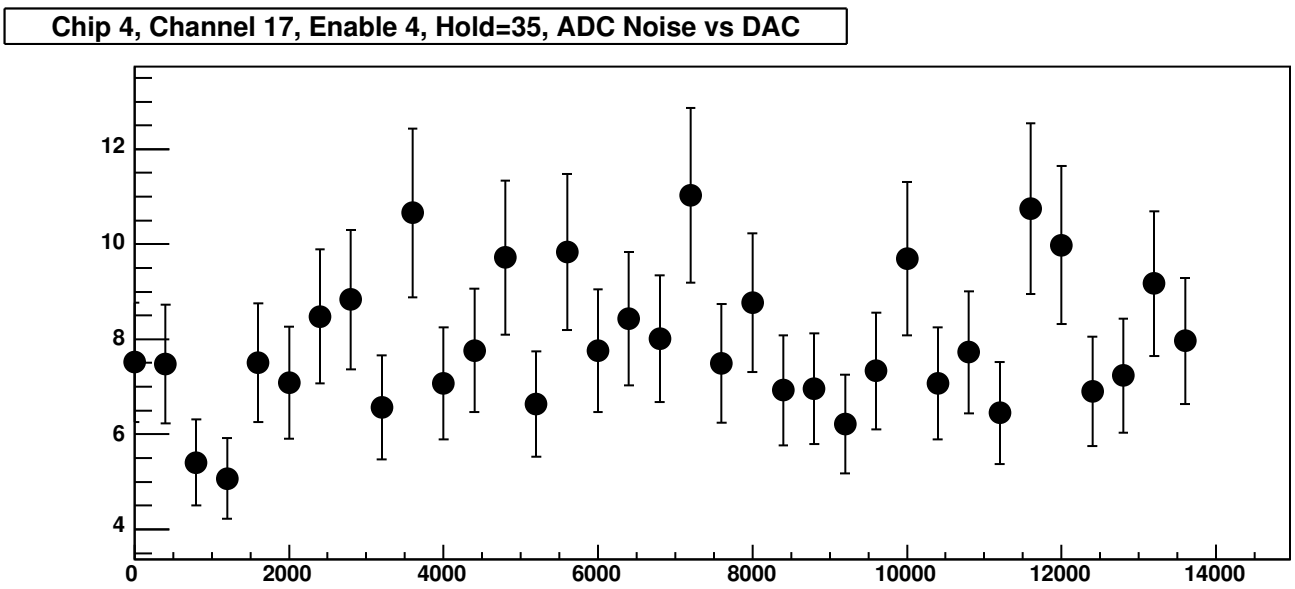

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

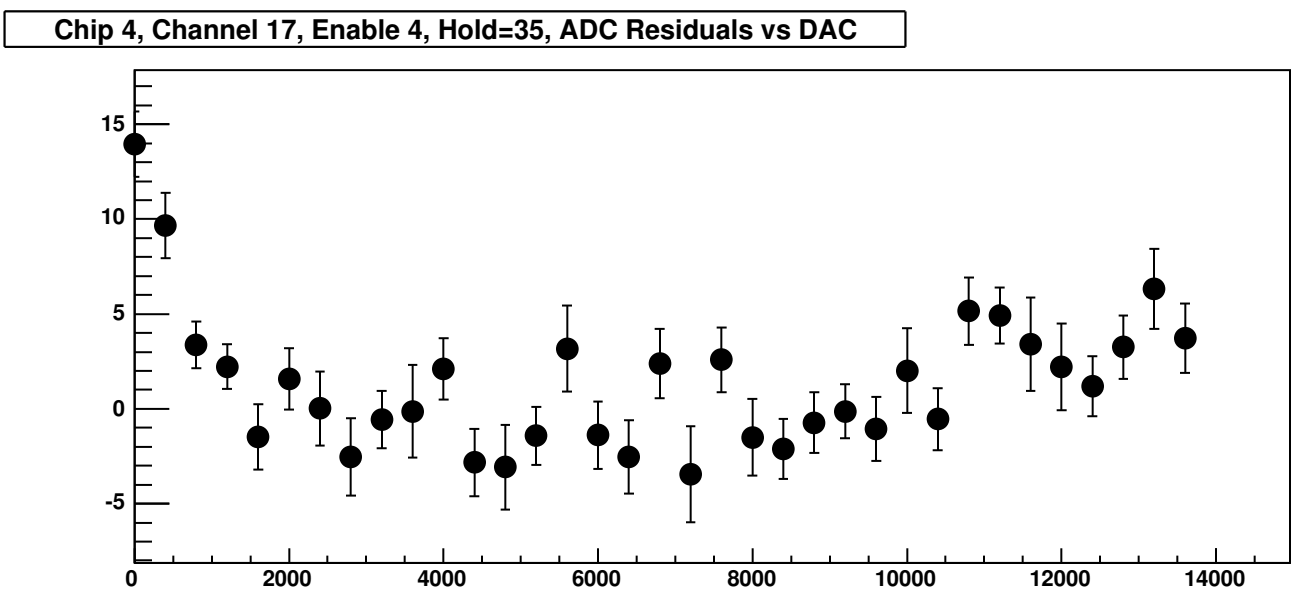

Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 52.64 / 23
p0 219.2 ± 0.7864
p1 0.01337 ± 0.0001278
420
400
380
360
340
320
300
280
260
240
220
0
2000
4000
6000
8000
10000
12000
14000

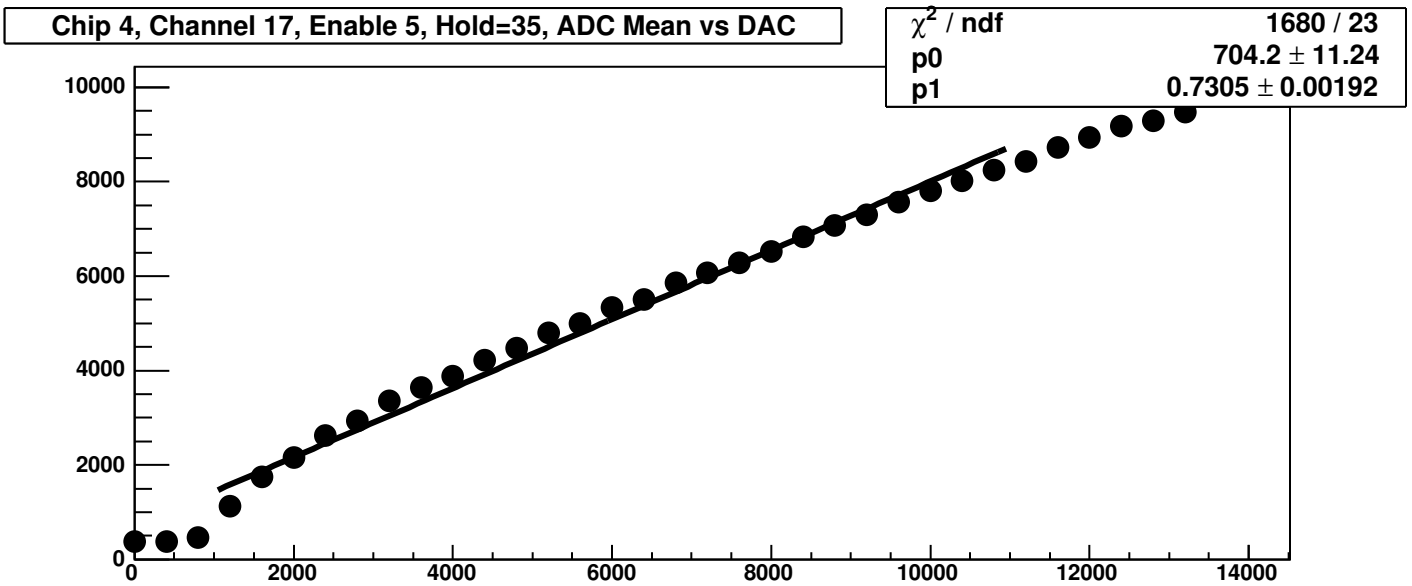

Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

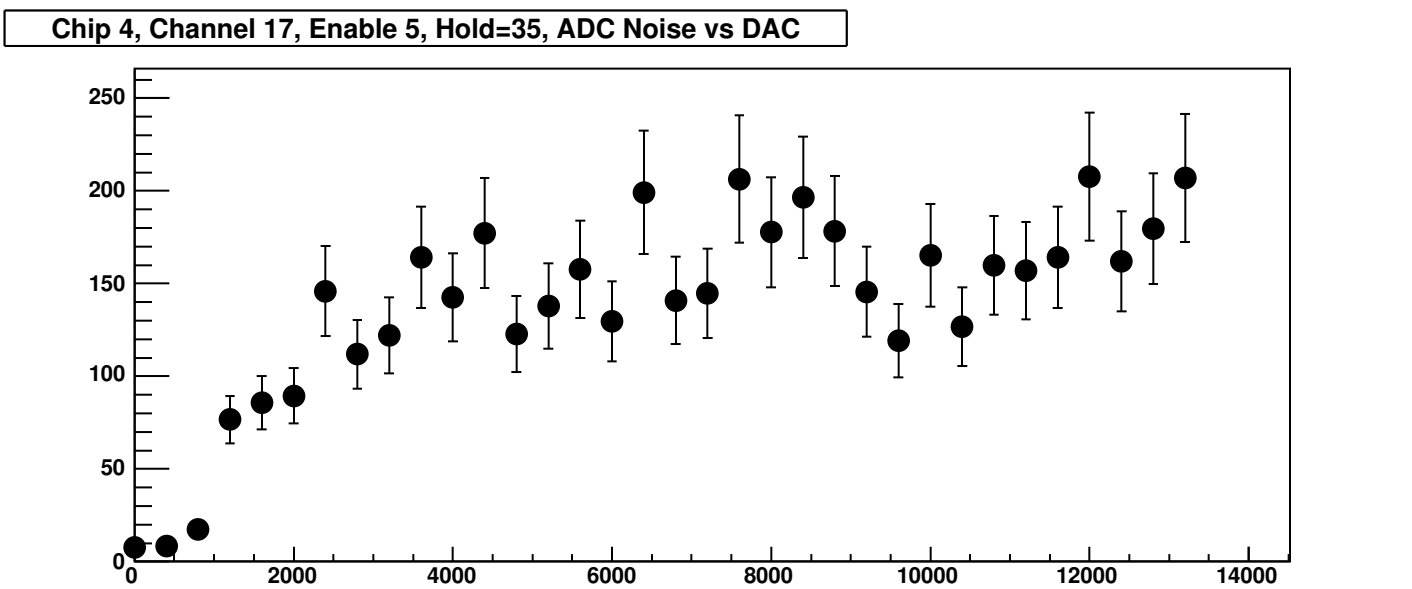

Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

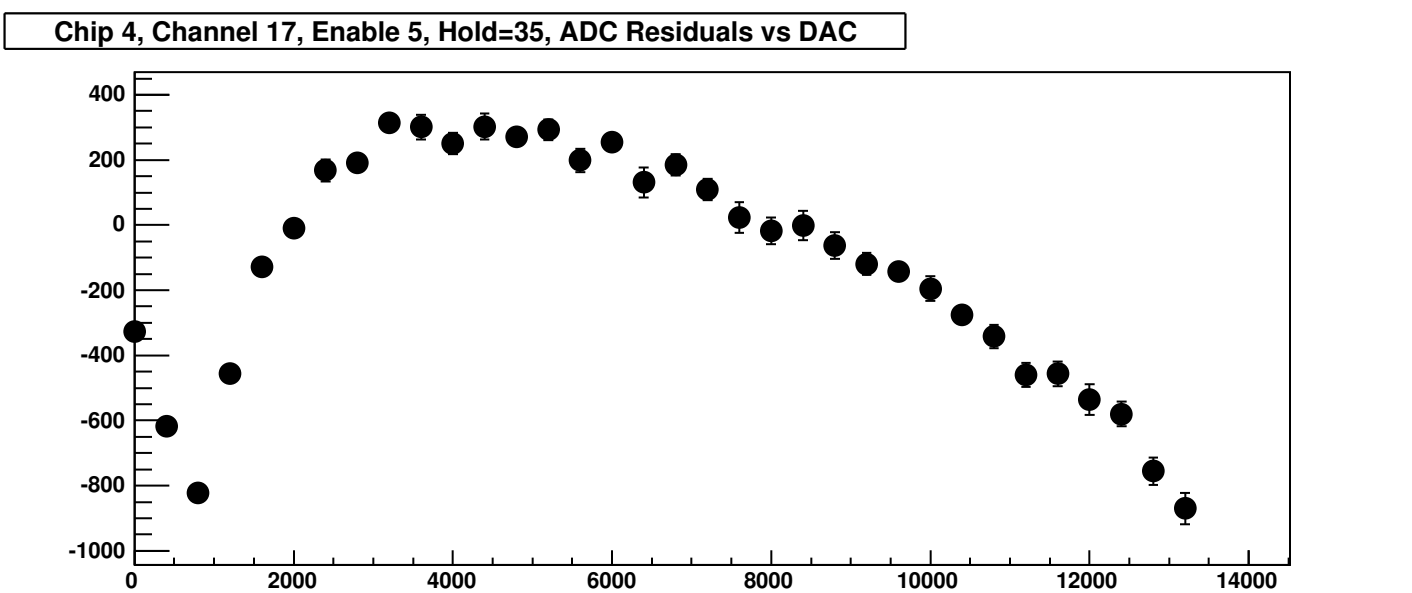

Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.47 / 23
p0 220.1 ± 0.7472
p1 0.01272 ± 0.0001133
400
380
360
340
320
300
280
260
240
220
0
2000
4000
6000
8000
10000
12000
14000

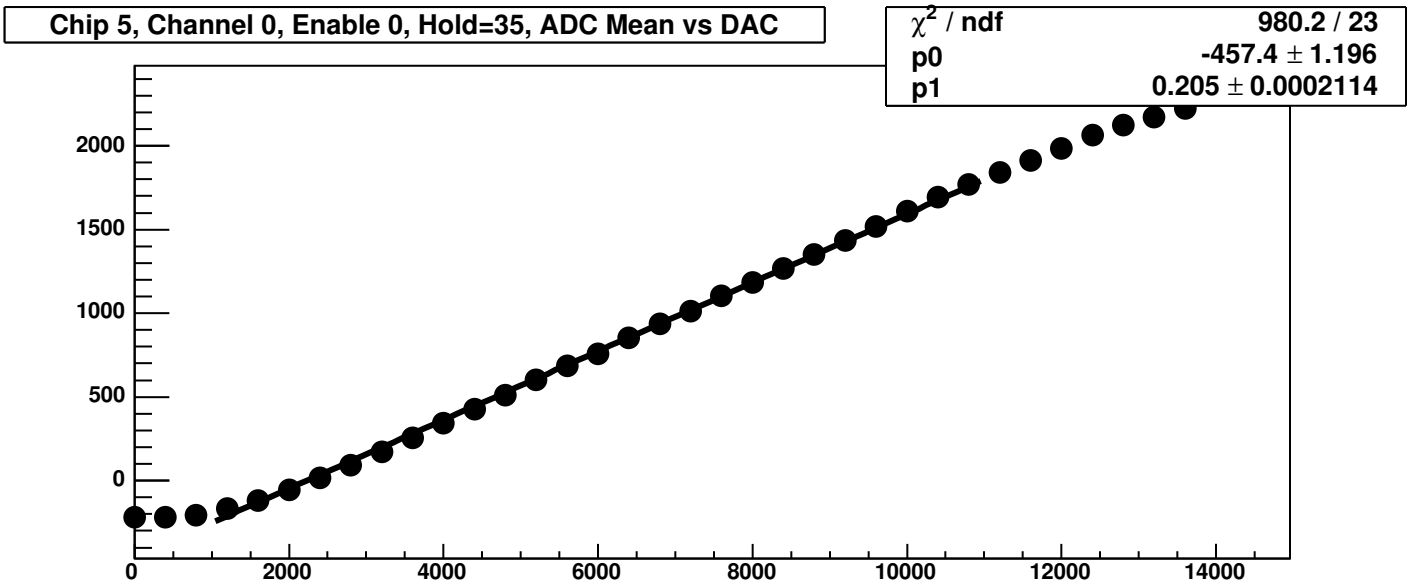

Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

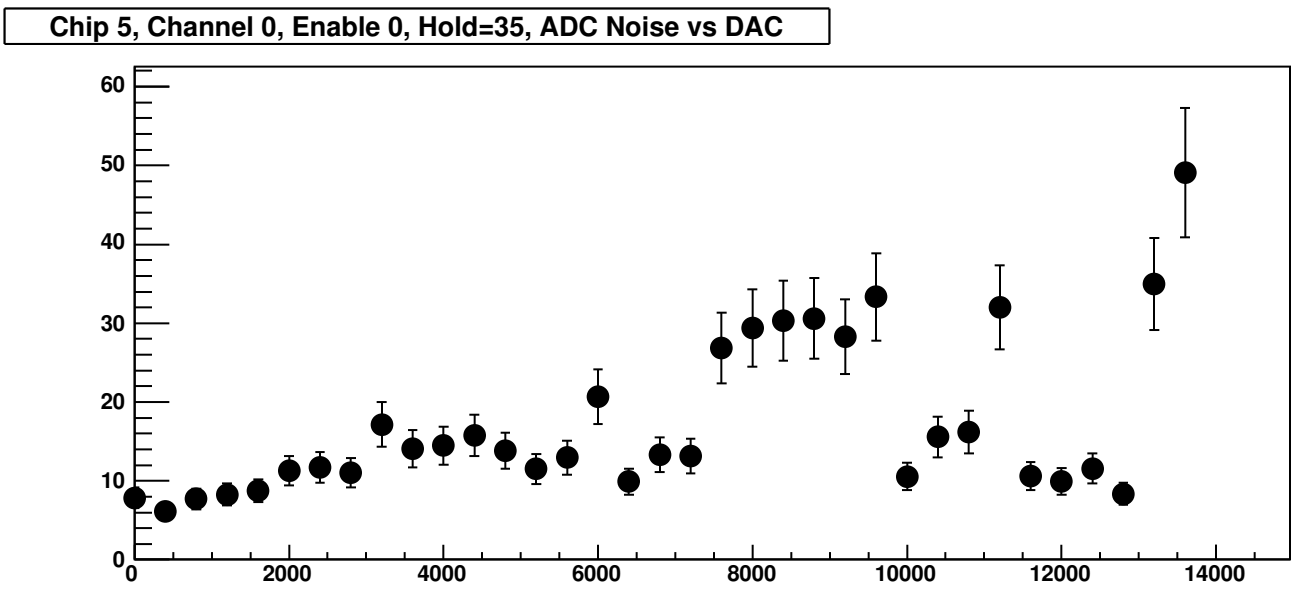

Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

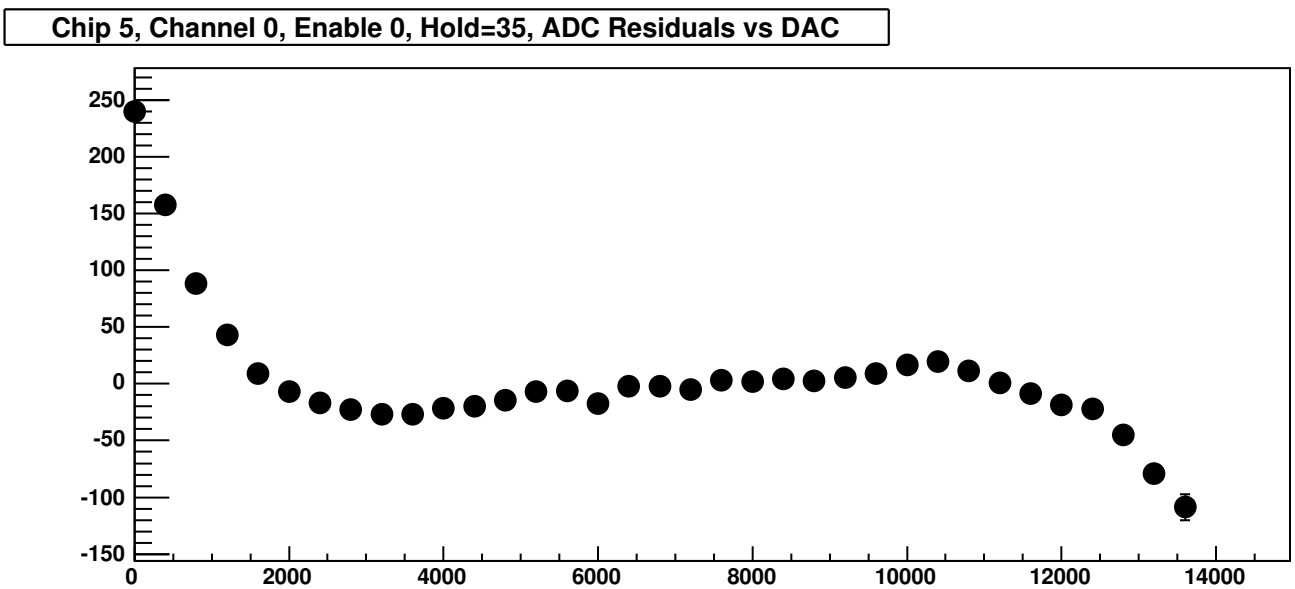

Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1680 / 23
p0 704.2 ± 11.24
p1 0.7305 ± 0.00192
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

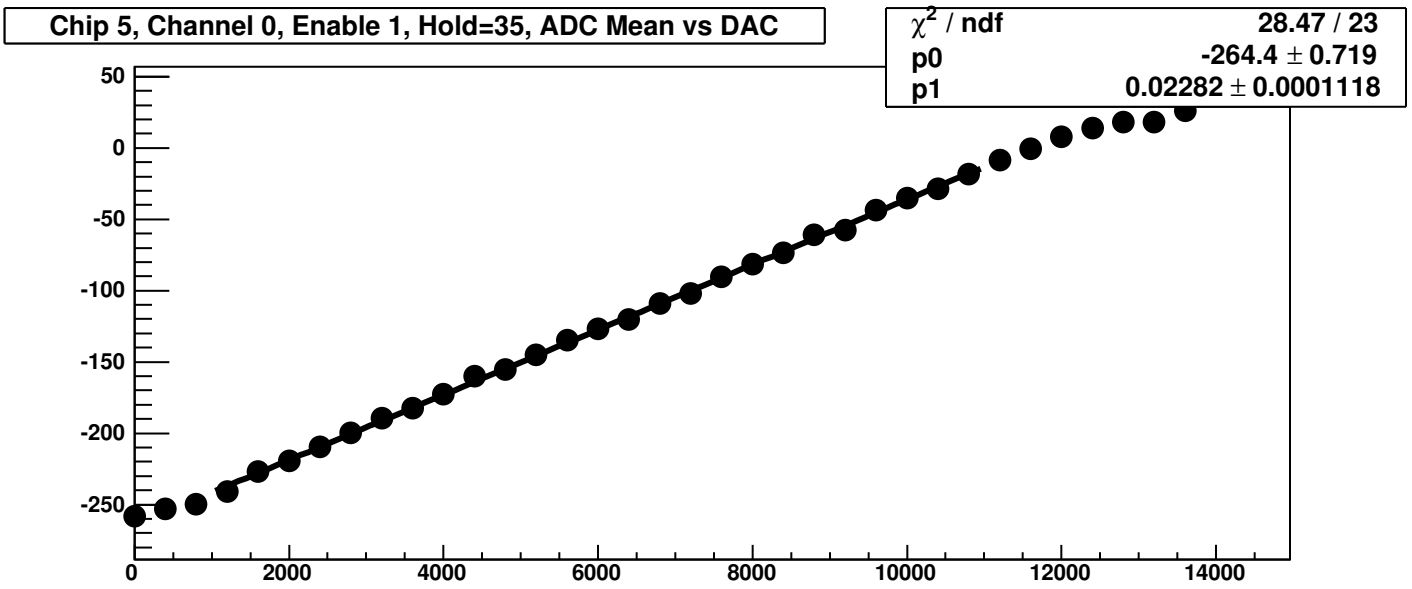

Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

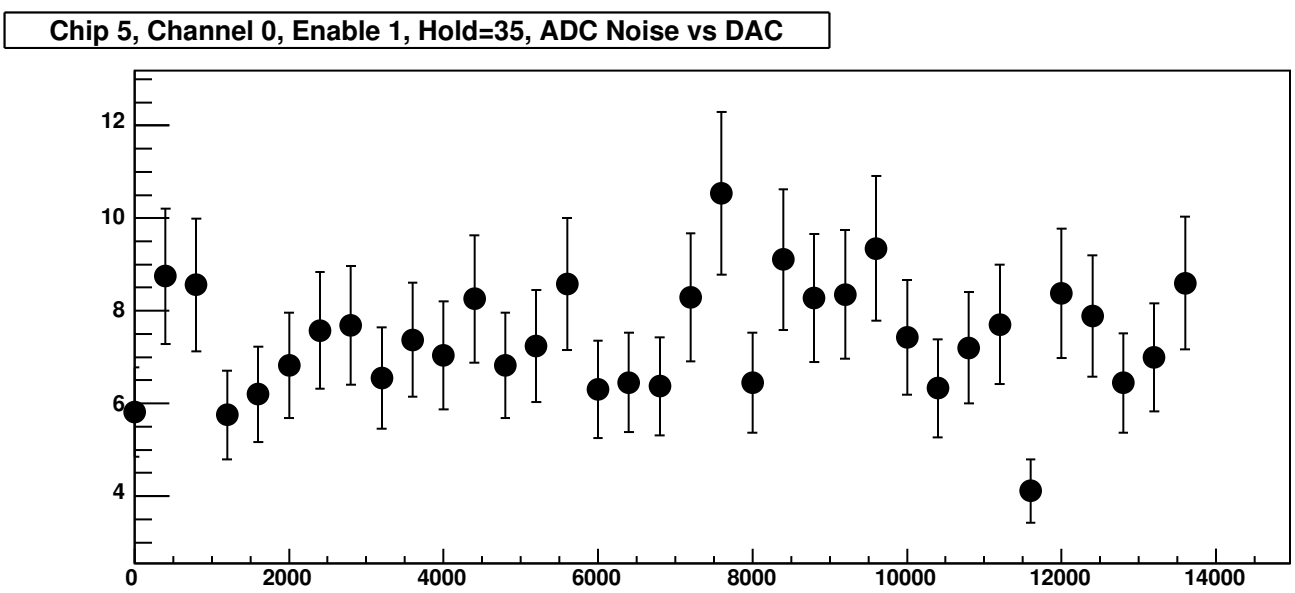

Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

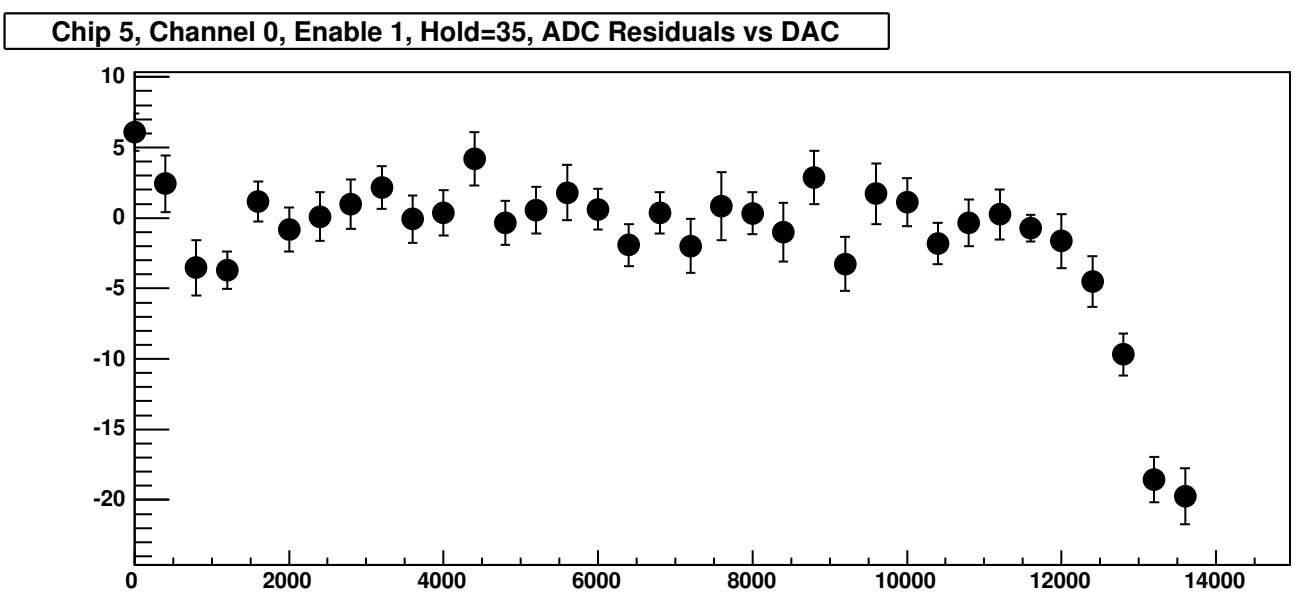

Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 980.2 / 23
p0 -457.4 ± 1.196
p1 0.205 ± 0.0002114
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

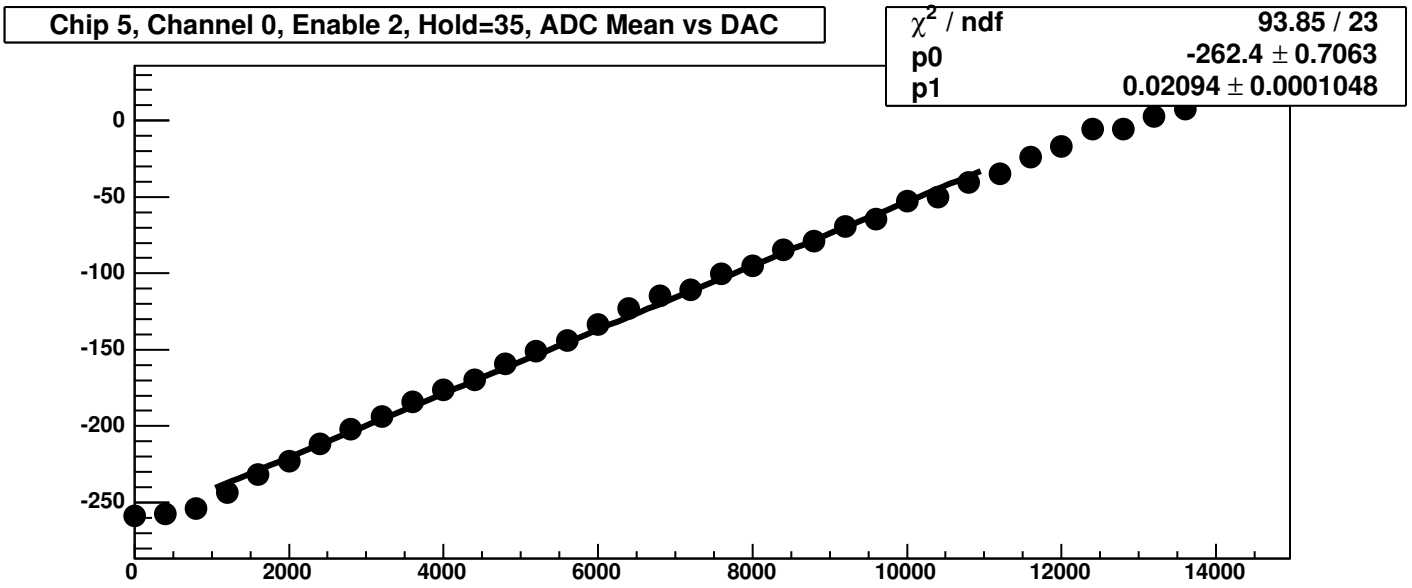

Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

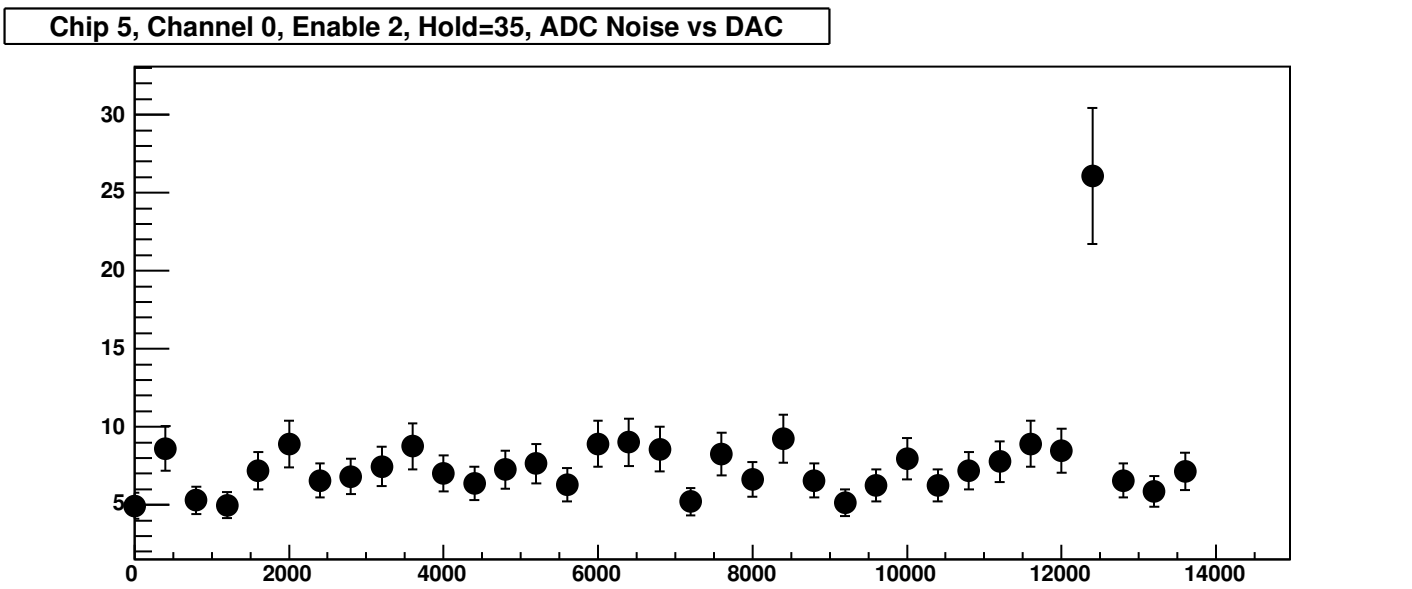

Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

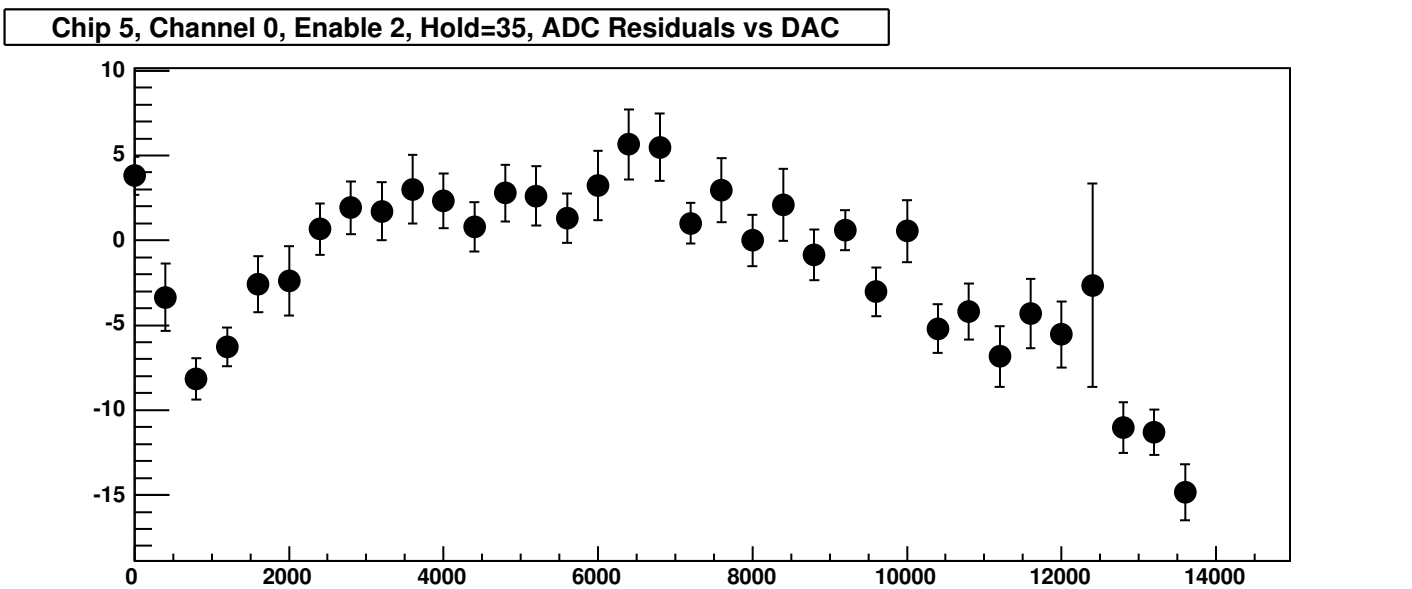

Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.47 / 23
p0 -264.4 ± 0.719
p1 0.02282 ± 0.0001118
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

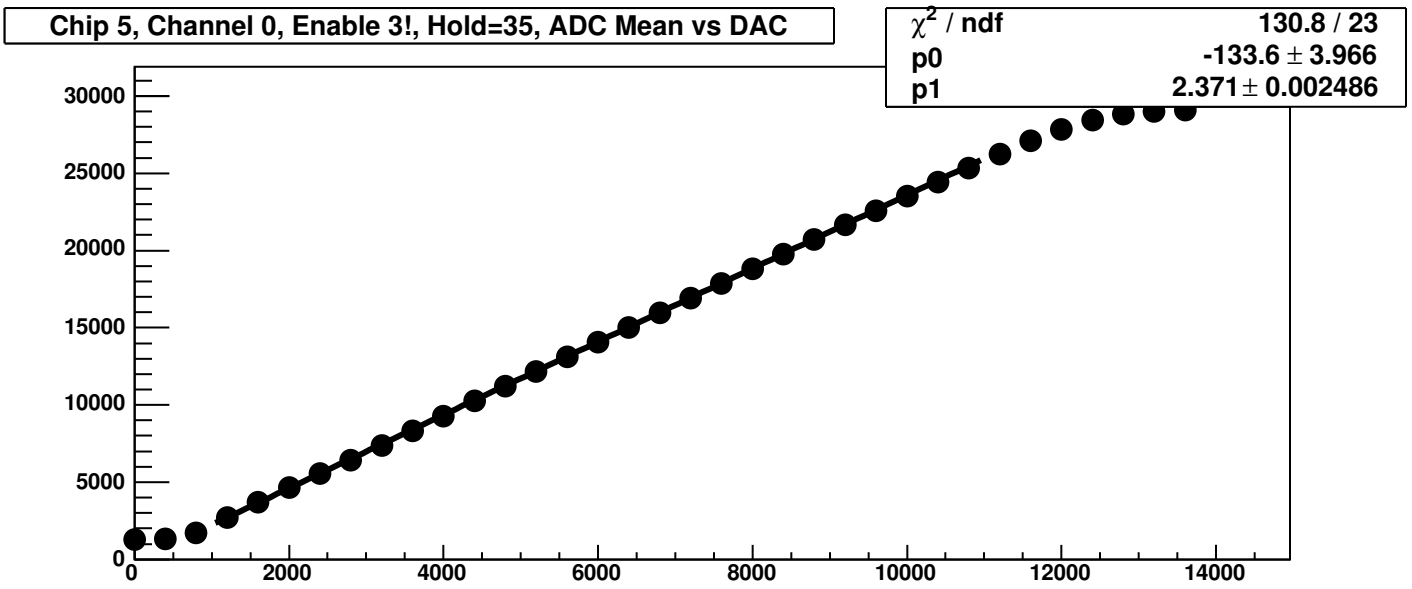

Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

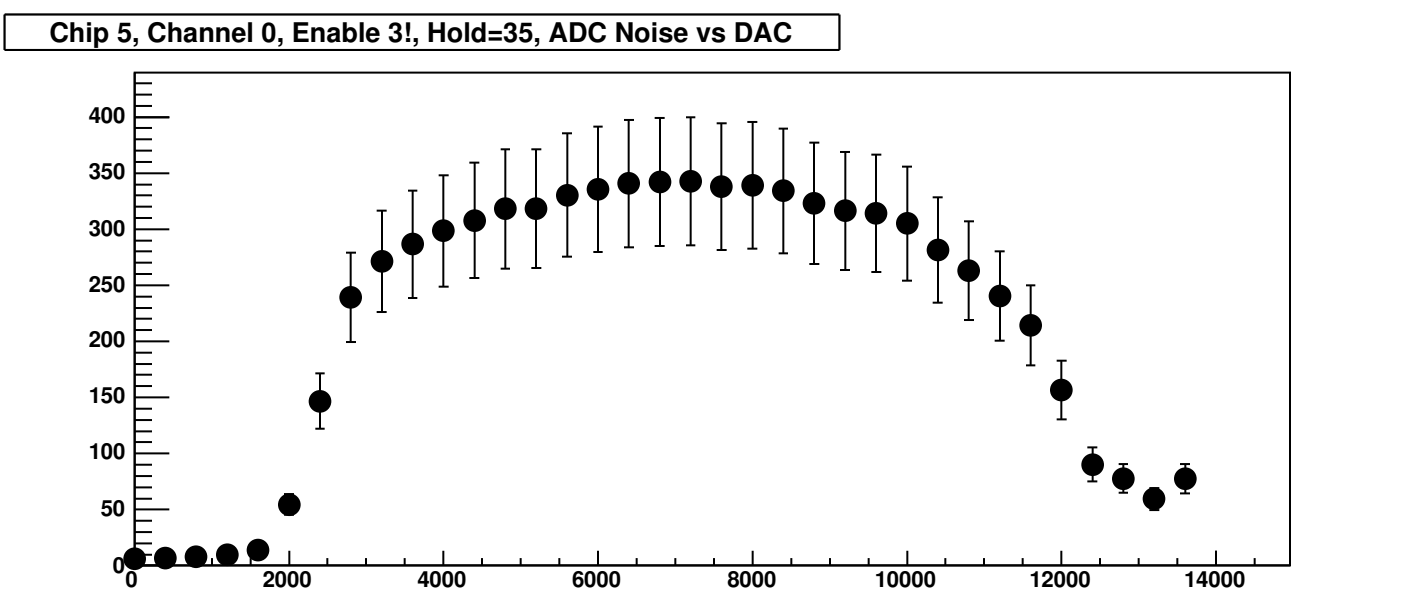

Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

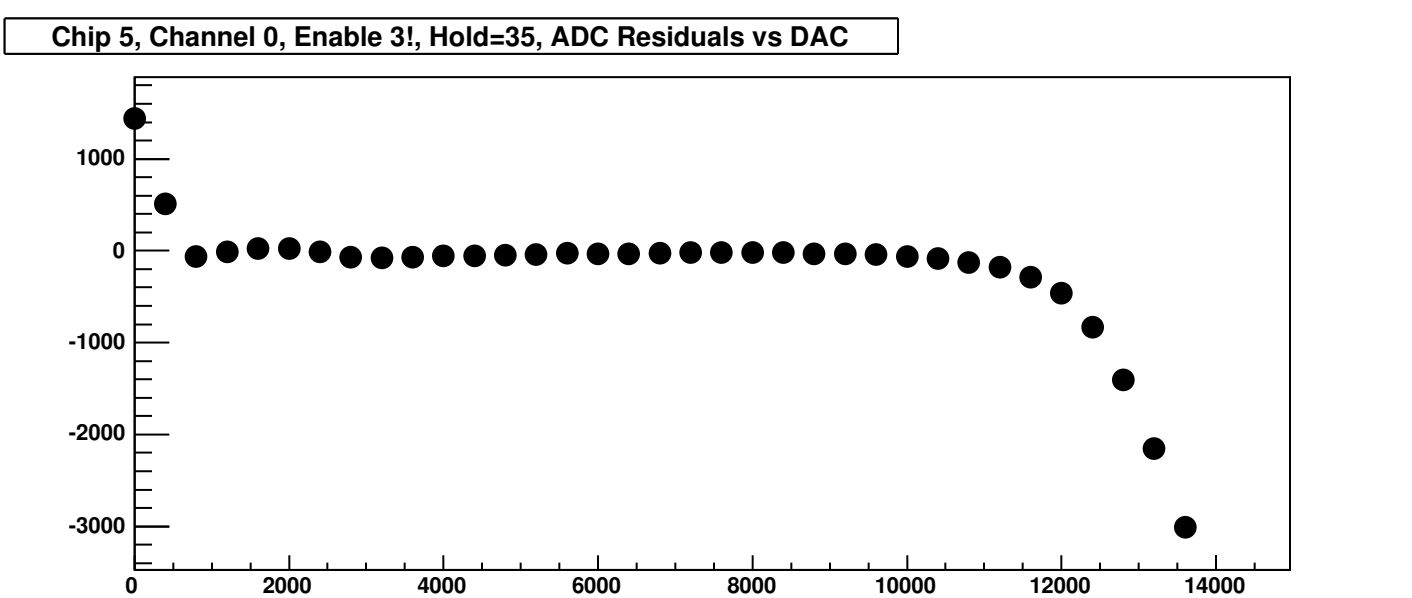

Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 93.85 / 23
p0 -262.4 ± 0.7063
p1 0.02094 ± 0.0001048
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

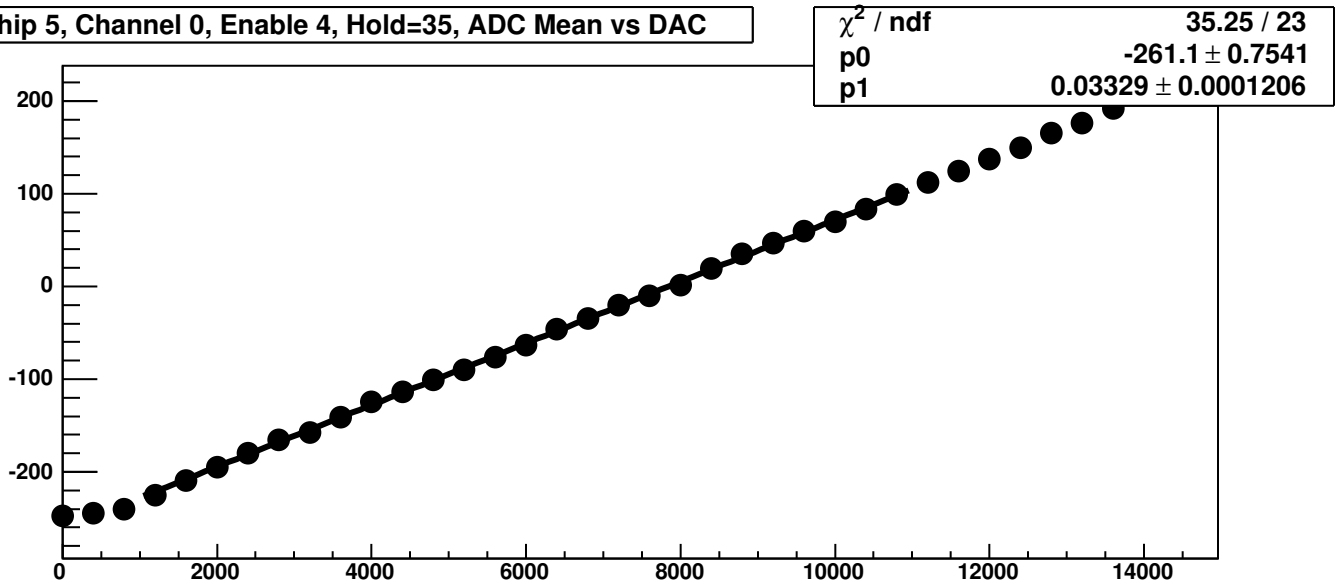

Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 130.8 / 23
p0 -133.6 ± 3.966
p1 2.371 ± 0.002486
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

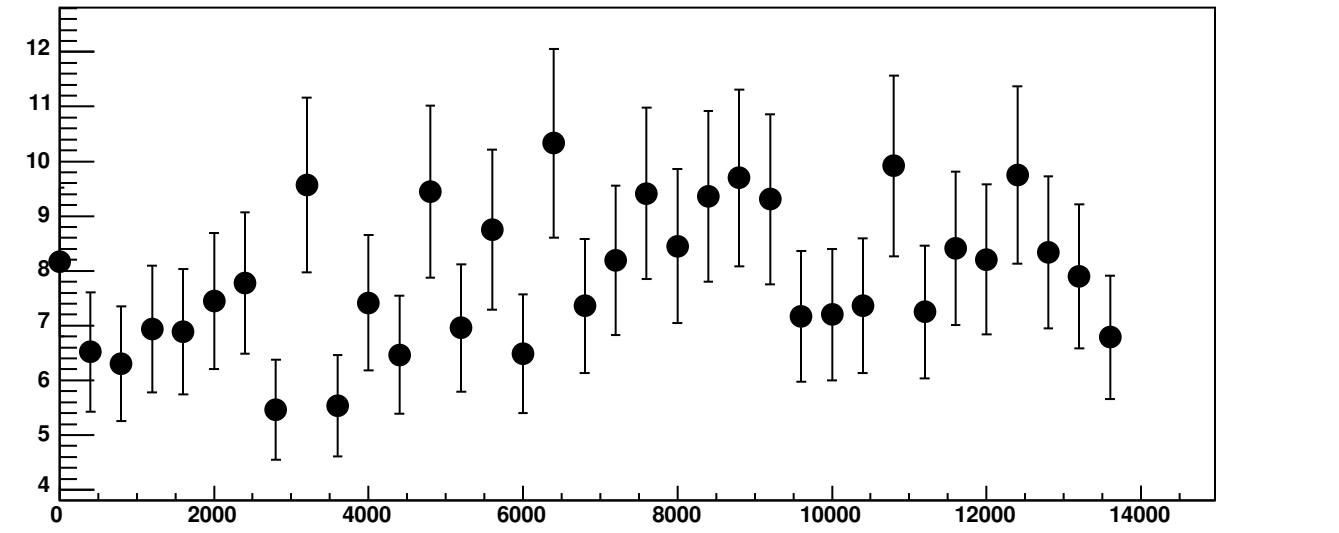

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

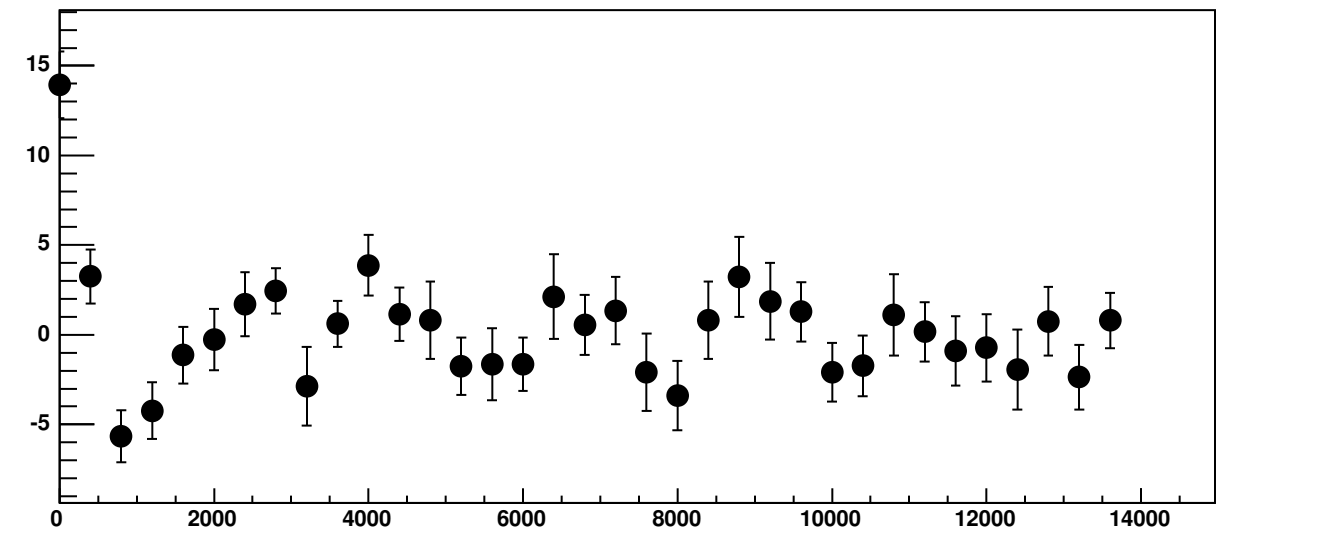
Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

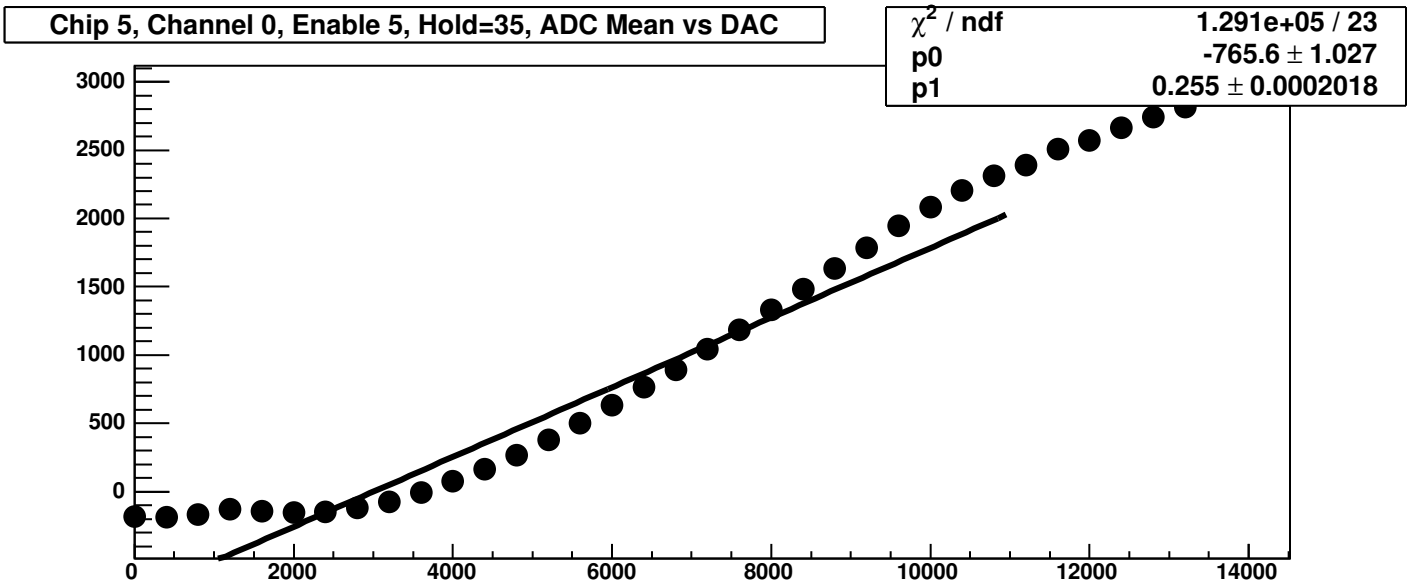


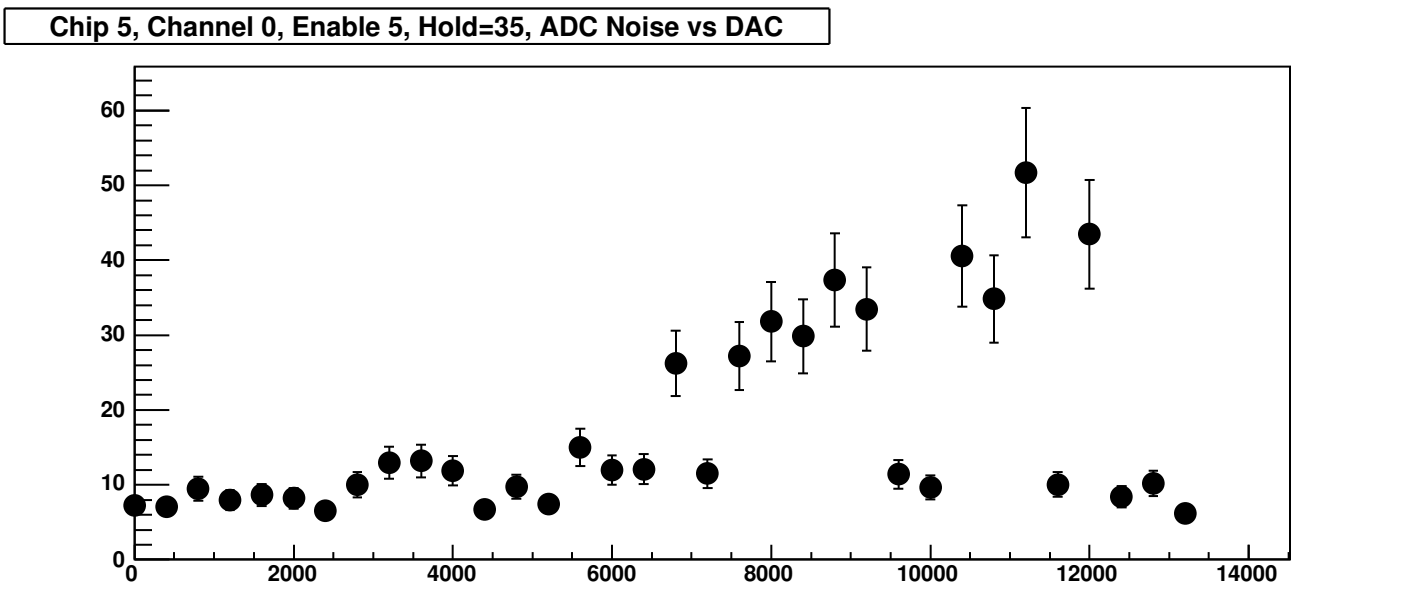
Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

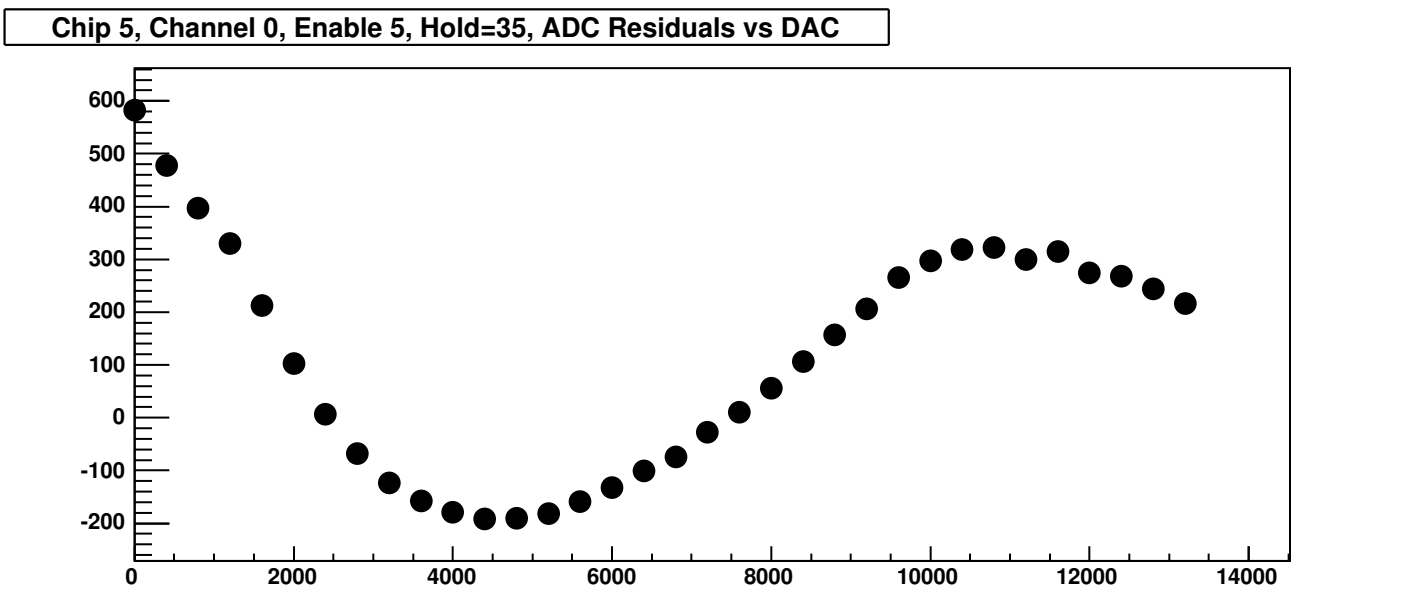


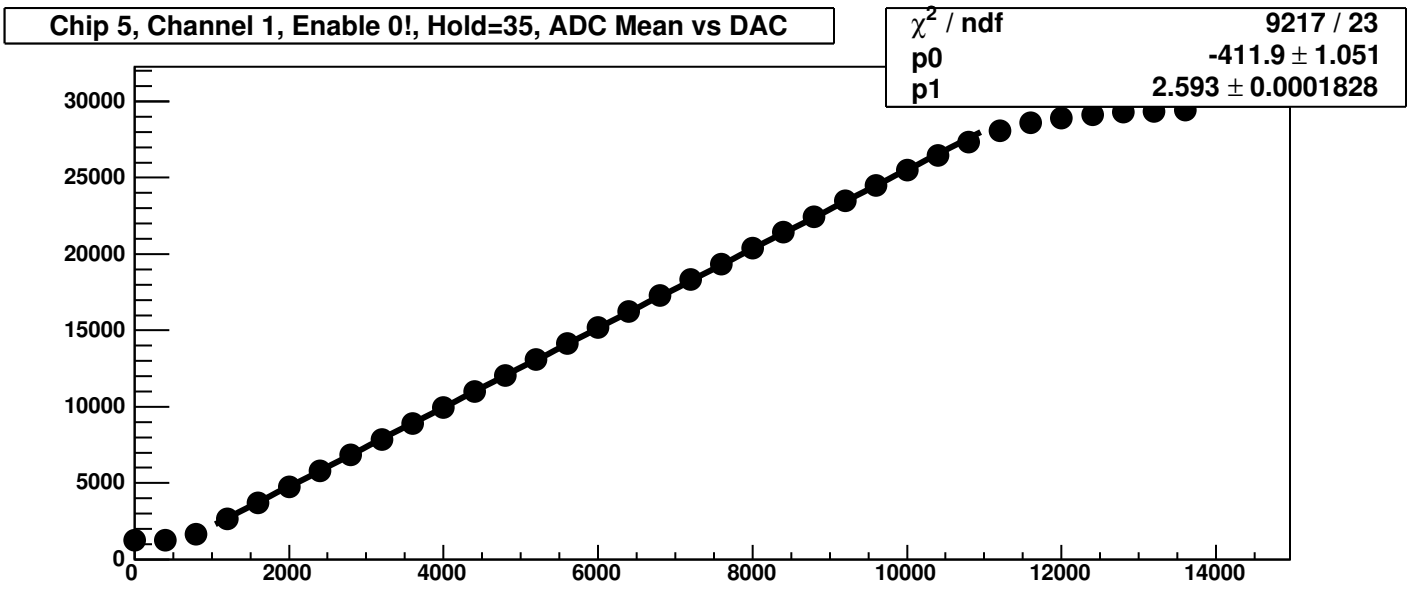
Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

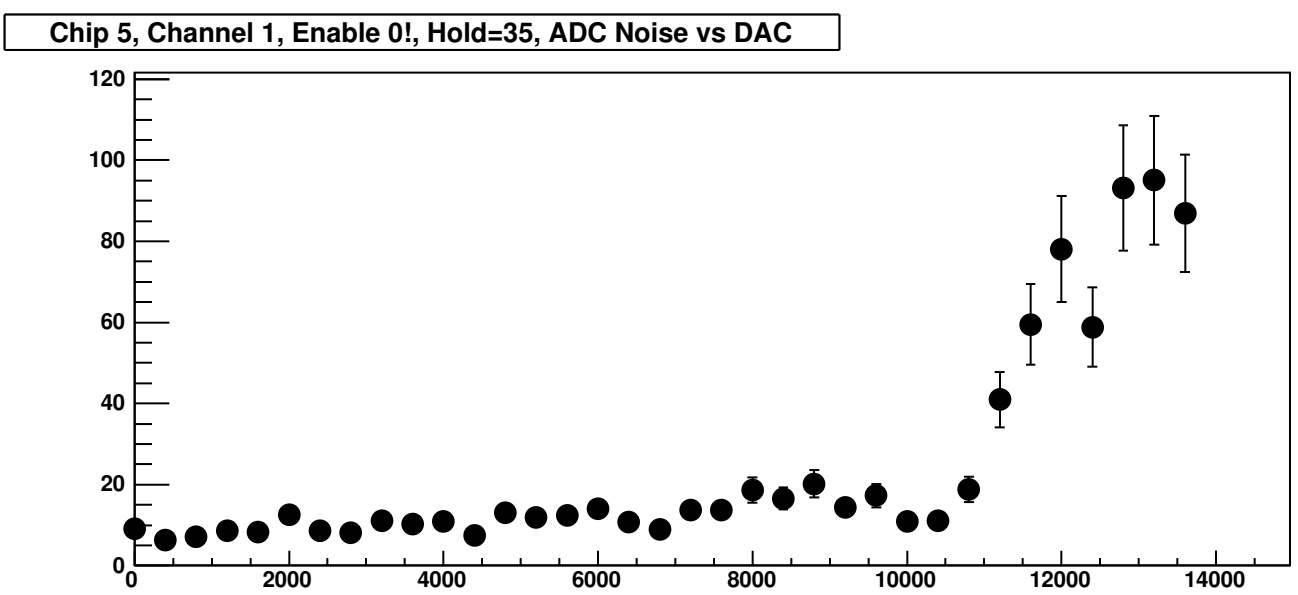


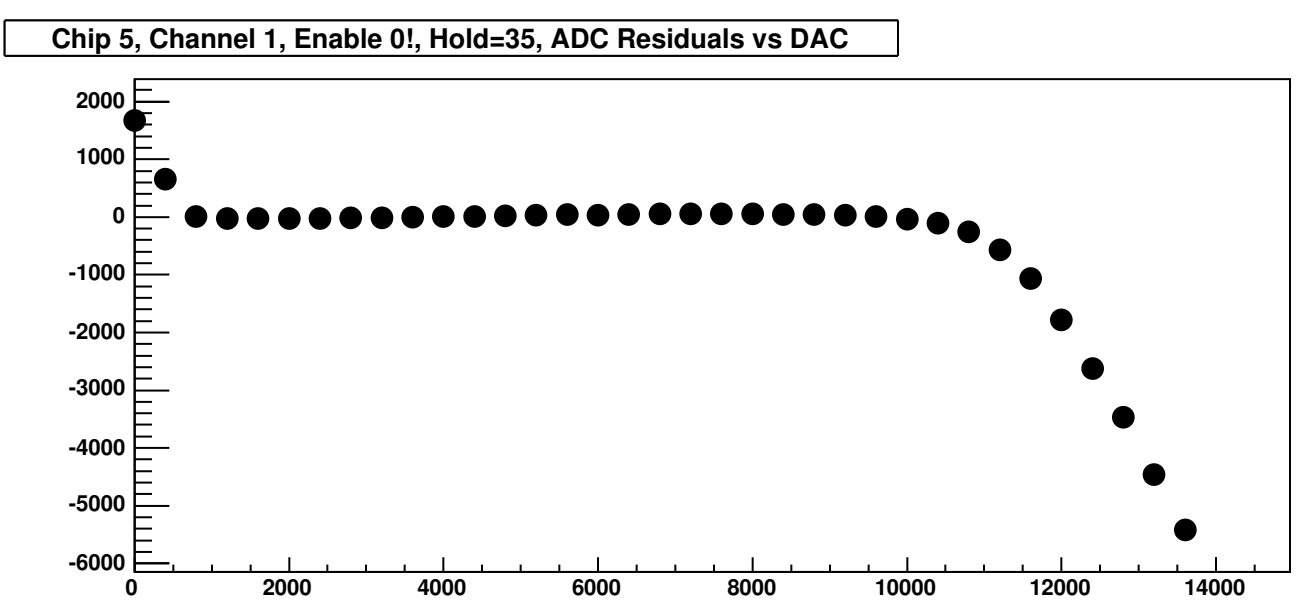

Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.291e+05 / 23
p0 -765.6 ± 1.027
p1 0.255 ± 0.0002018
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

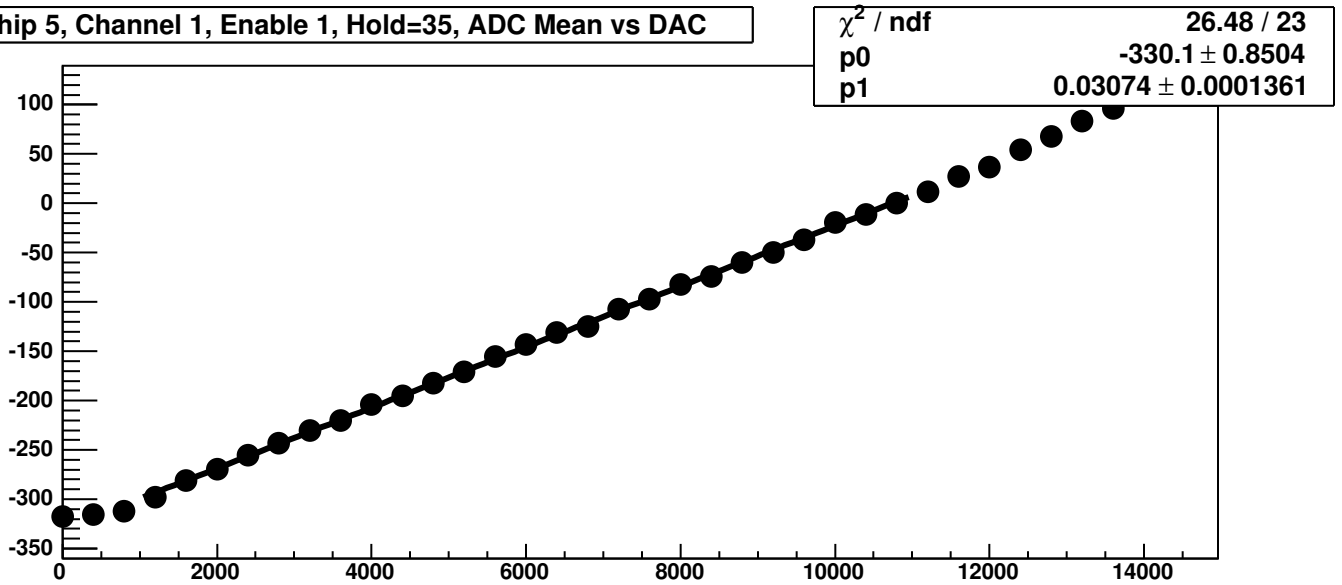

Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 9217 / 23
p0 -411.9 ± 1.051
p1 2.593 ± 0.0001828
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

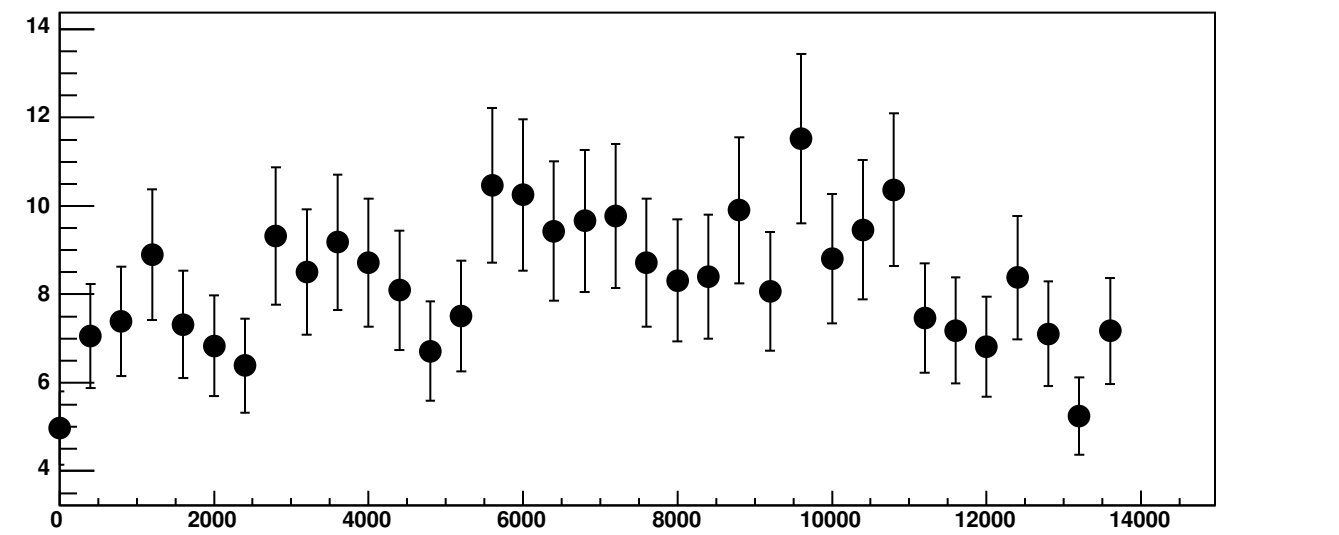

Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

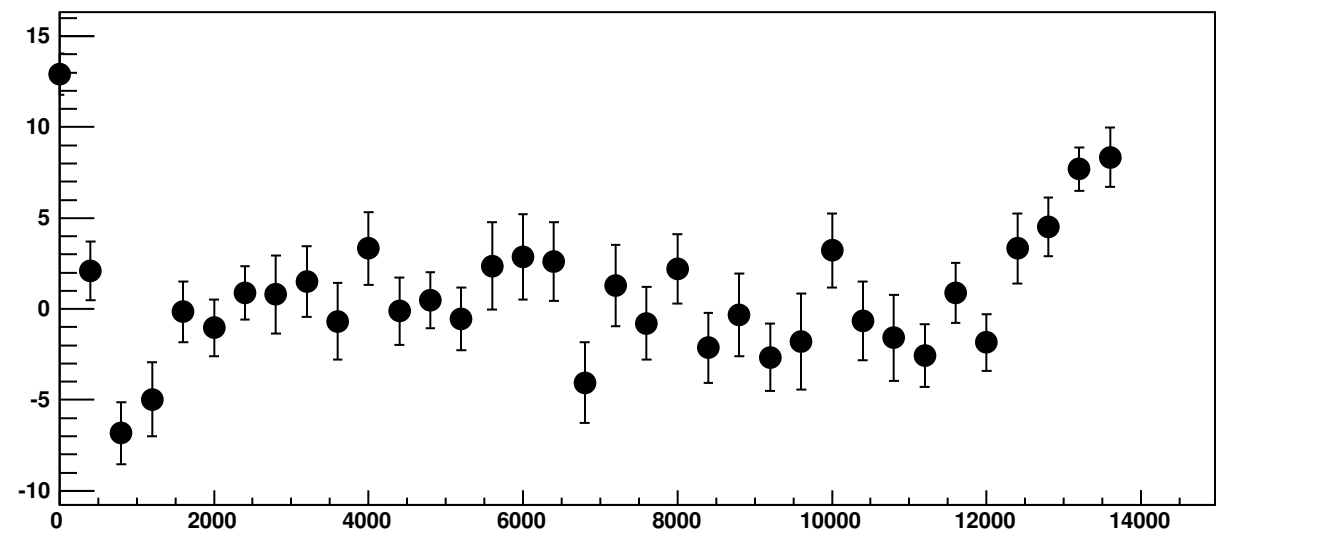
Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



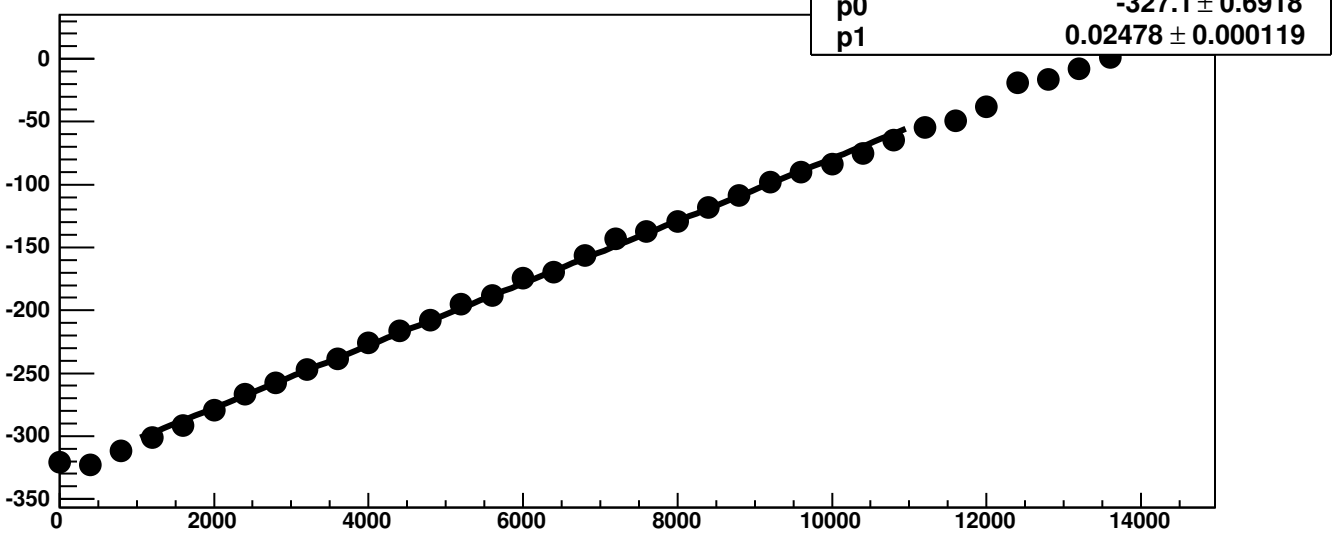
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



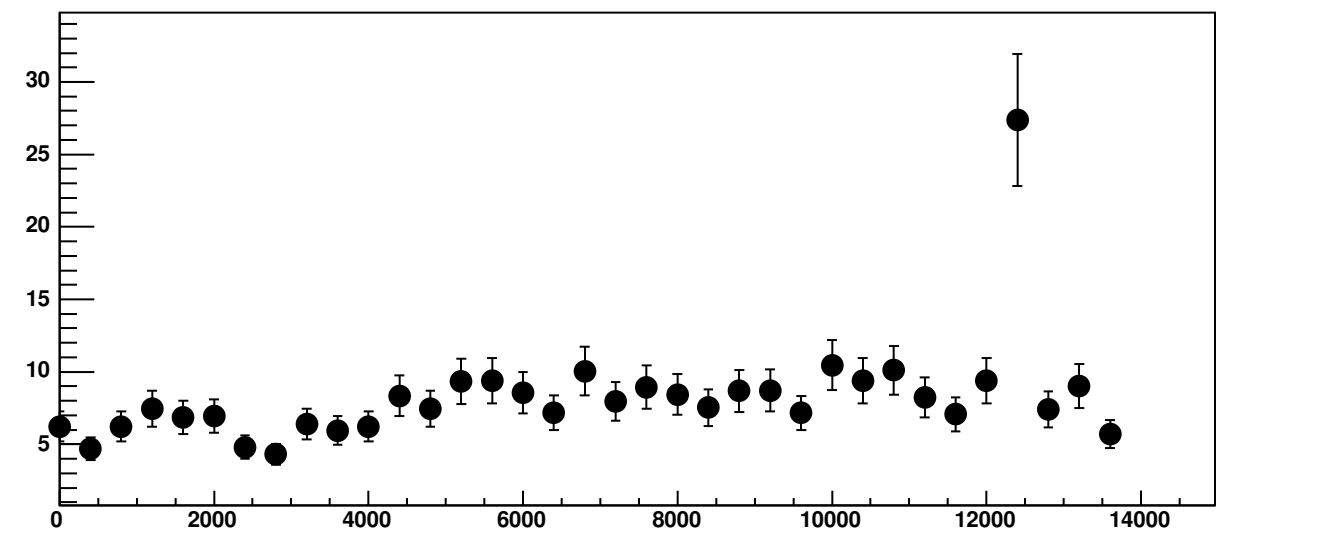
Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



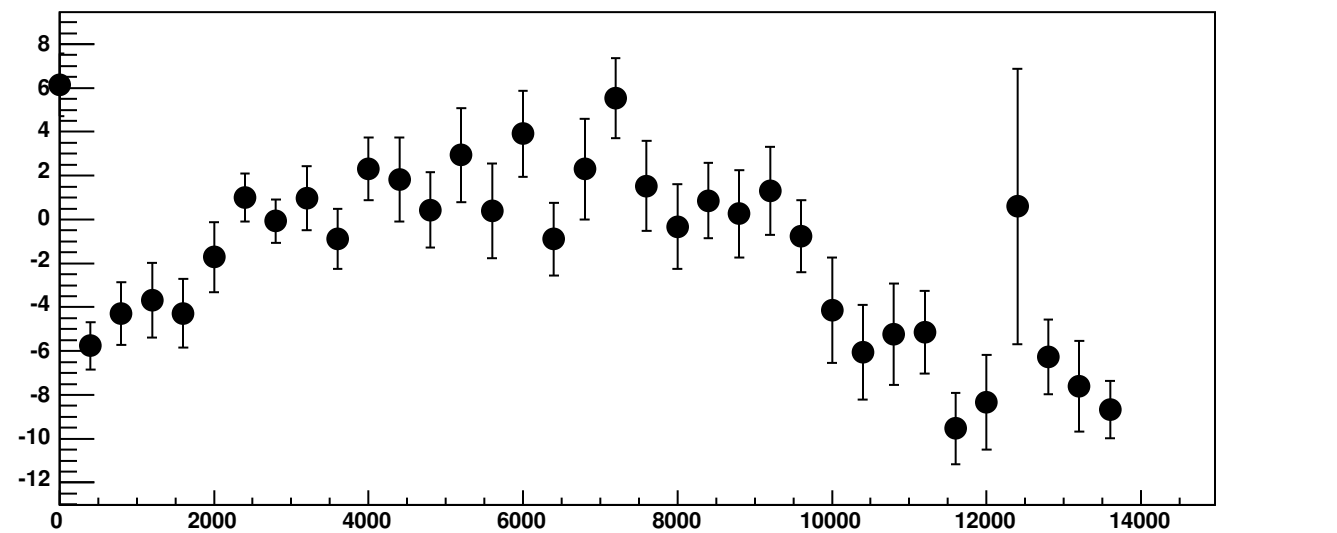
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

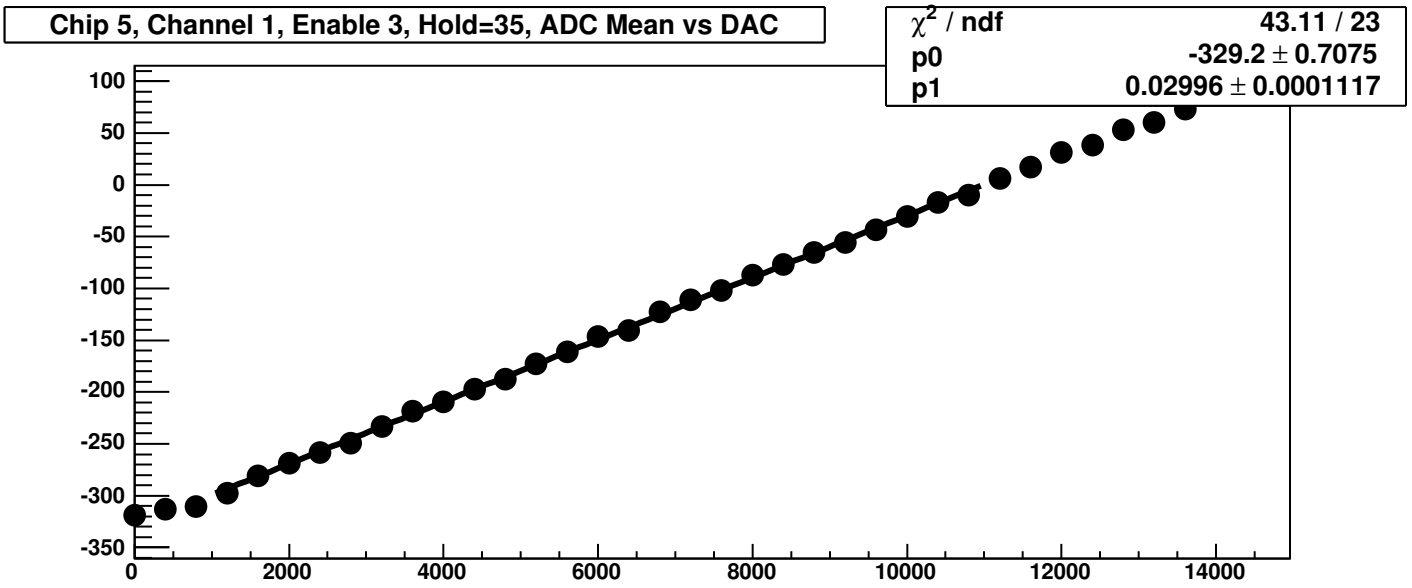


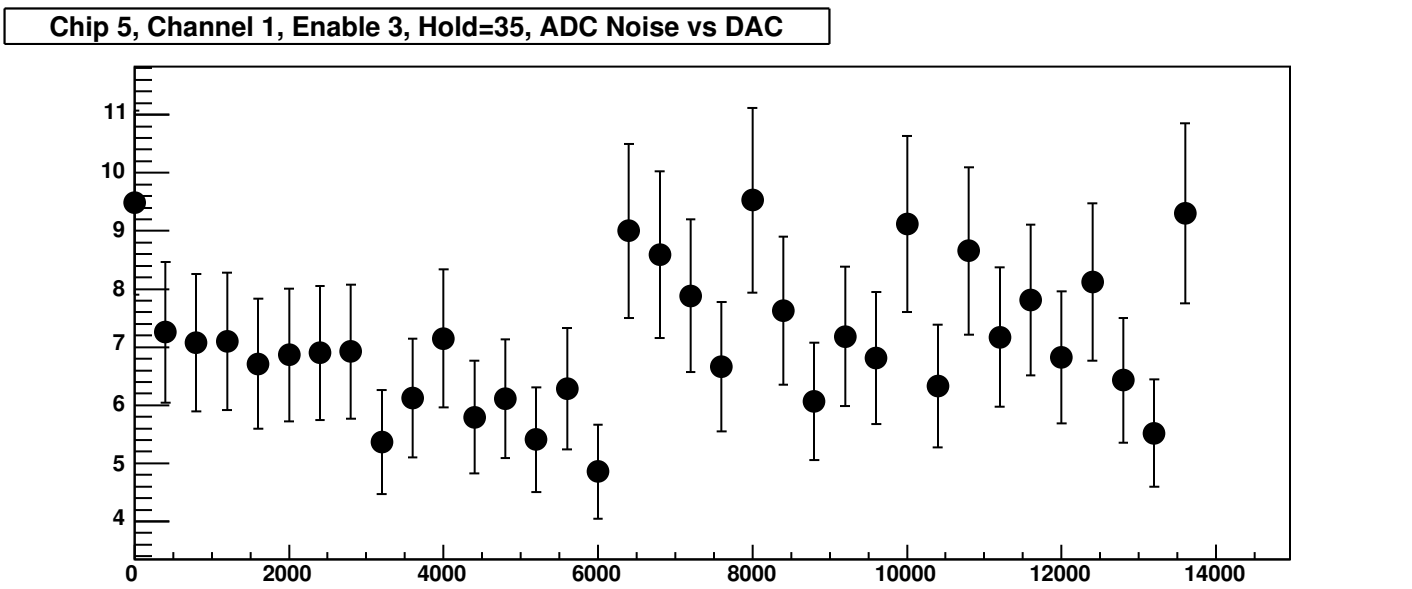
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

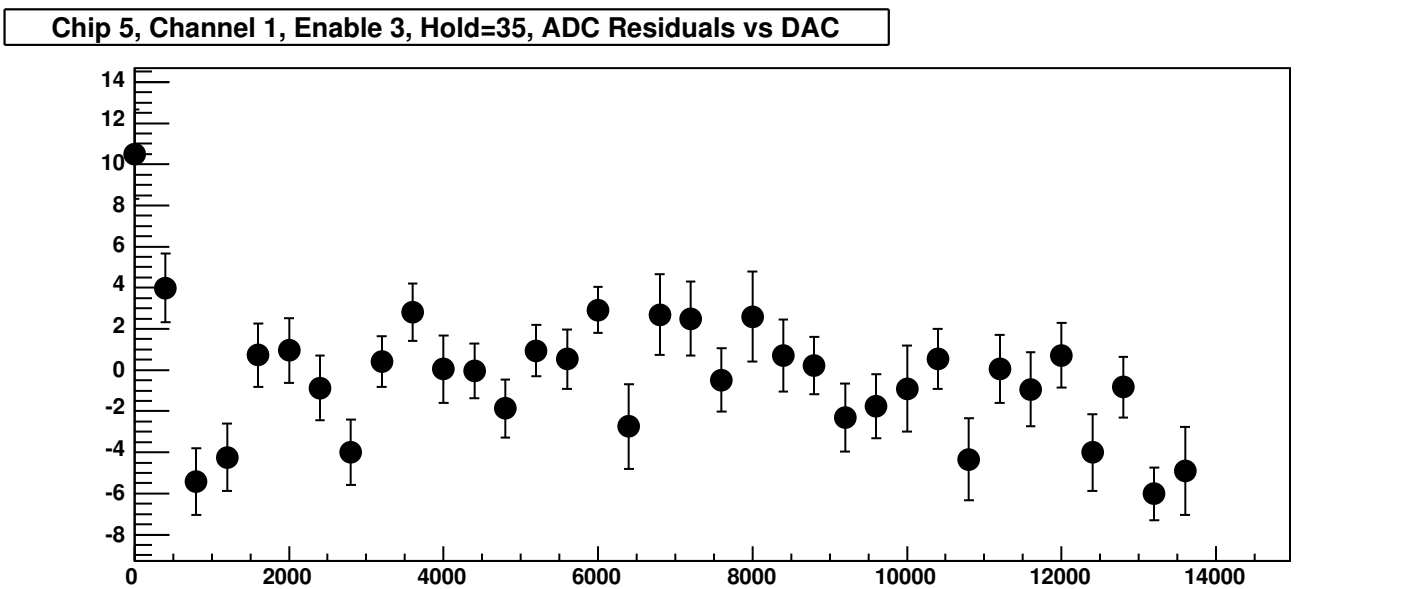


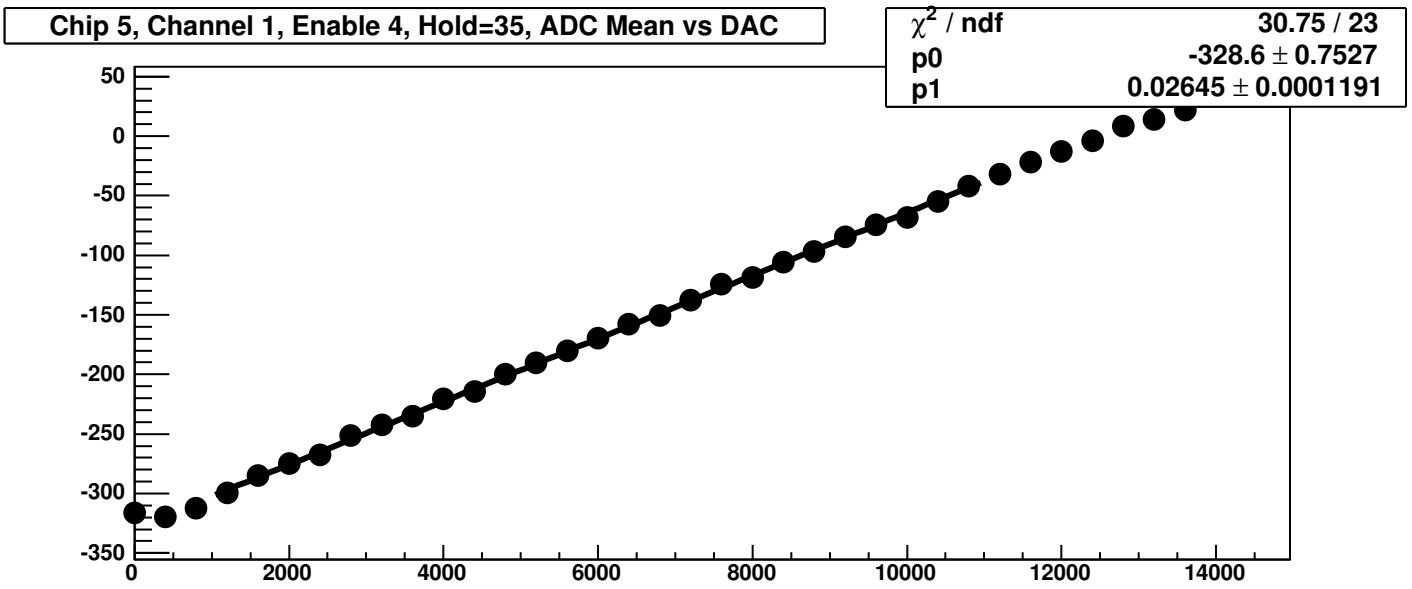
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

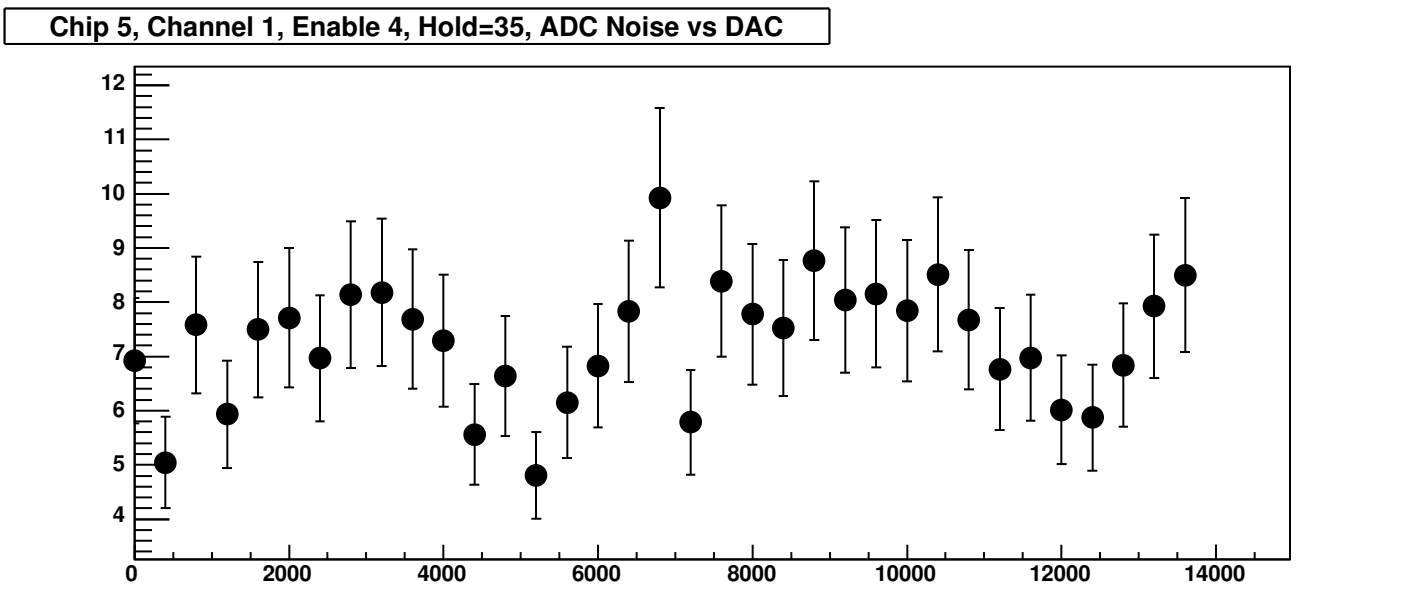


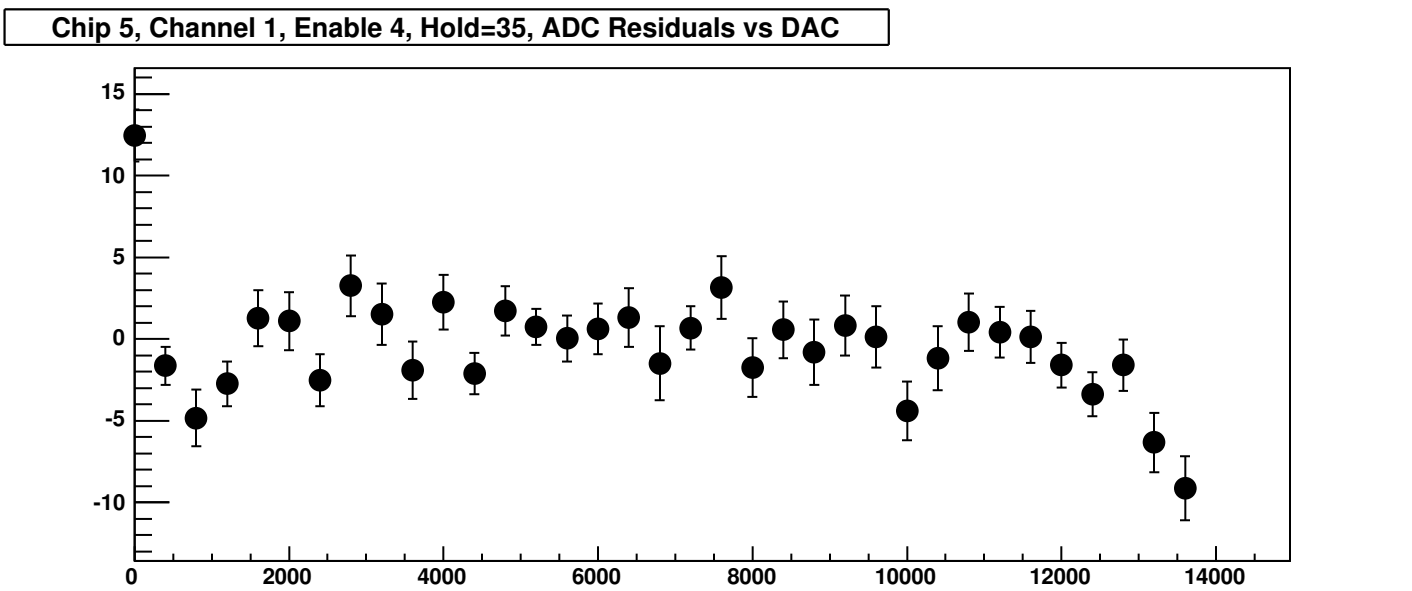

Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.11 / 23
p0 -329.2 ± 0.7075
p1 0.02996 ± 0.0001117
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

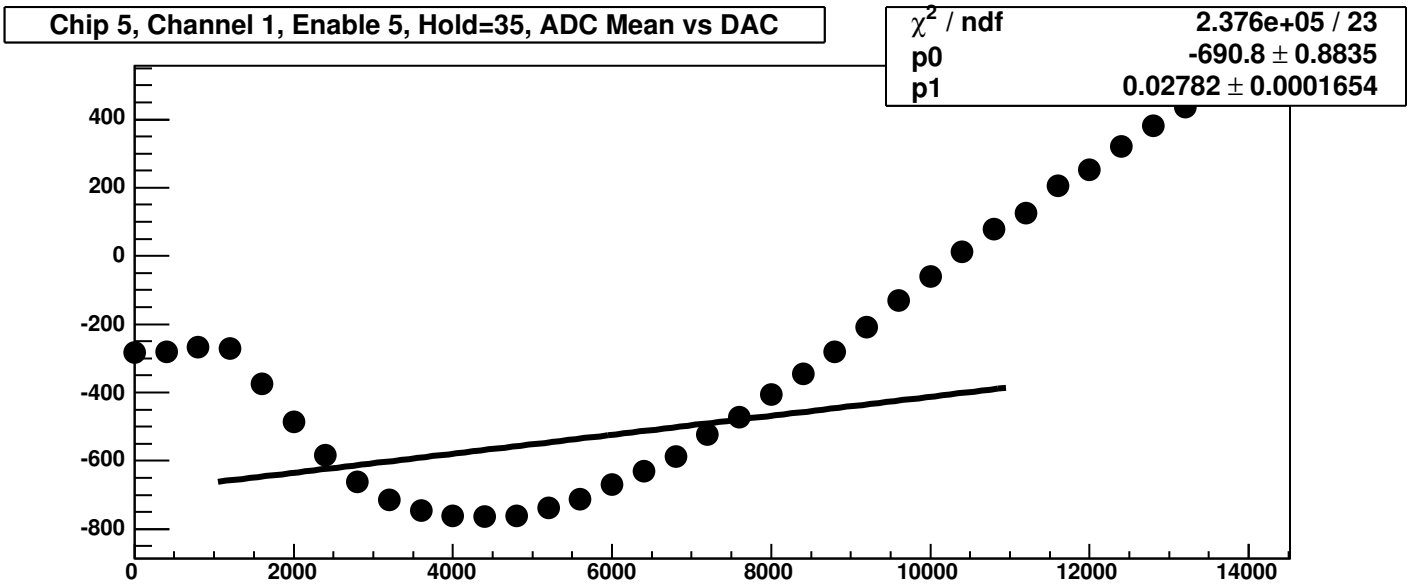

Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

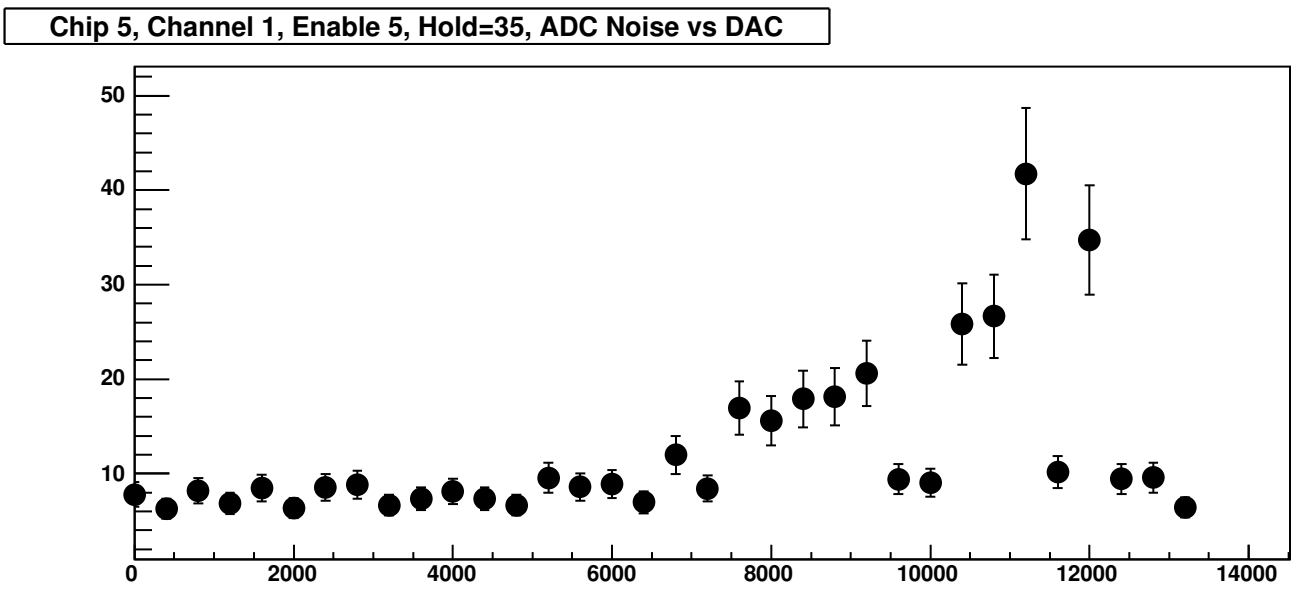

Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

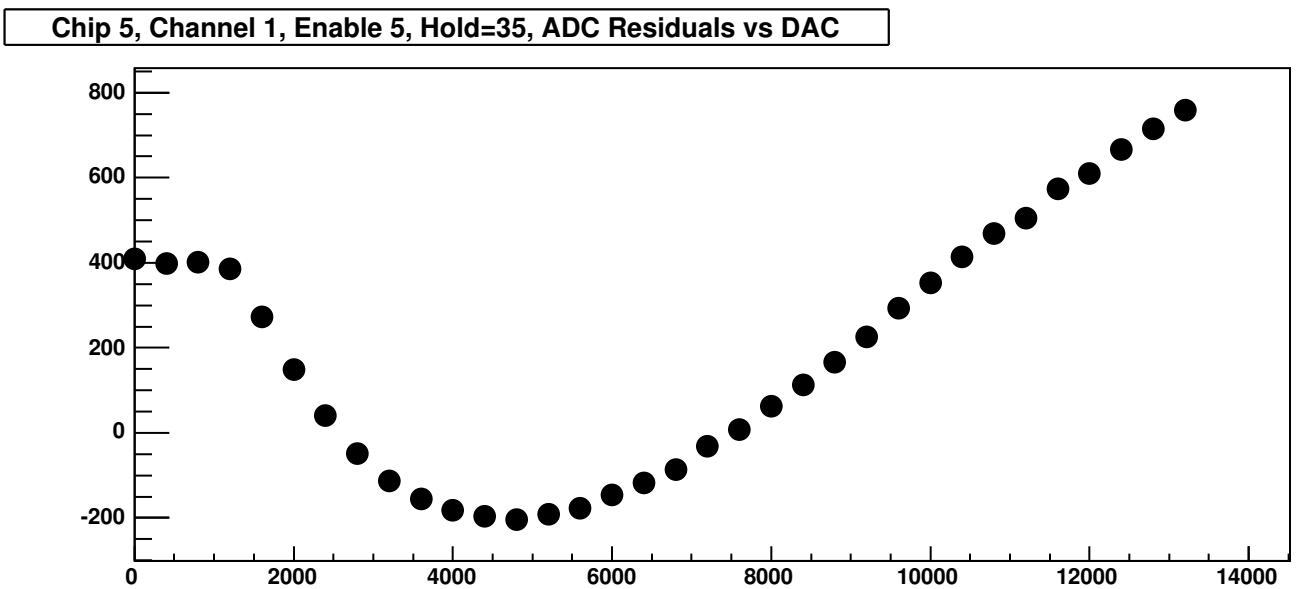

Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.75 / 23
p0 -328.6 ± 0.7527
p1 0.02645 ± 0.0001191
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

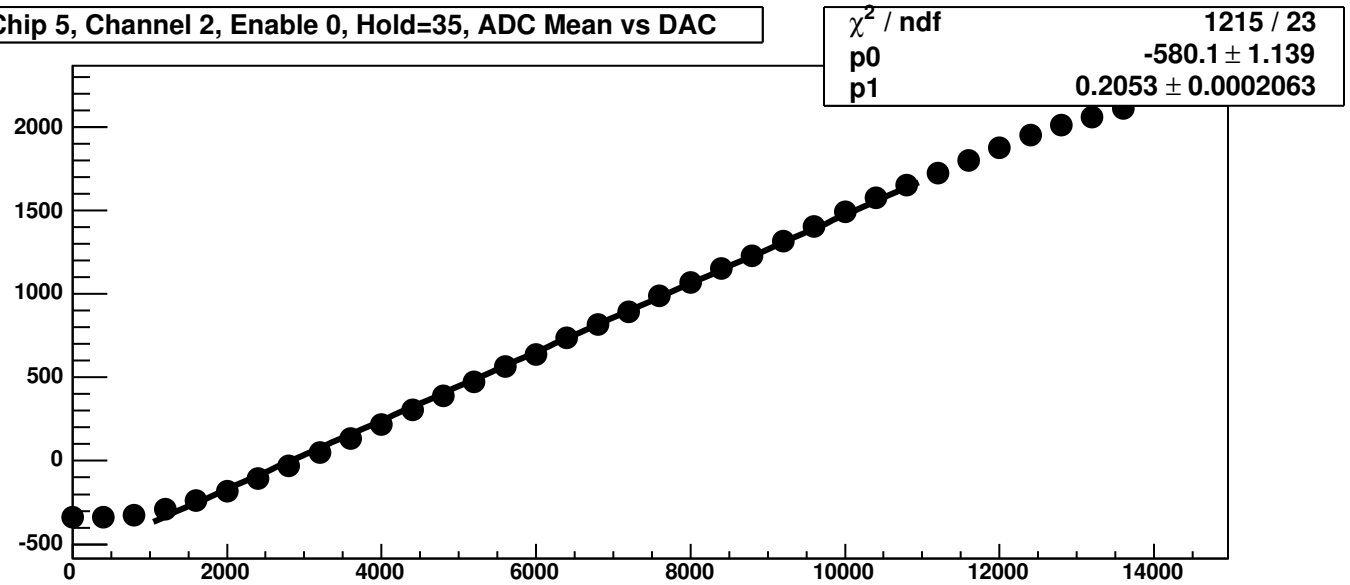

Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.376e+05 / 23
p0 -690.8 ± 0.8835
p1 0.02782 ± 0.0001654
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

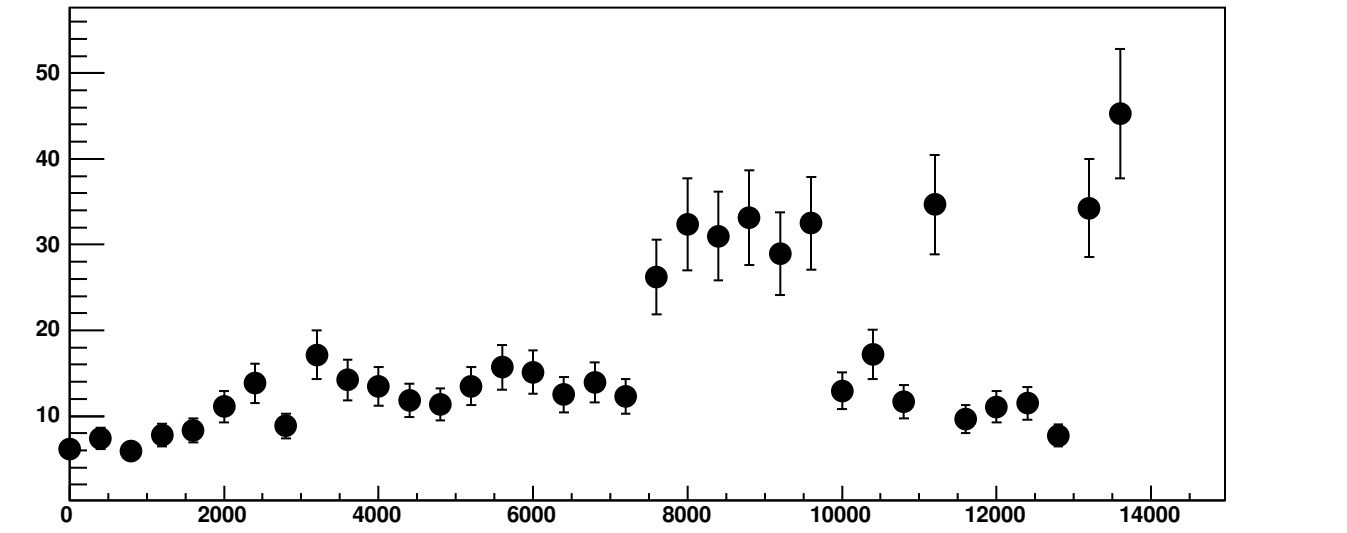

Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

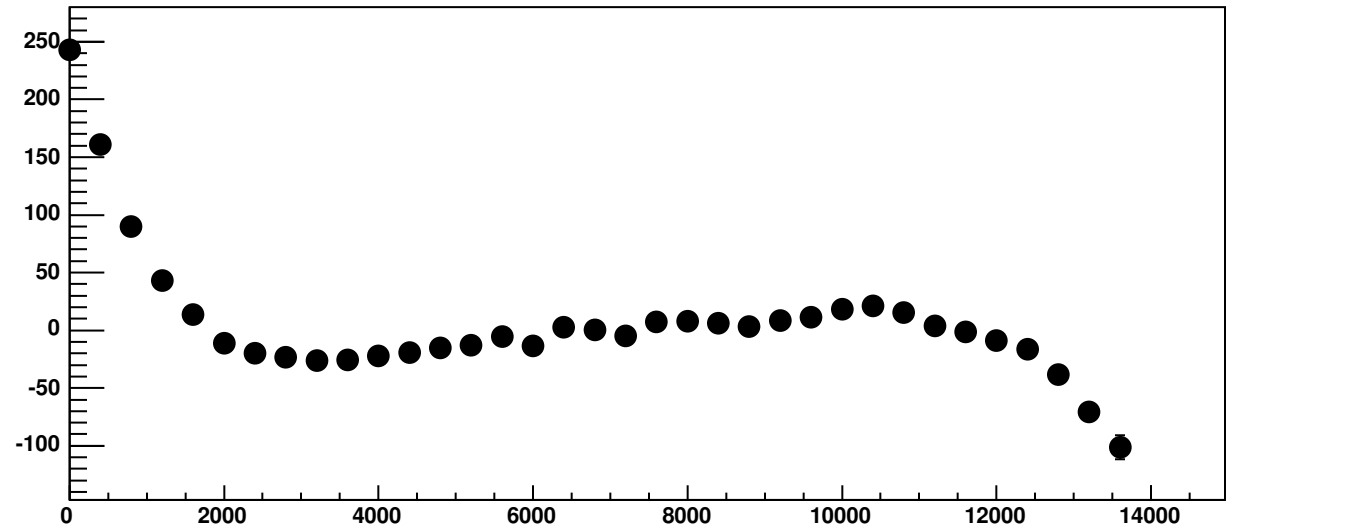
Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

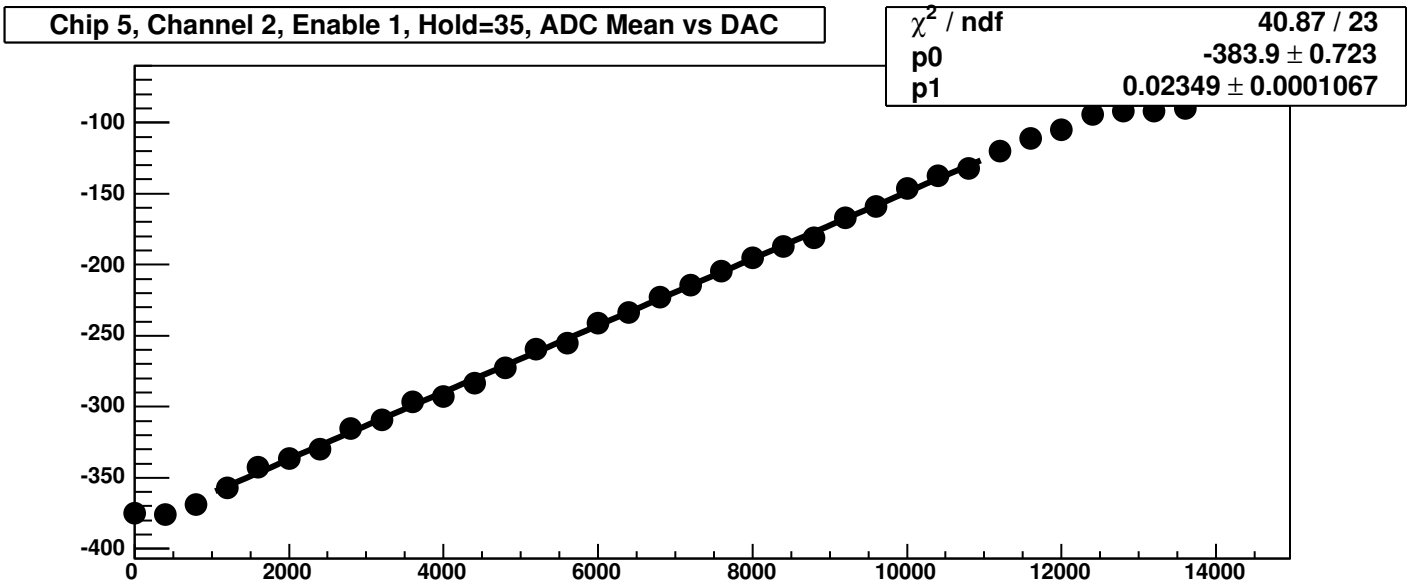


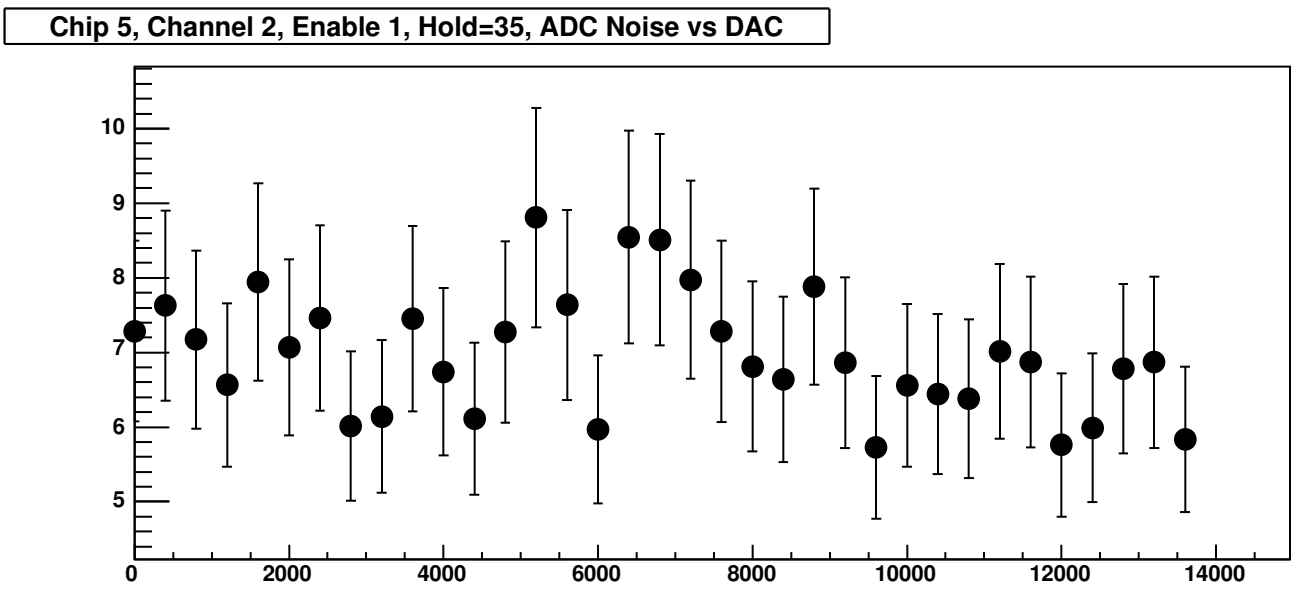
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC

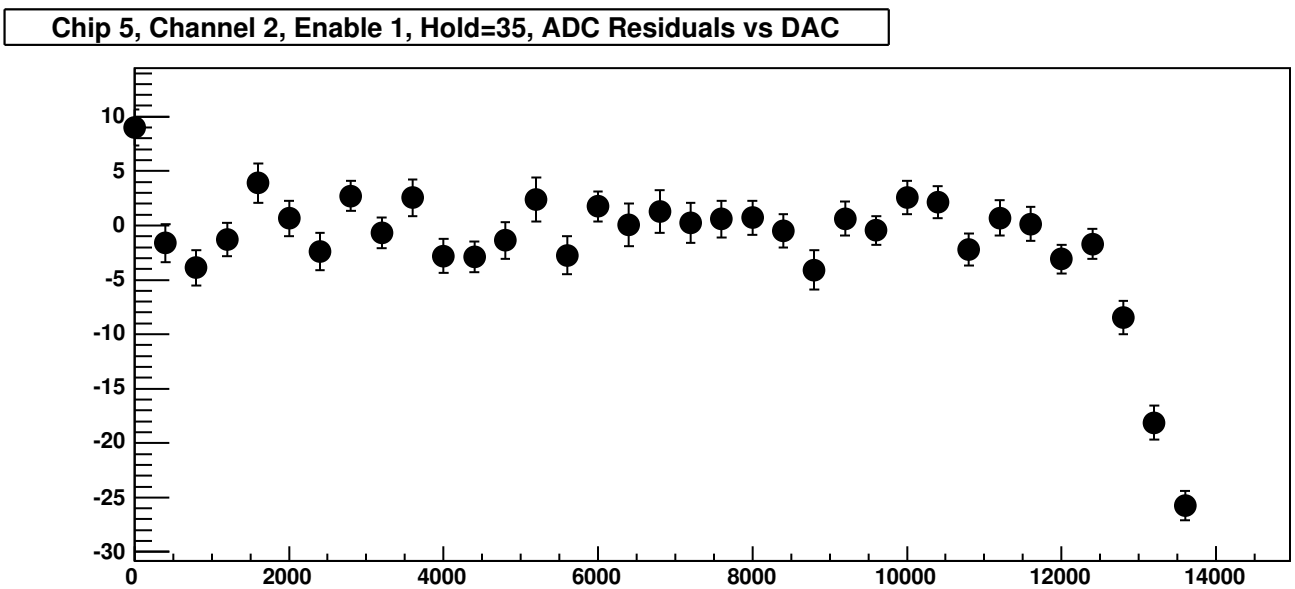


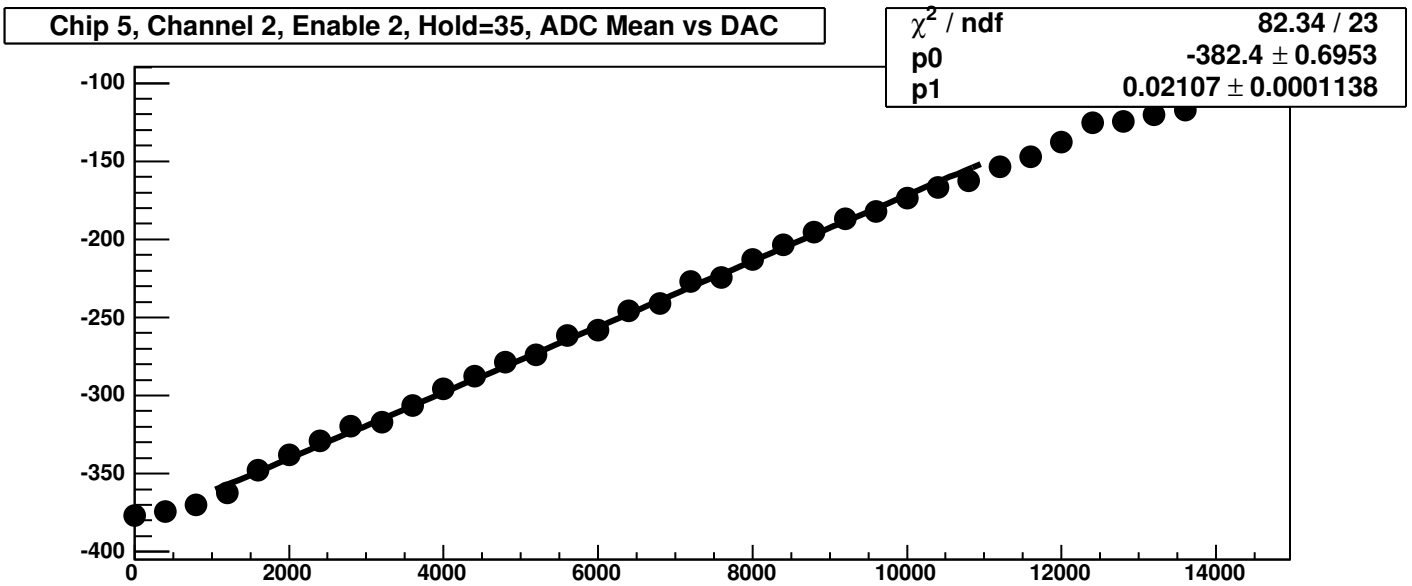
Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

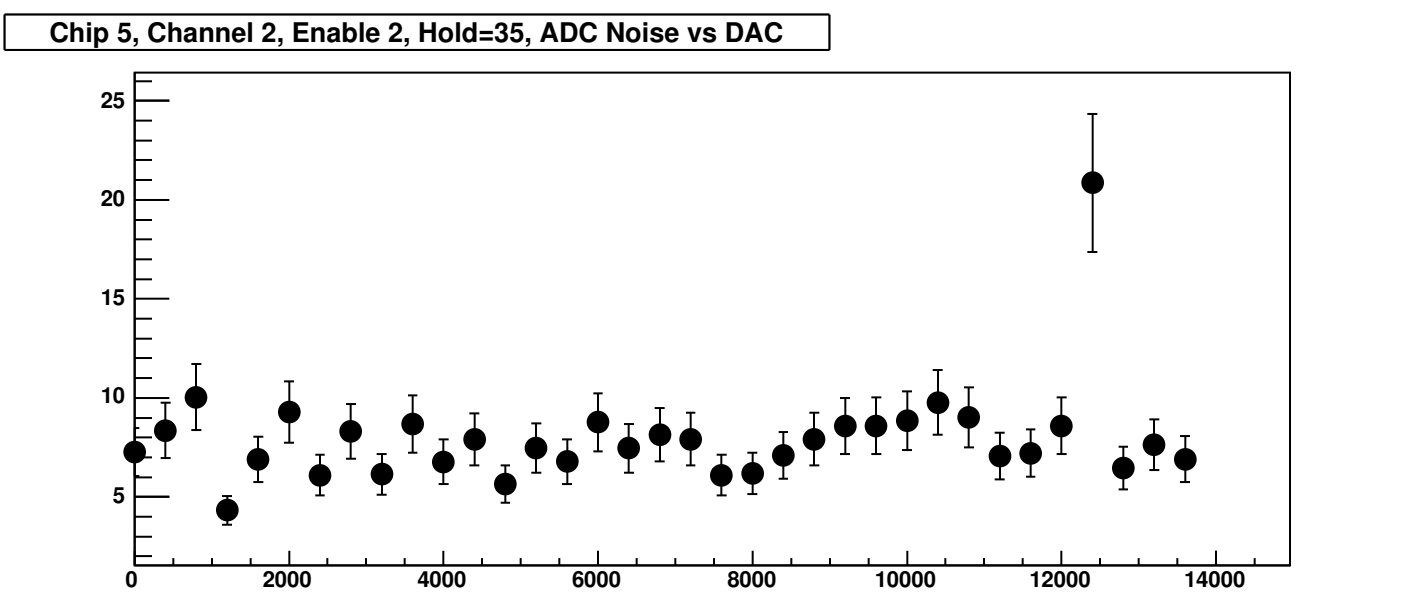


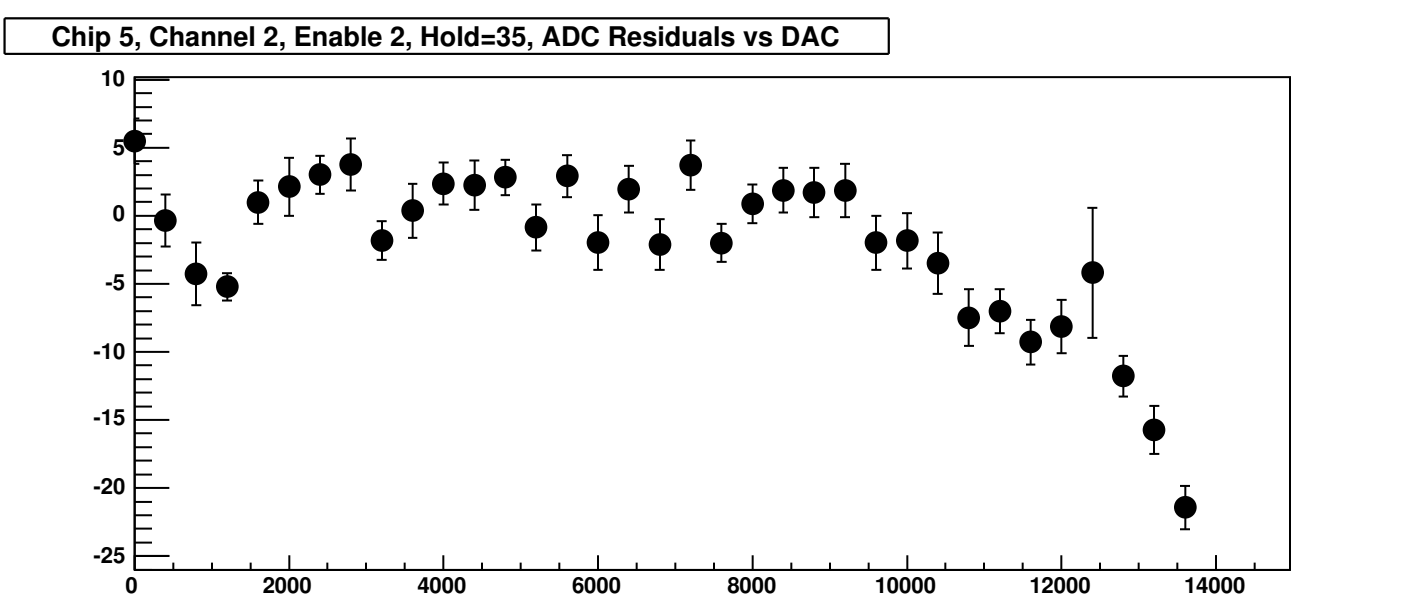

Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.87 / 23
p0 -383.9 ± 0.723
p1 0.02349 ± 0.0001067
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

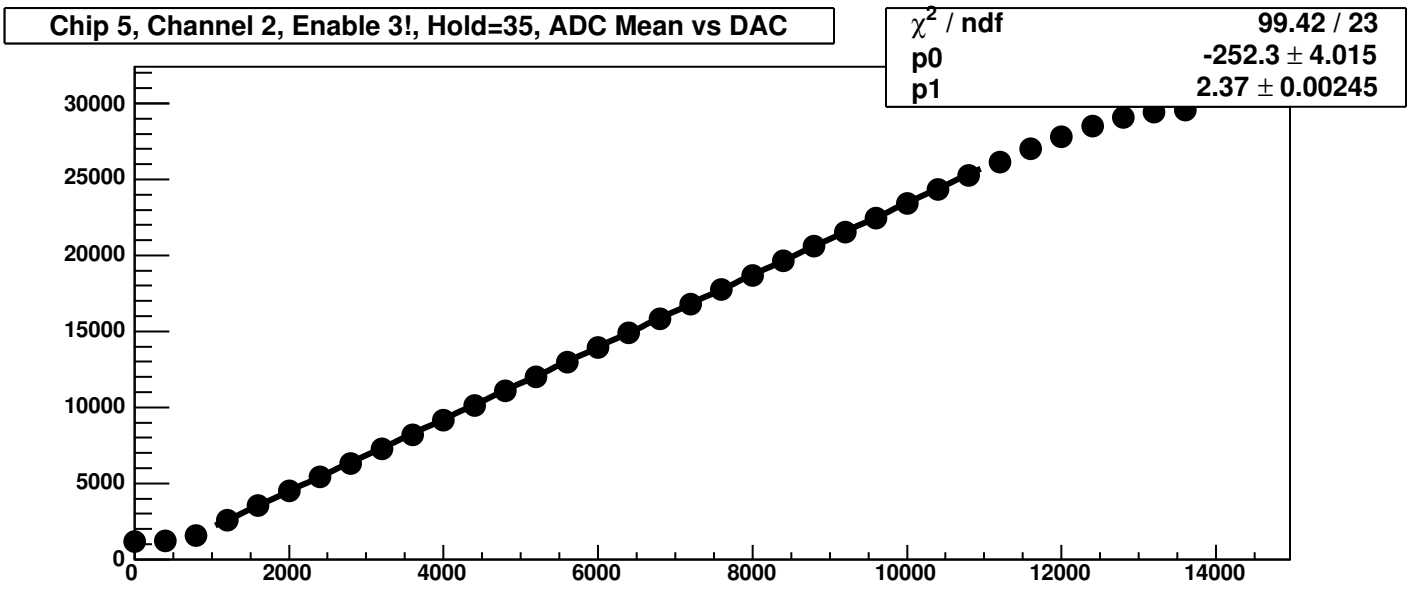

Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

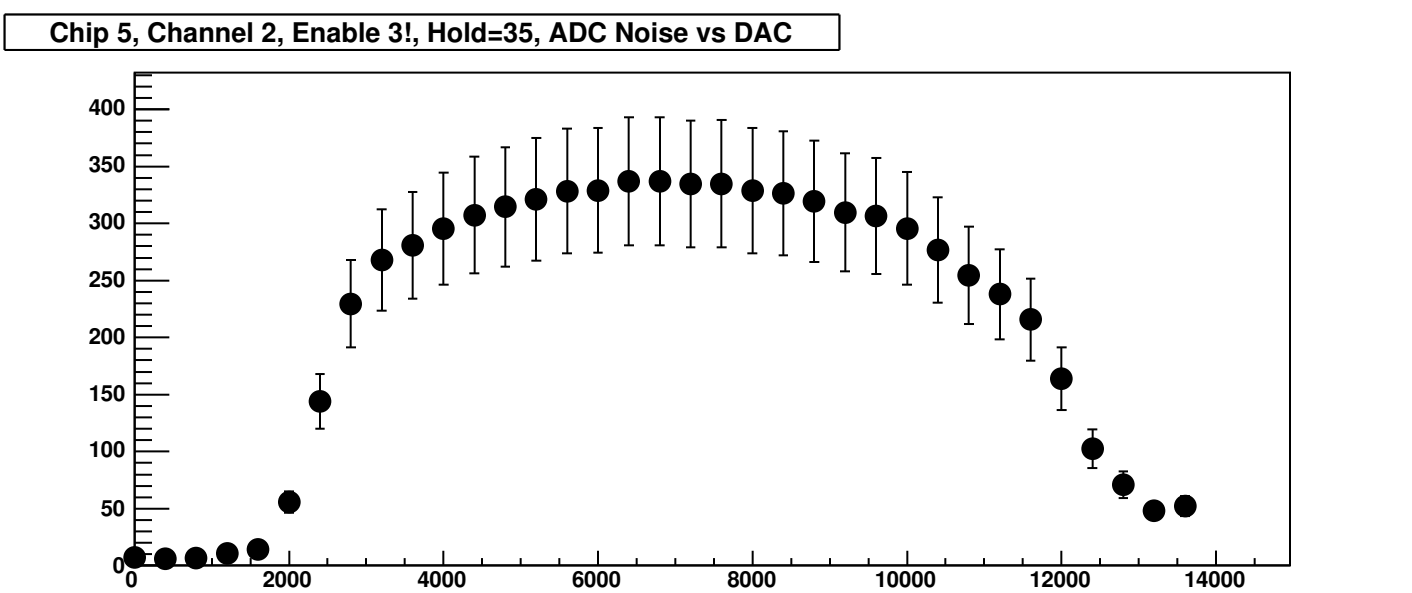

Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

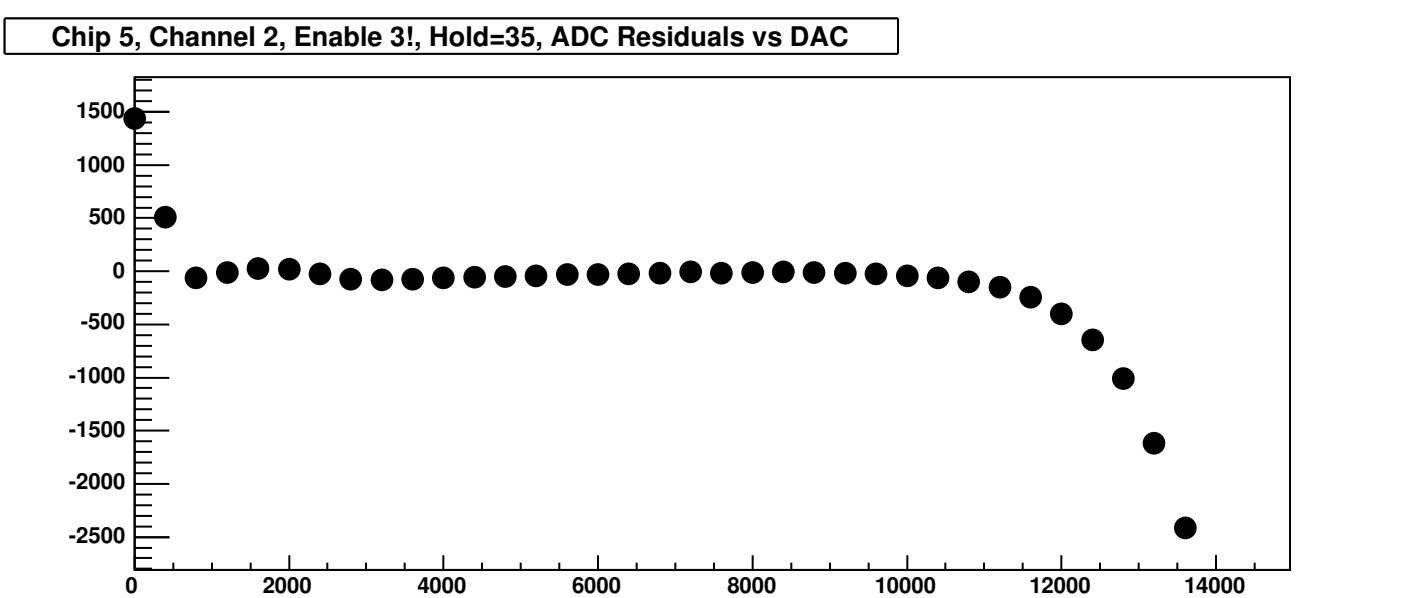

Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 82.34 / 23
p0 -382.4 ± 0.6953
p1 0.02107 ± 0.0001138
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

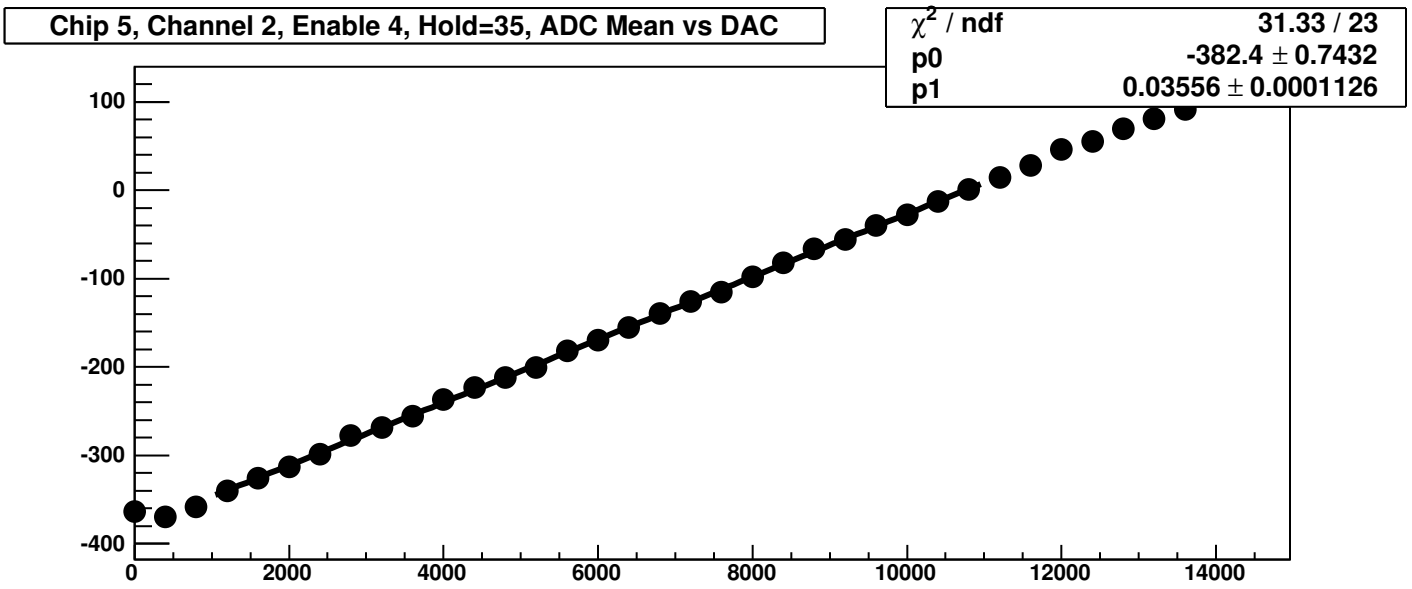

Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

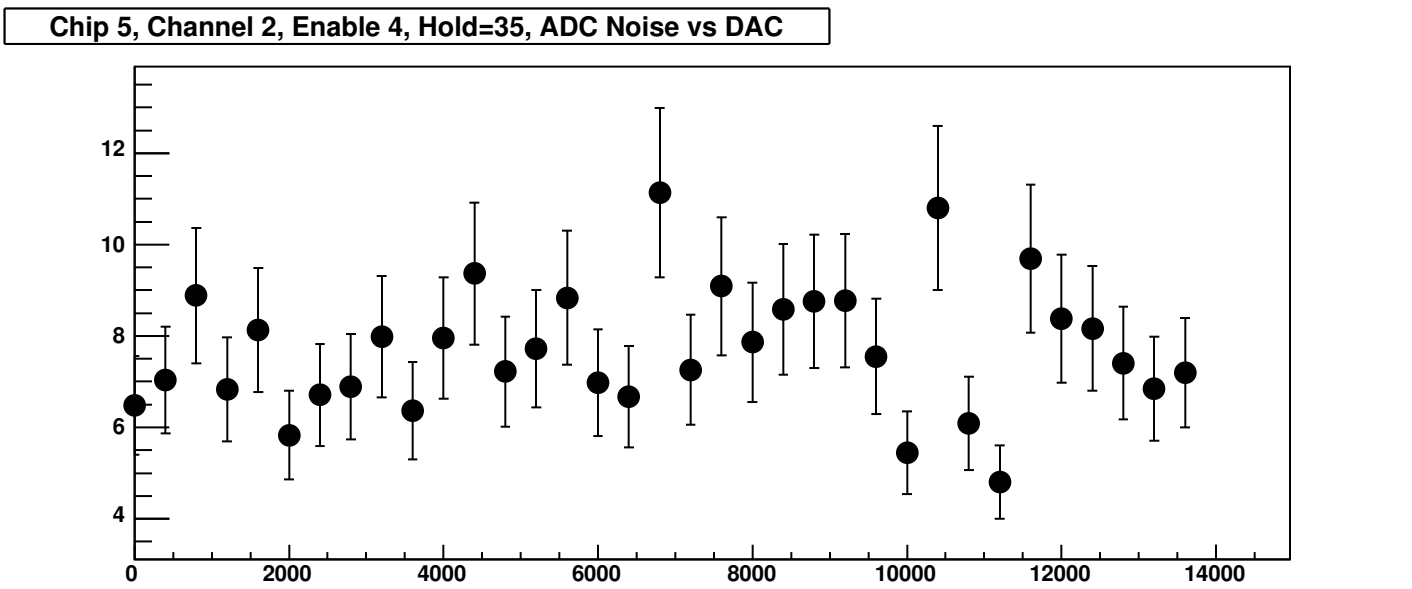

Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

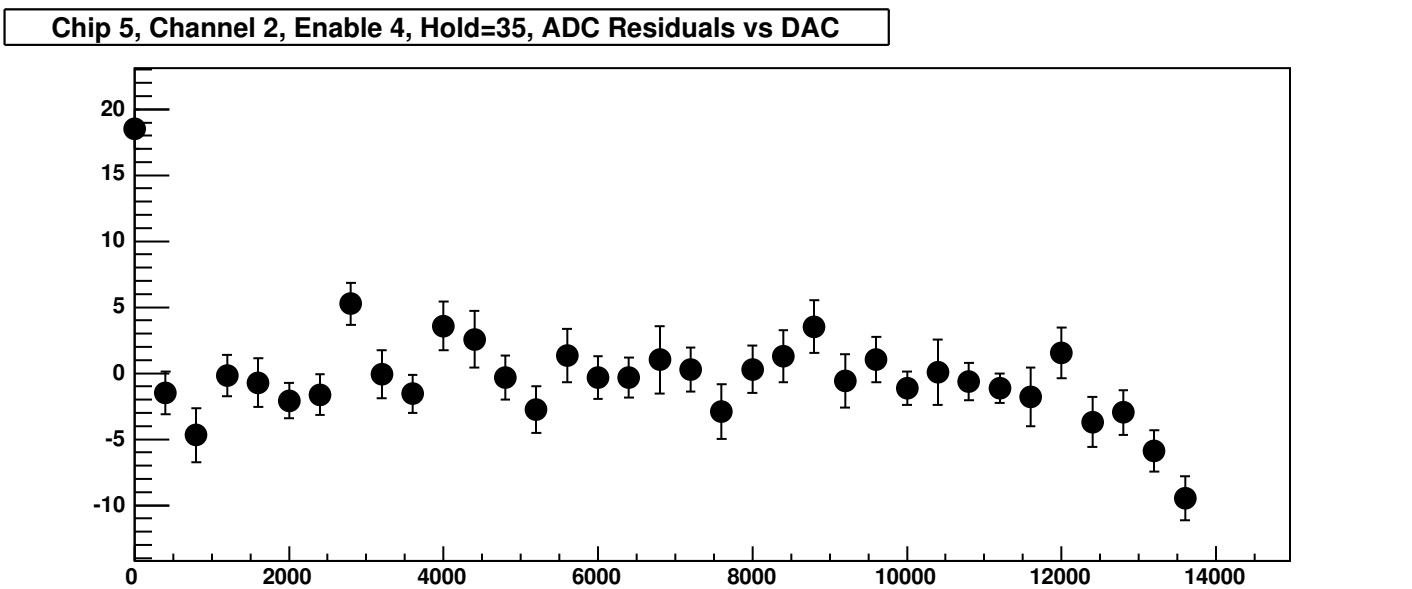

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 99.42 / 23
p0 -252.3 ± 4.015
p1 2.37 ± 0.00245
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

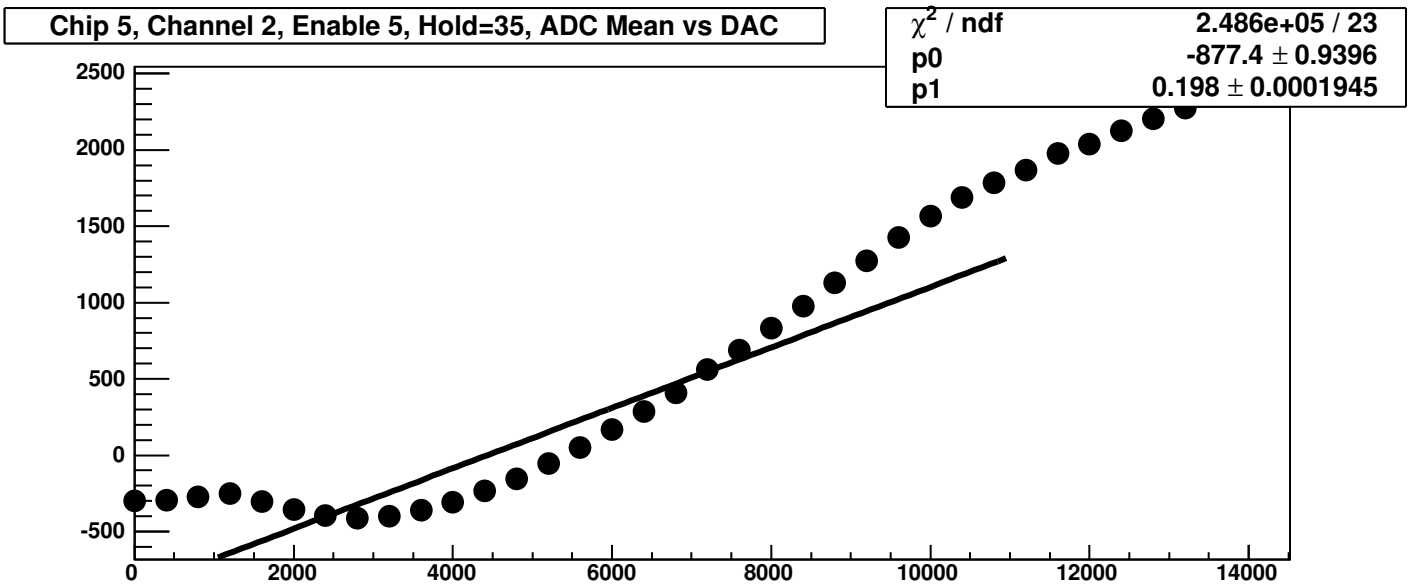

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

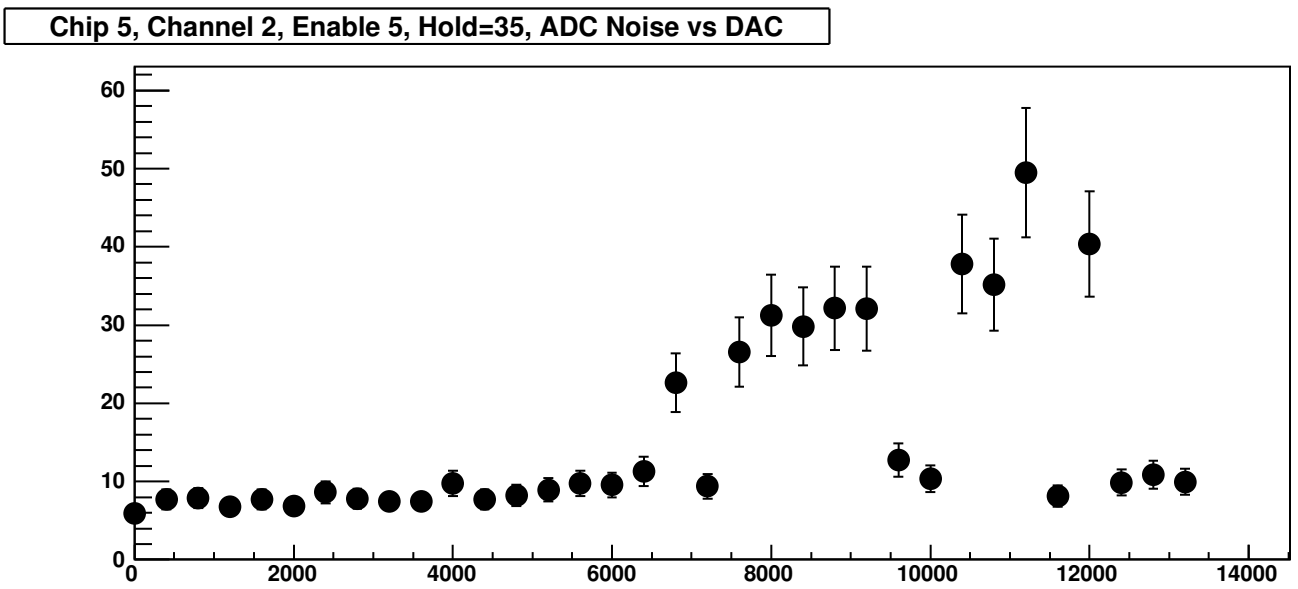

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

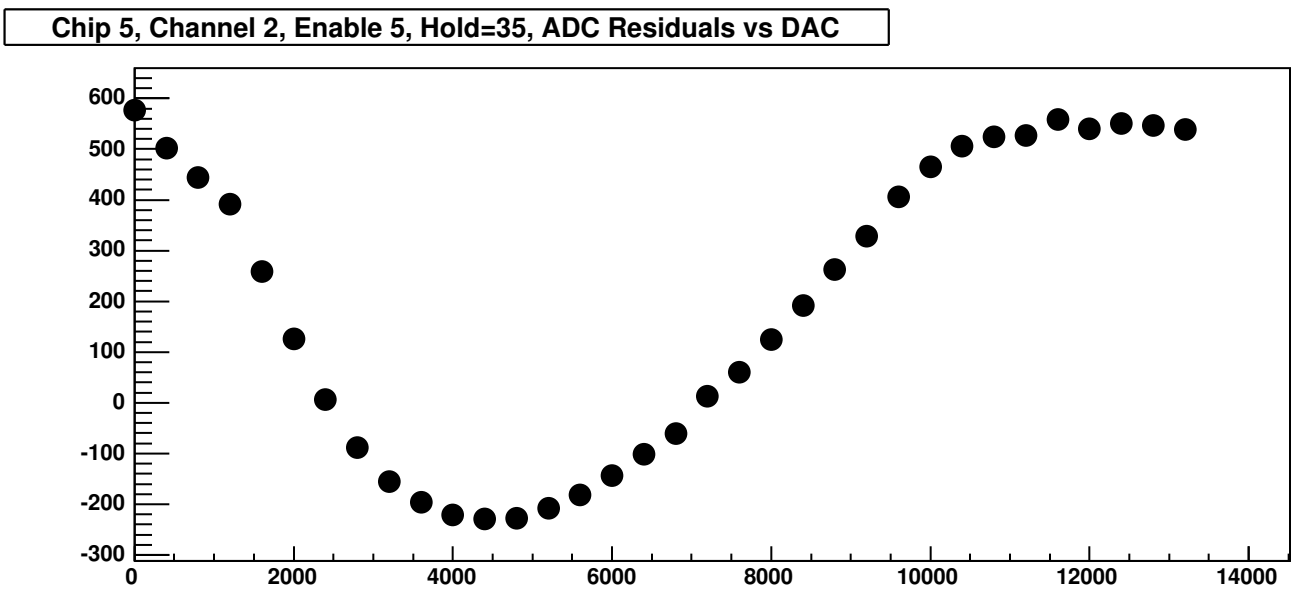

Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.33 / 23
p0 -382.4 ± 0.7432
p1 0.03556 ± 0.0001126
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

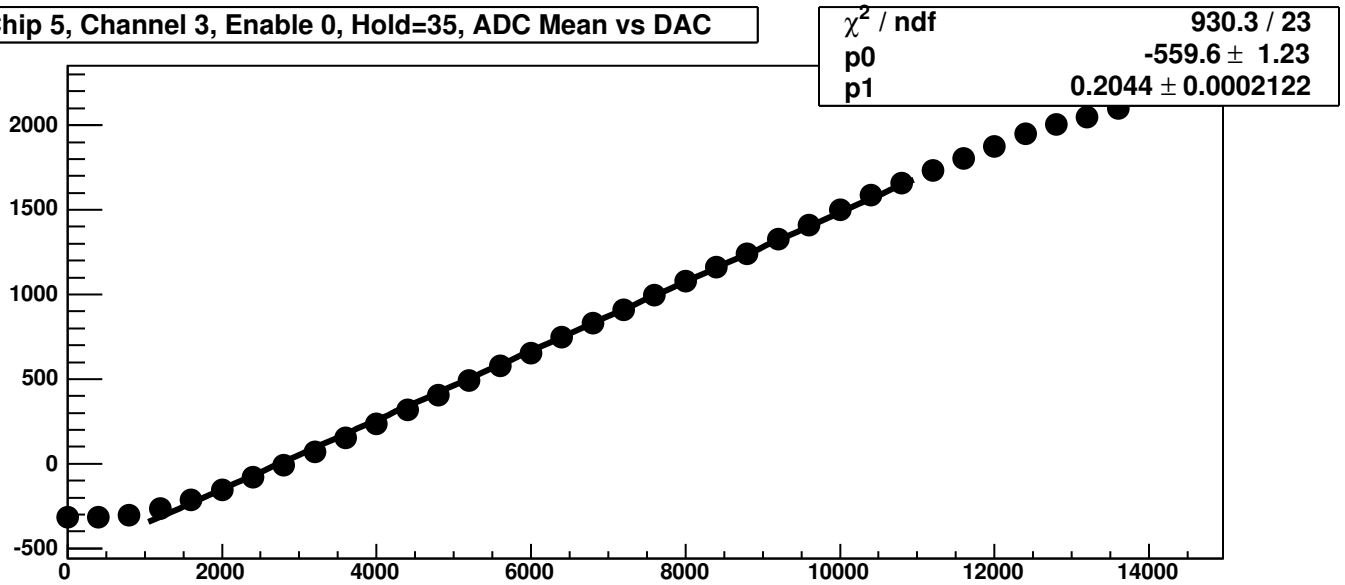

Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.486e+05 / 23
p0 -877.4 ± 0.9396
p1 0.198 ± 0.0001945
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

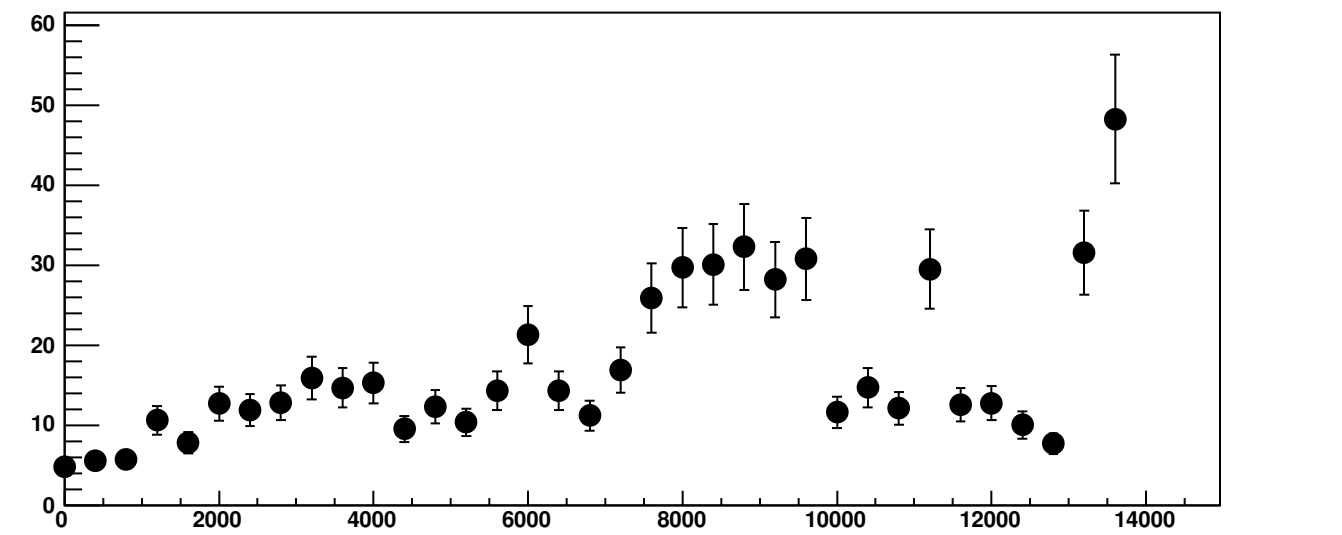

Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

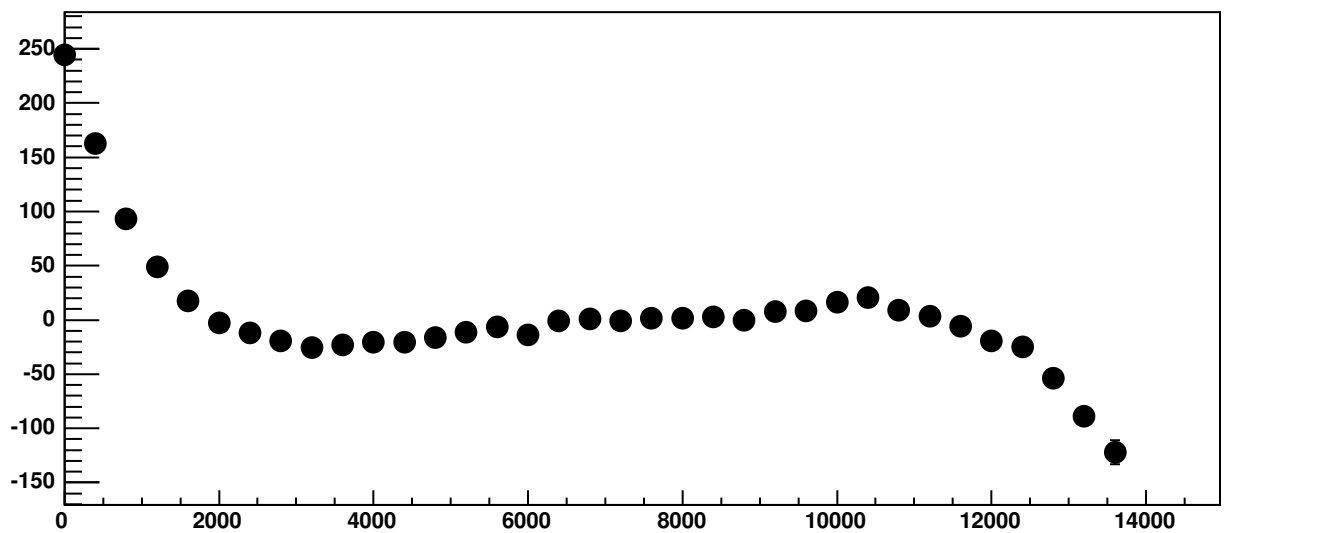
Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

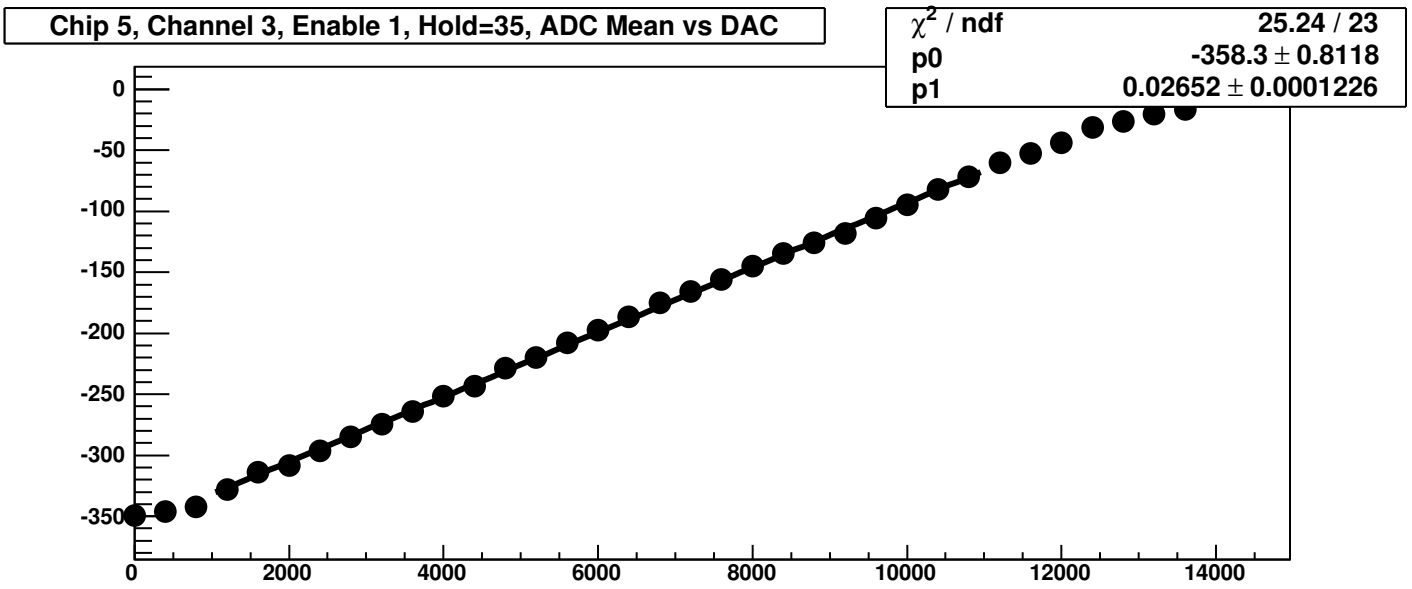


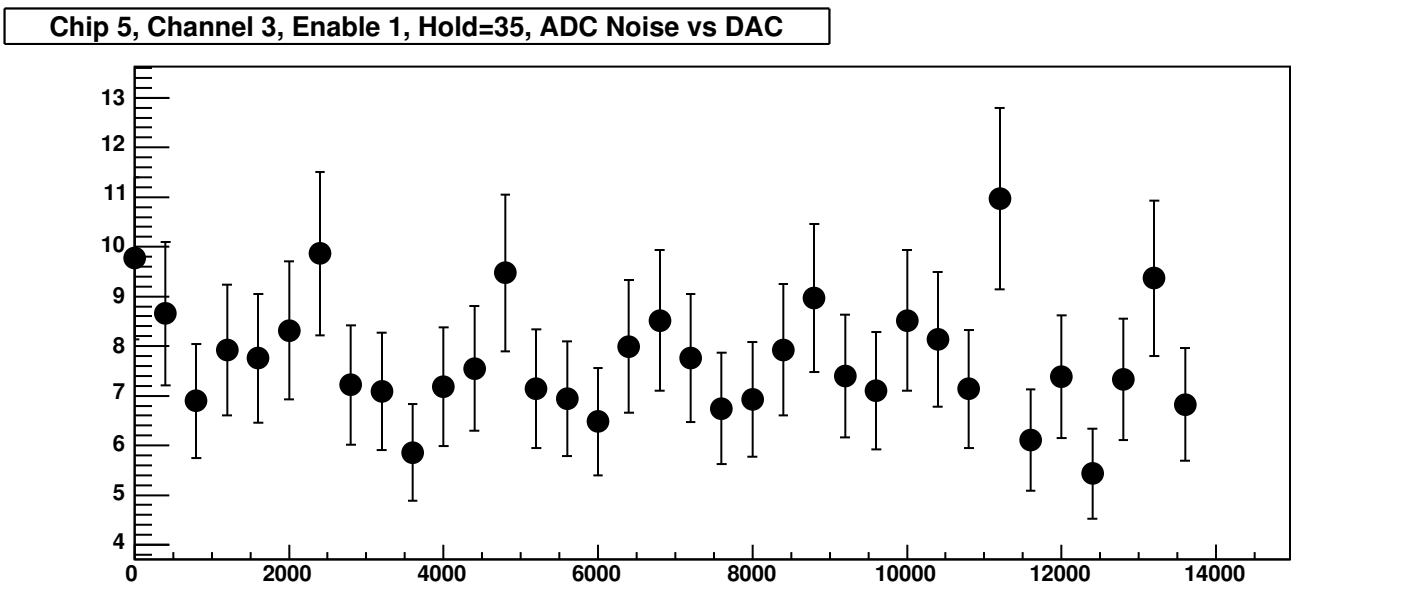
Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

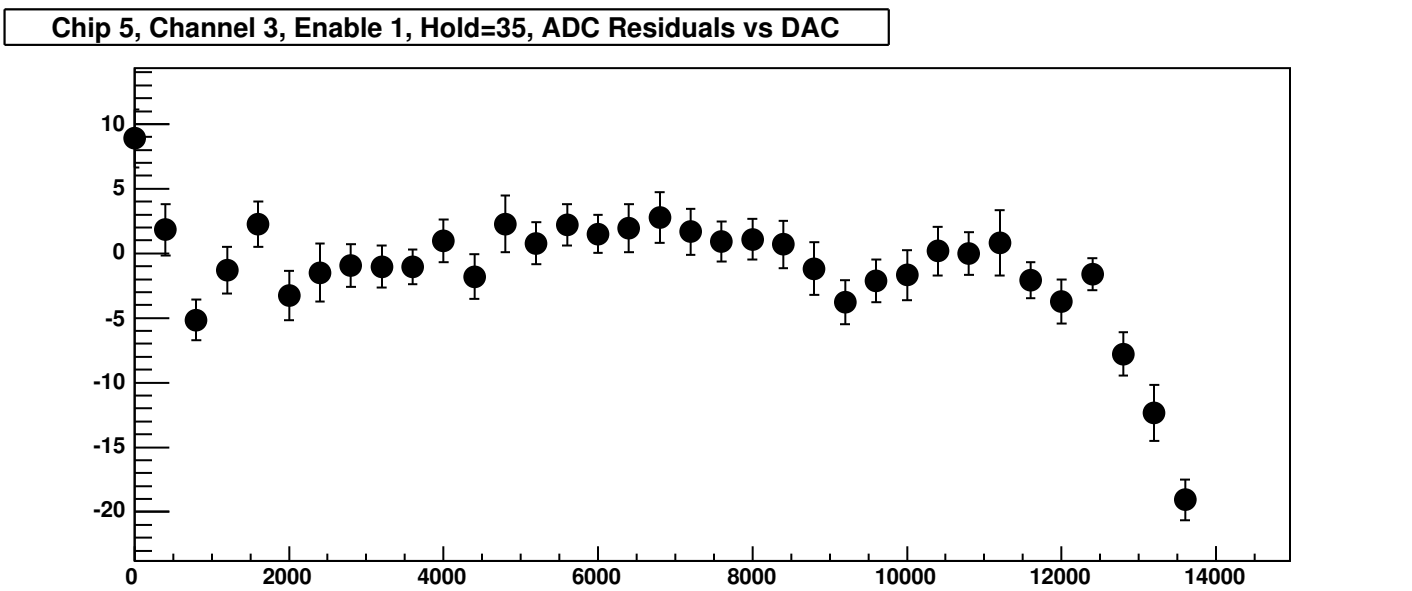


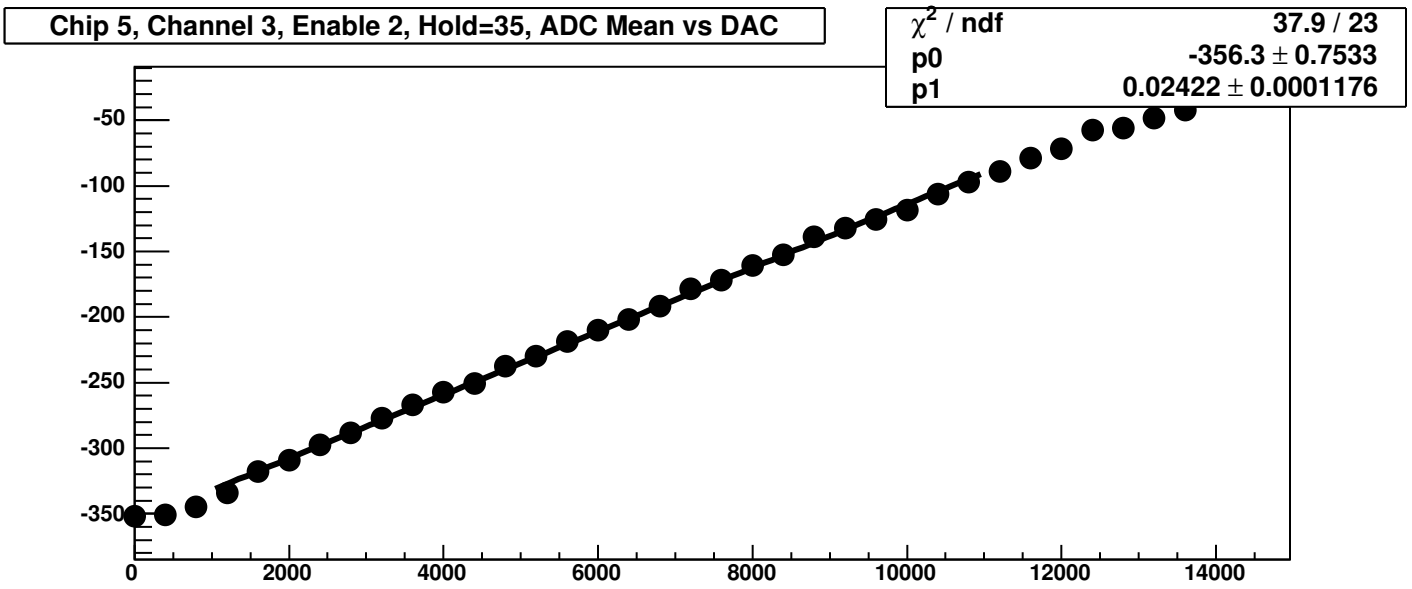
Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

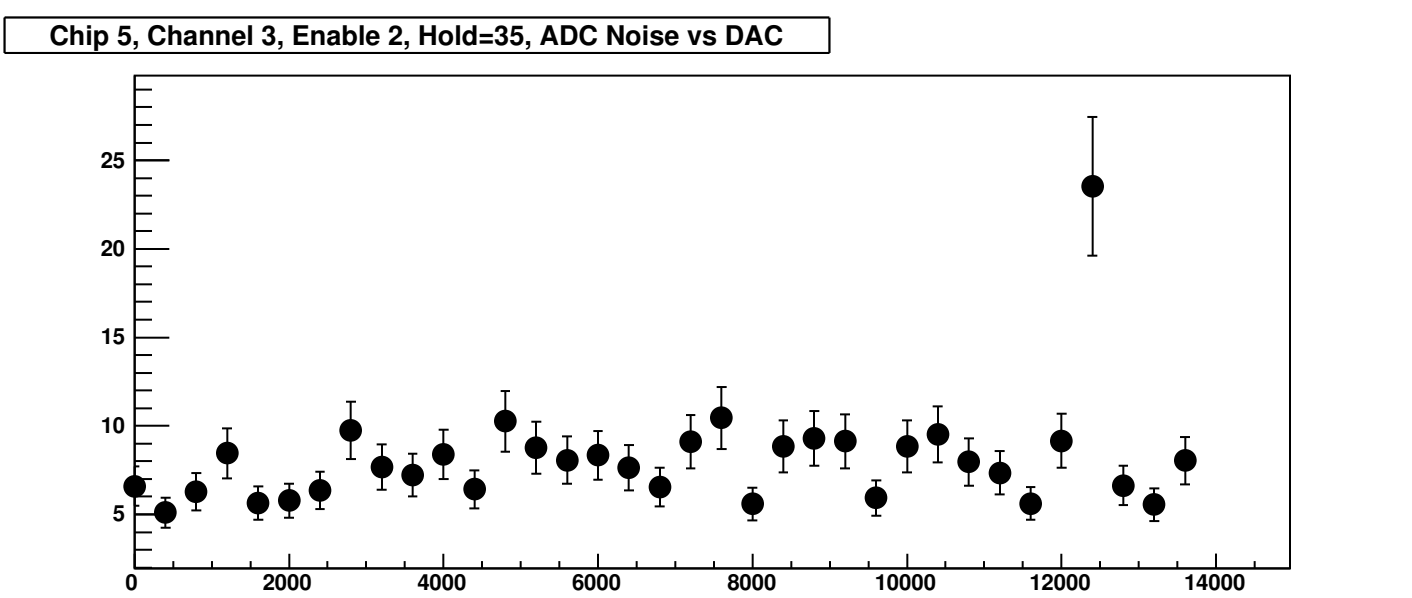


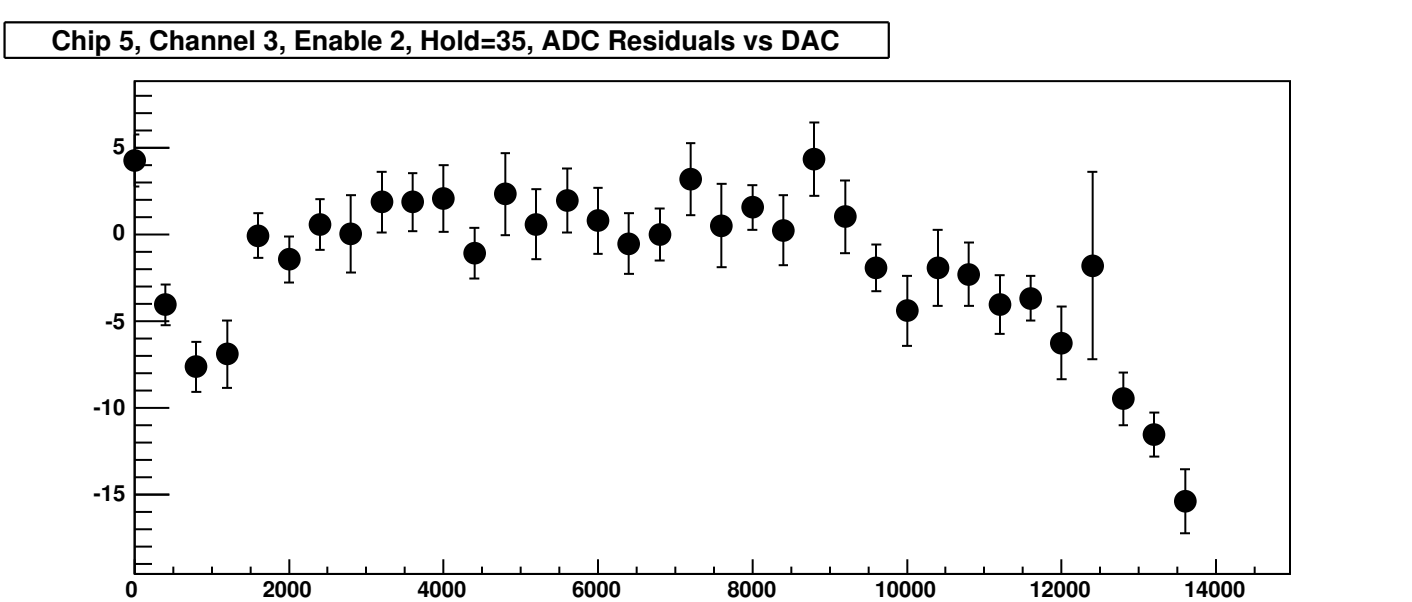

Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.24 / 23
p0 -358.3 ± 0.8118
p1 0.02652 ± 0.0001226
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

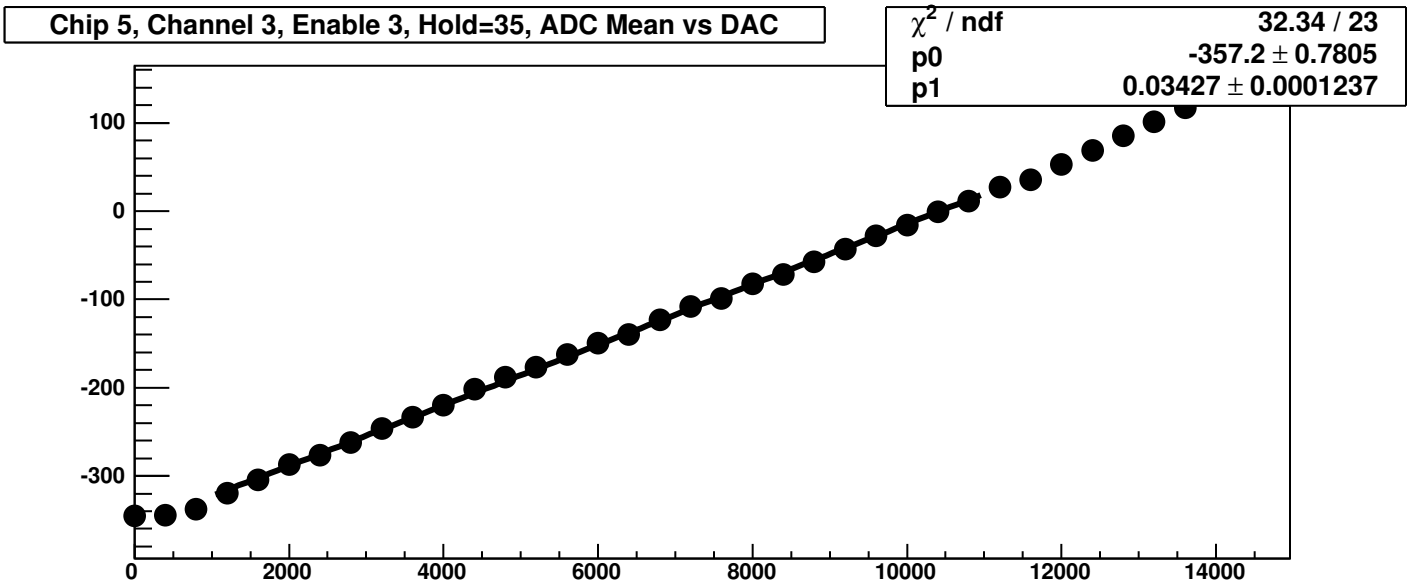

Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

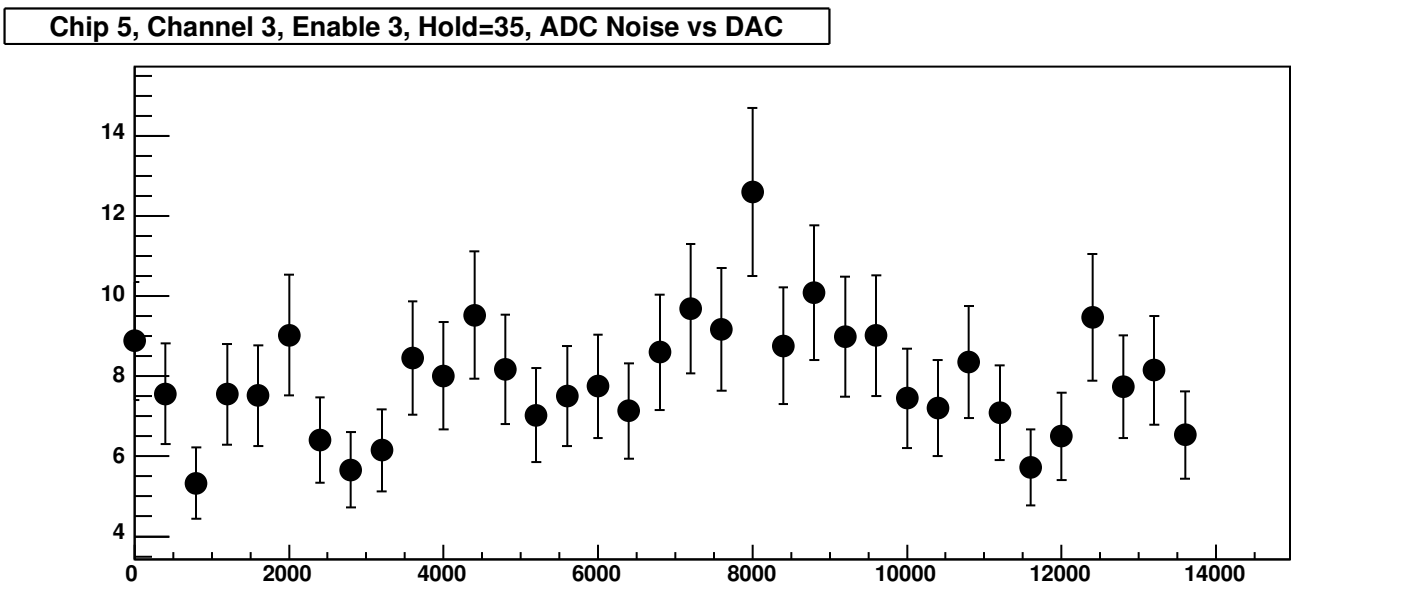

Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

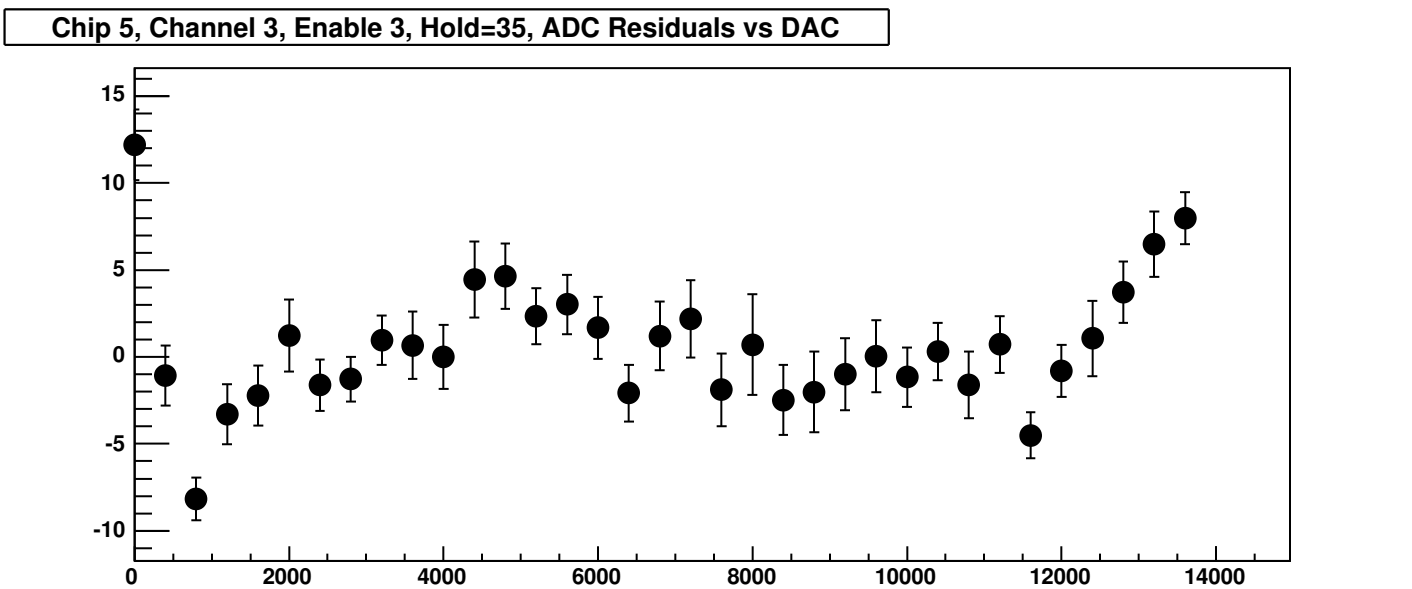

Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 37.9 / 23
p0 -356.3 ± 0.7533
p1 0.02422 ± 0.0001176
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

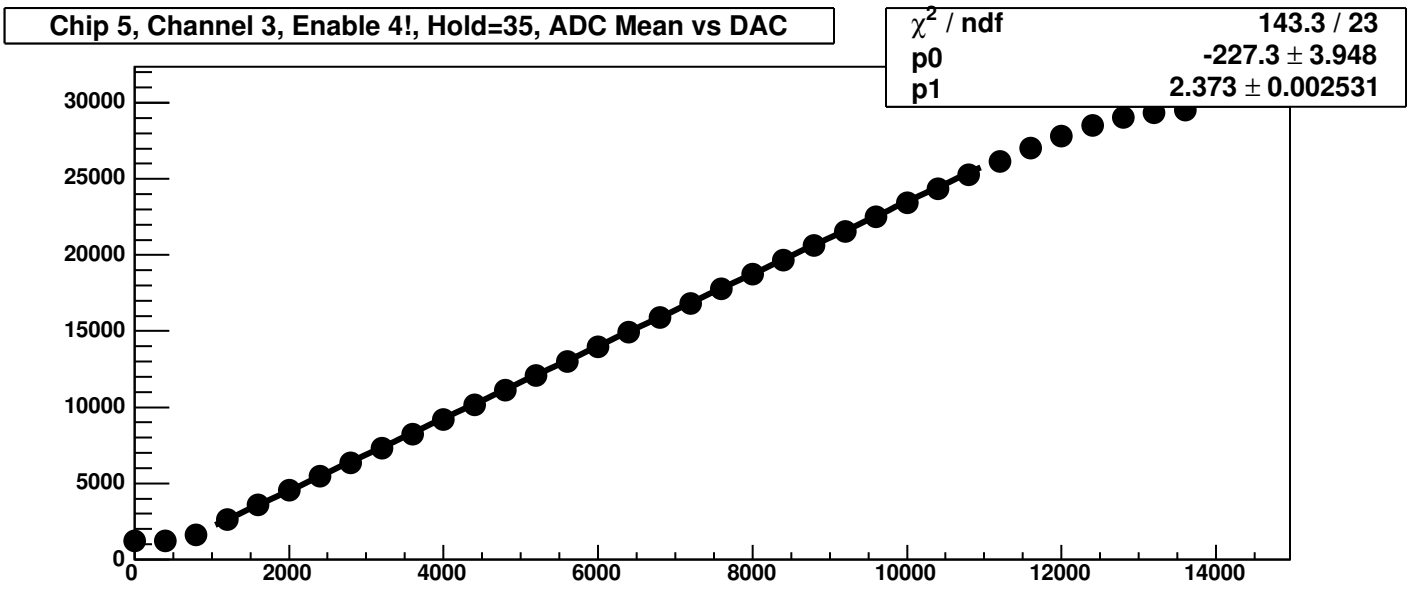

Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

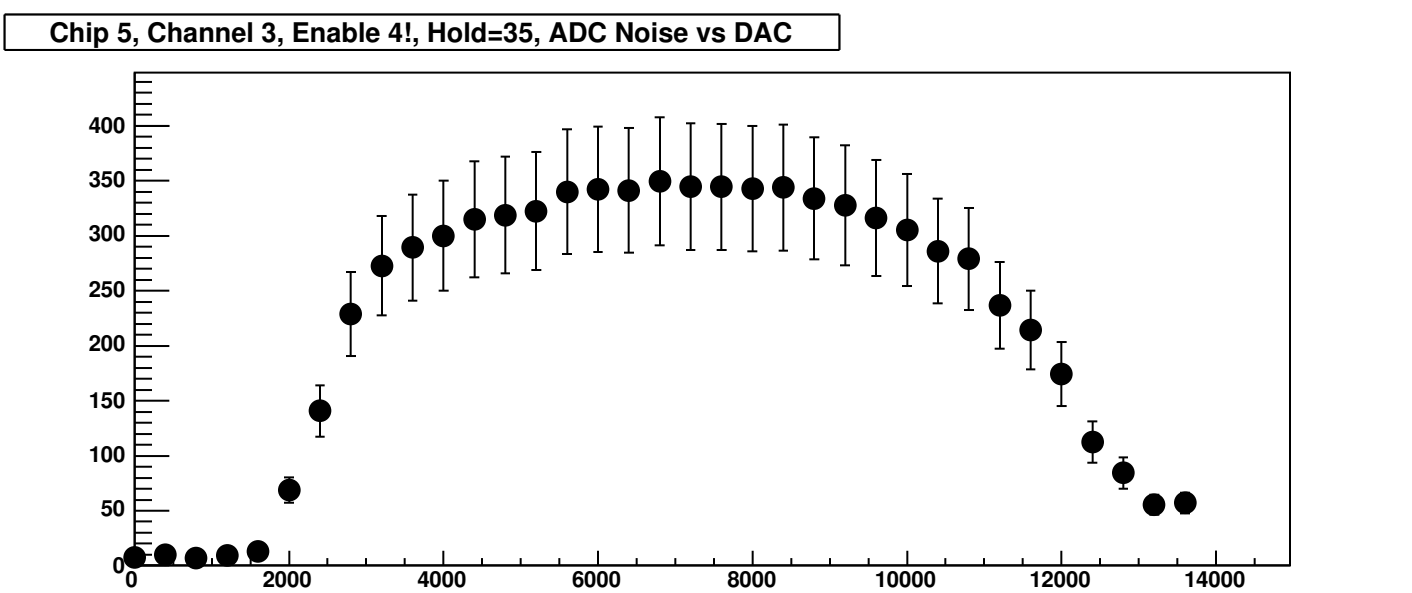

Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

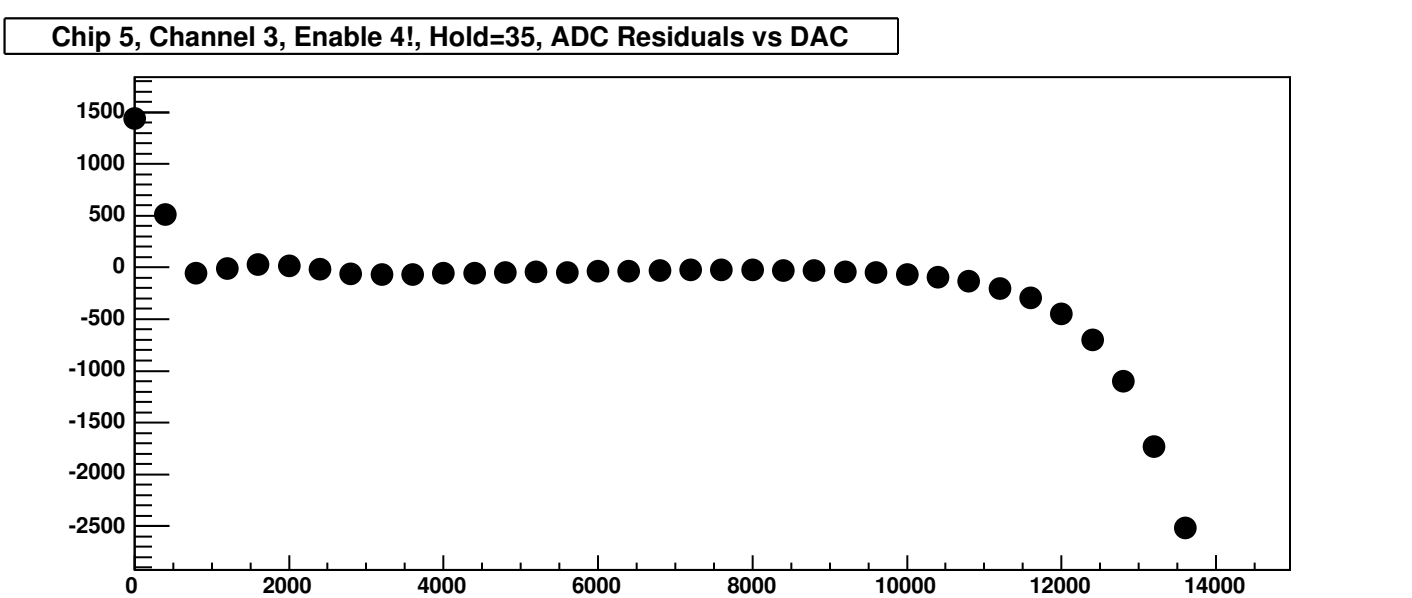

Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.34 / 23
p0 -357.2 ± 0.7805
p1 0.03427 ± 0.0001237
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

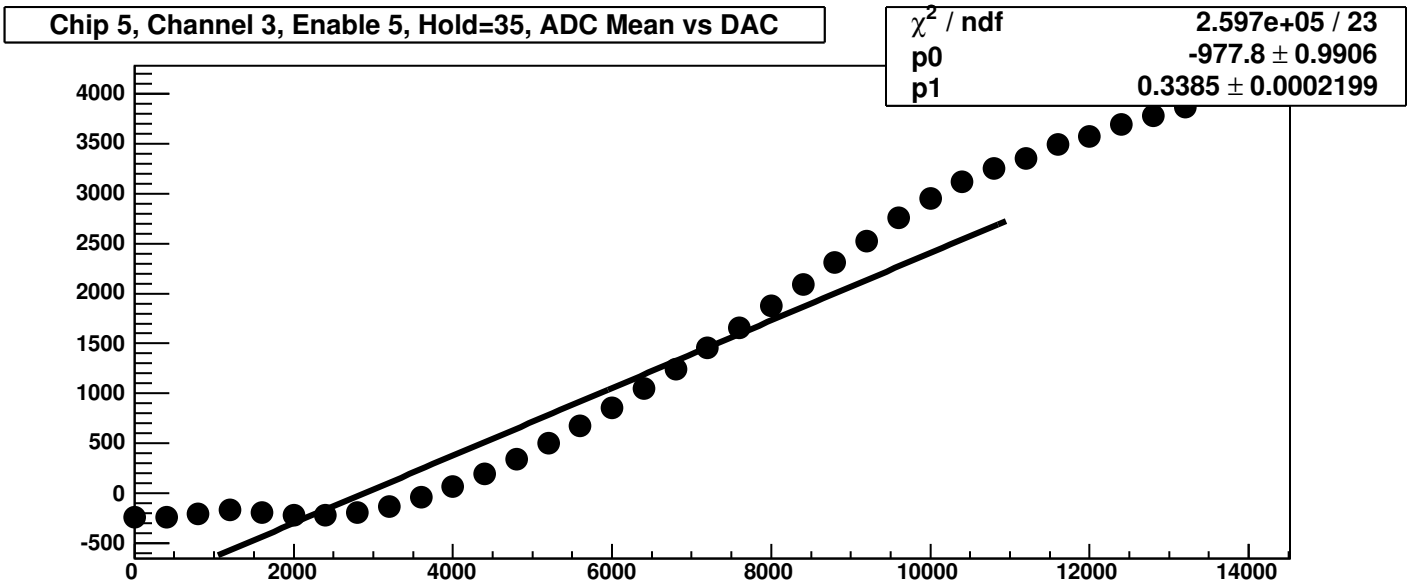

Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

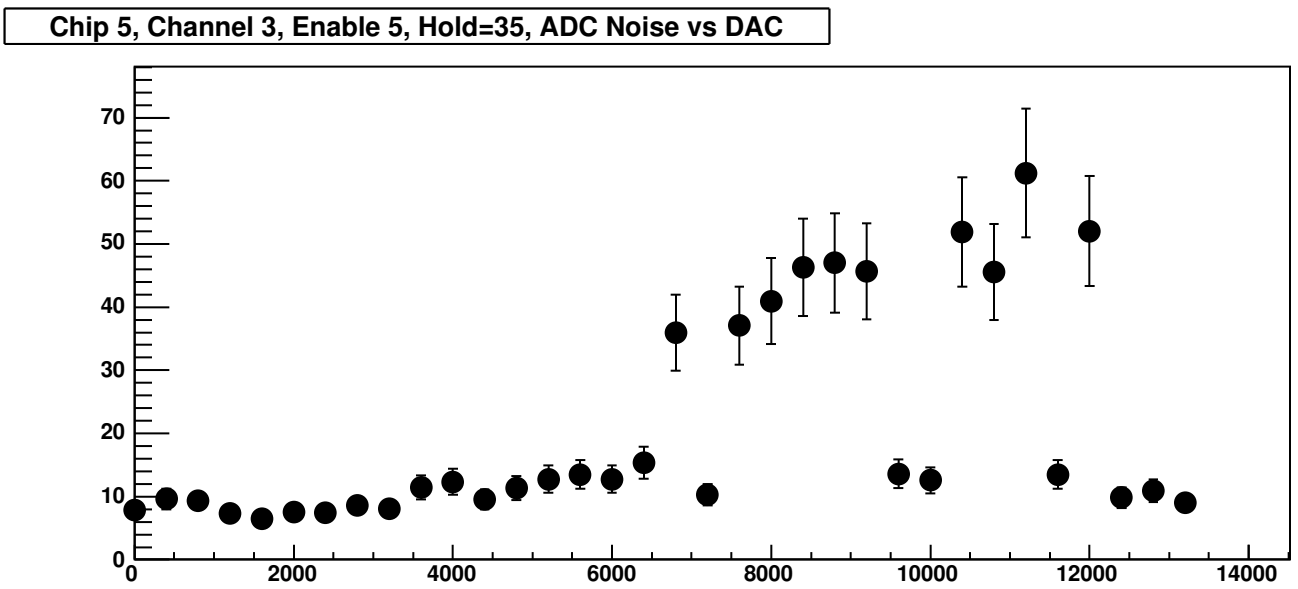

Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

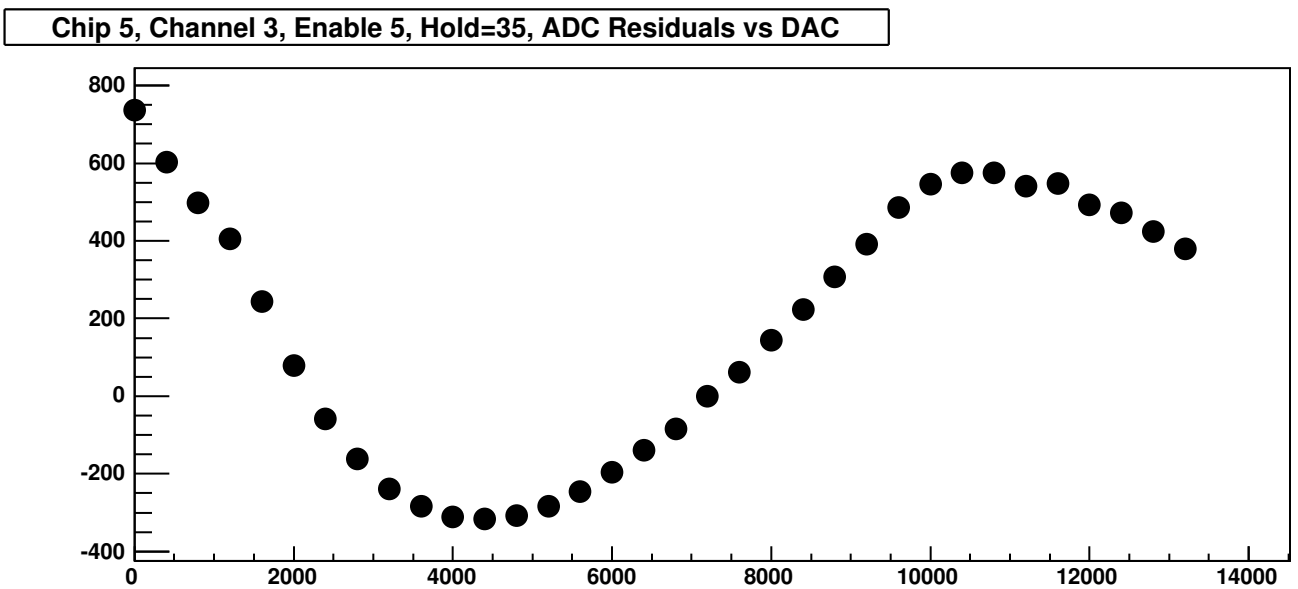

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 143.3 / 23
p0 -227.3 ± 3.948
p1 2.373 ± 0.002531
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

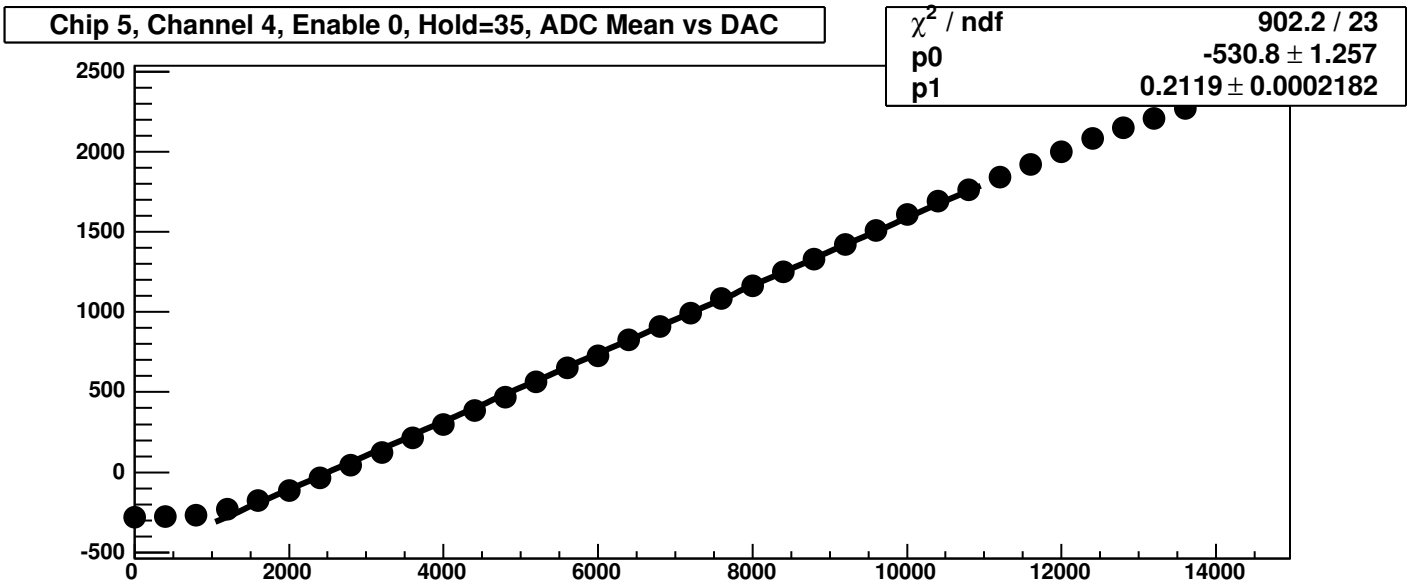

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

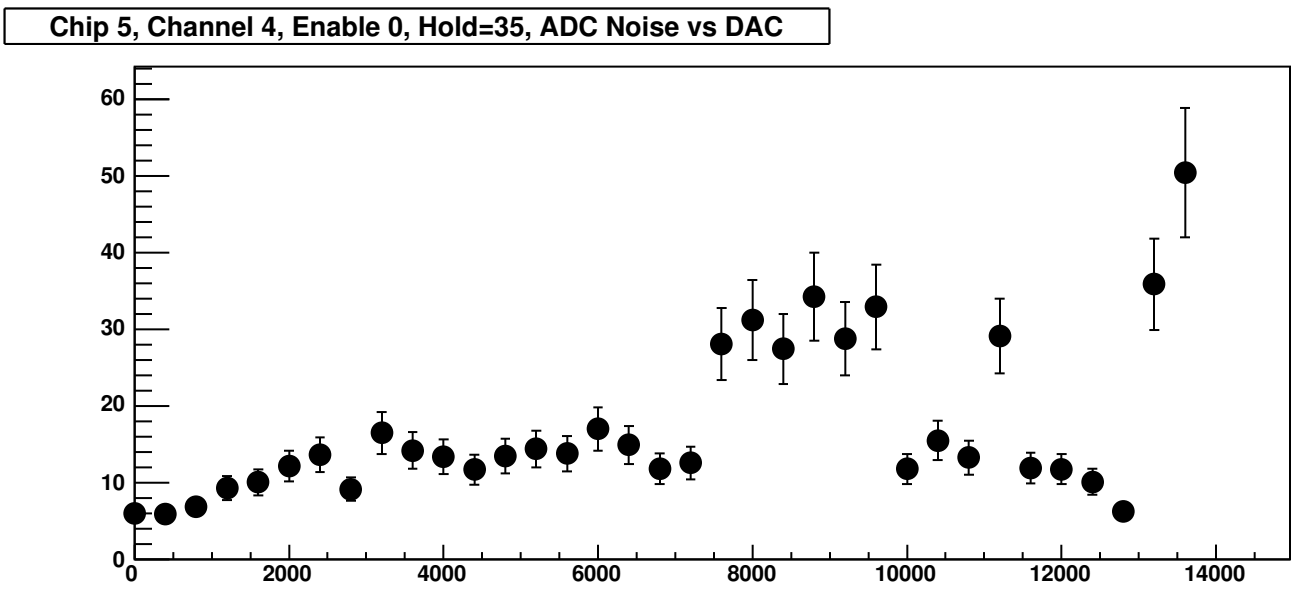

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

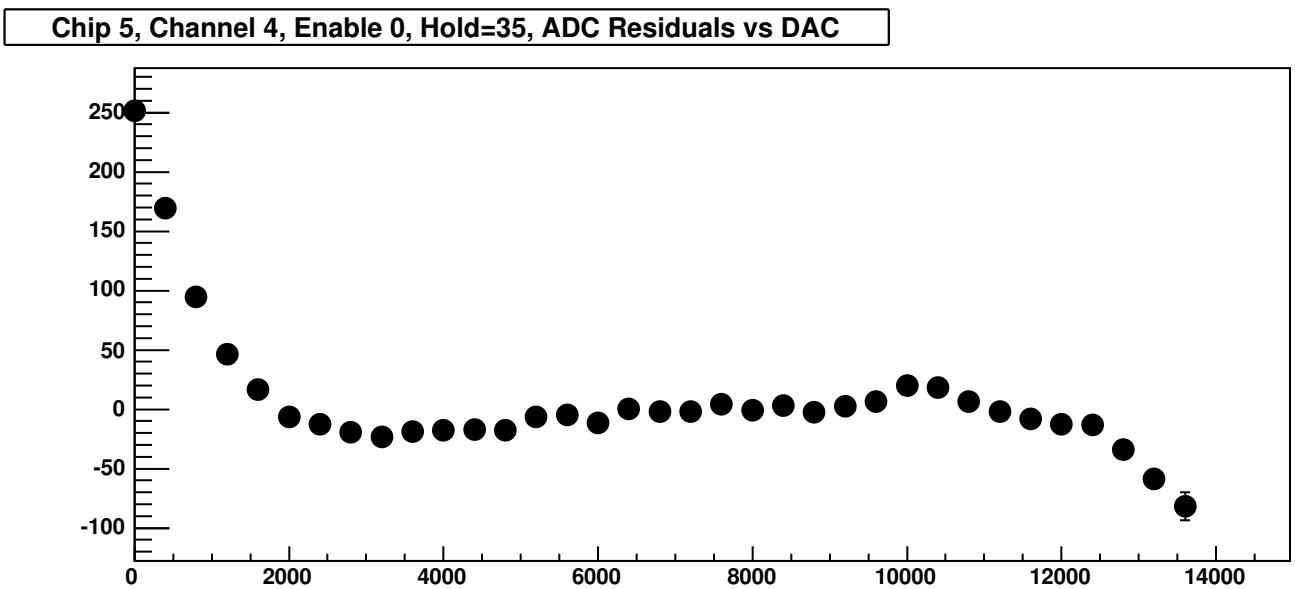

Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.597e+05 / 23
p0 -977.8 ± 0.9906
p1 0.3385 ± 0.0002199
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

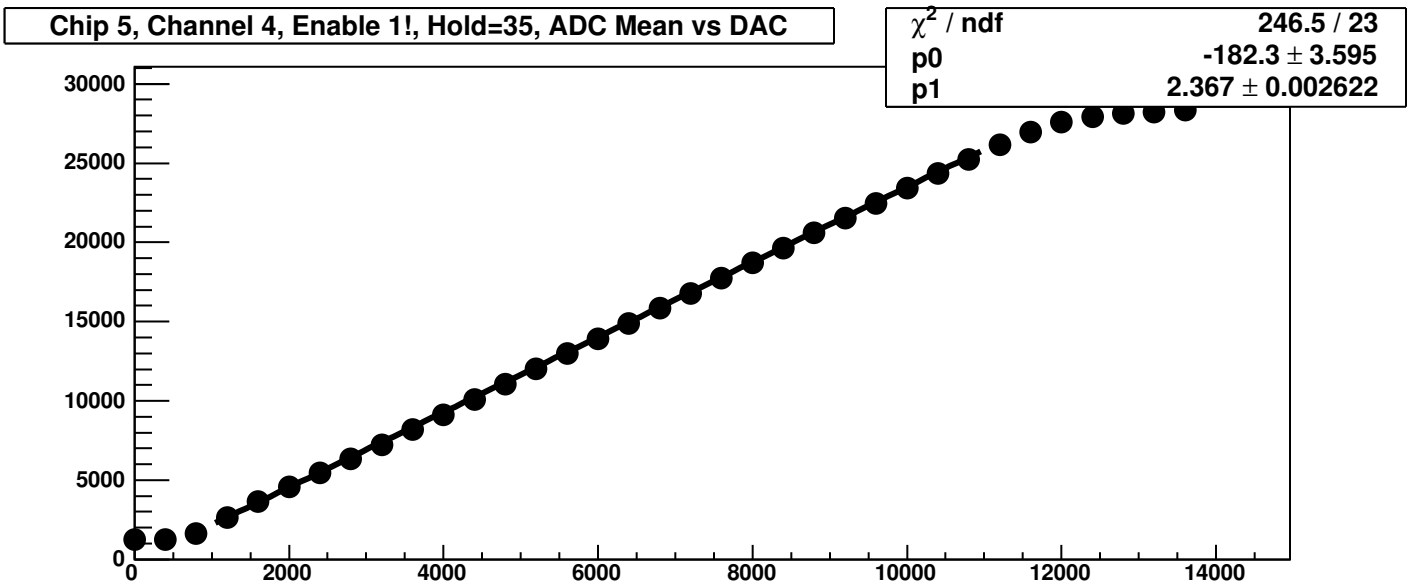

Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

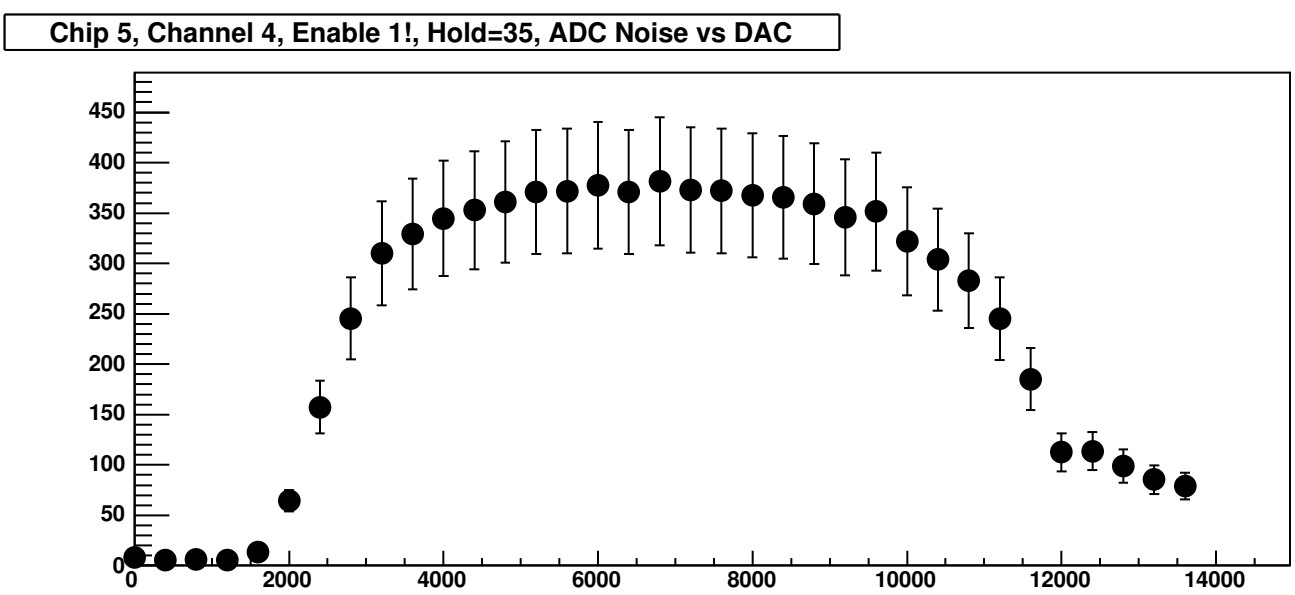

Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

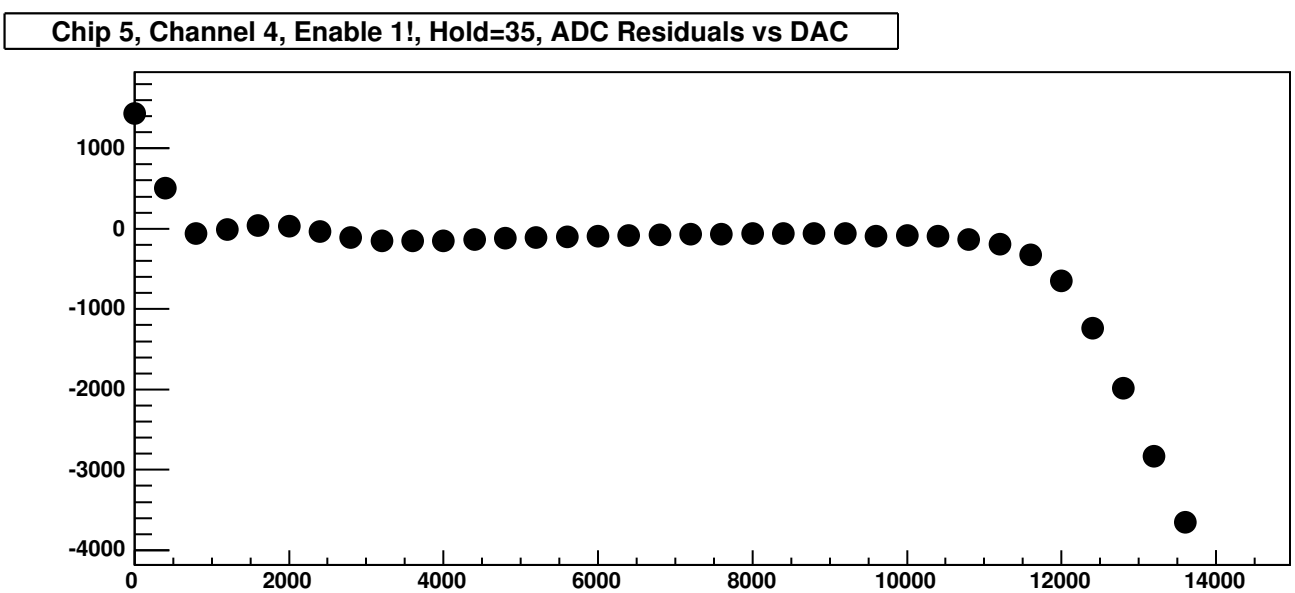

Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 902.2 / 23
p0 -530.8 ± 1.257
p1 0.2119 ± 0.0002182
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

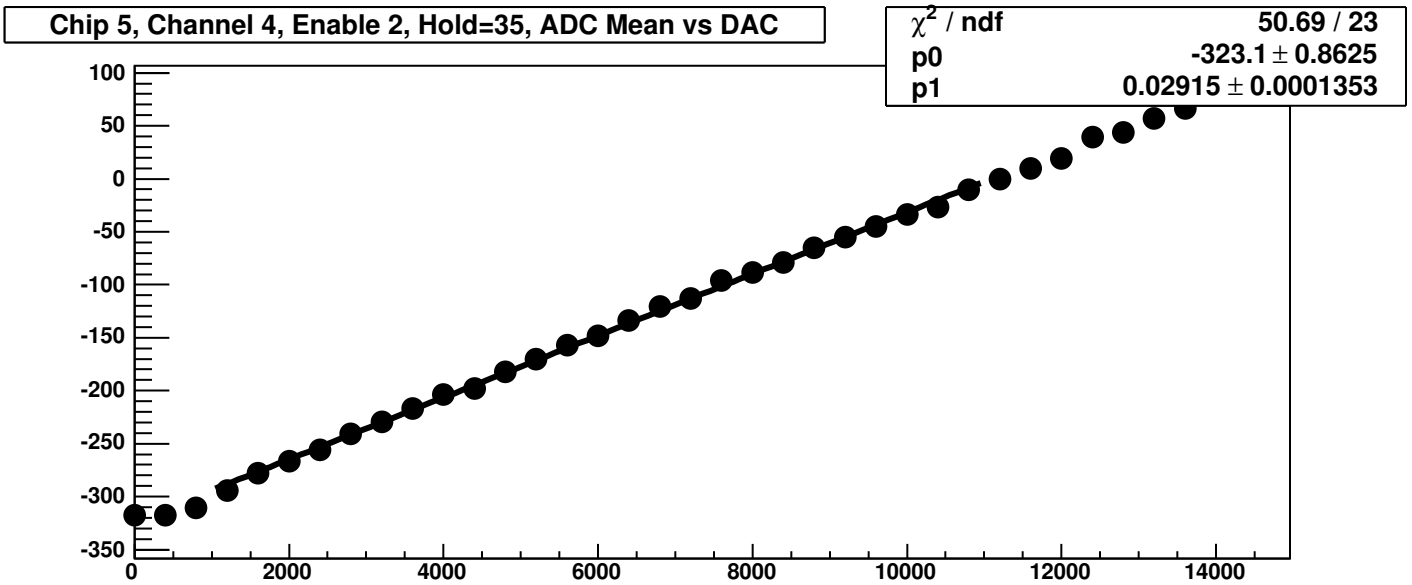

Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

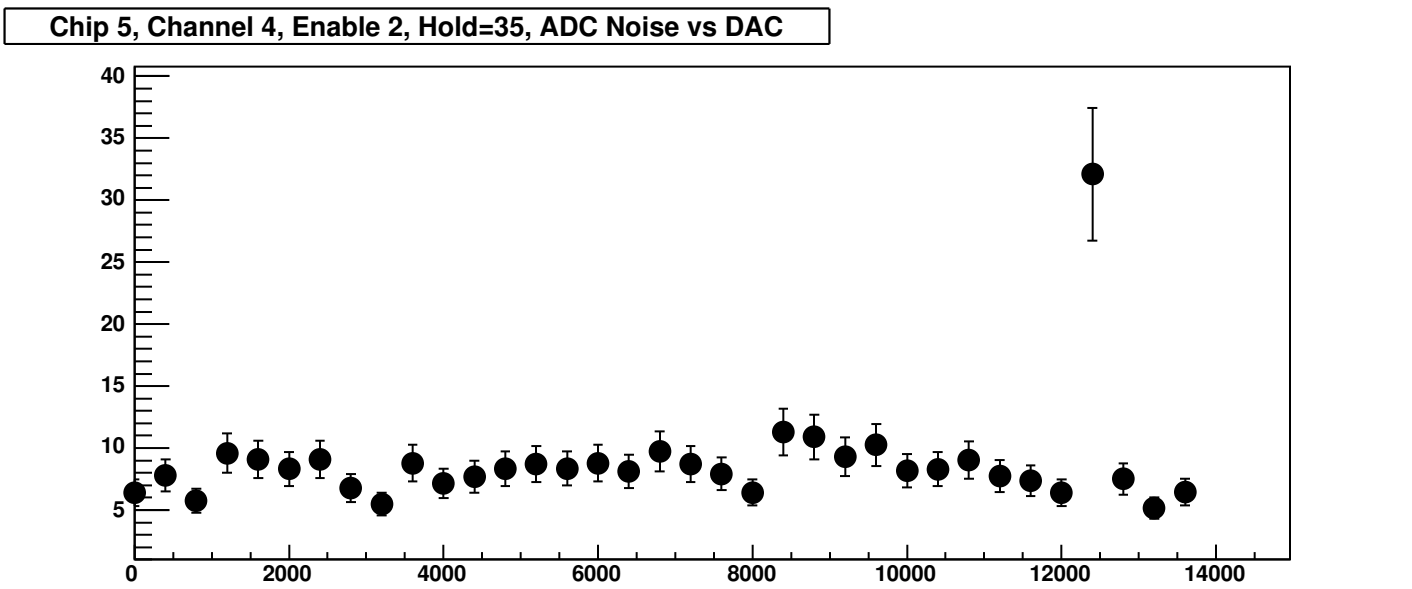

Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

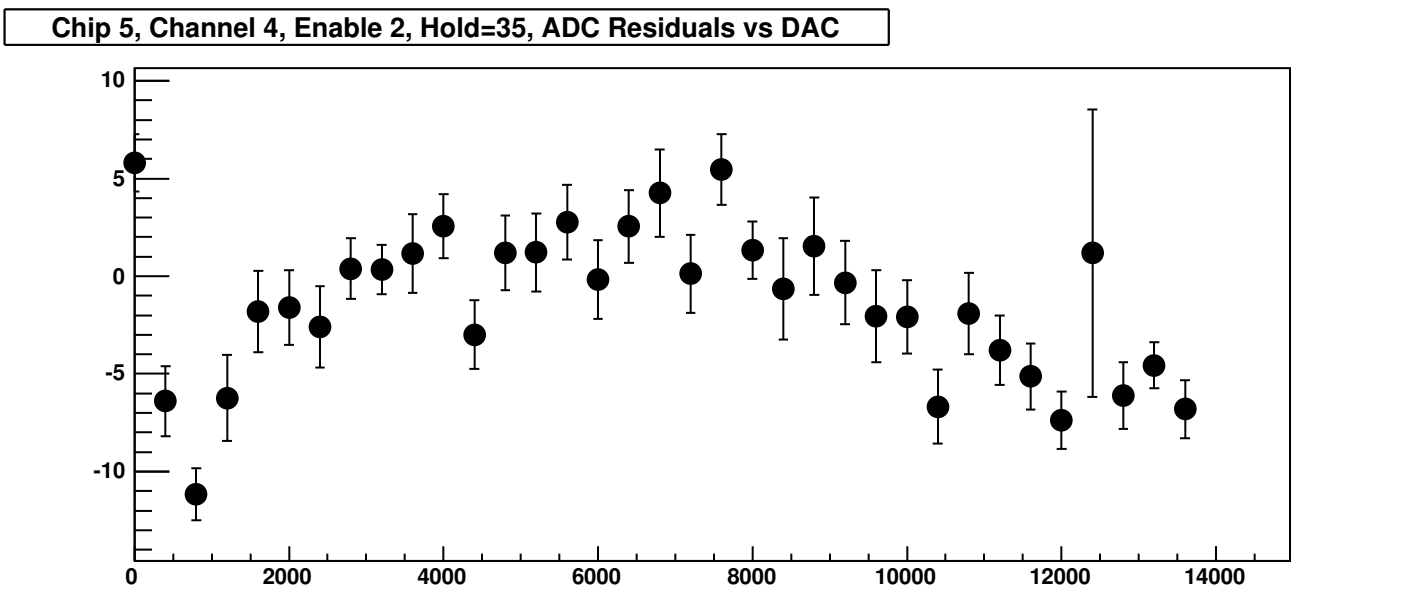

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 246.5 / 23
p0 -182.3 ± 3.595
p1 2.367 ± 0.002622
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

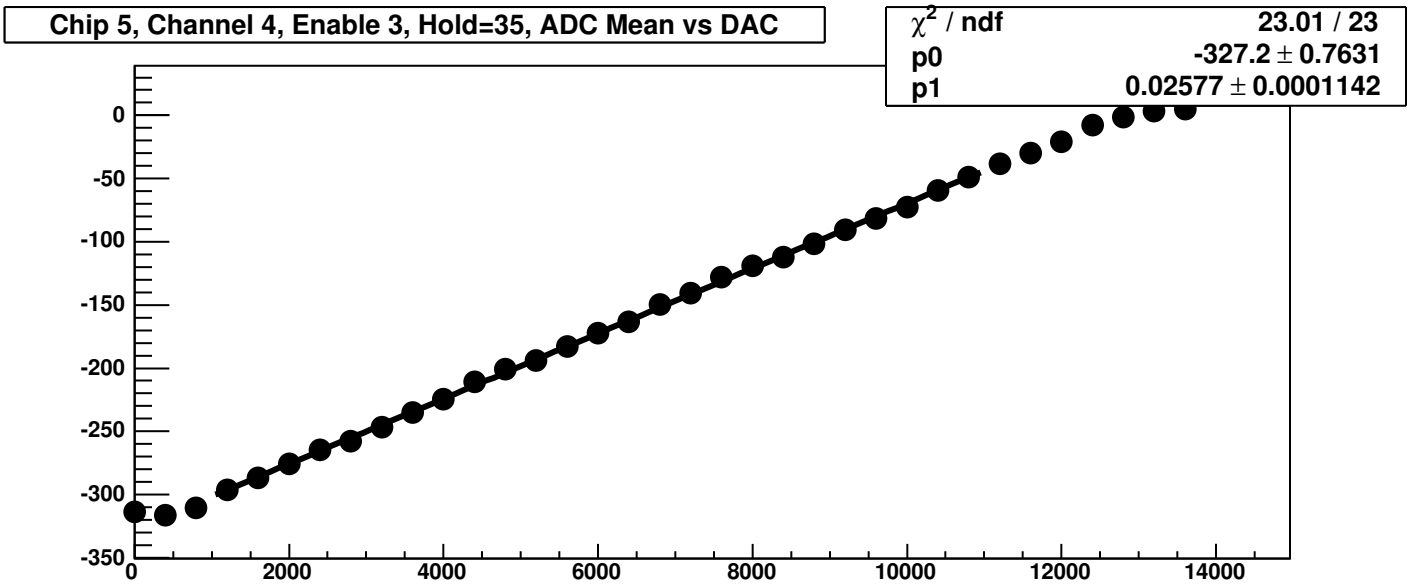

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

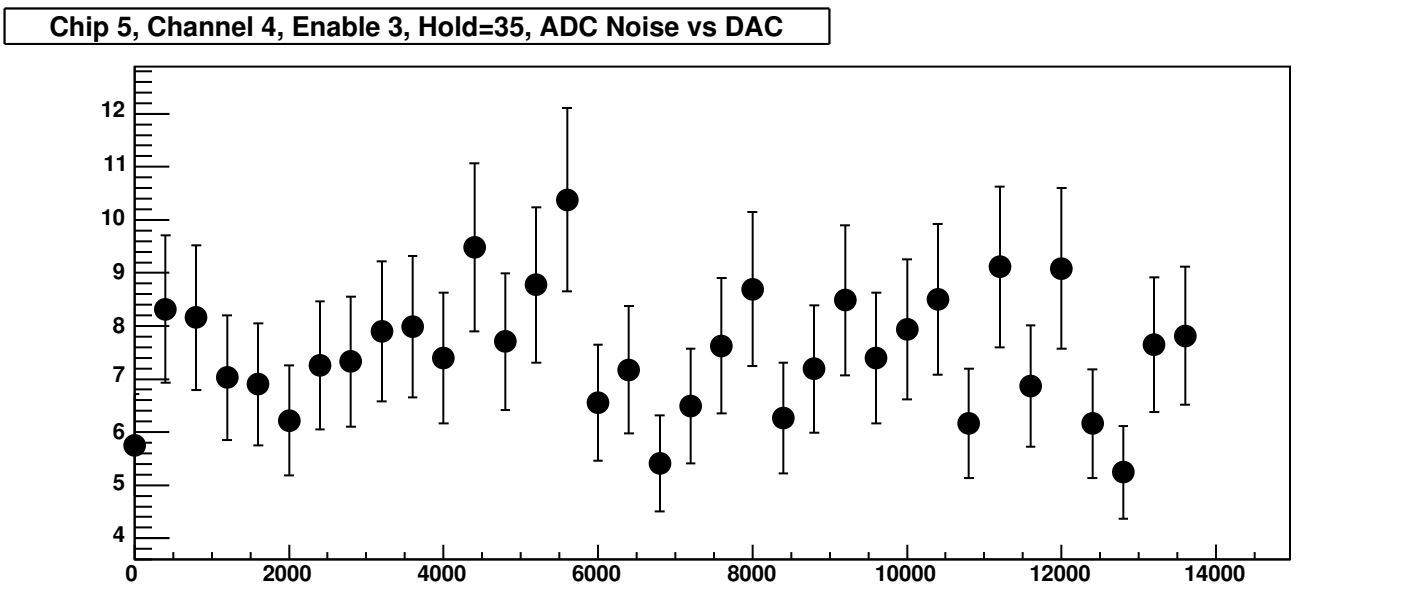

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

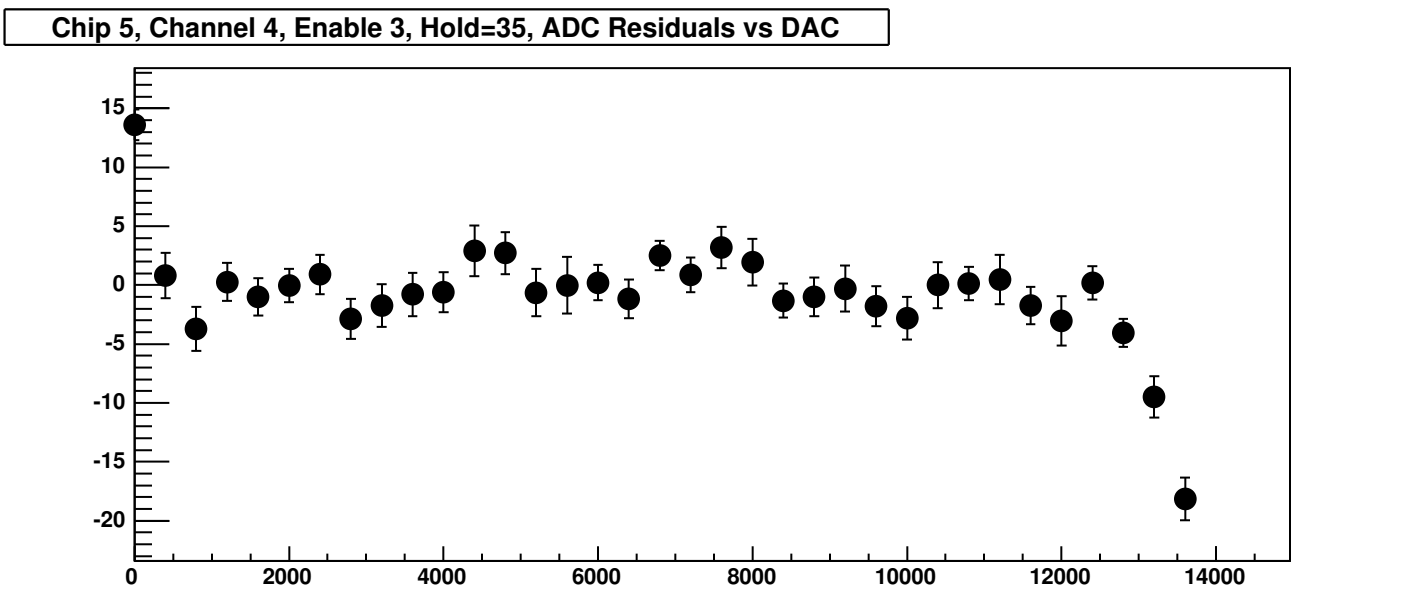

Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 50.69 / 23
p0 -323.1 ± 0.8625
p1 0.02915 ± 0.0001353
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

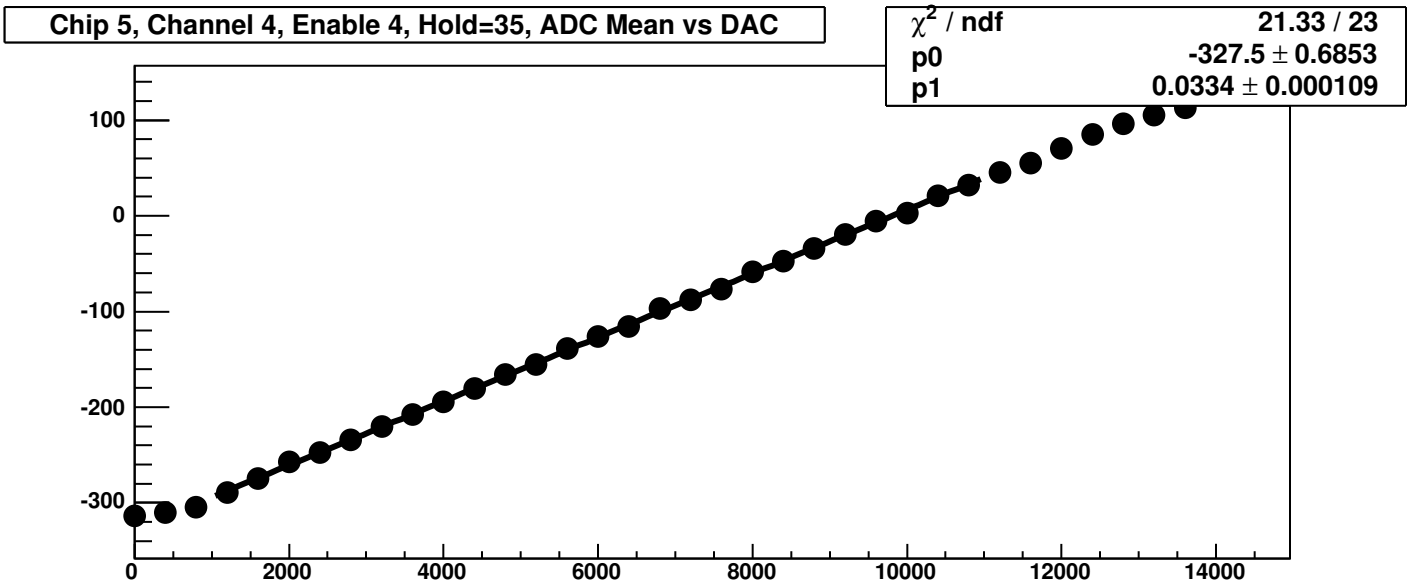

Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

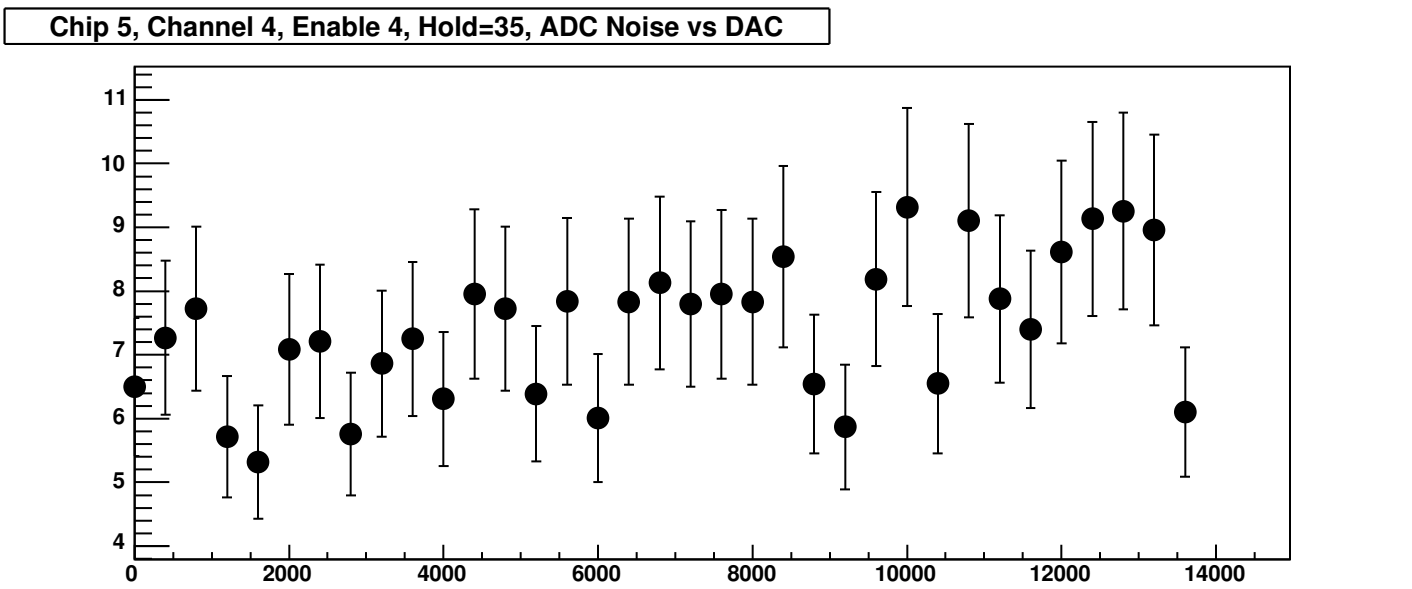

Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

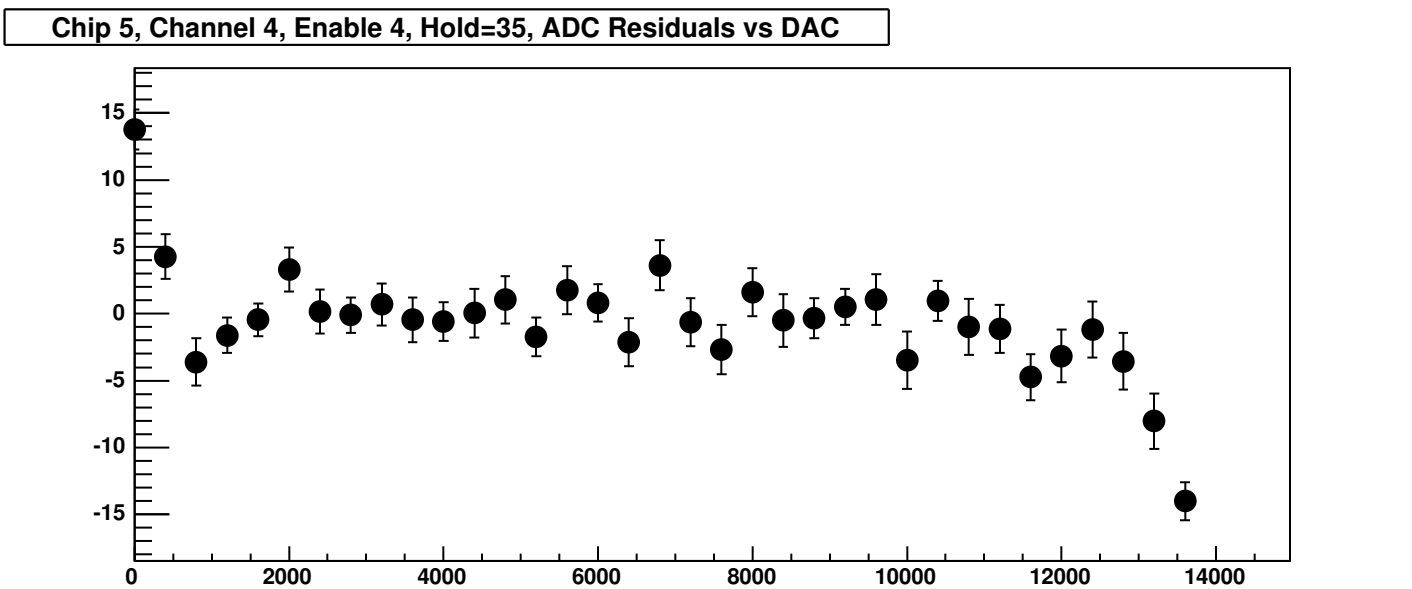

Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.01 / 23
p0 -327.2 ± 0.7631
p1 0.02577 ± 0.0001142
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

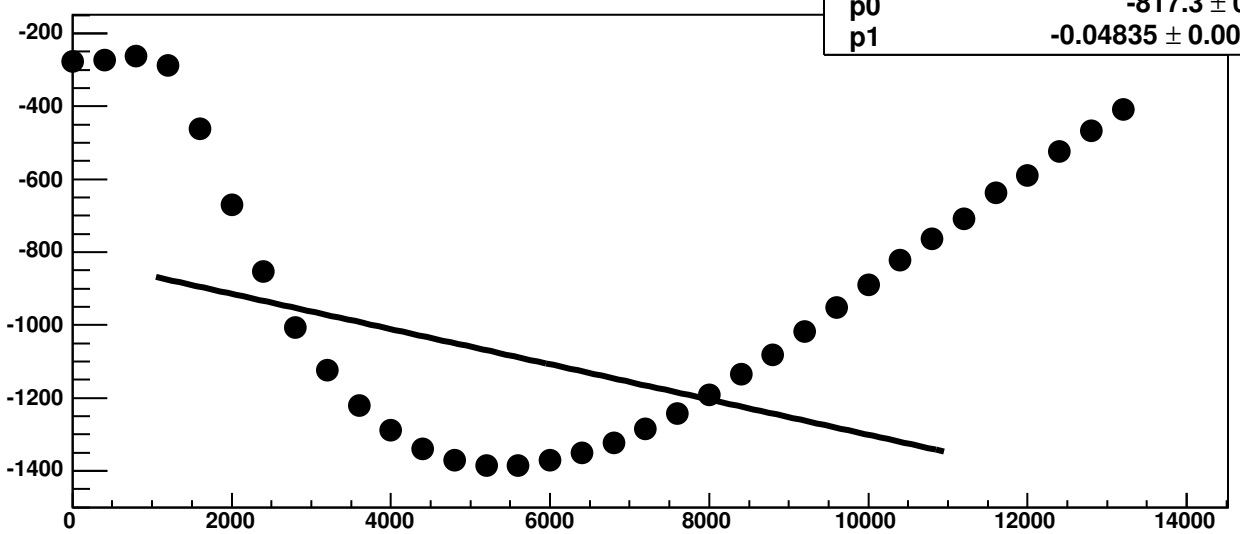

Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.33 / 23
p0 -327.5 ± 0.6853
p1 0.0334 ± 0.000109
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

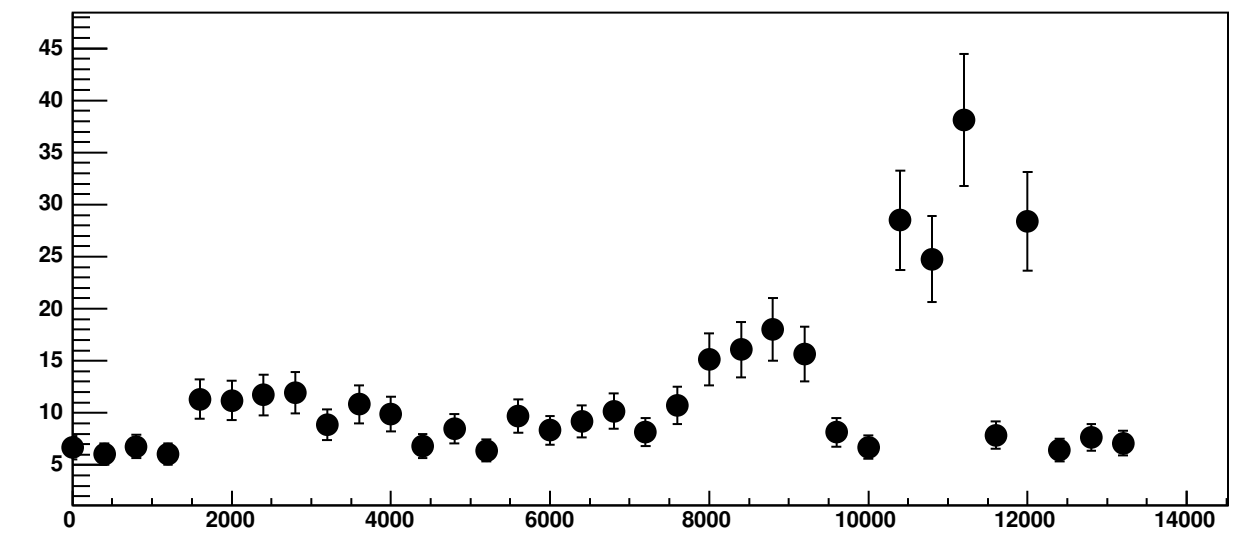

Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

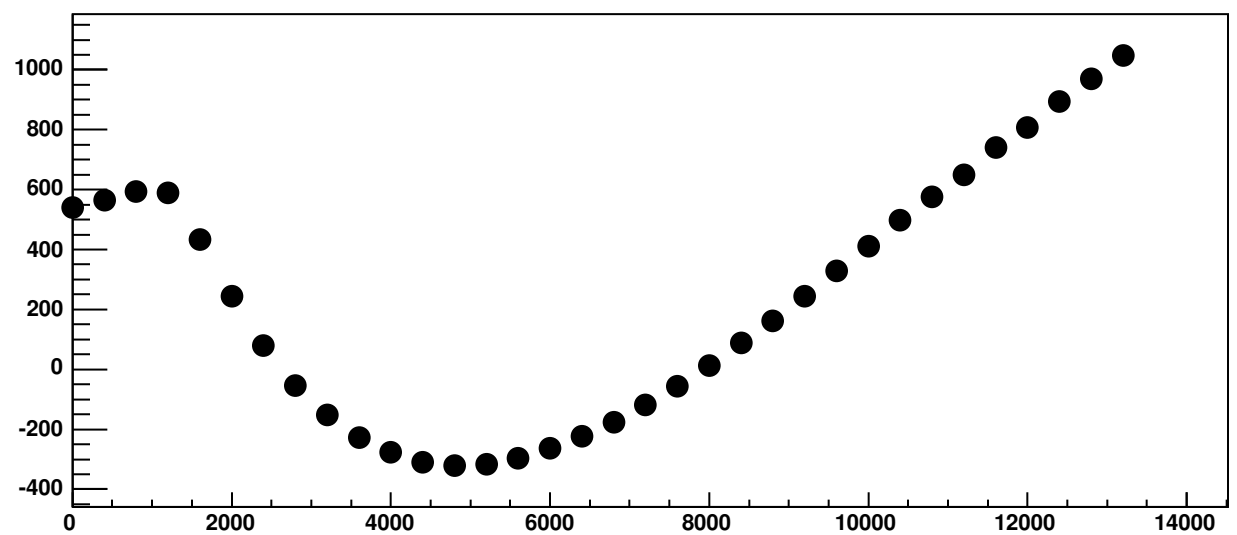
χ^2 / ndf	5.447e+05 / 23
p0	-817.3 ± 0.9756
p1	-0.04835 ± 0.0001607



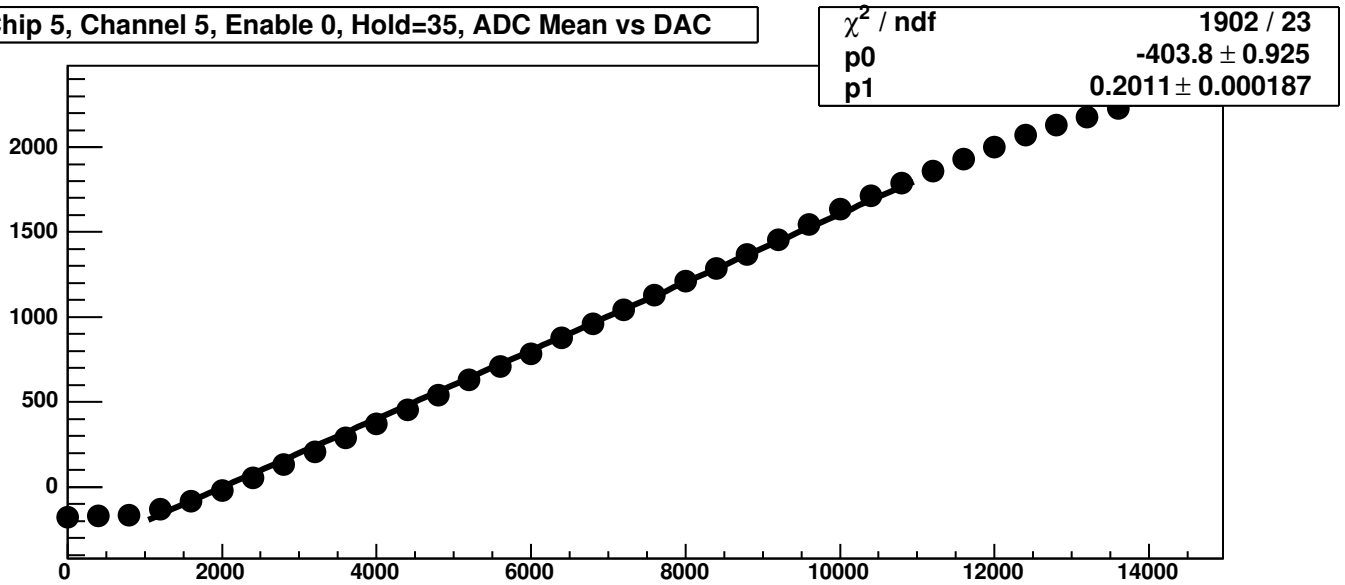
Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



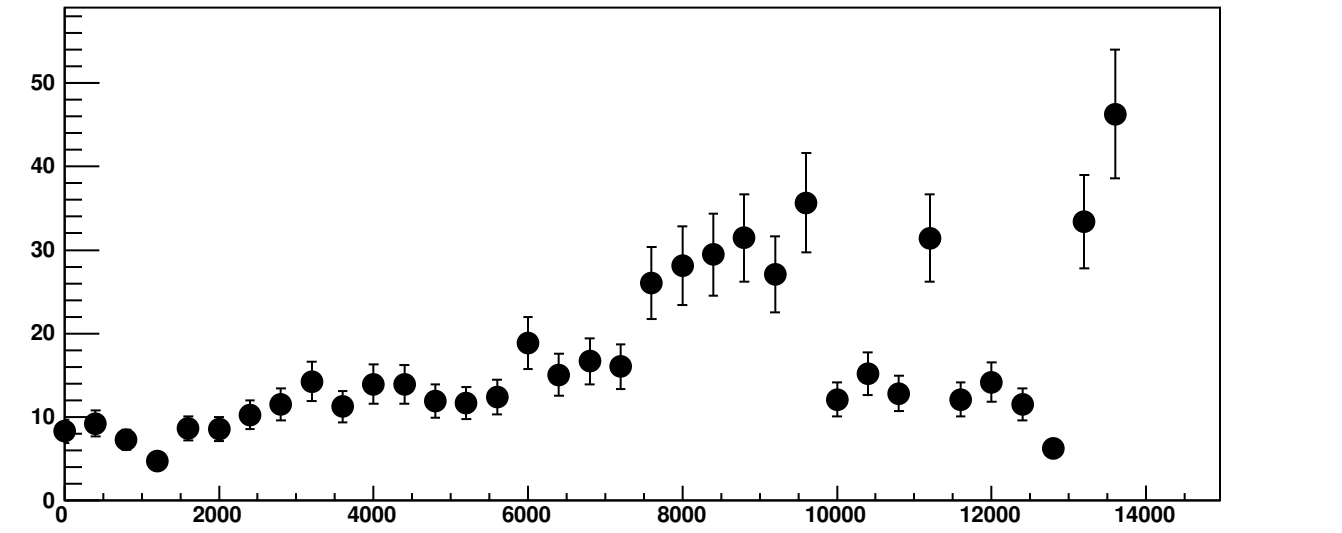
Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



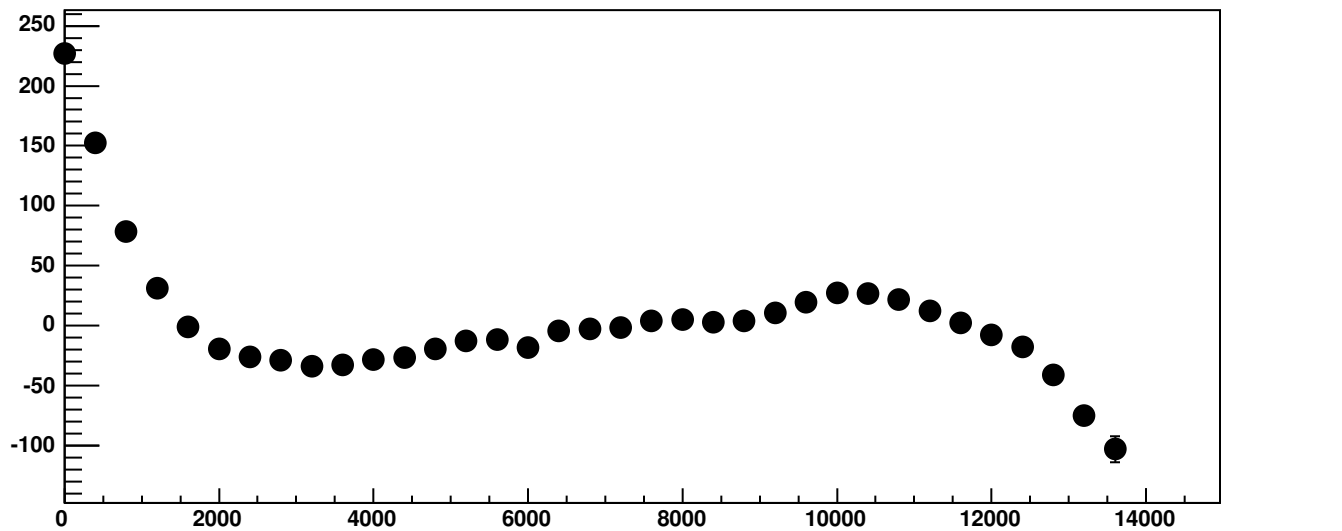
Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

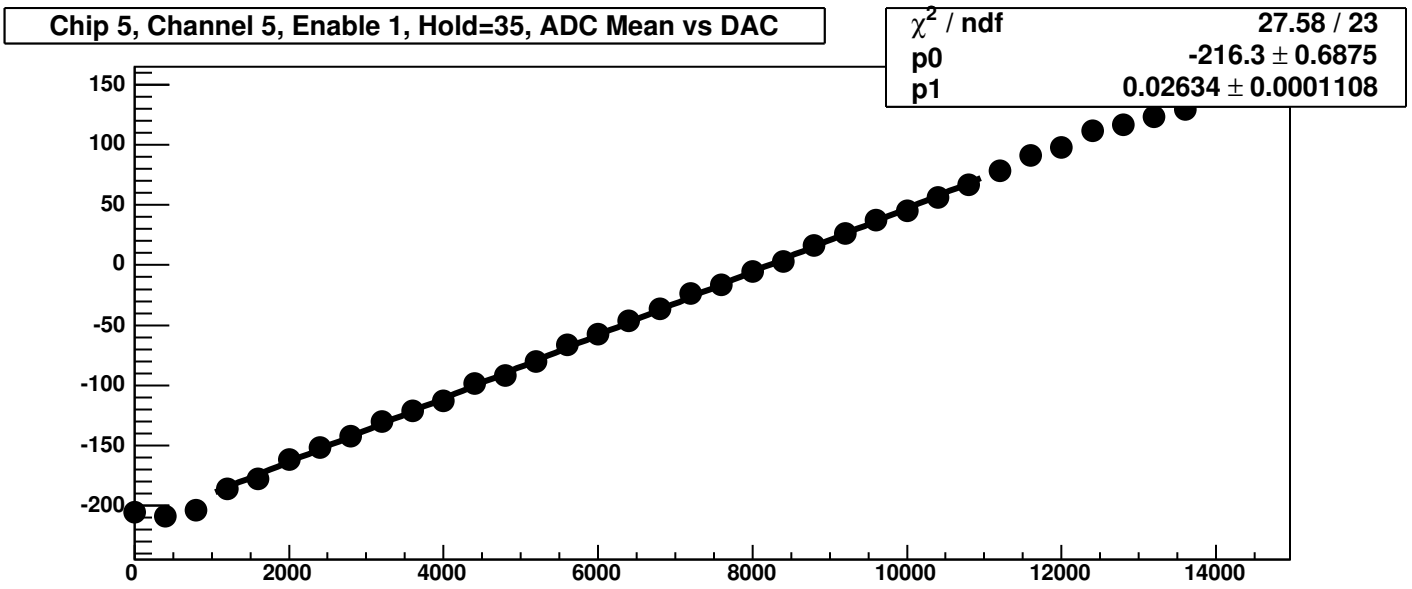


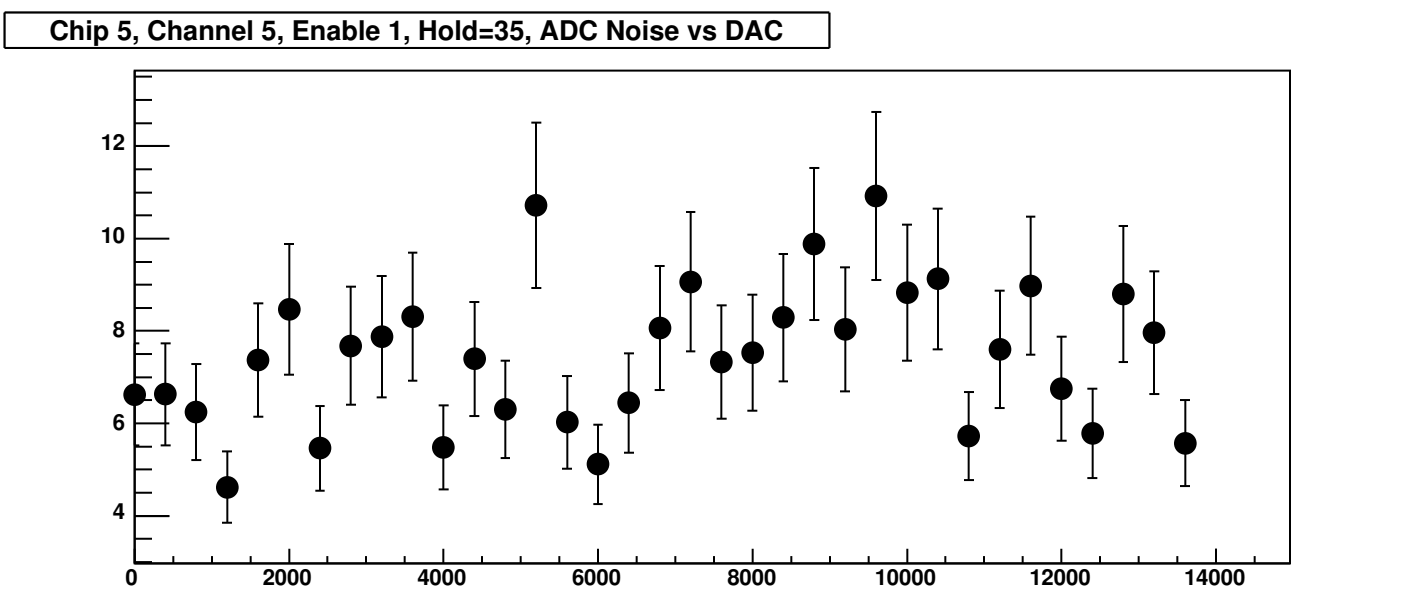
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

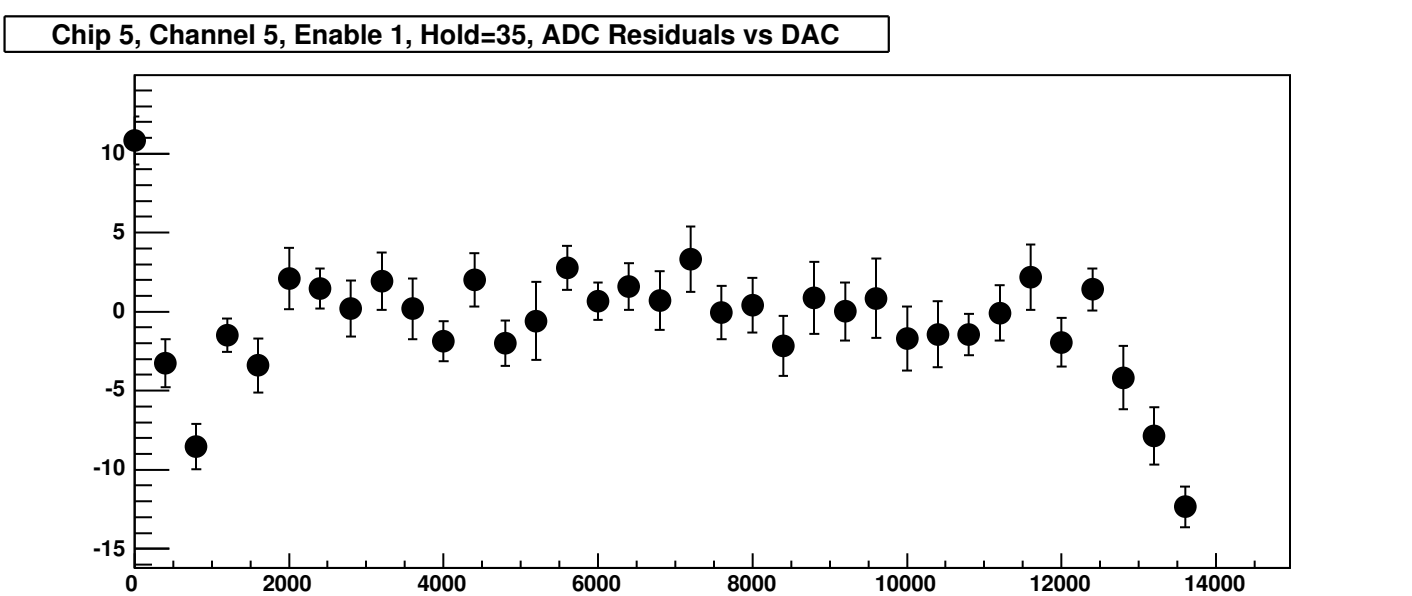


Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

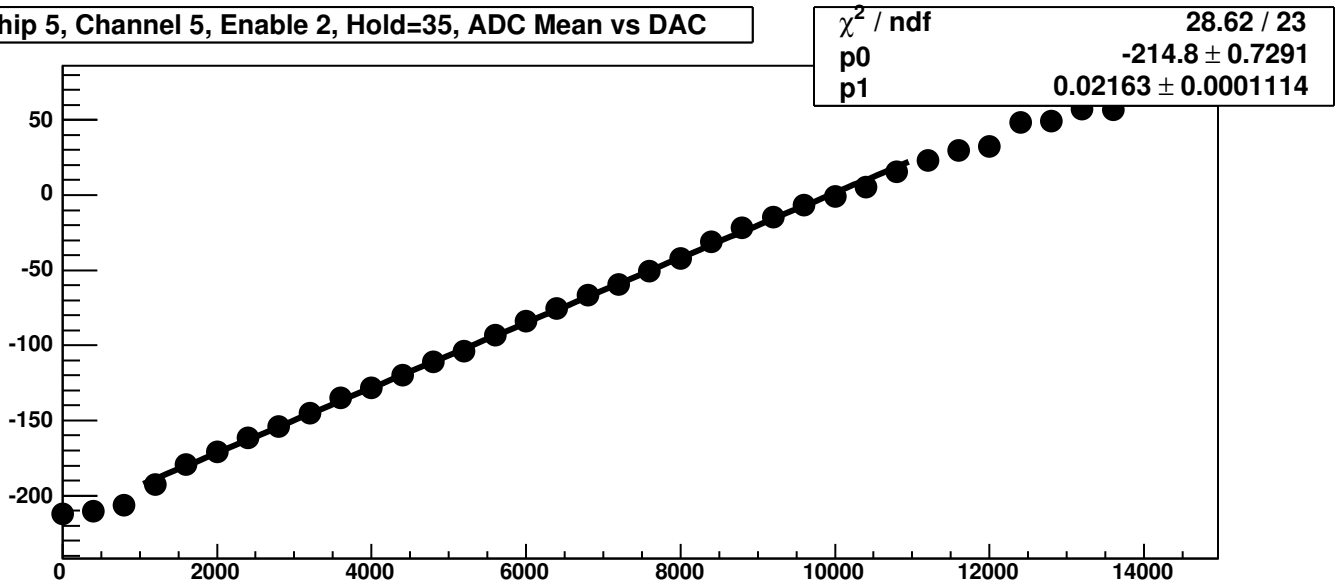



Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.58 / 23
p0 -216.3 ± 0.6875
p1 0.02634 ± 0.0001108
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

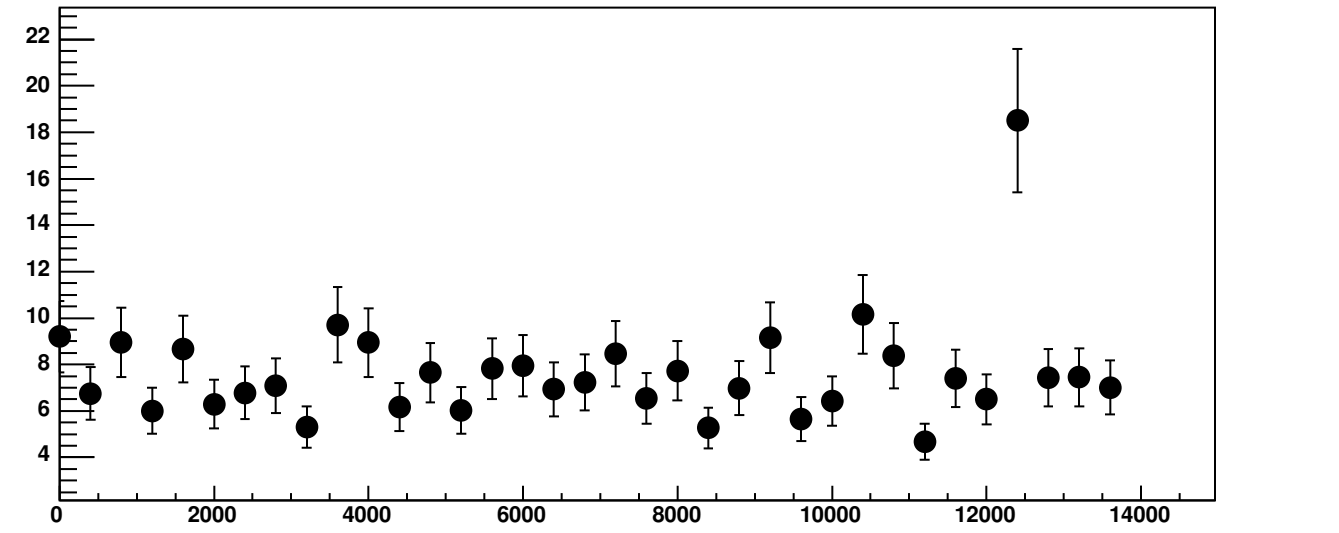

Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

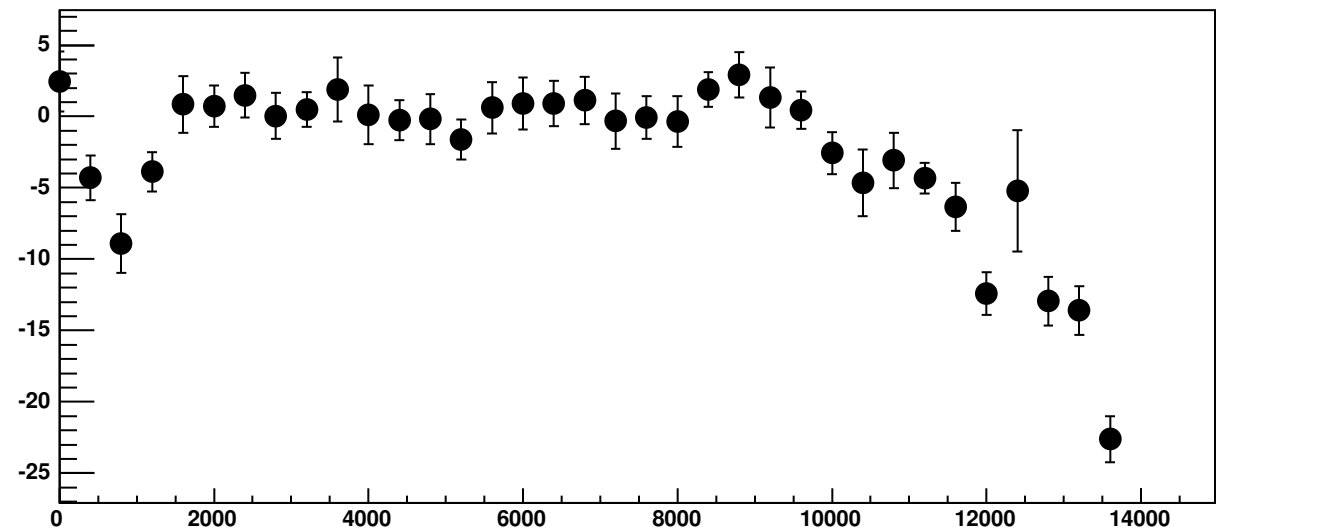
Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

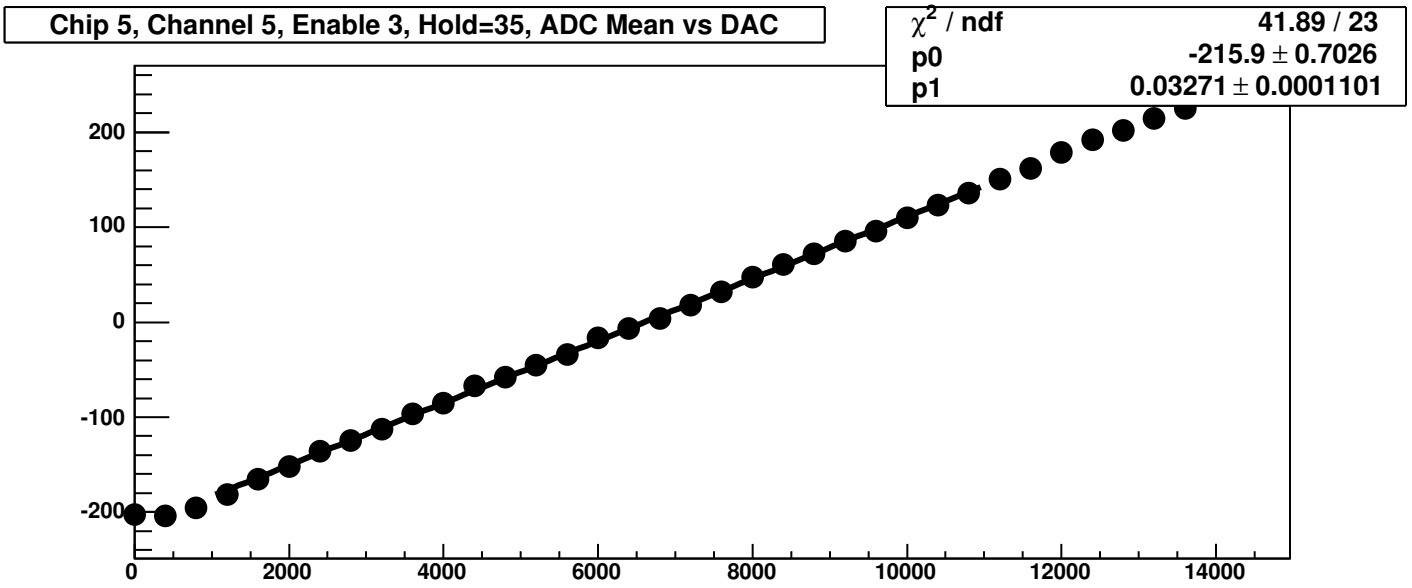


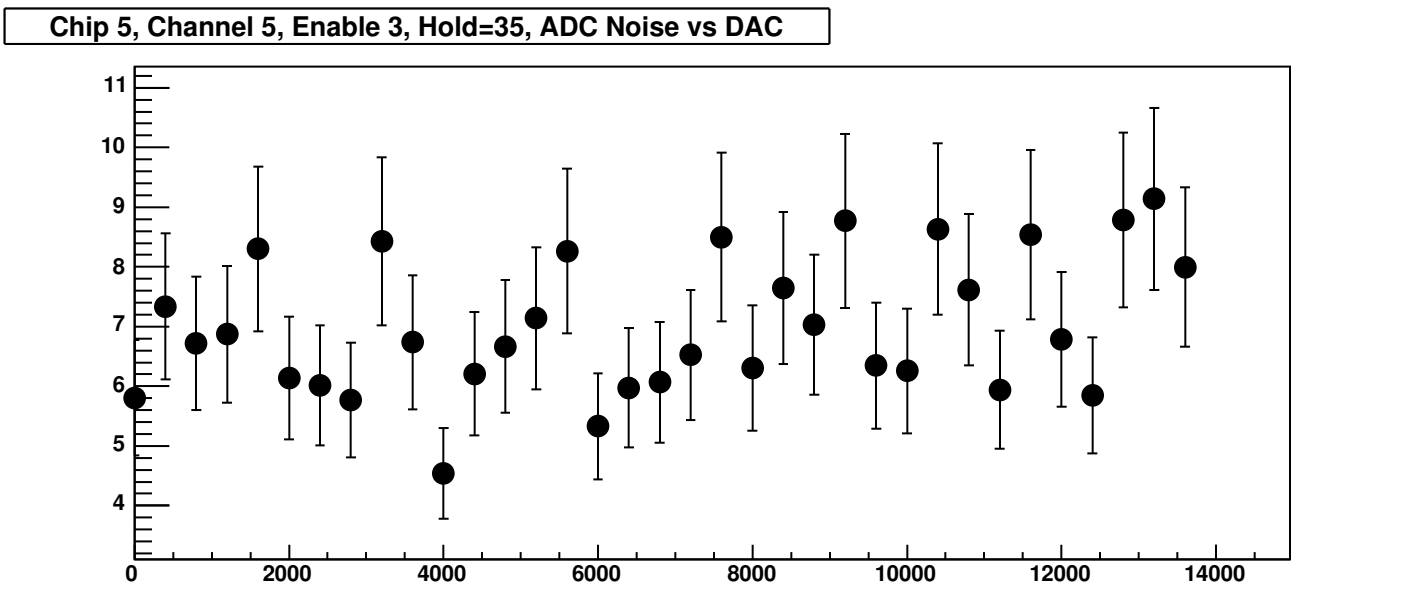
Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

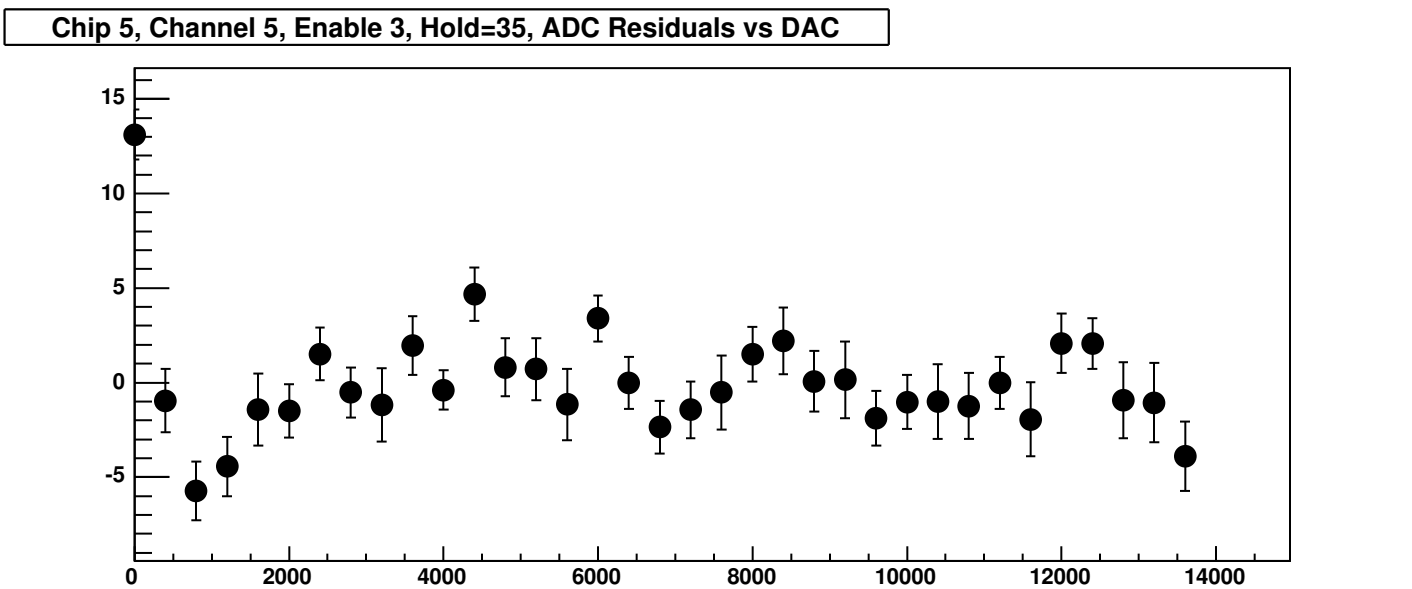


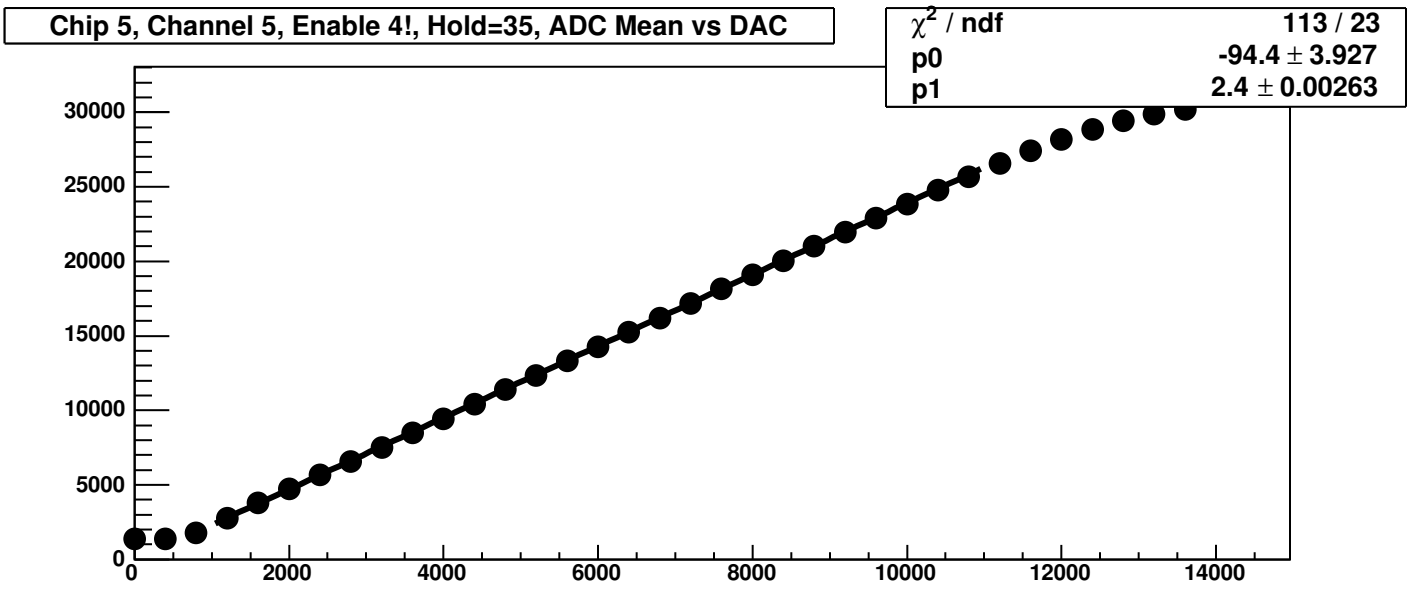
Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

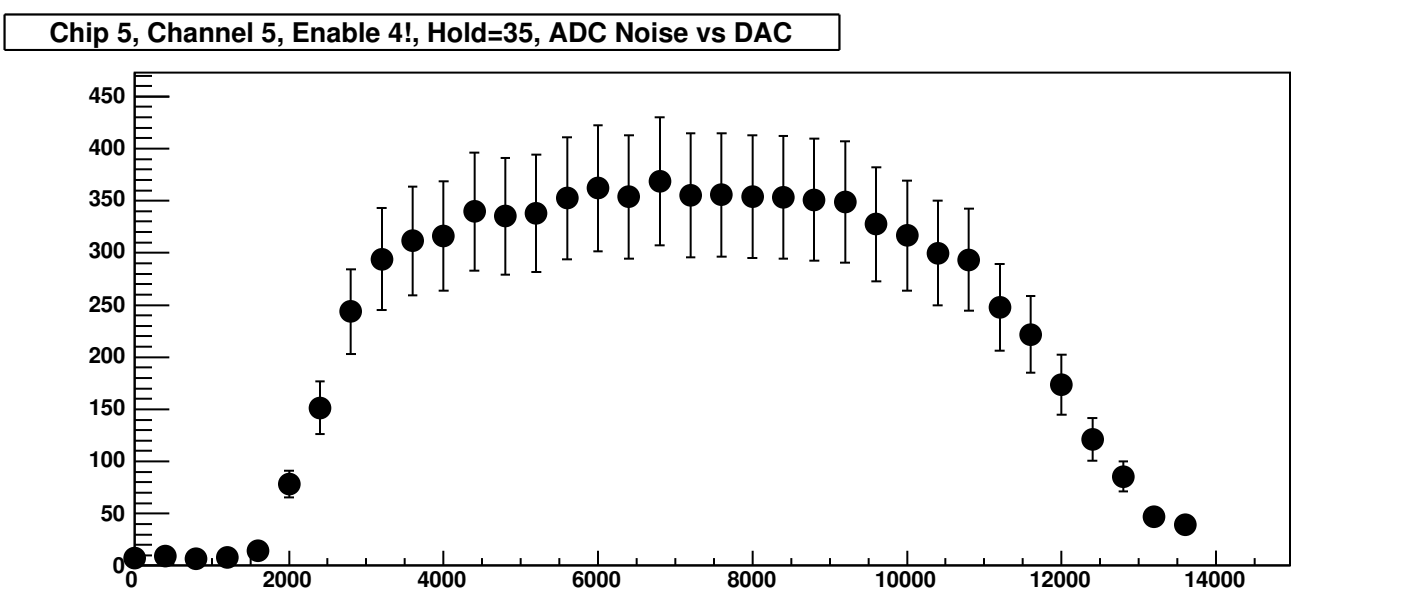


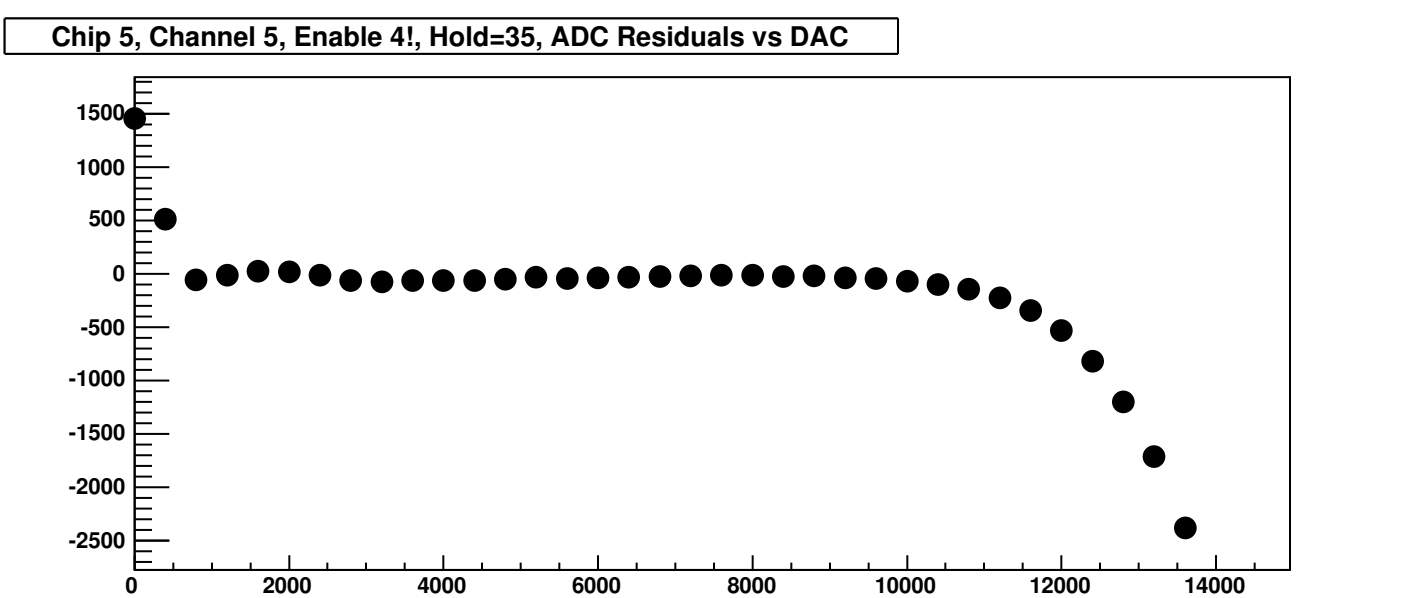

Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.89 / 23
p0 -215.9 ± 0.7026
p1 0.03271 ± 0.0001101
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

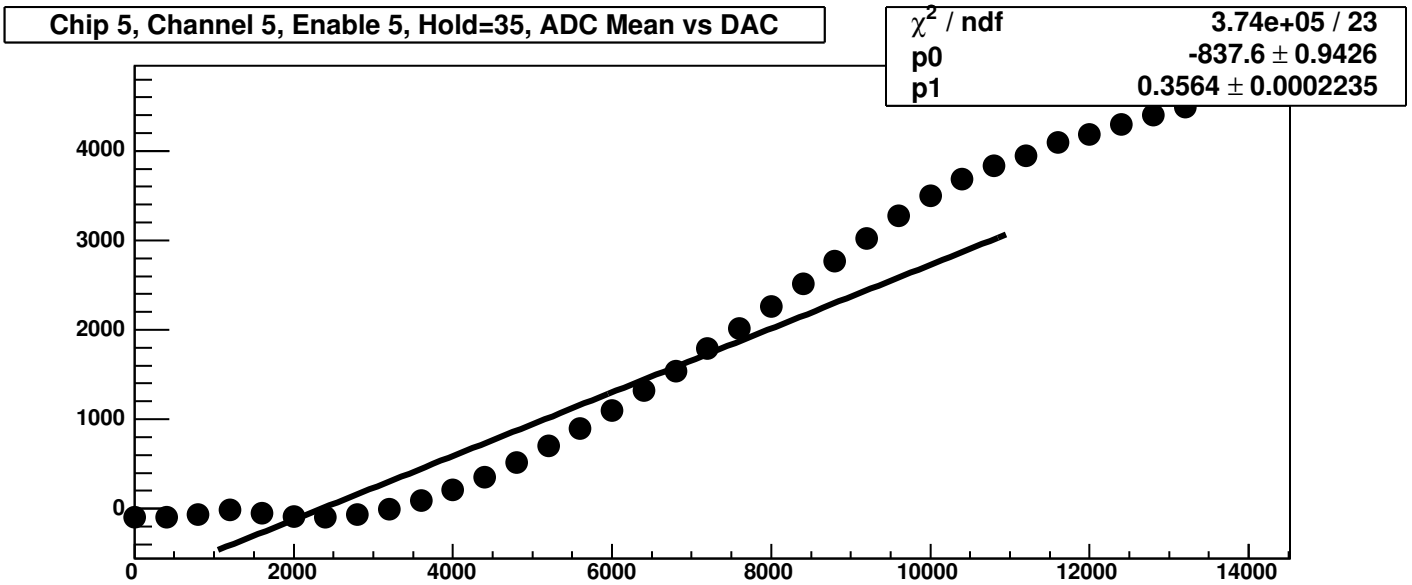

Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

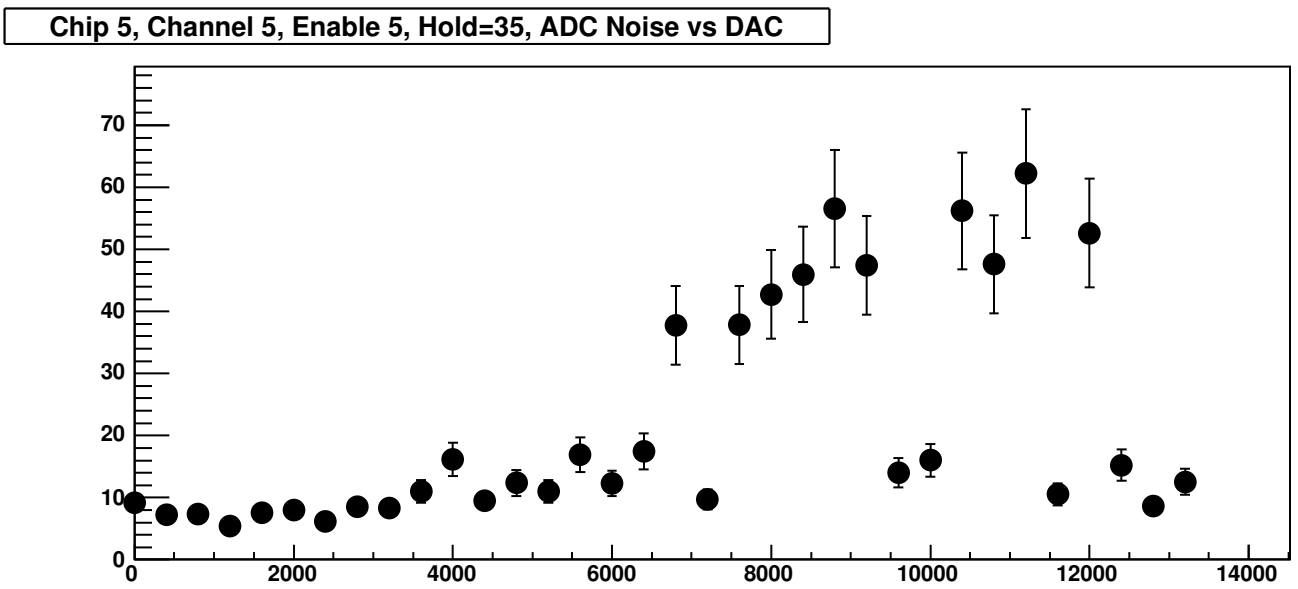

Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

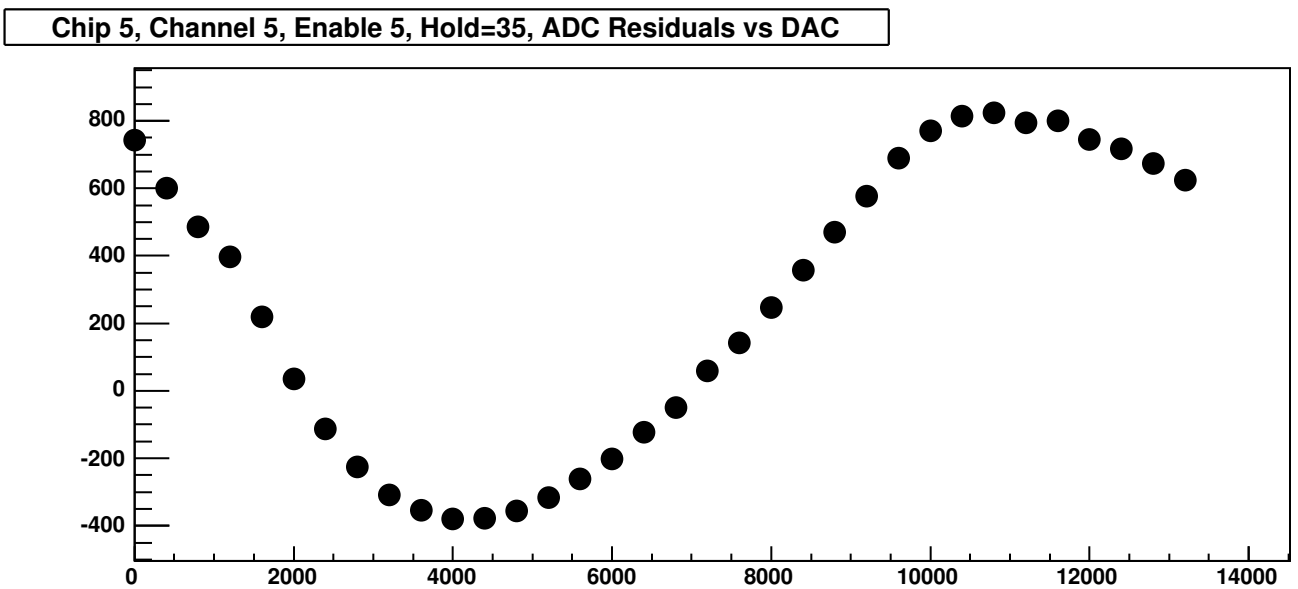

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 113 / 23
p0 -94.4 ± 3.927
p1 2.4 ± 0.00263
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

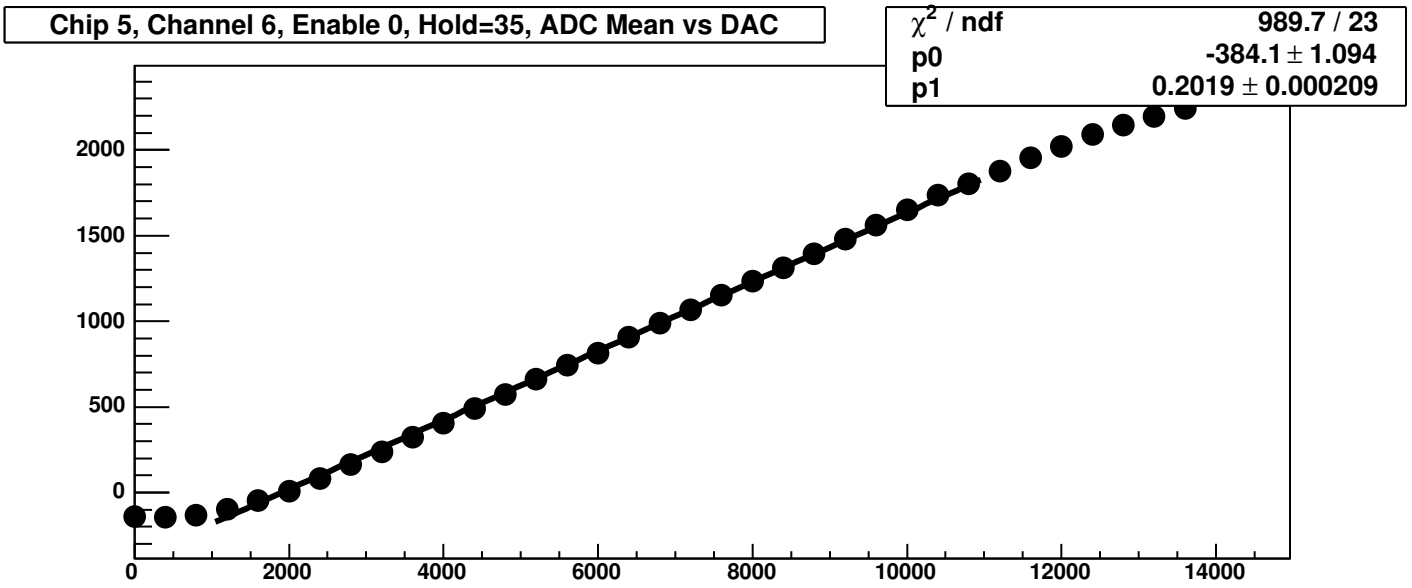

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

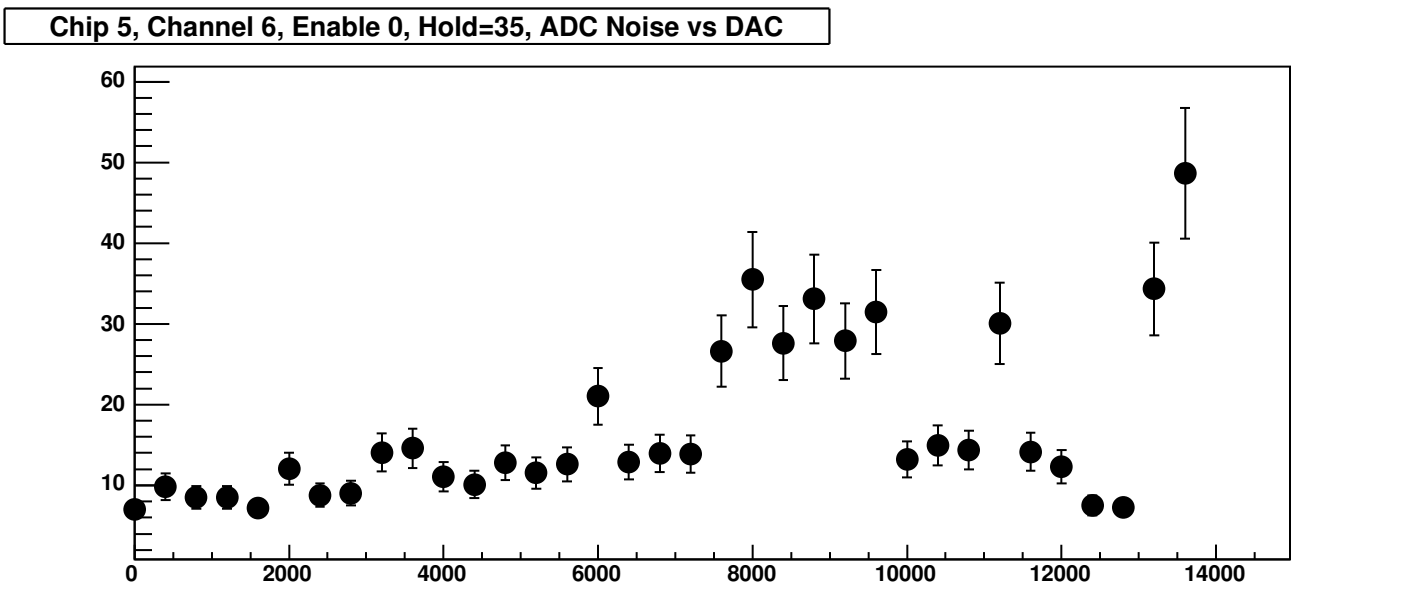

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

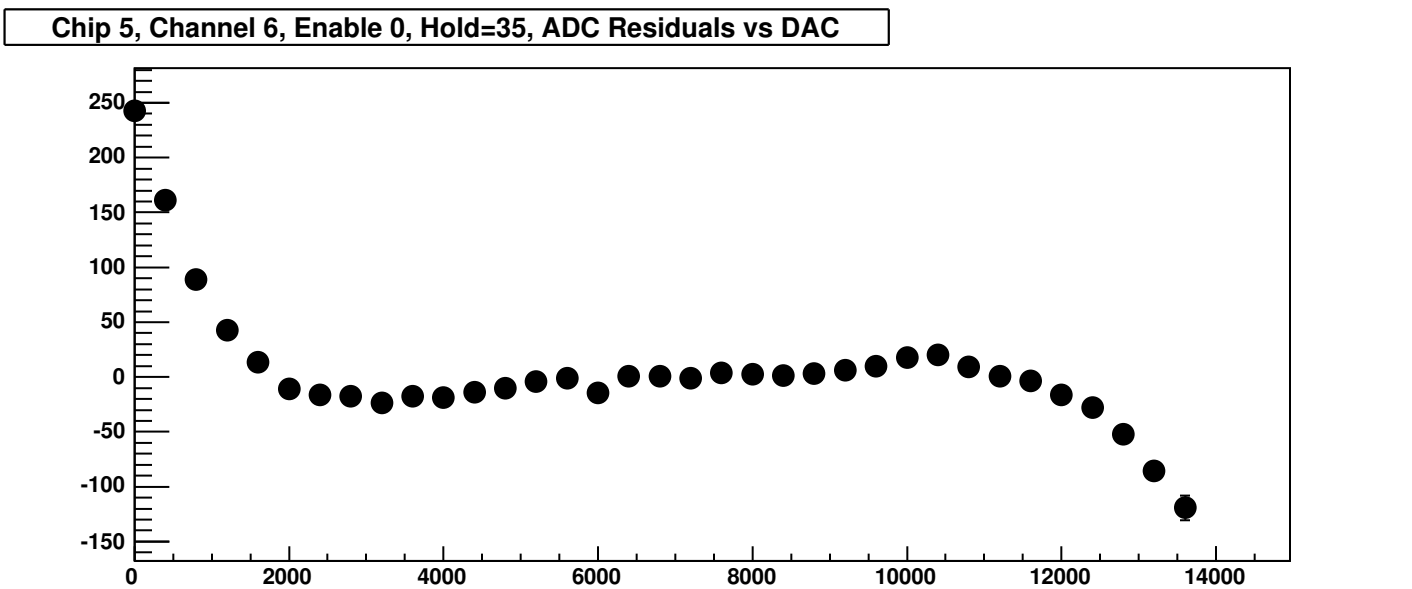

Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.74e+05 / 23
p0 -837.6 ± 0.9426
p1 0.3564 ± 0.0002235
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

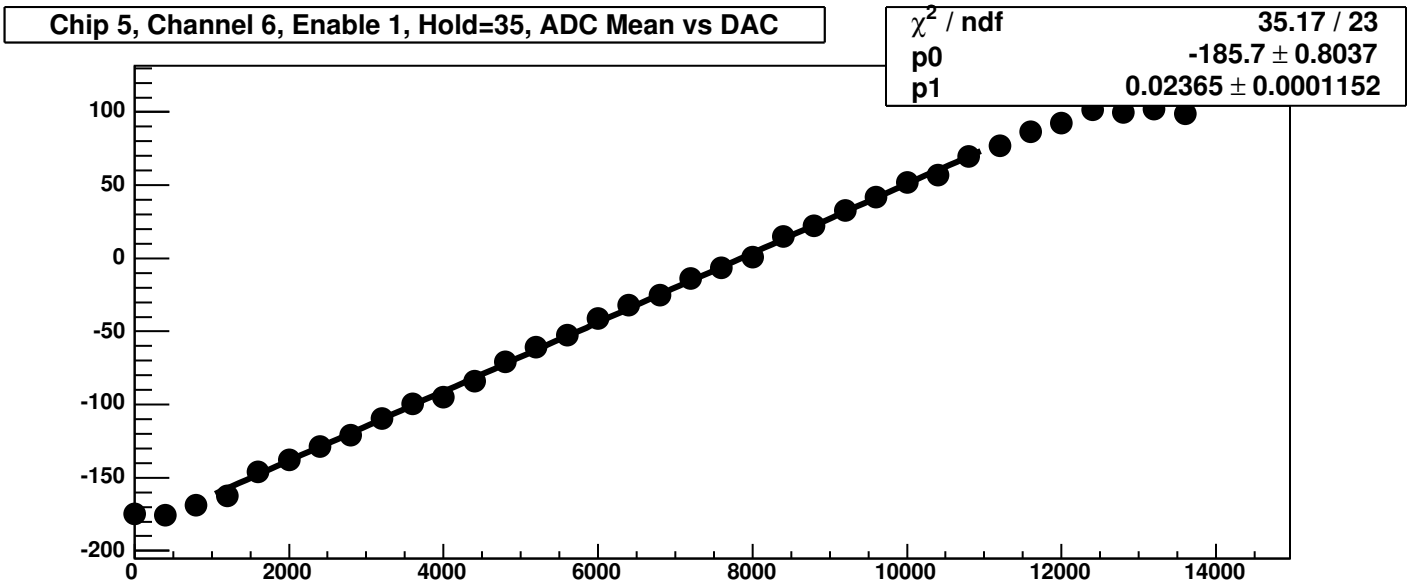

Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

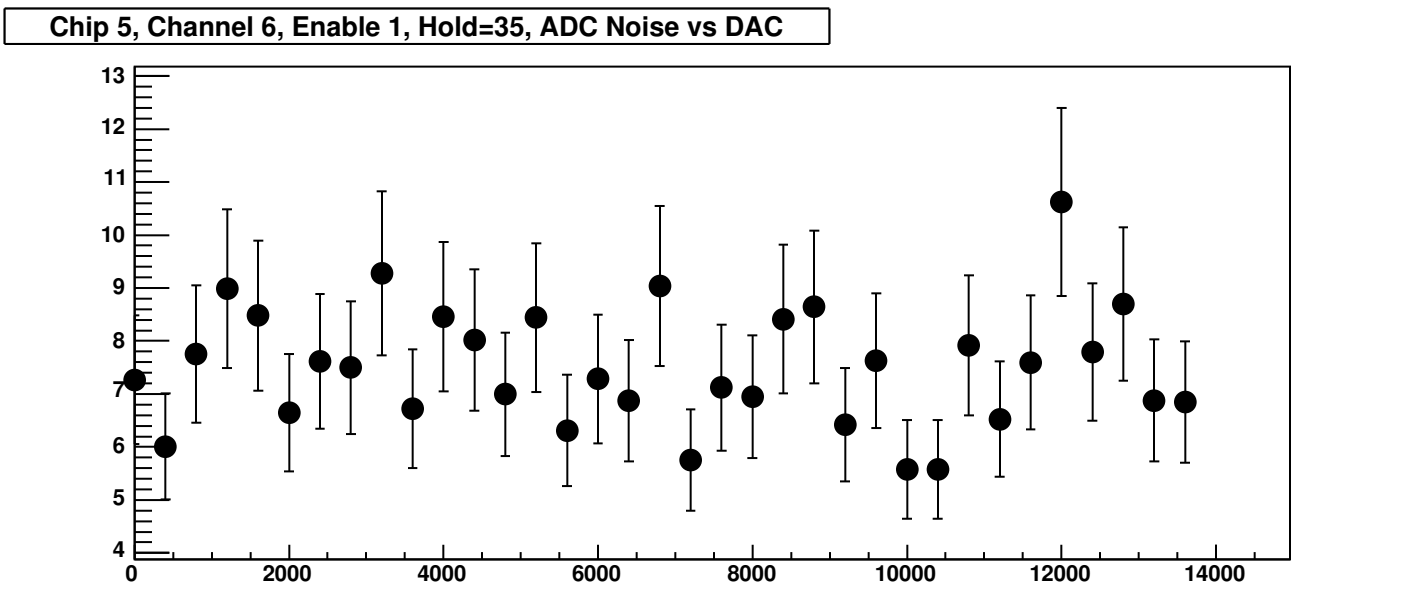

Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

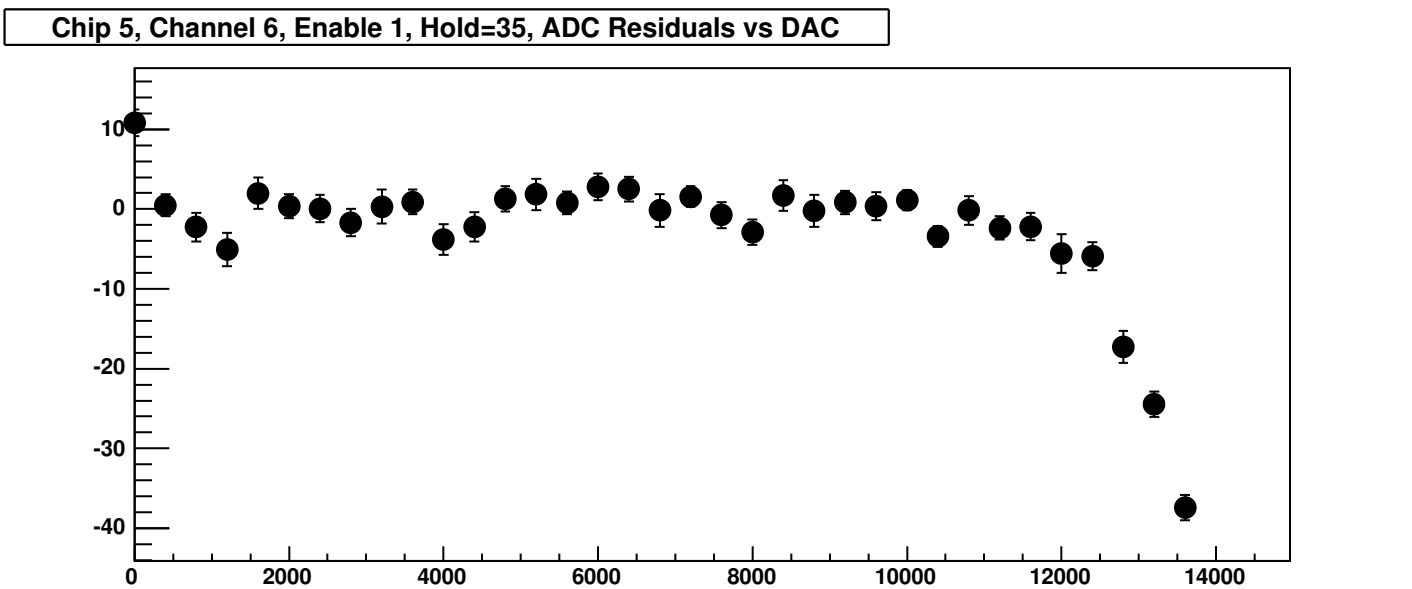

Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 989.7 / 23
p0 -384.1 ± 1.094
p1 0.2019 ± 0.000209
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

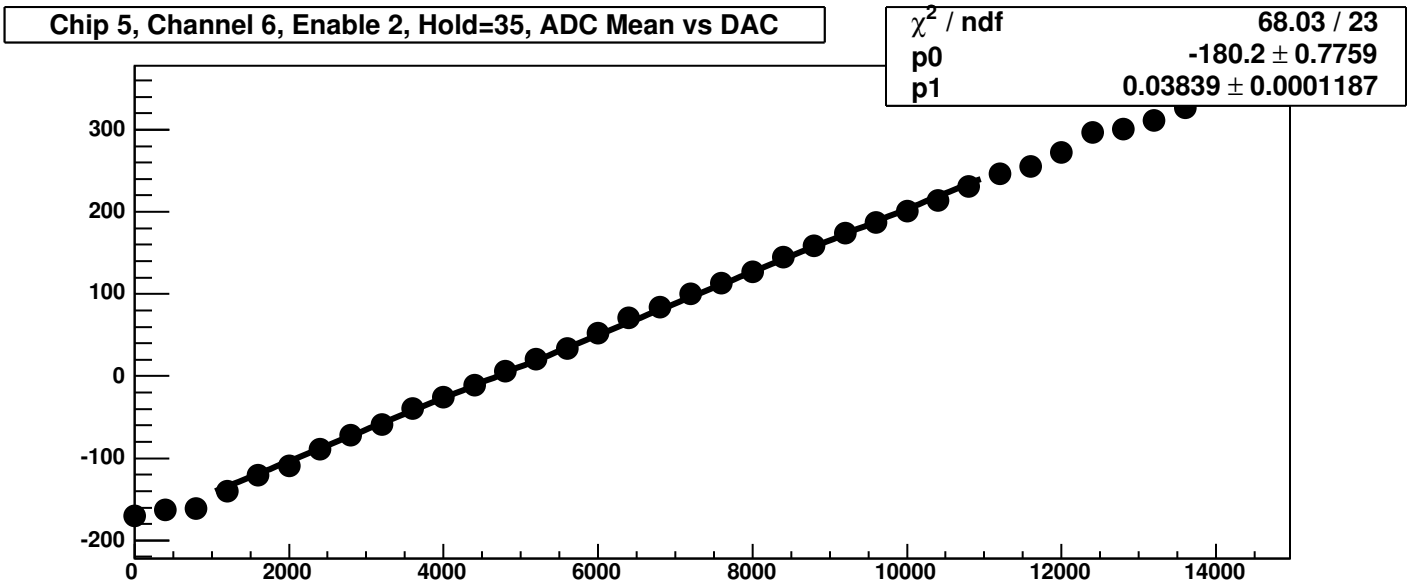

Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

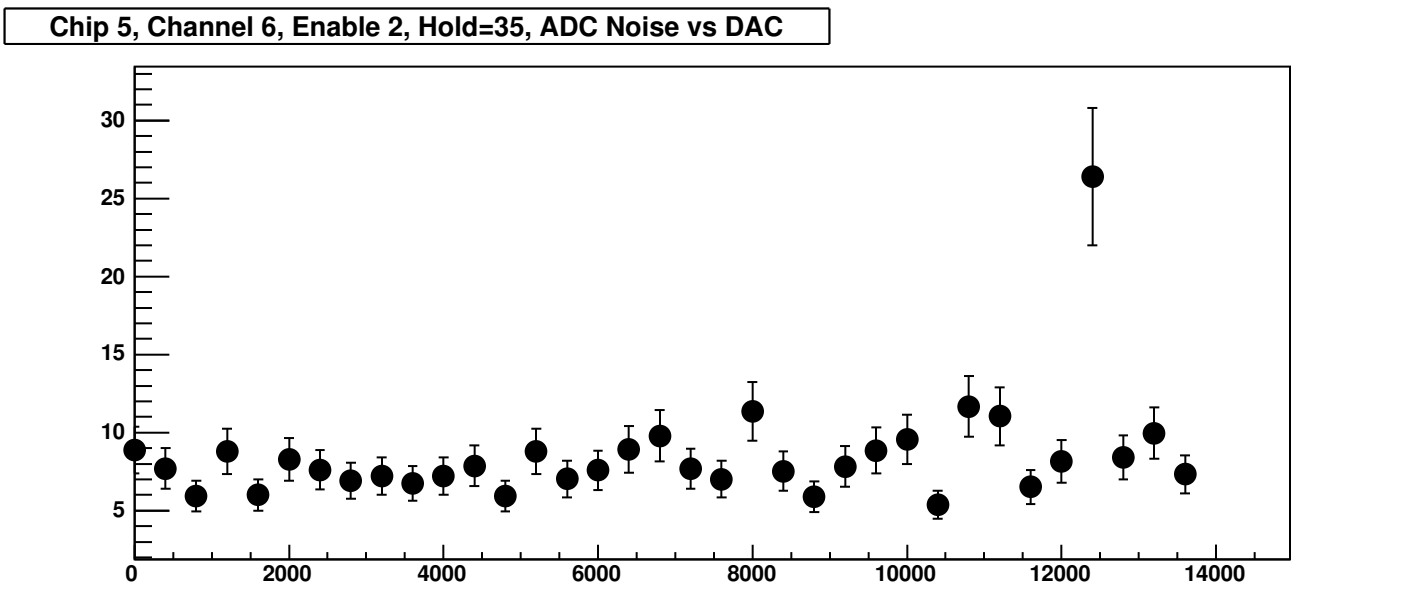

Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

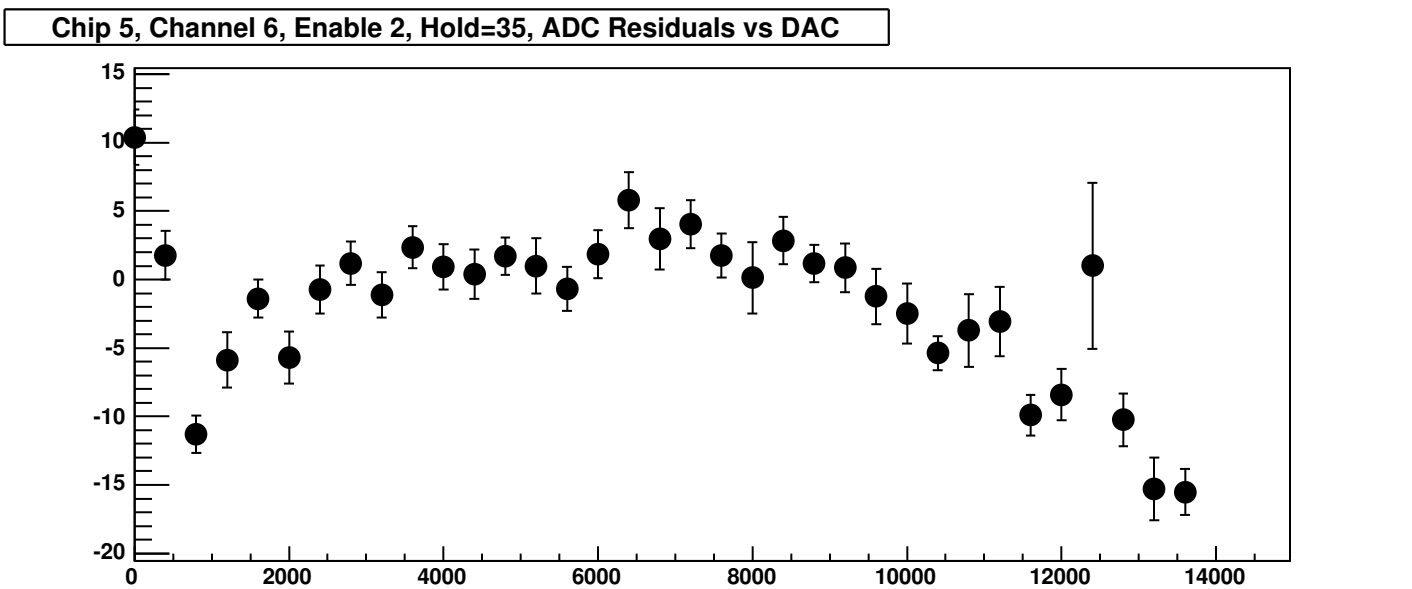

Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.17 / 23
p0 -185.7 ± 0.8037
p1 0.02365 ± 0.0001152
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

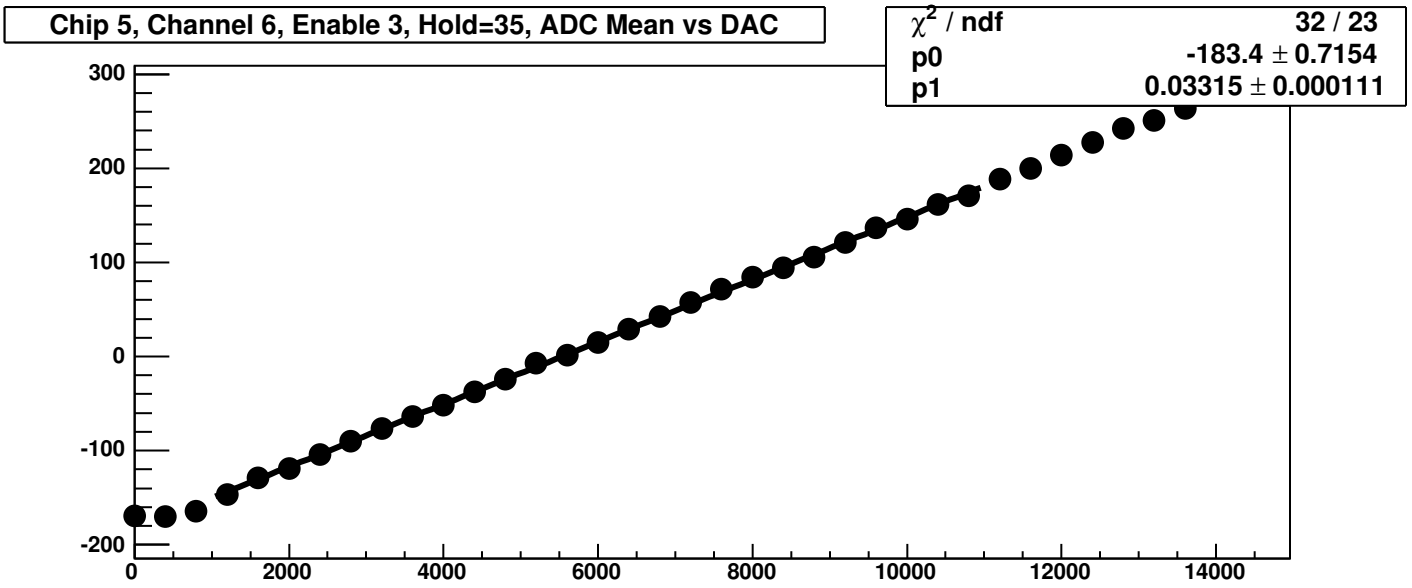

Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

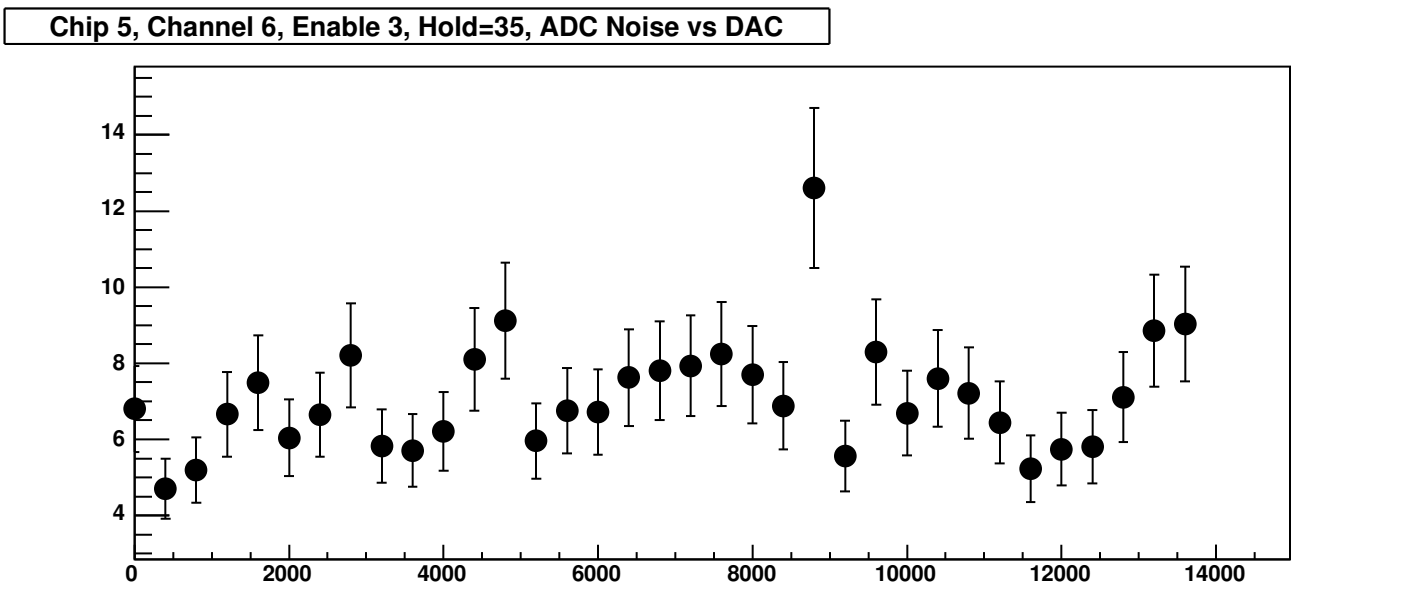

Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

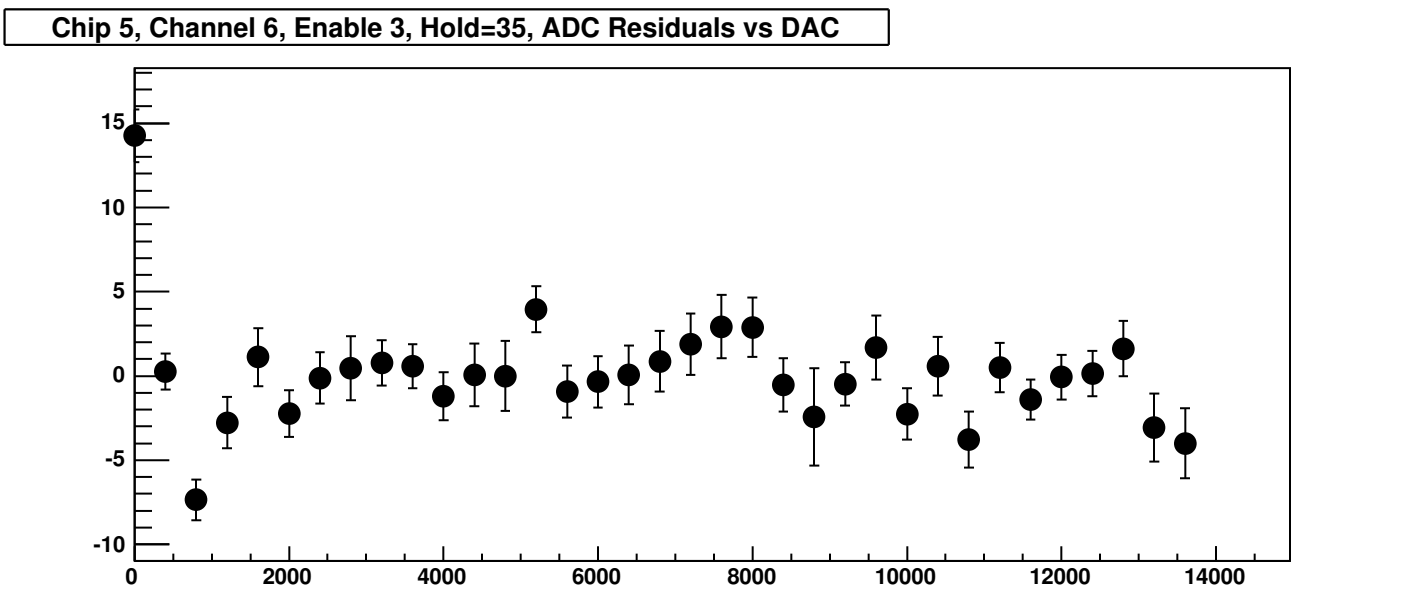

Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 68.03 / 23
p0 -180.2 ± 0.7759
p1 0.03839 ± 0.0001187
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

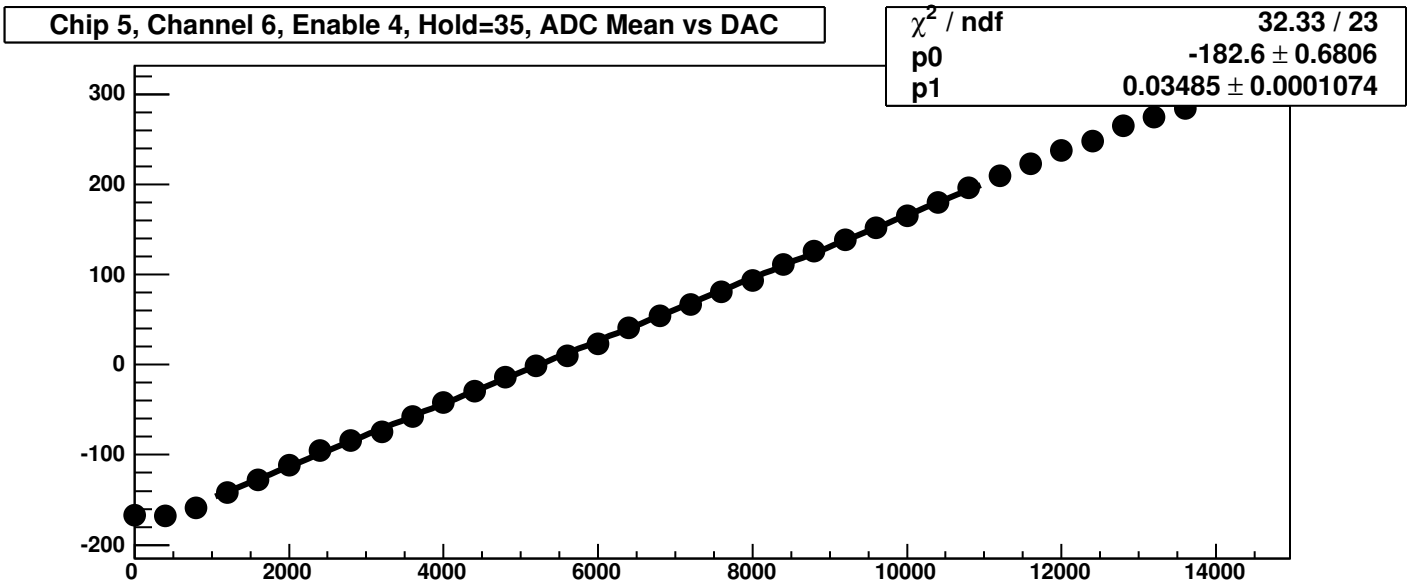

Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

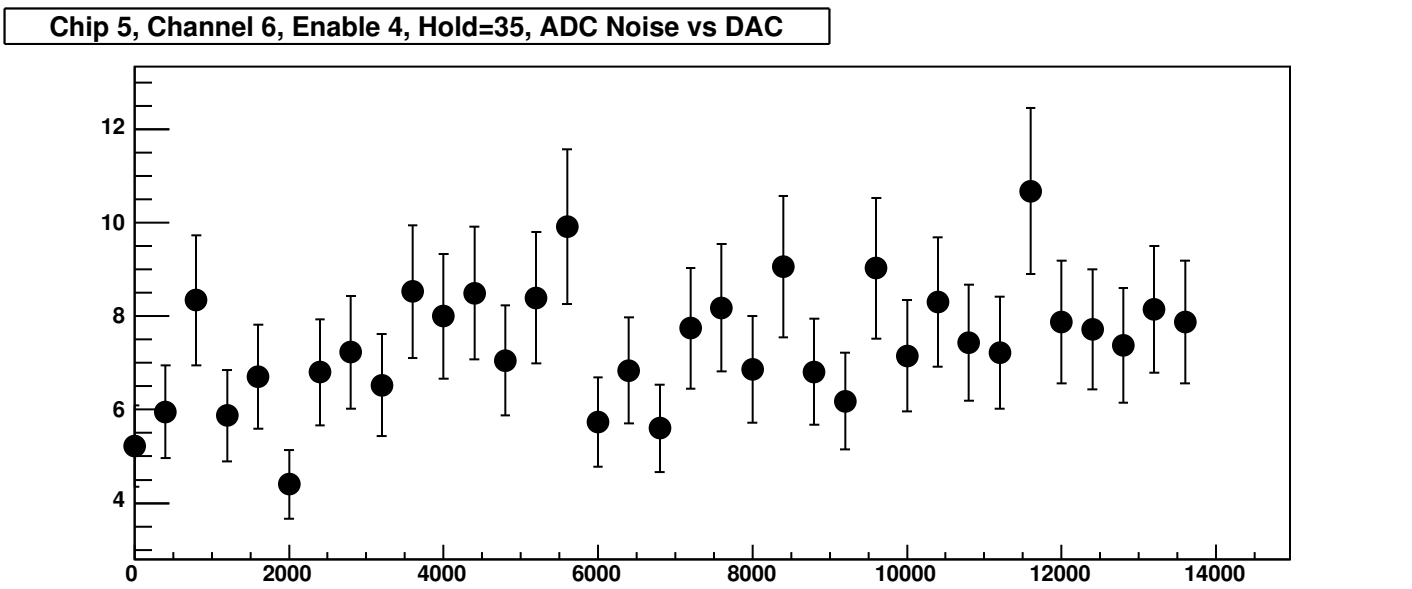

Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

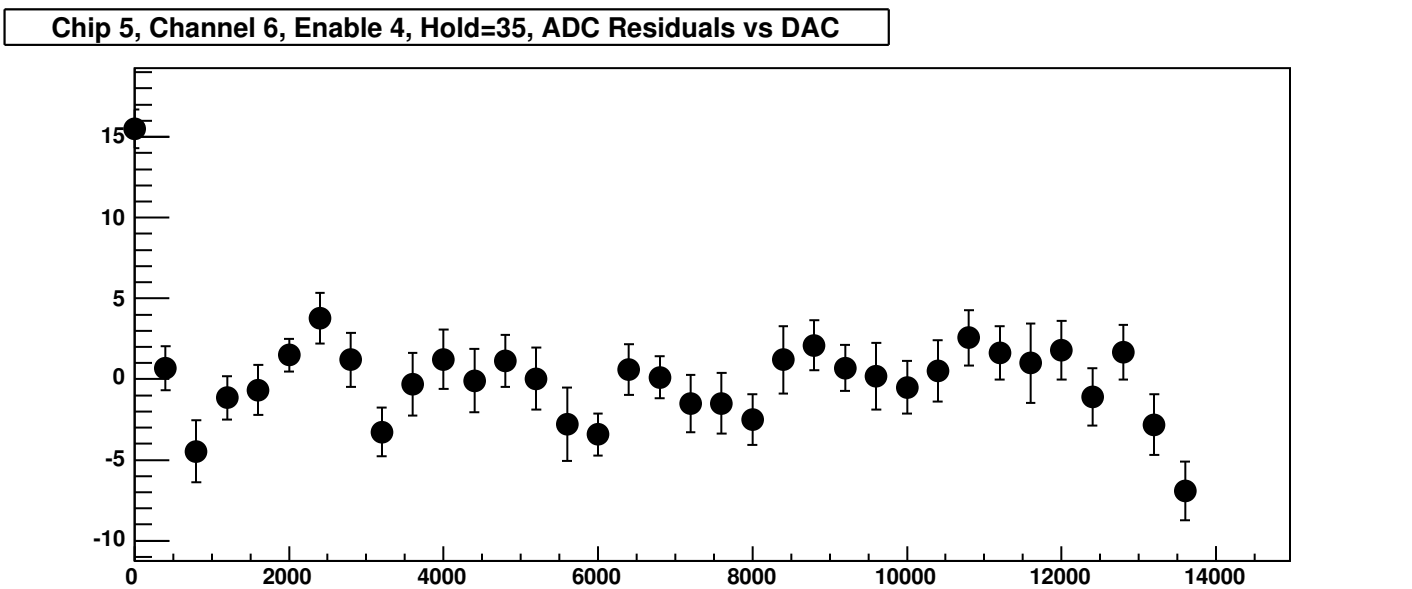

Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32 / 23
p0 -183.4 ± 0.7154
p1 0.03315 ± 0.000111
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

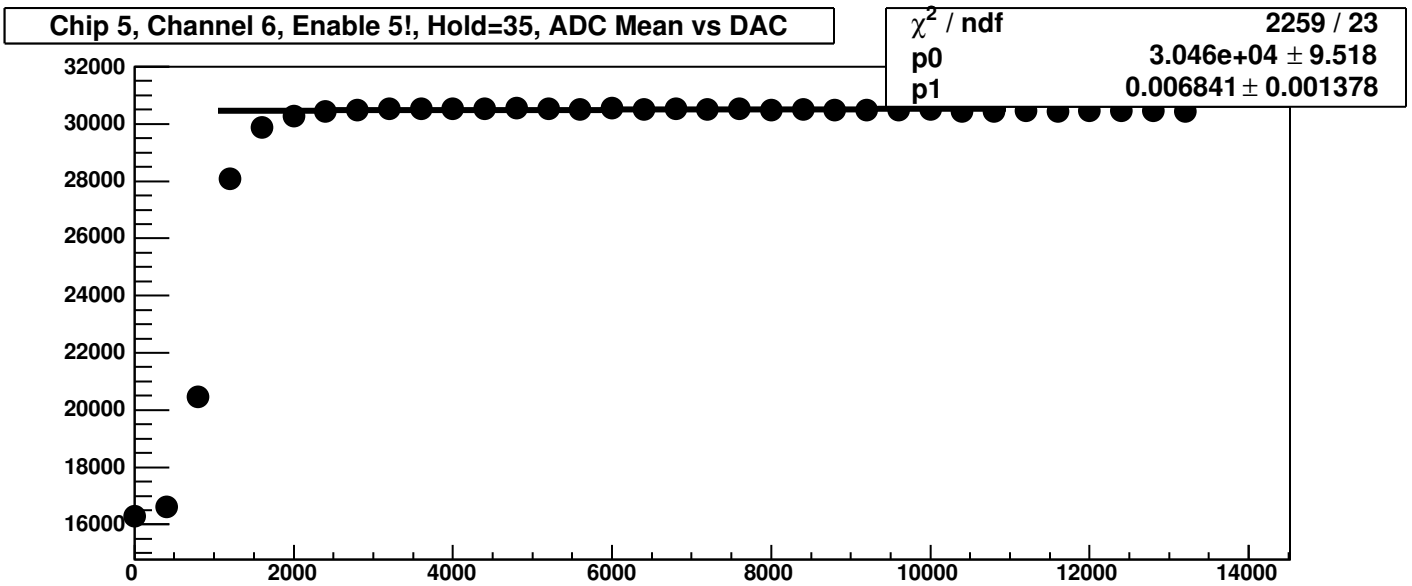

Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

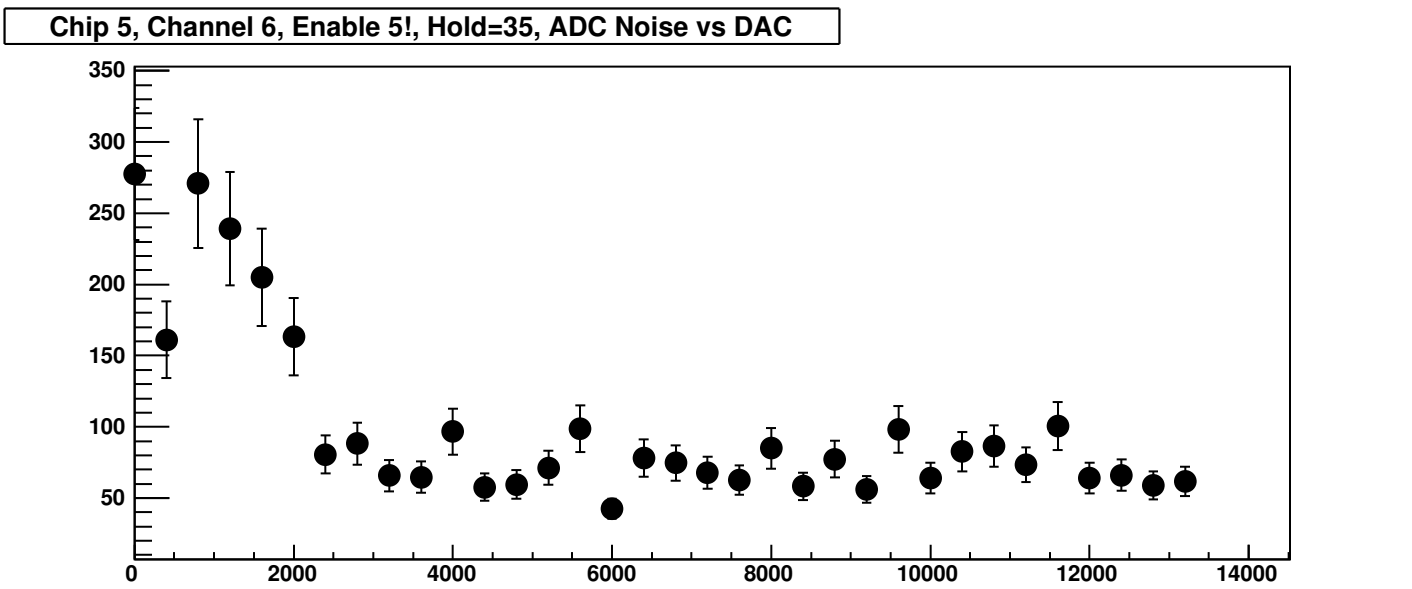

Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

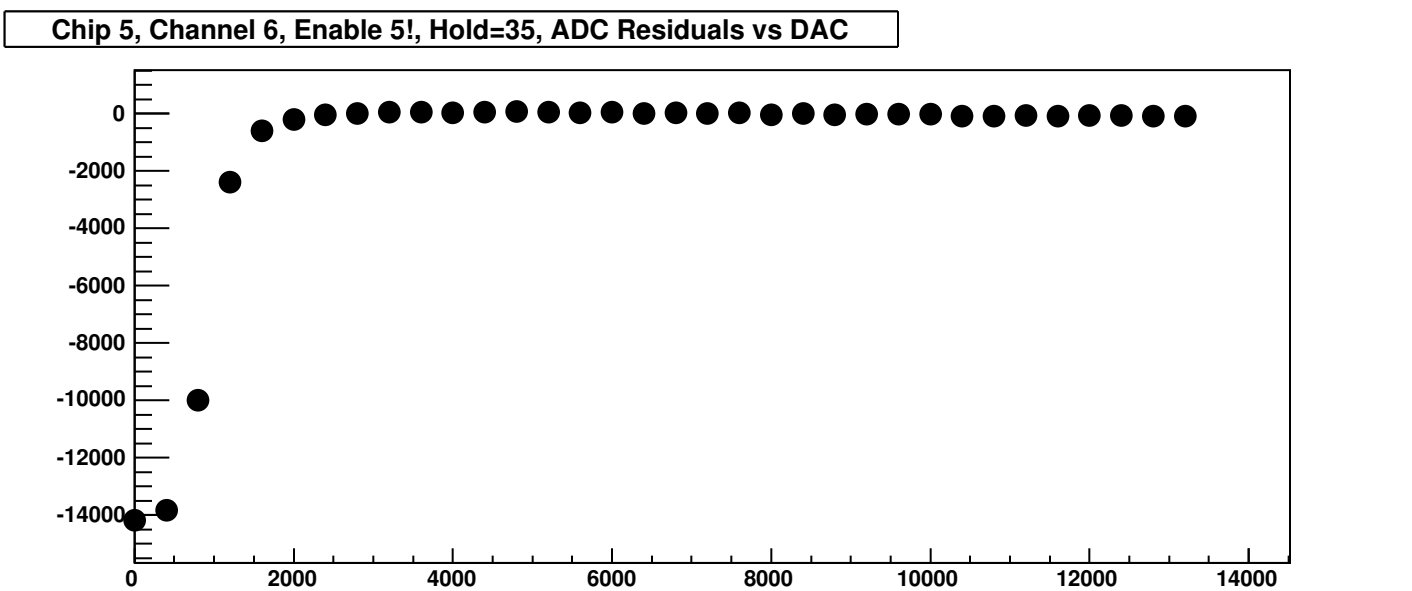

Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.33 / 23
p0 -182.6 ± 0.6806
p1 0.03485 ± 0.0001074
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

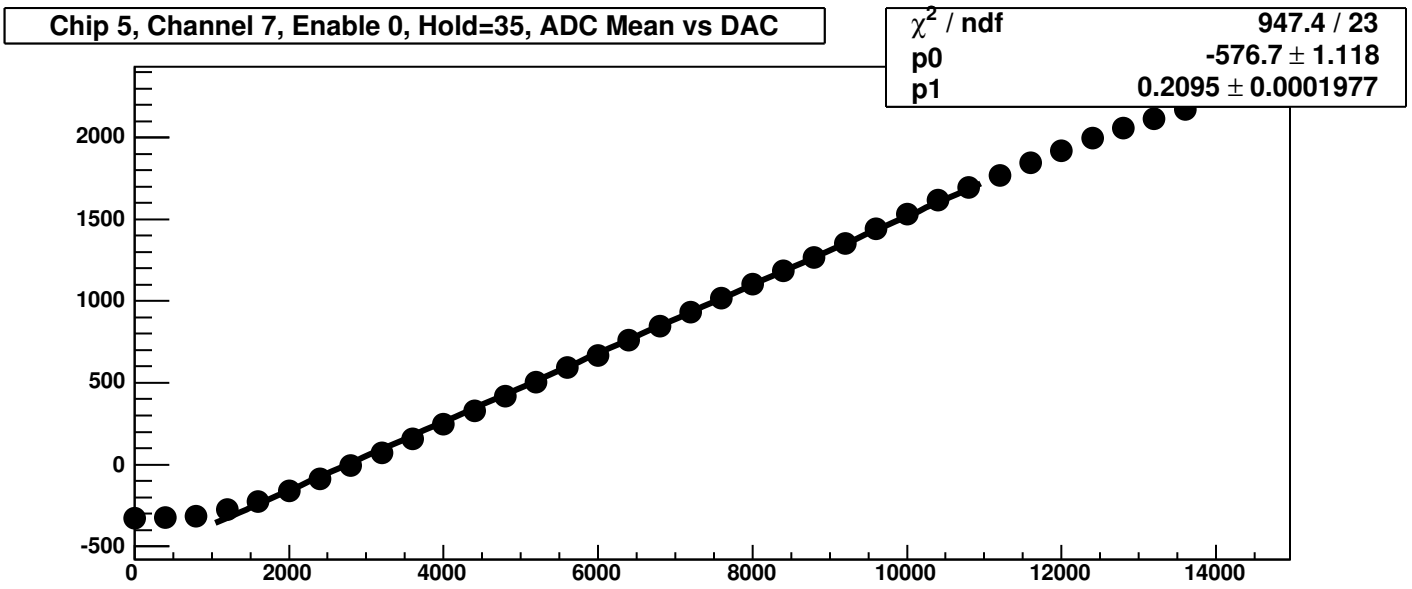

Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

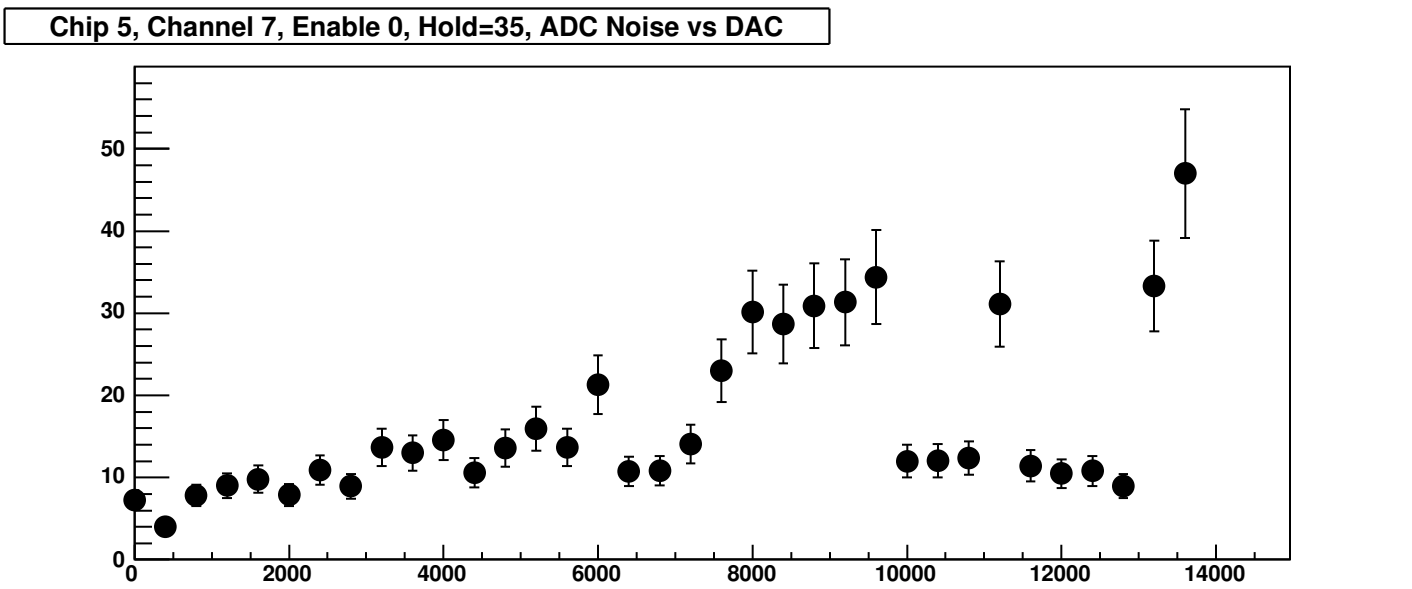

Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

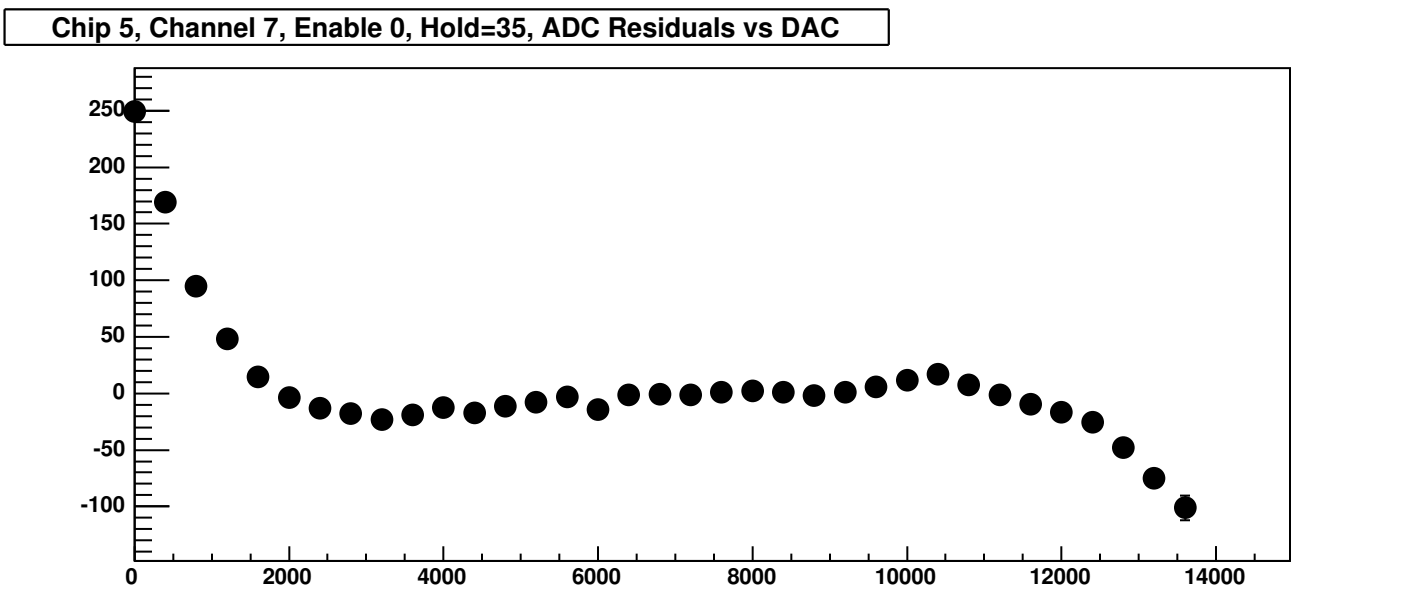

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2259 / 23
p0 3.046e+04 ± 9.518
p1 0.006841 ± 0.001378
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

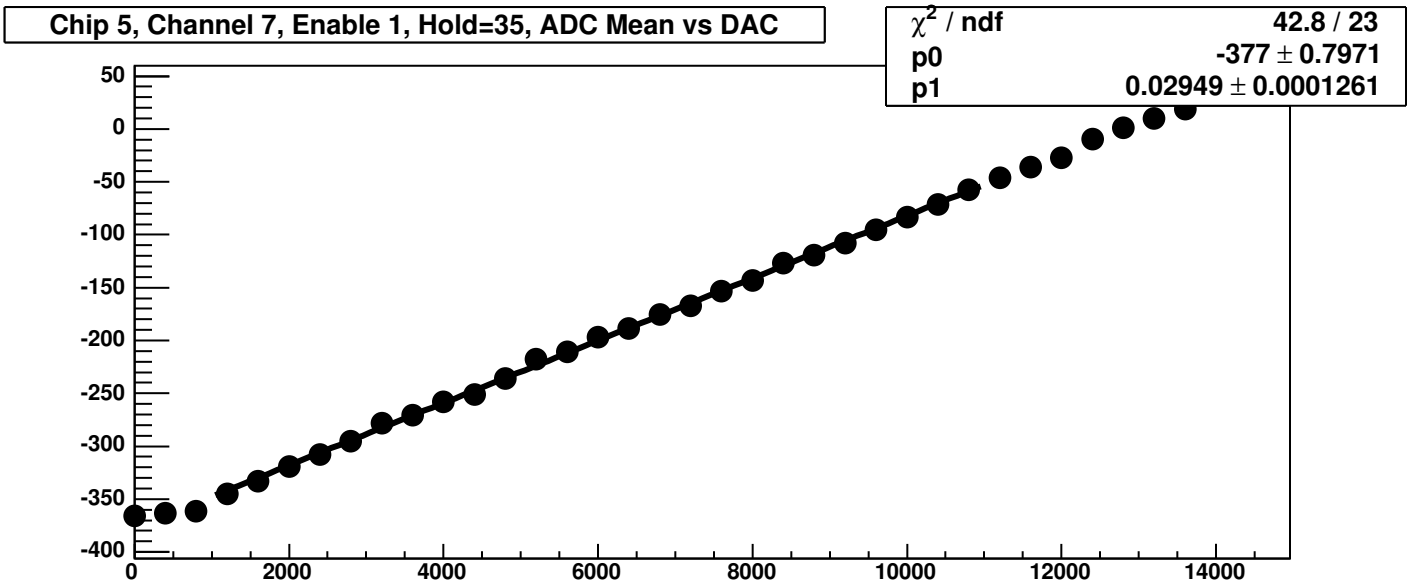

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

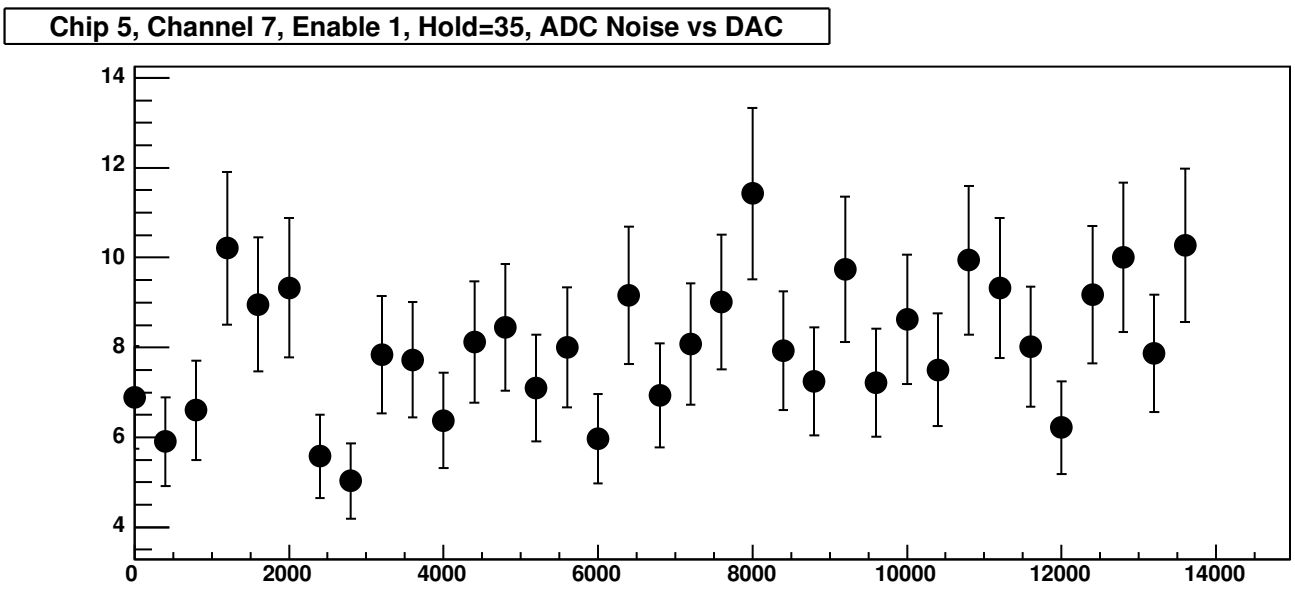

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

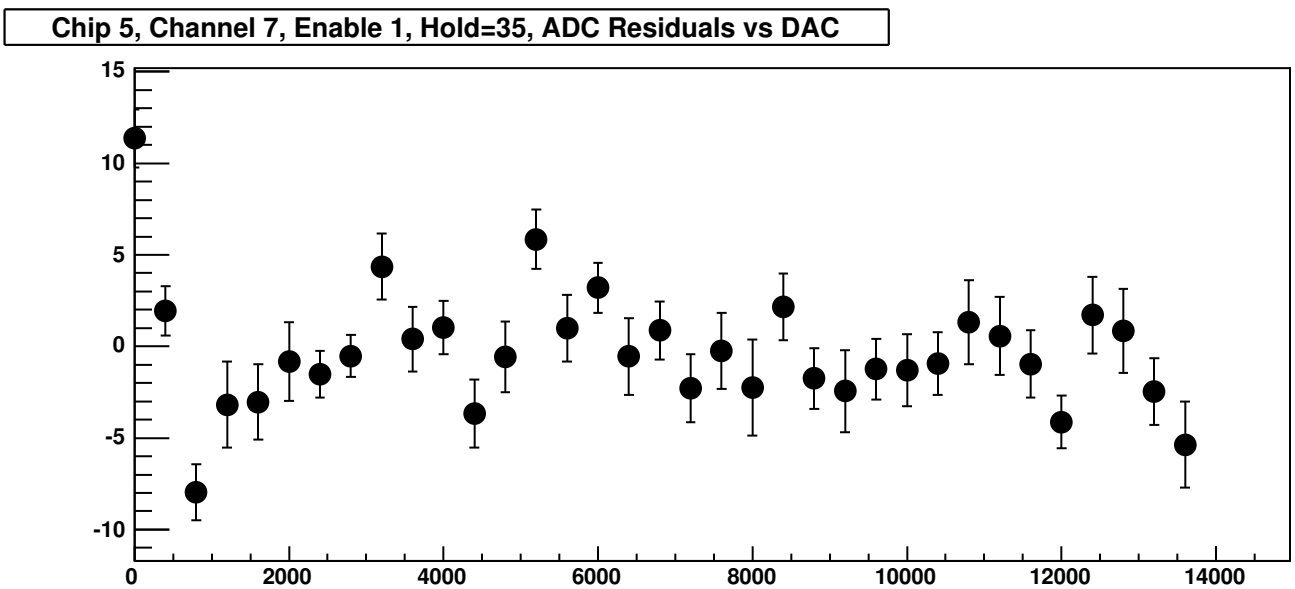

Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 947.4 / 23
p0 -576.7 ± 1.118
p1 0.2095 ± 0.0001977
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

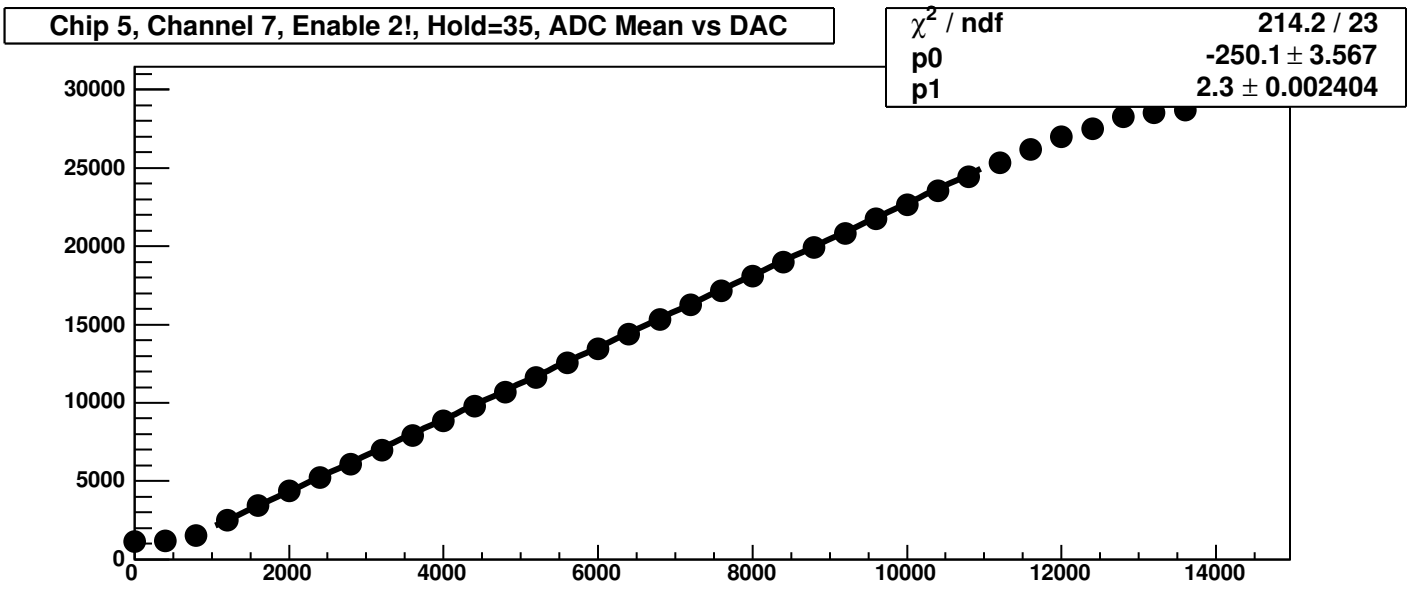

Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

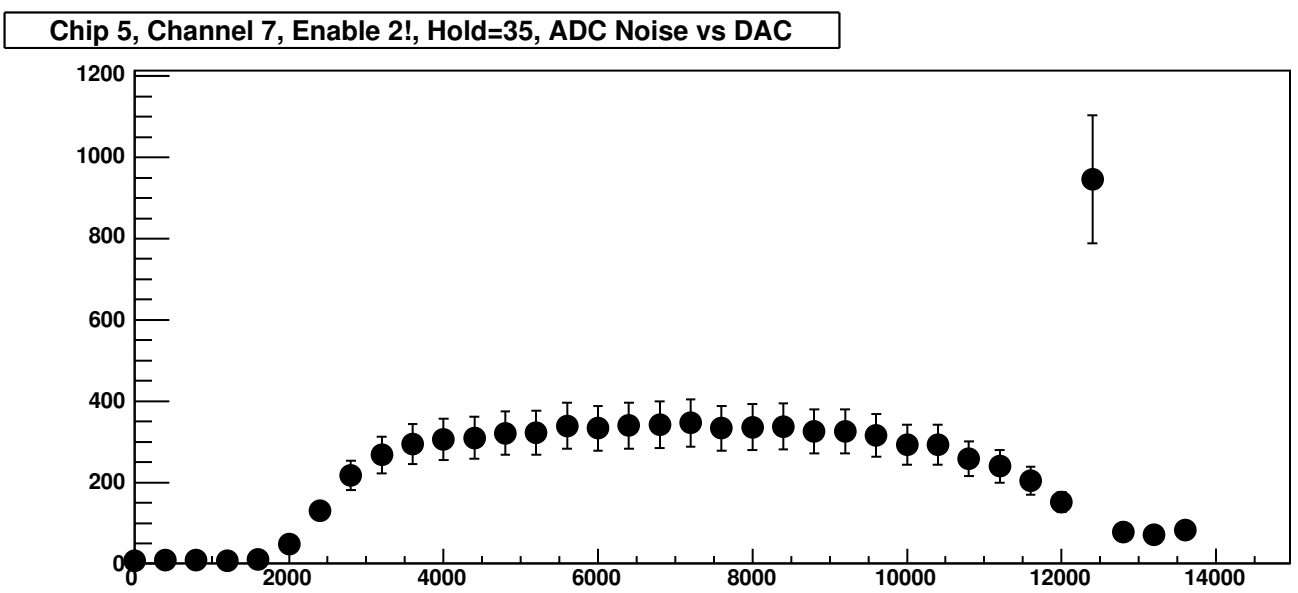

Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

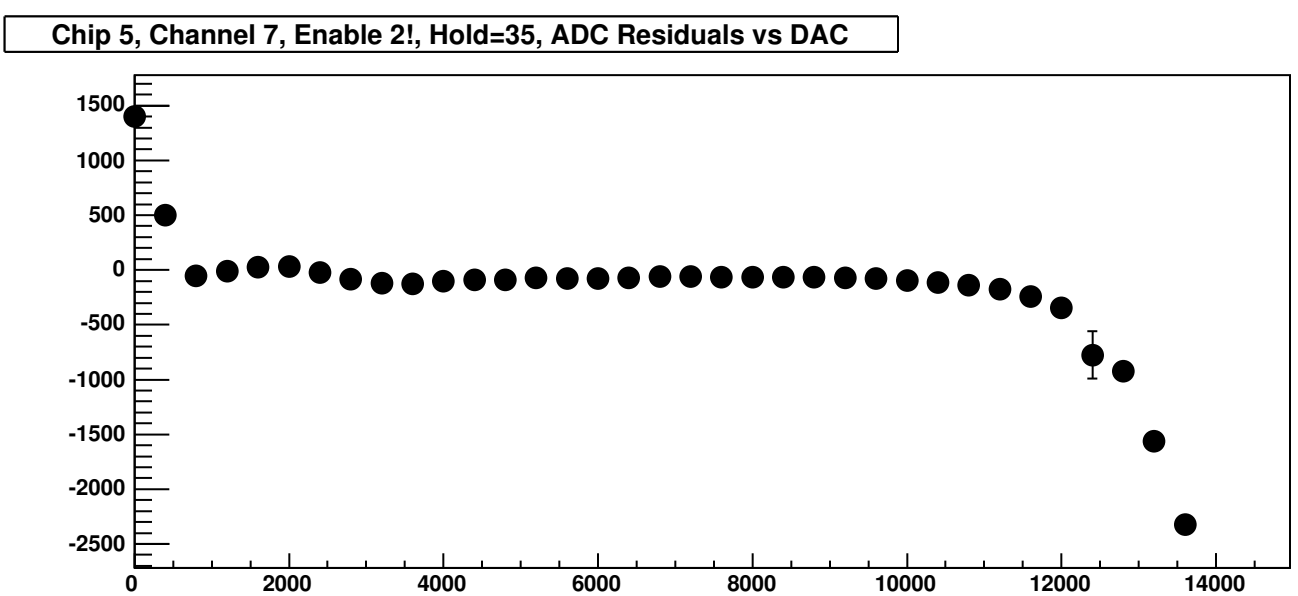

Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.8 / 23
p0 -377 ± 0.7971
p1 0.02949 ± 0.0001261
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

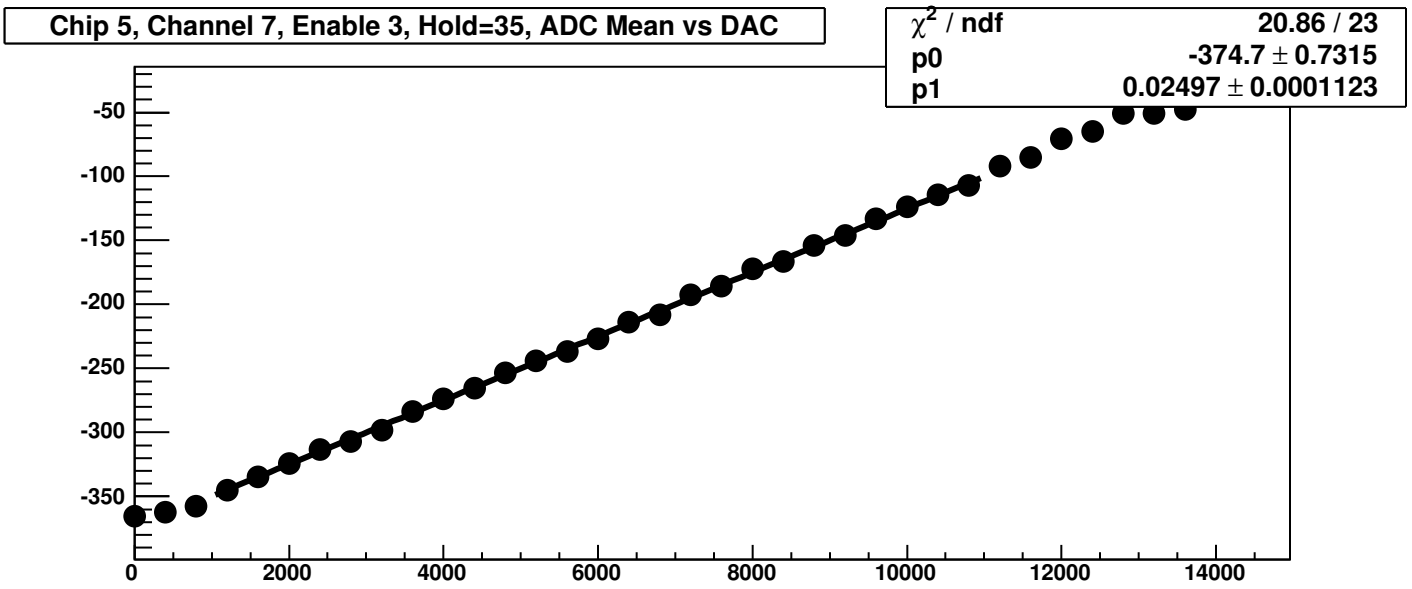

Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

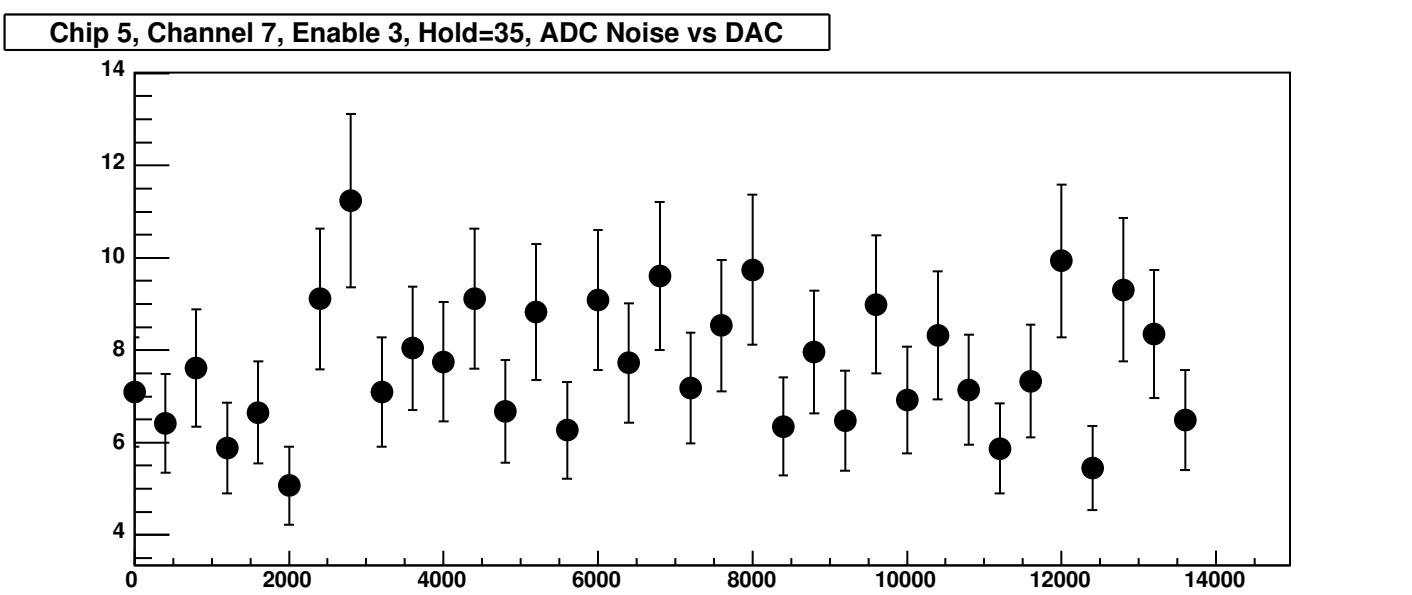

Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

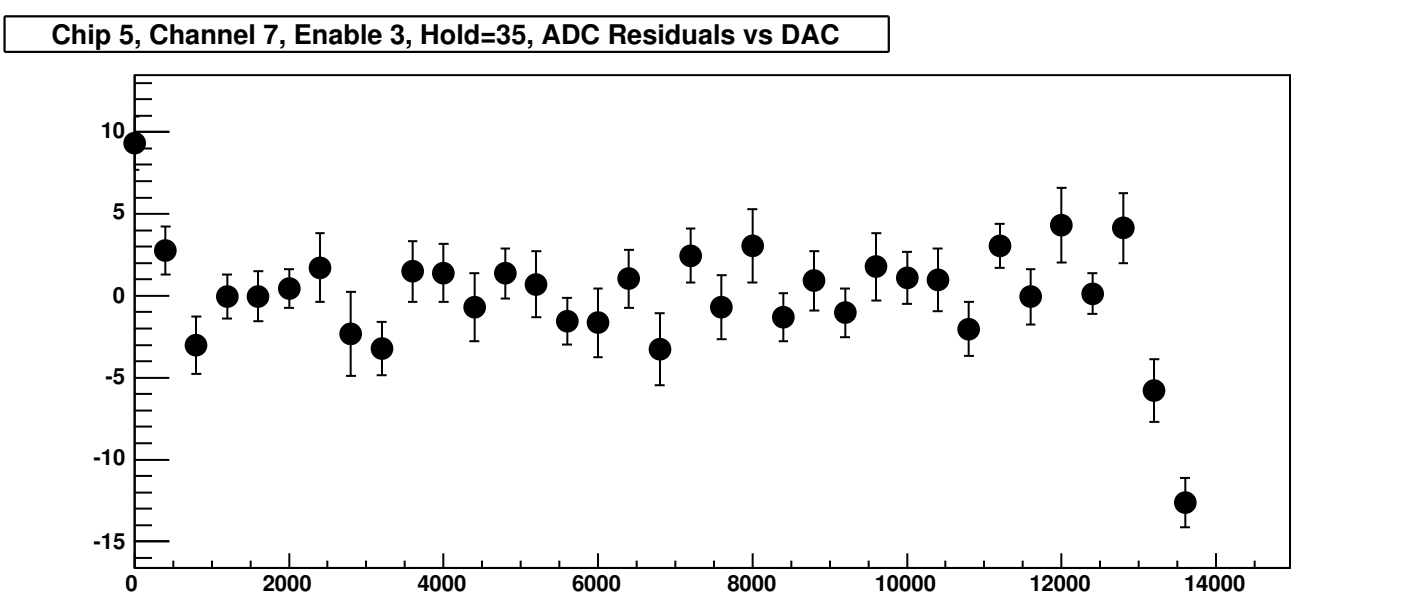

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 214.2 / 23
p0 -250.1 ± 3.567
p1 2.3 ± 0.002404
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

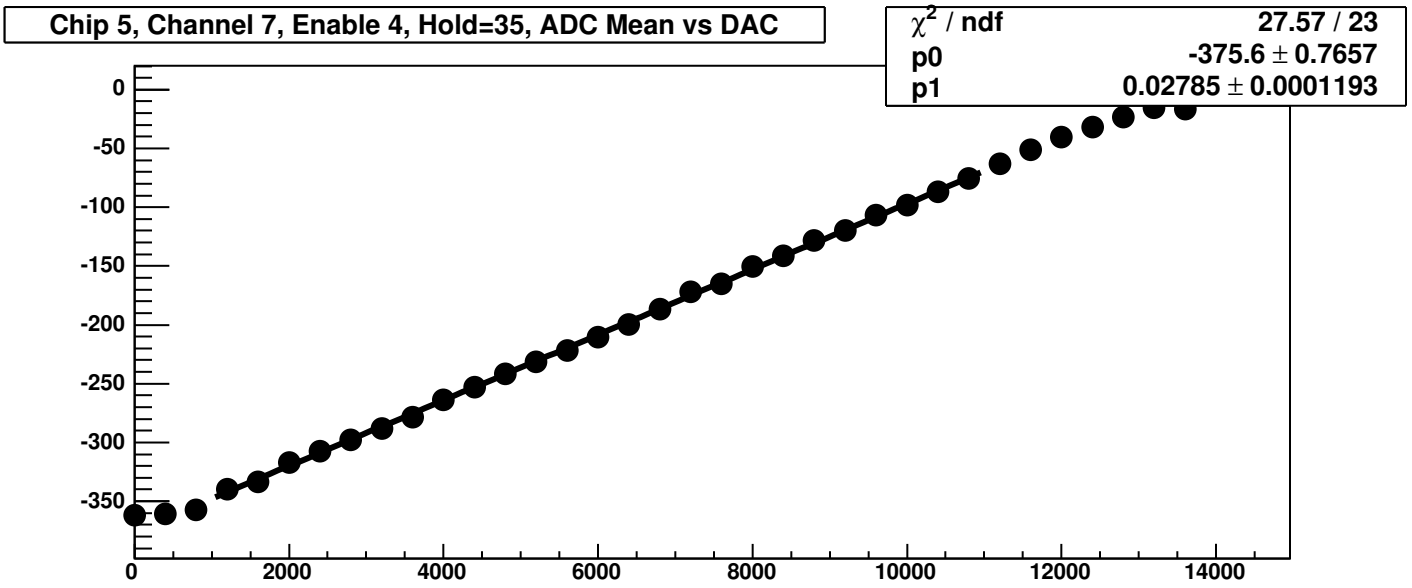

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

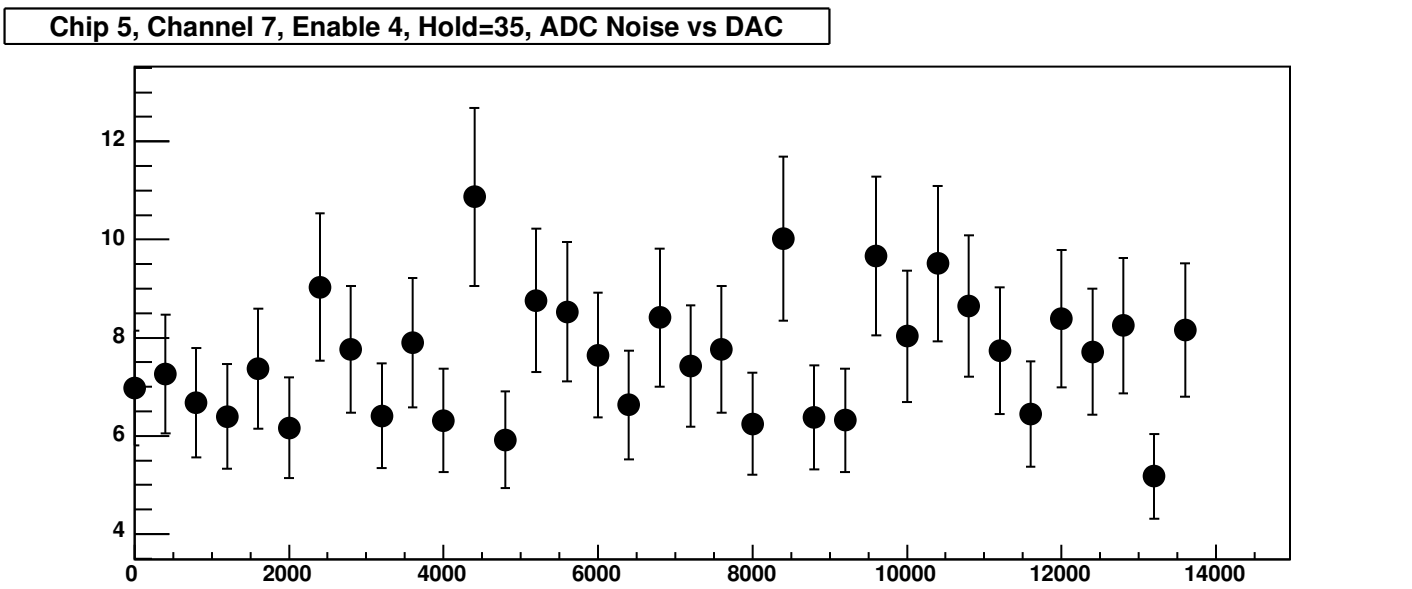

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

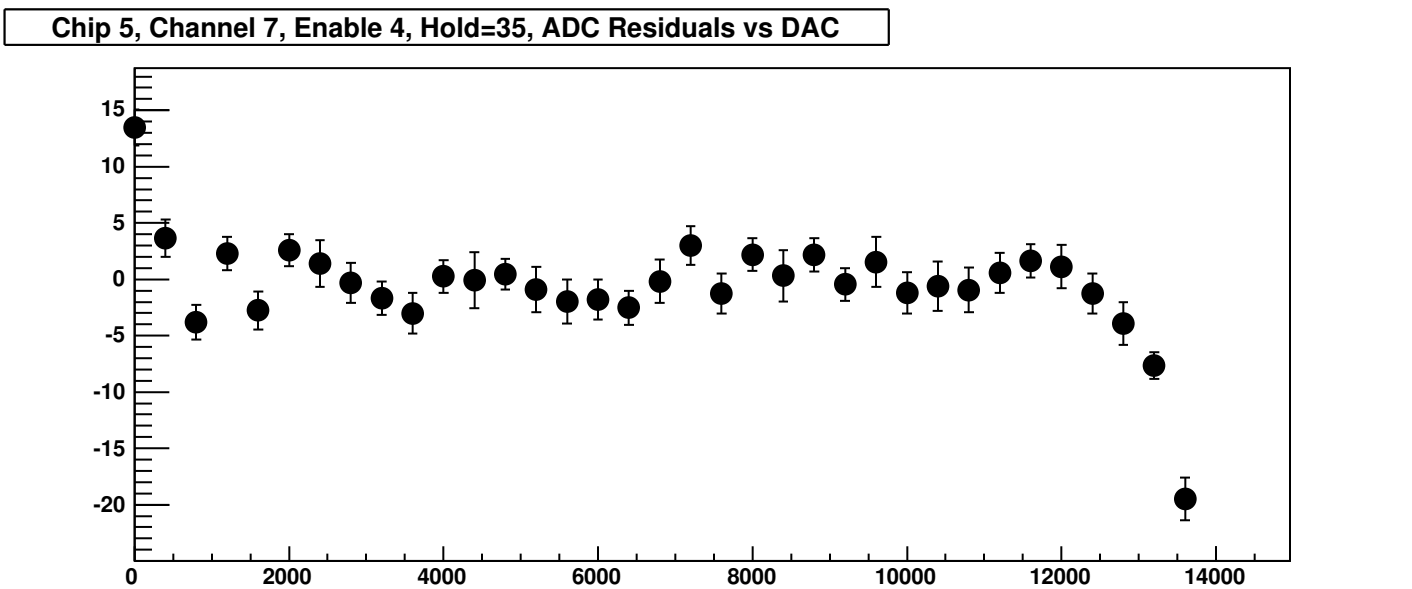

Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.86 / 23
p0 -374.7 ± 0.7315
p1 0.02497 ± 0.0001123
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

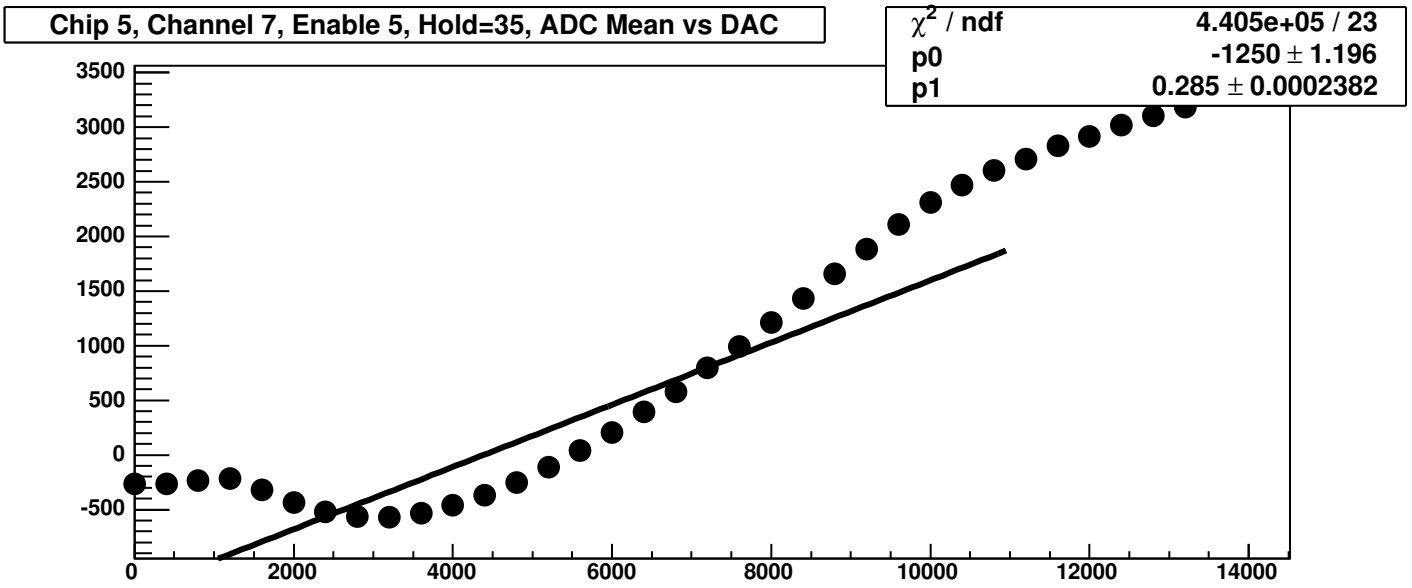

Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

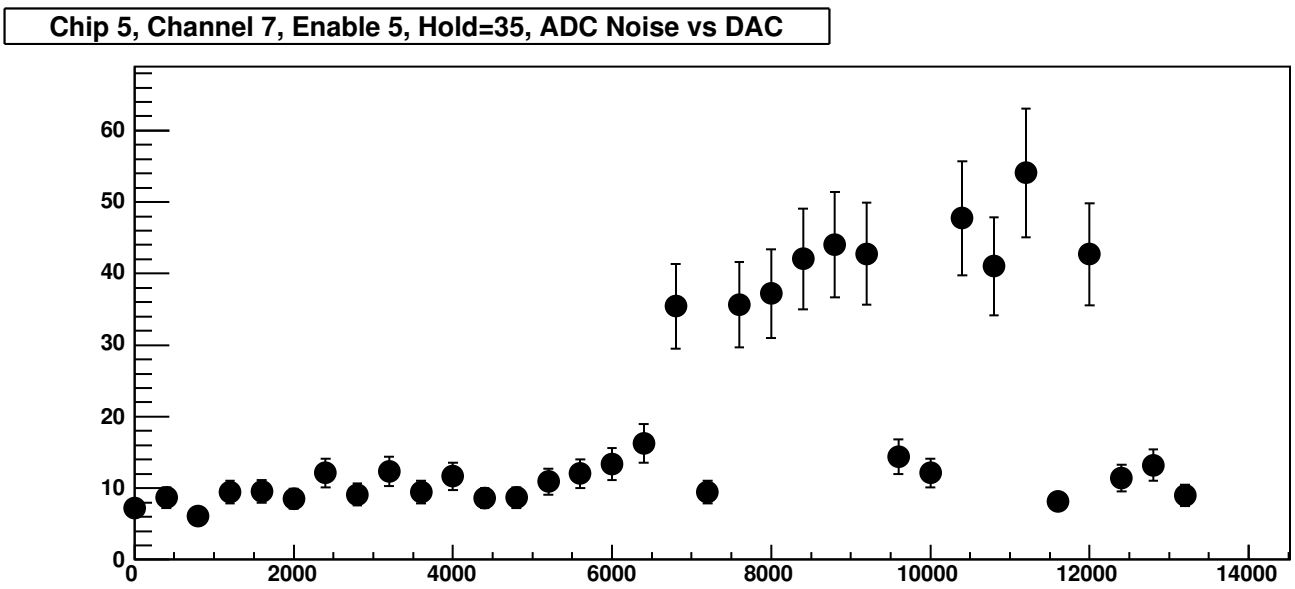

Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

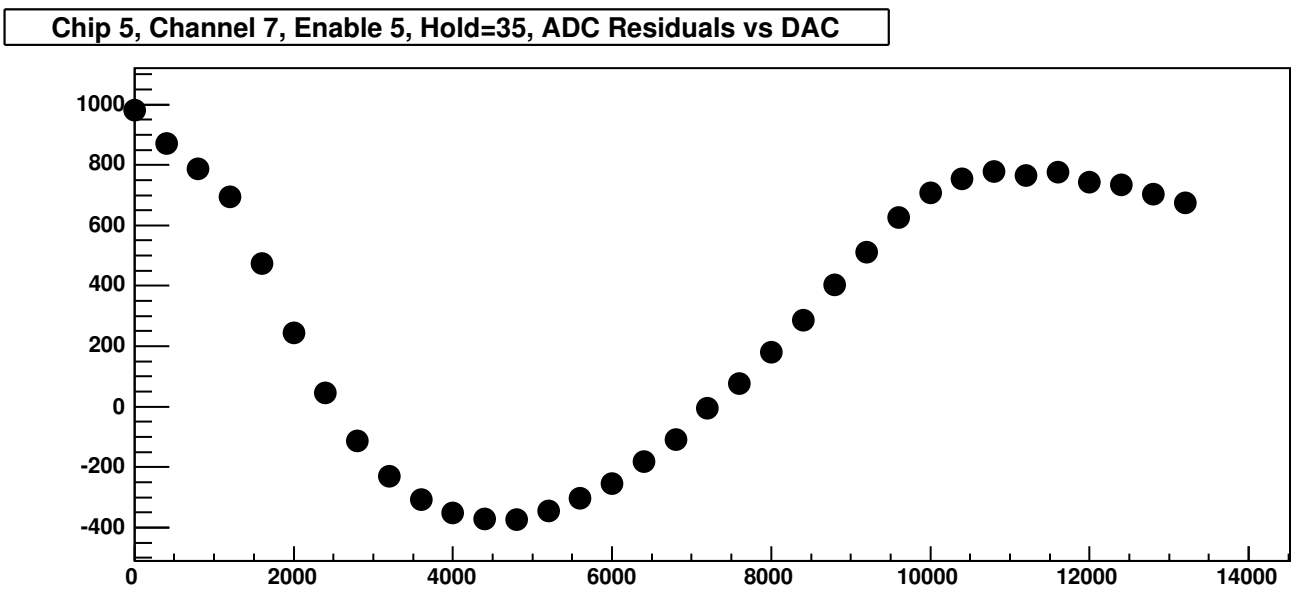

Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.57 / 23
p0 -375.6 ± 0.7657
p1 0.02785 ± 0.0001193
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

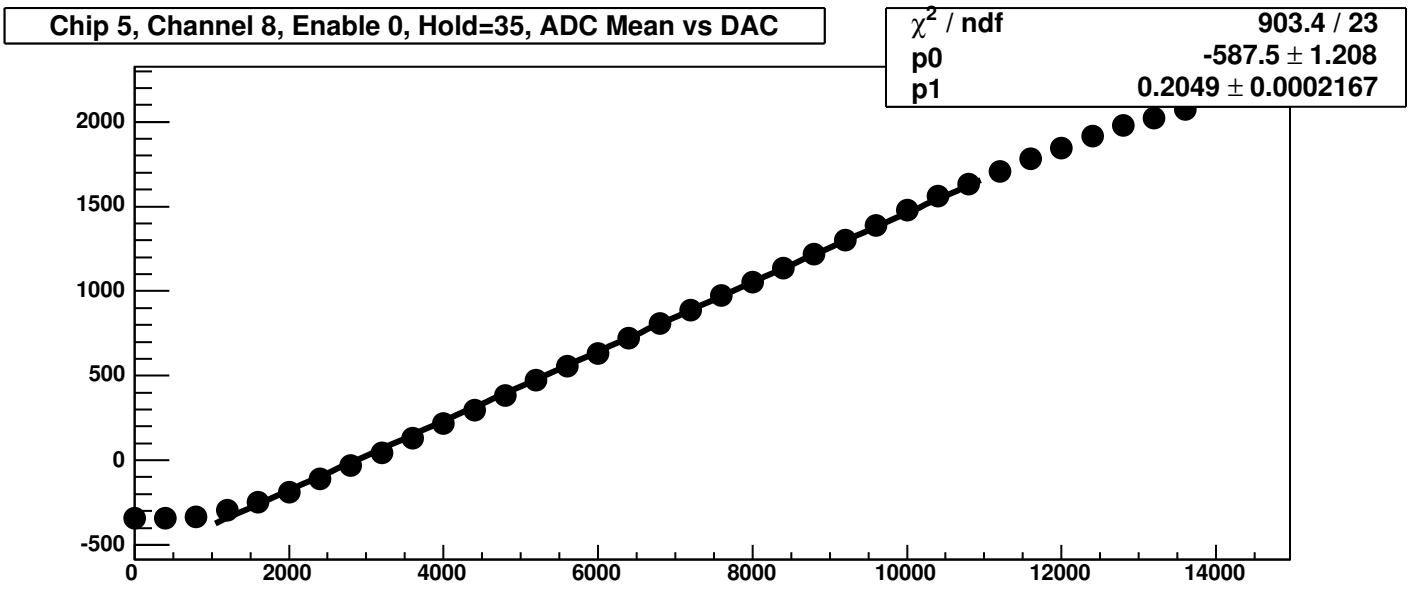

Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

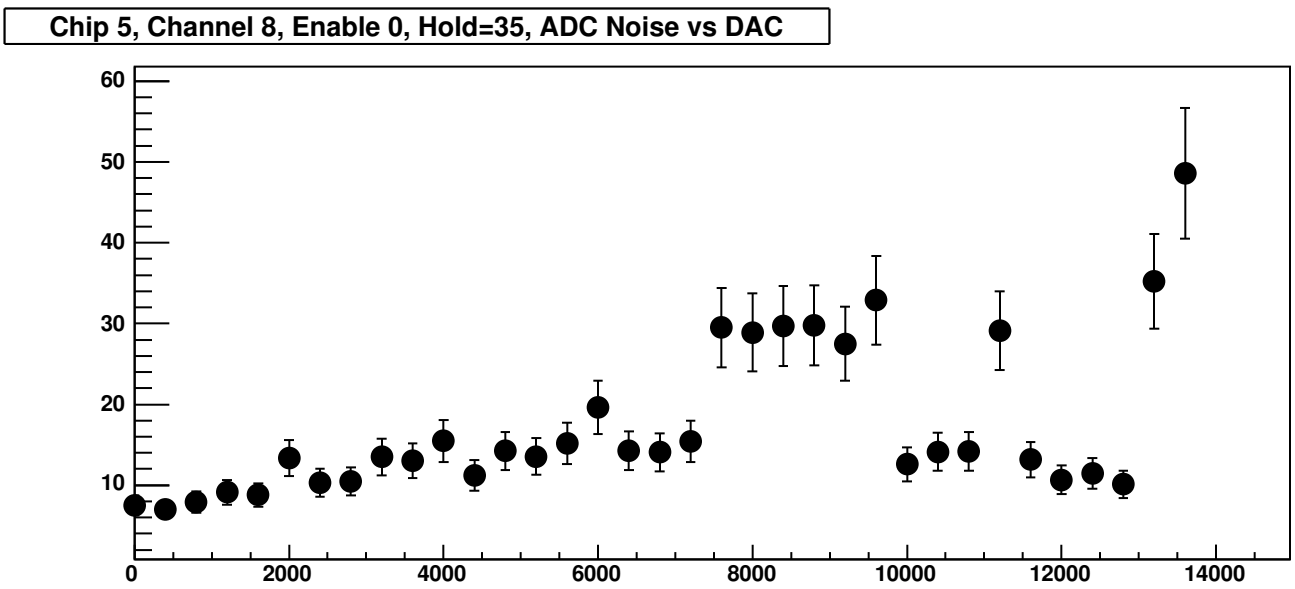

Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

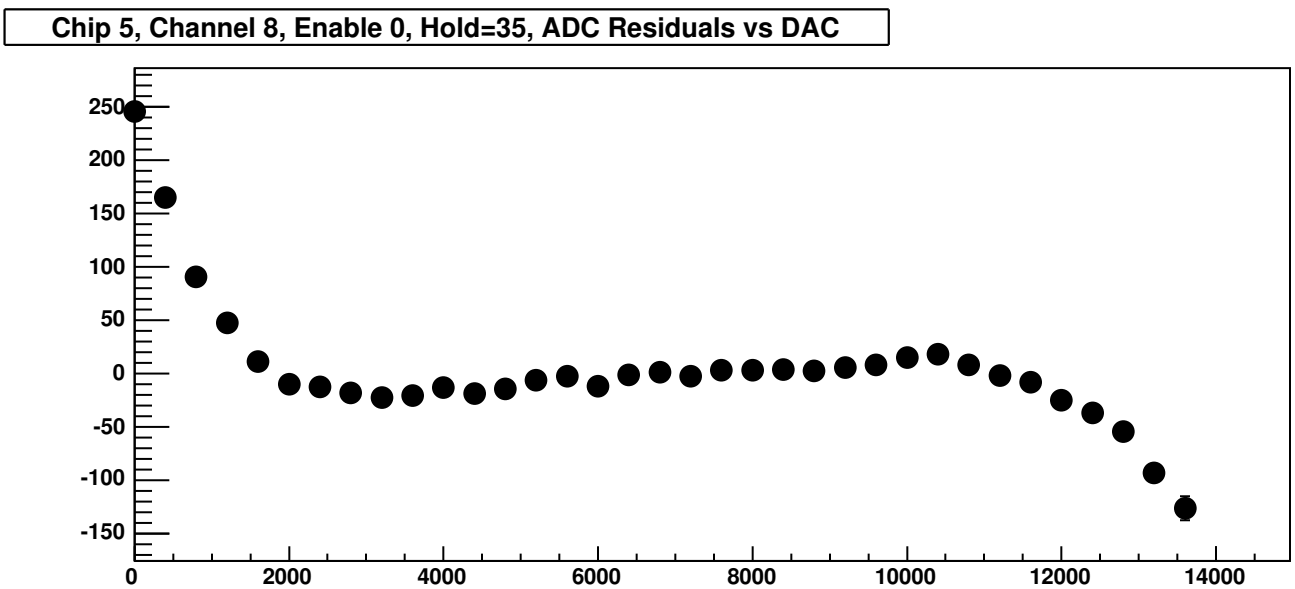

Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.405e+05 / 23
p0 -1250 ± 1.196
p1 0.285 ± 0.0002382
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

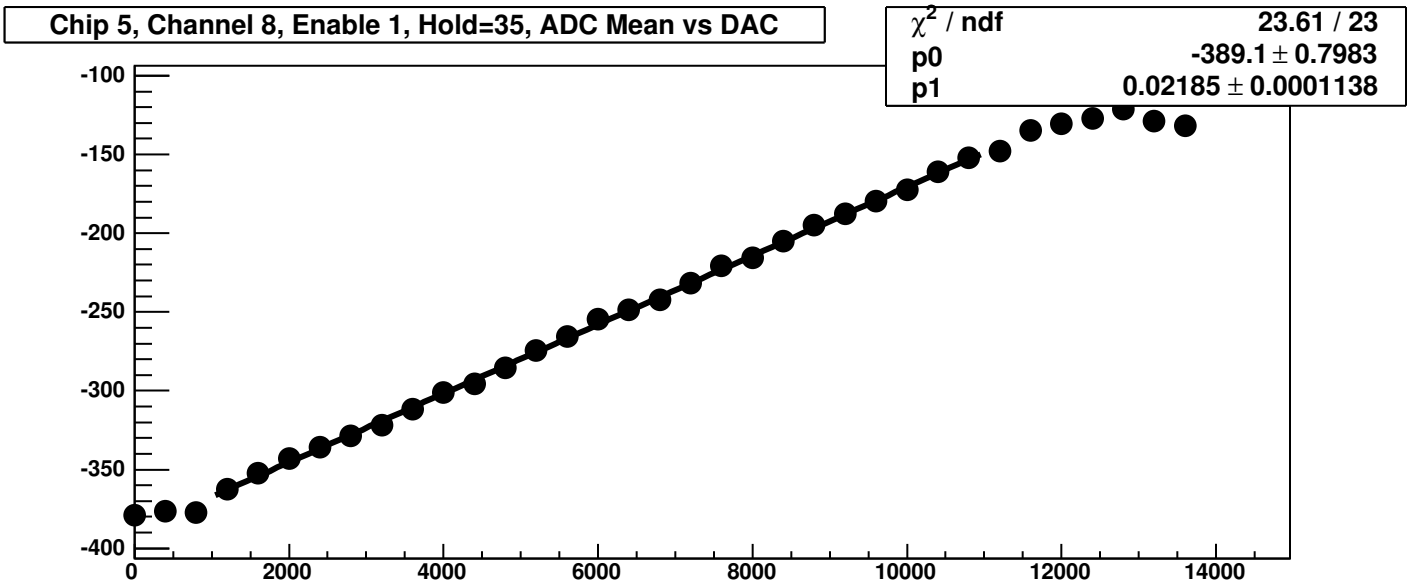

Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

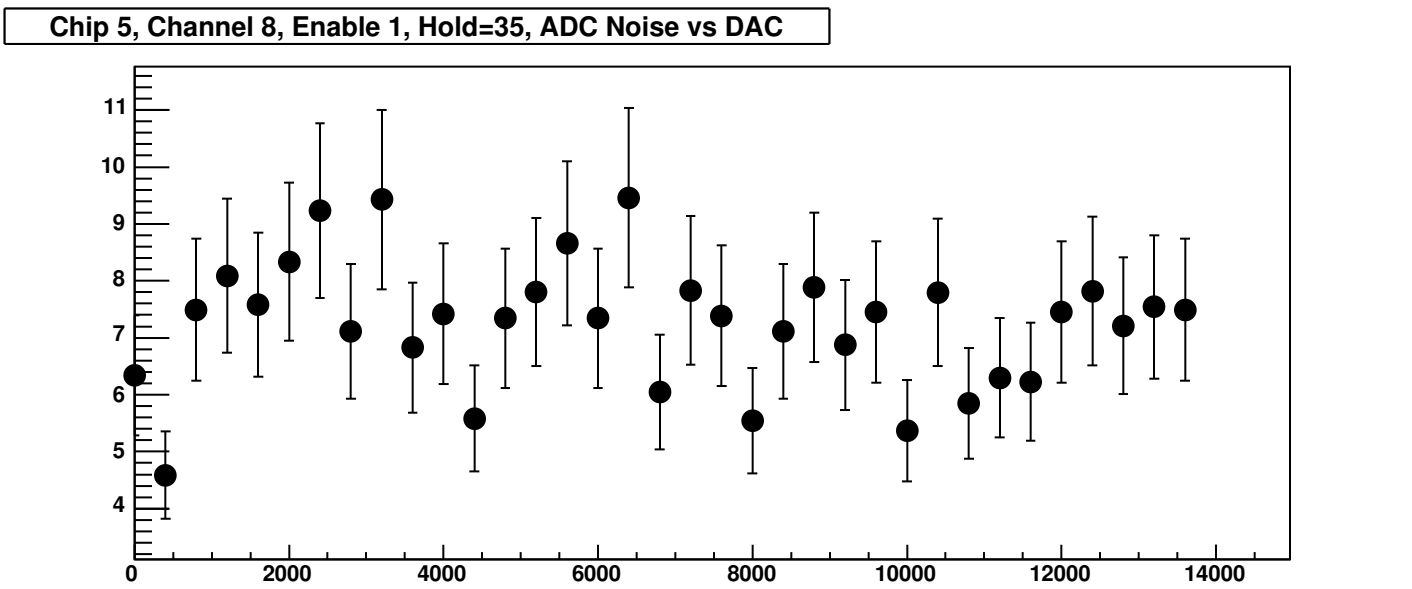

Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

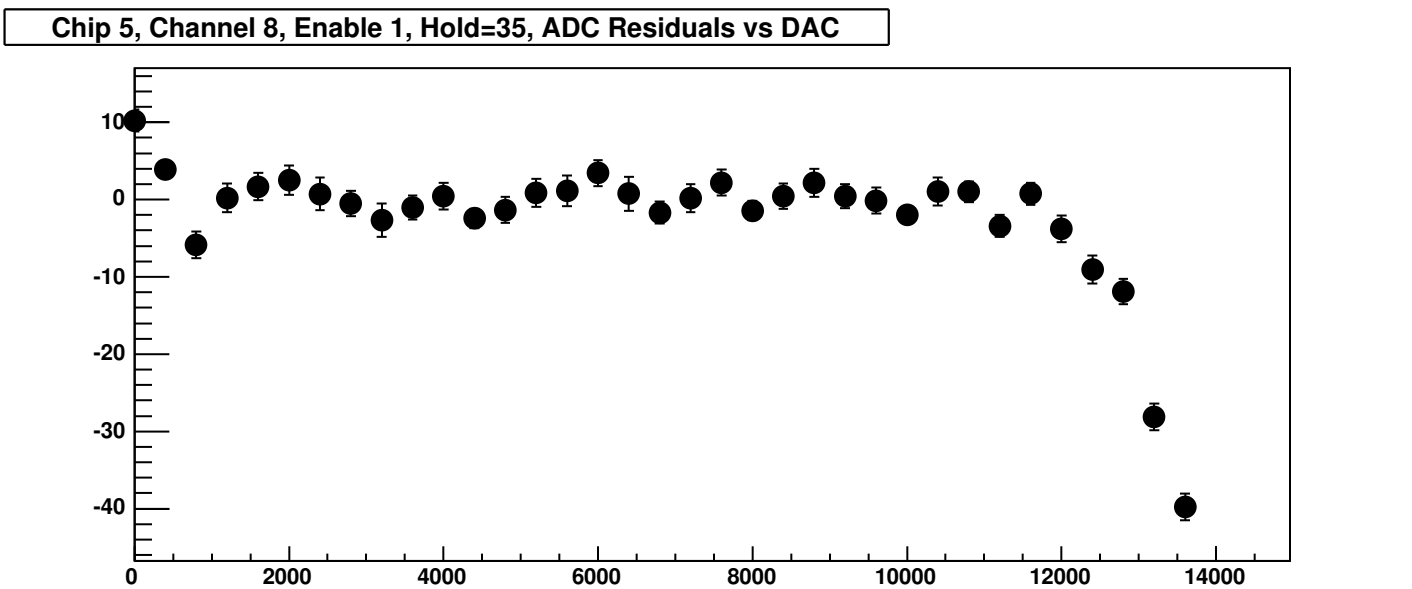

Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 903.4 / 23
p0 -587.5 ± 1.208
p1 0.2049 ± 0.0002167
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

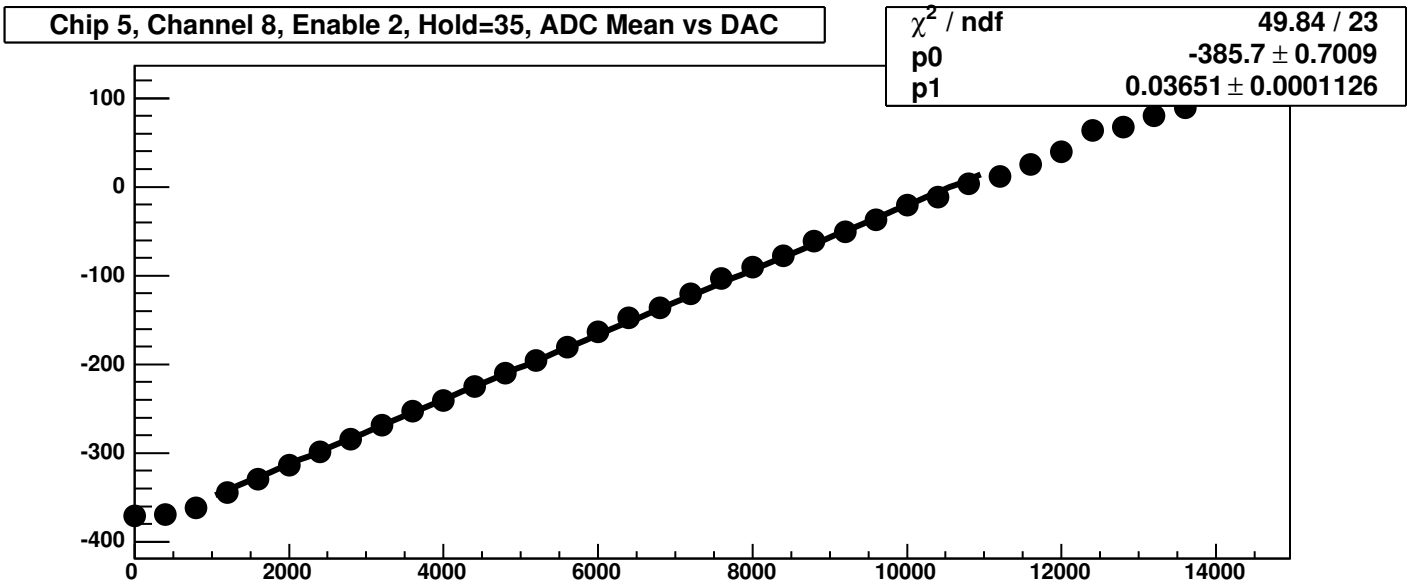

Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

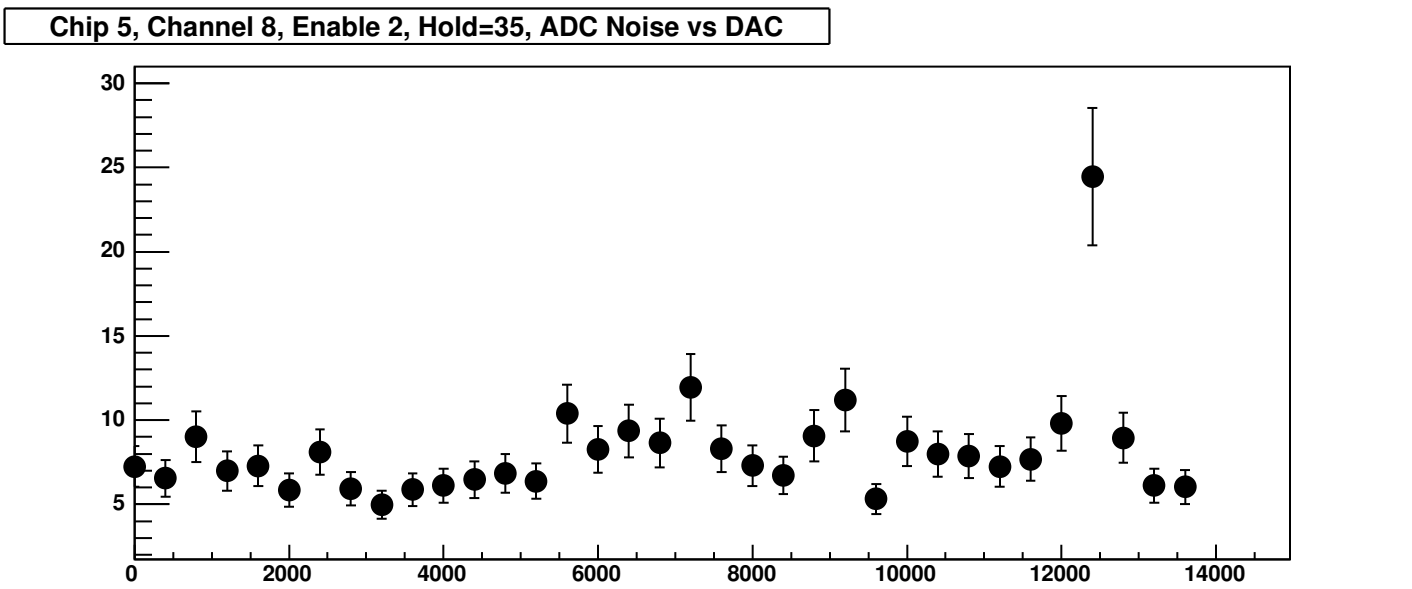

Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

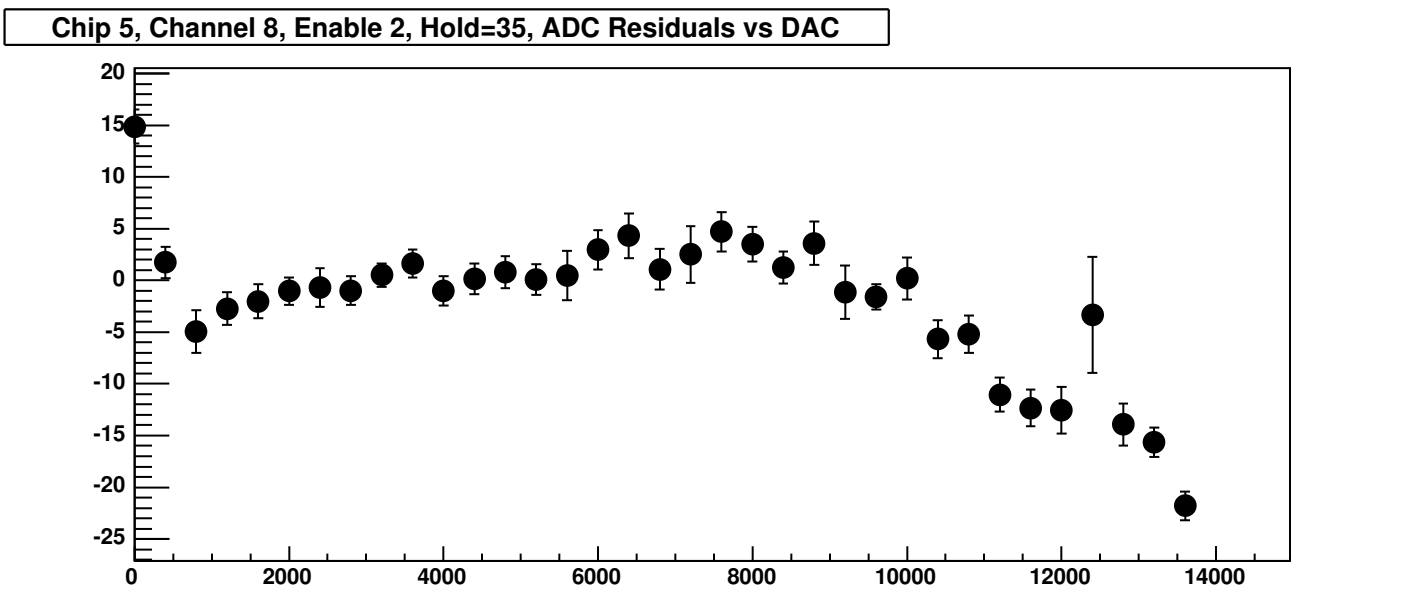

Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.61 / 23
p0 -389.1 ± 0.7983
p1 0.02185 ± 0.0001138
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

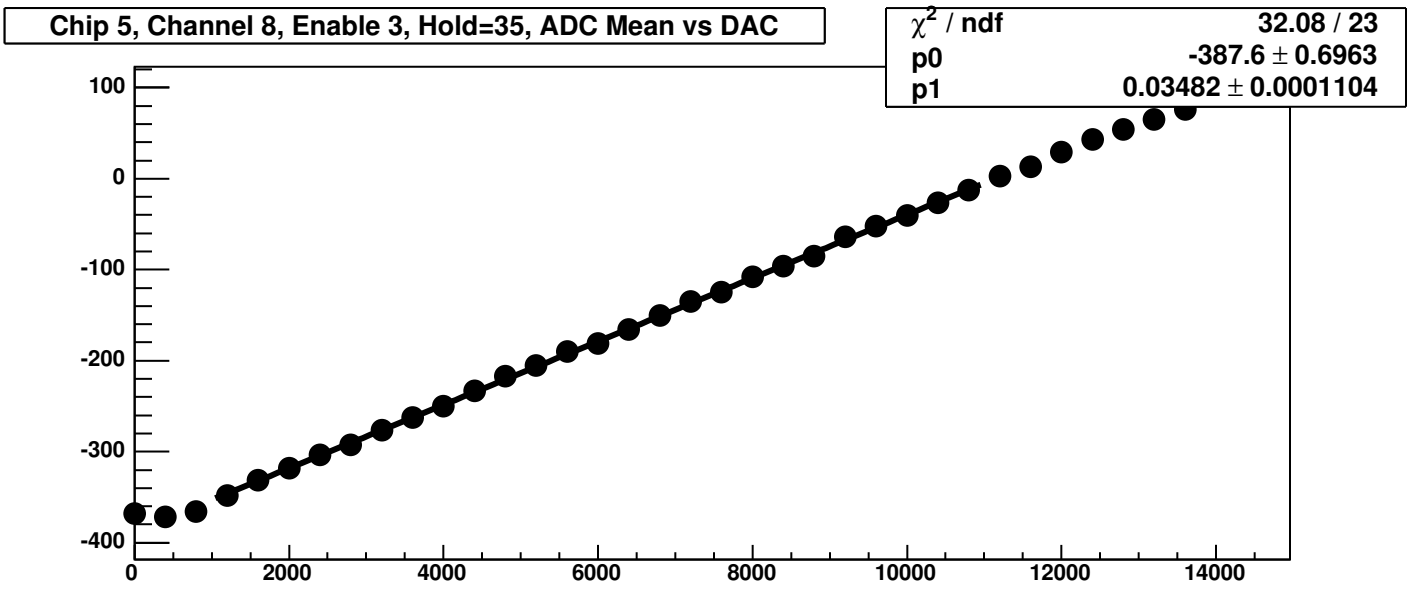

Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

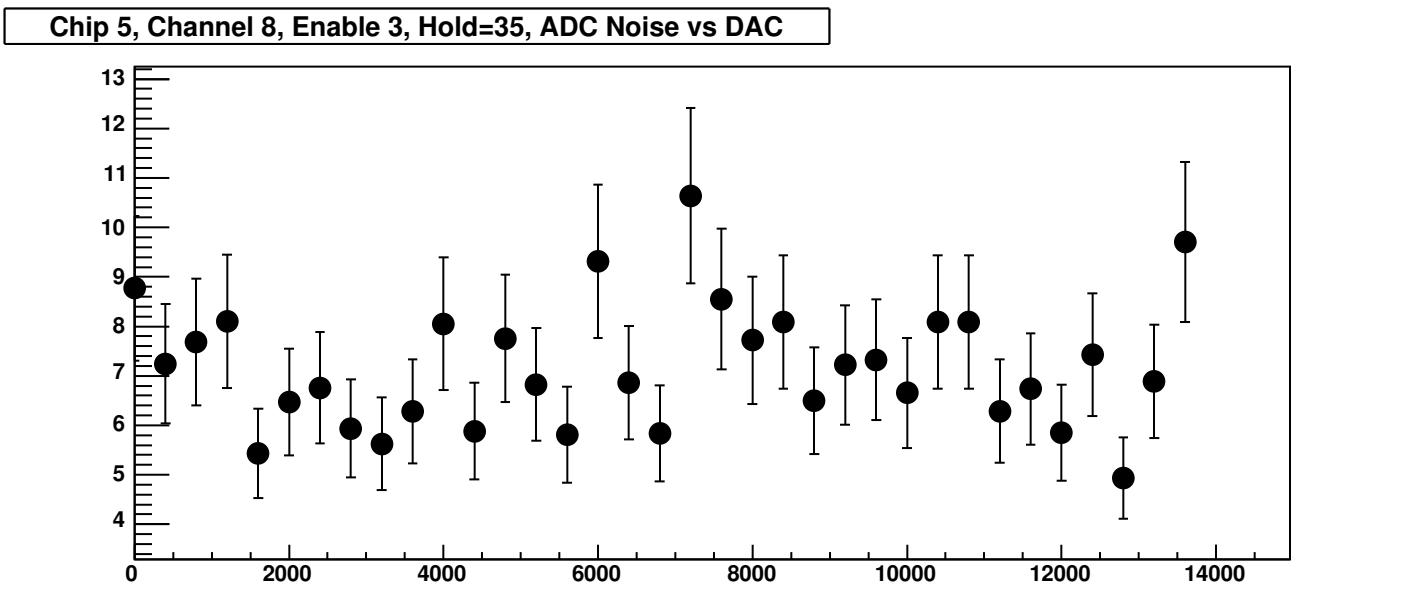

Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

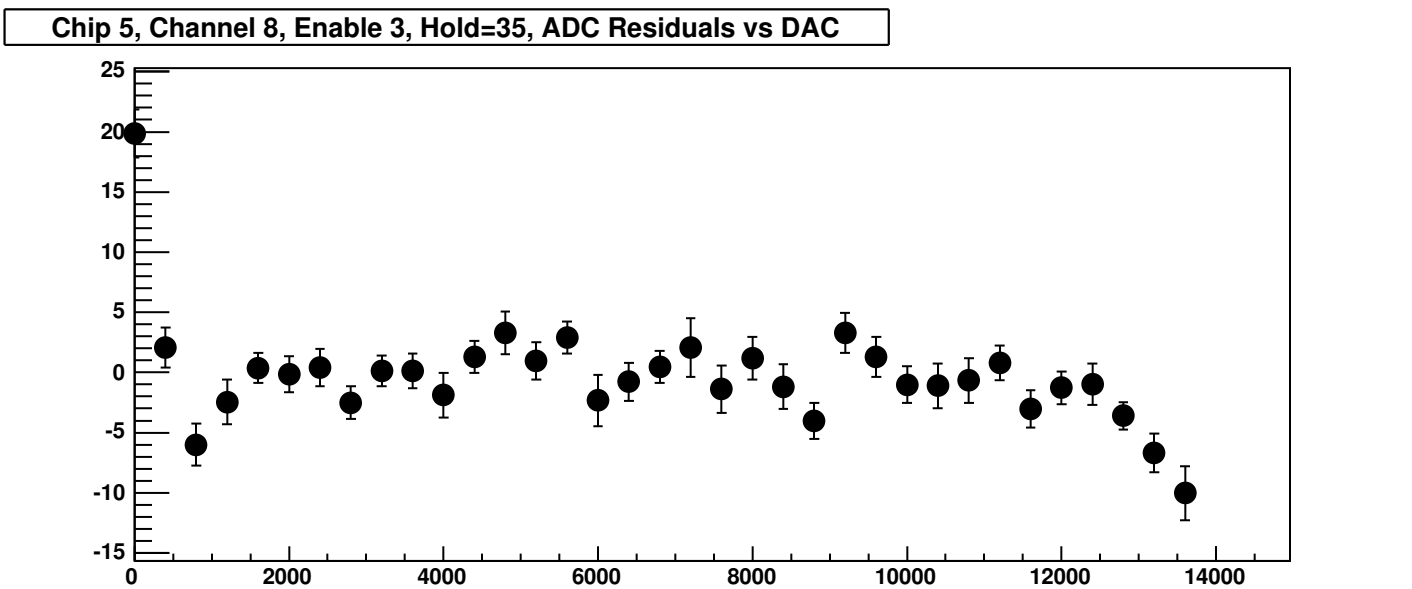

Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 49.84 / 23
p0 -385.7 ± 0.7009
p1 0.03651 ± 0.0001126
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

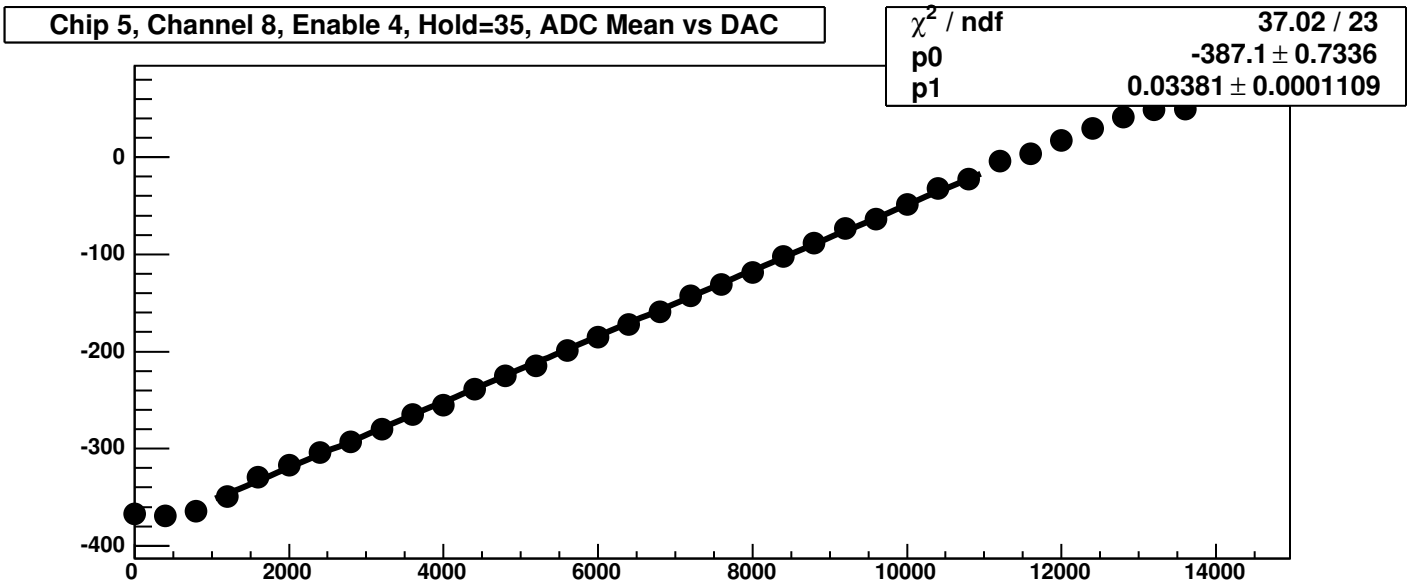

Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

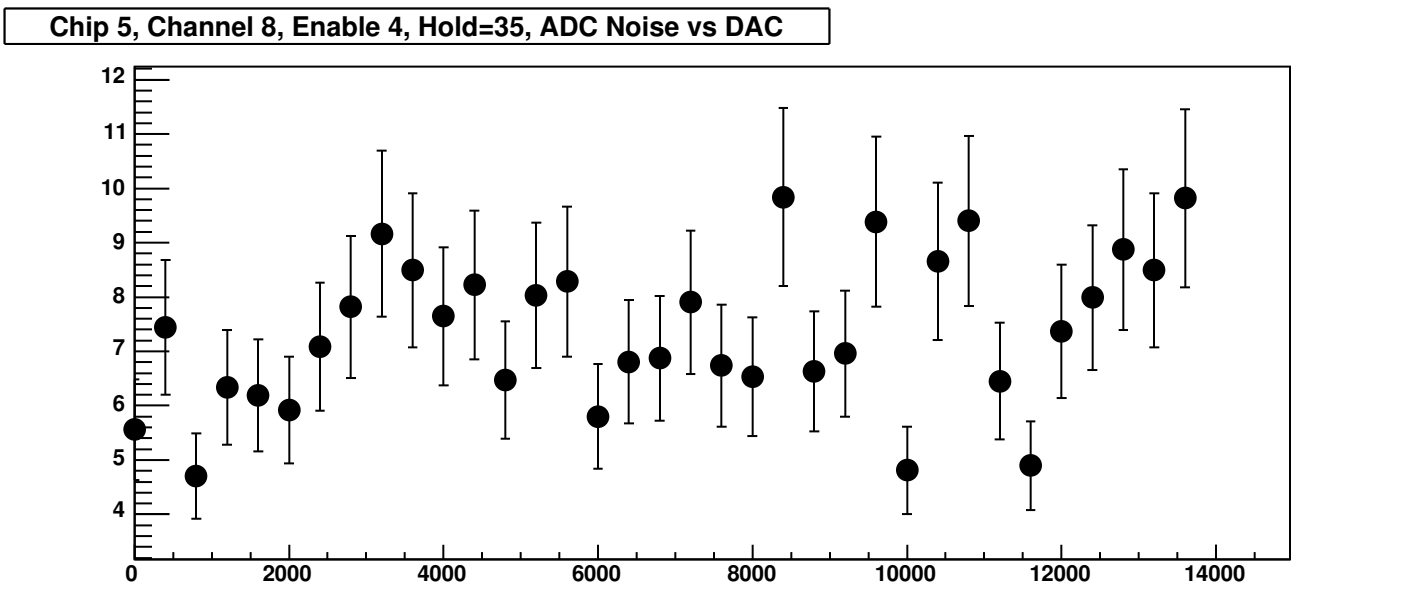

Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

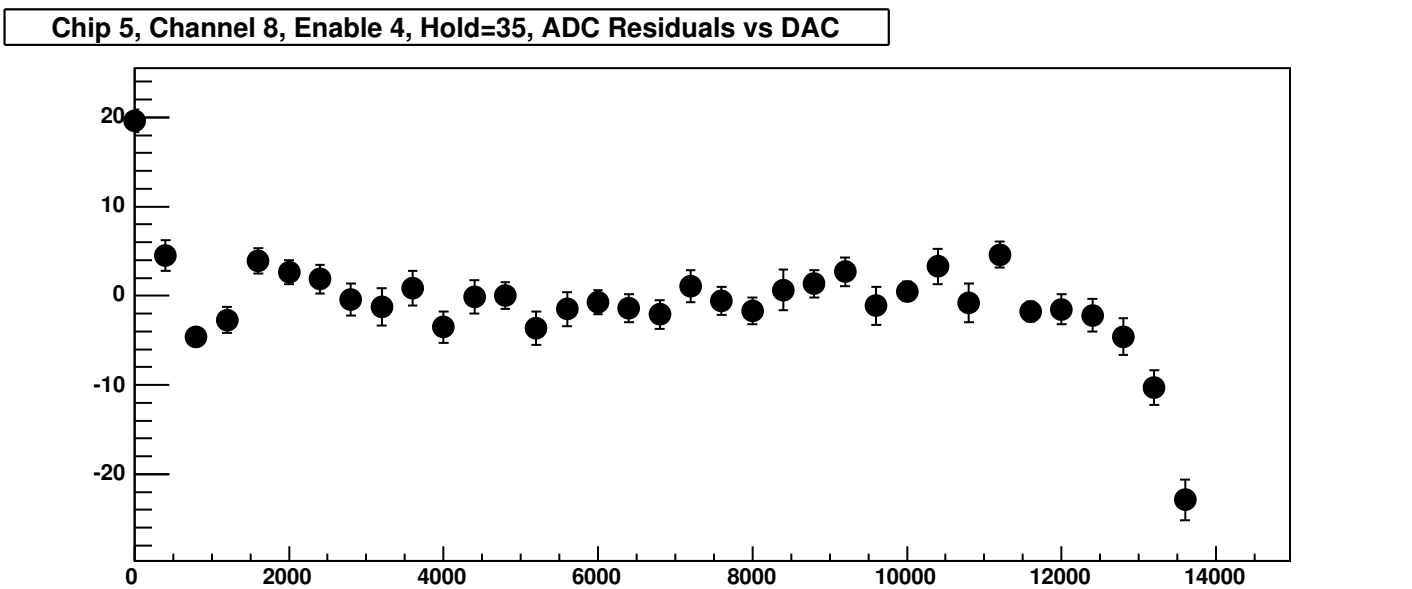

Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.08 / 23
p0 -387.6 ± 0.6963
p1 0.03482 ± 0.0001104
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

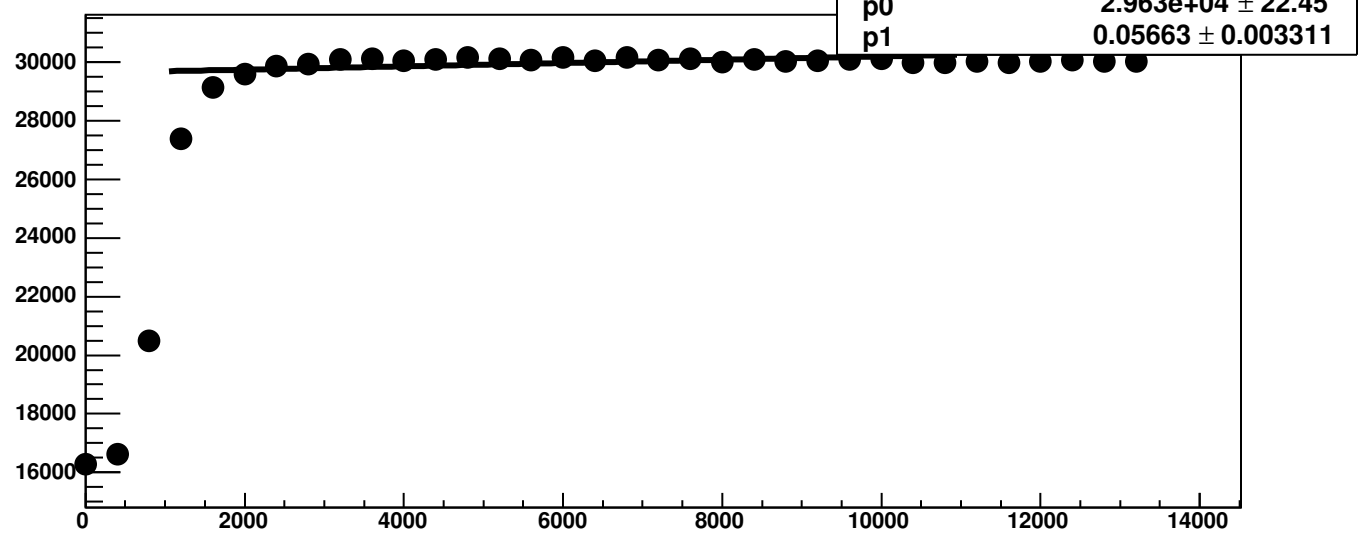

Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.02 / 23
p0 -387.1 ± 0.7336
p1 0.03381 ± 0.0001109
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

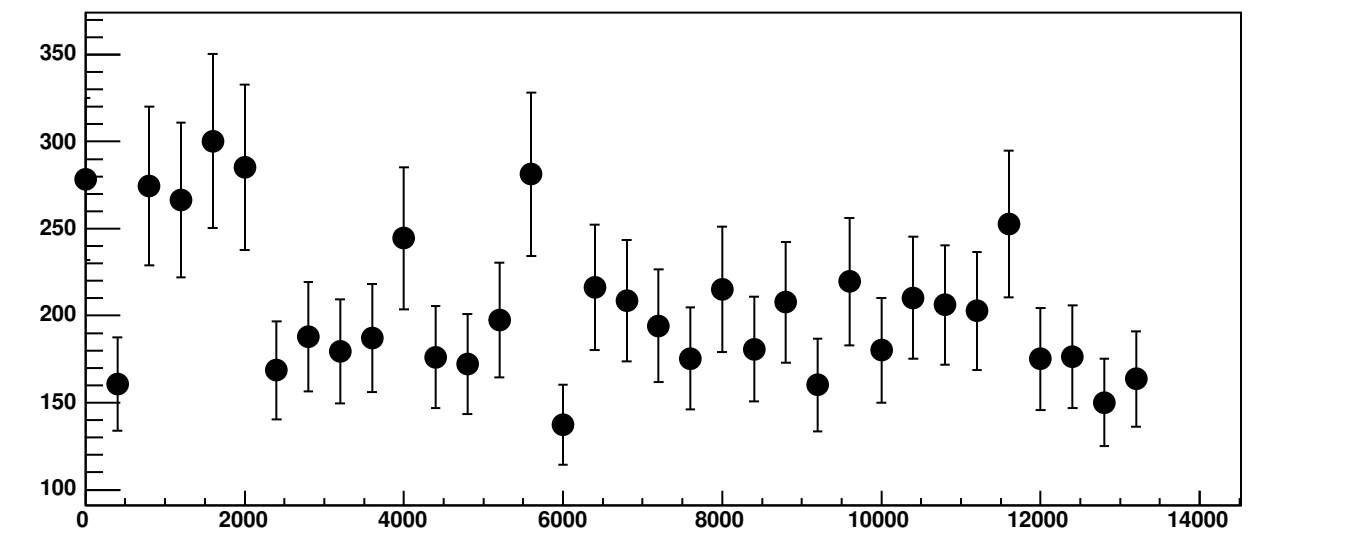

Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

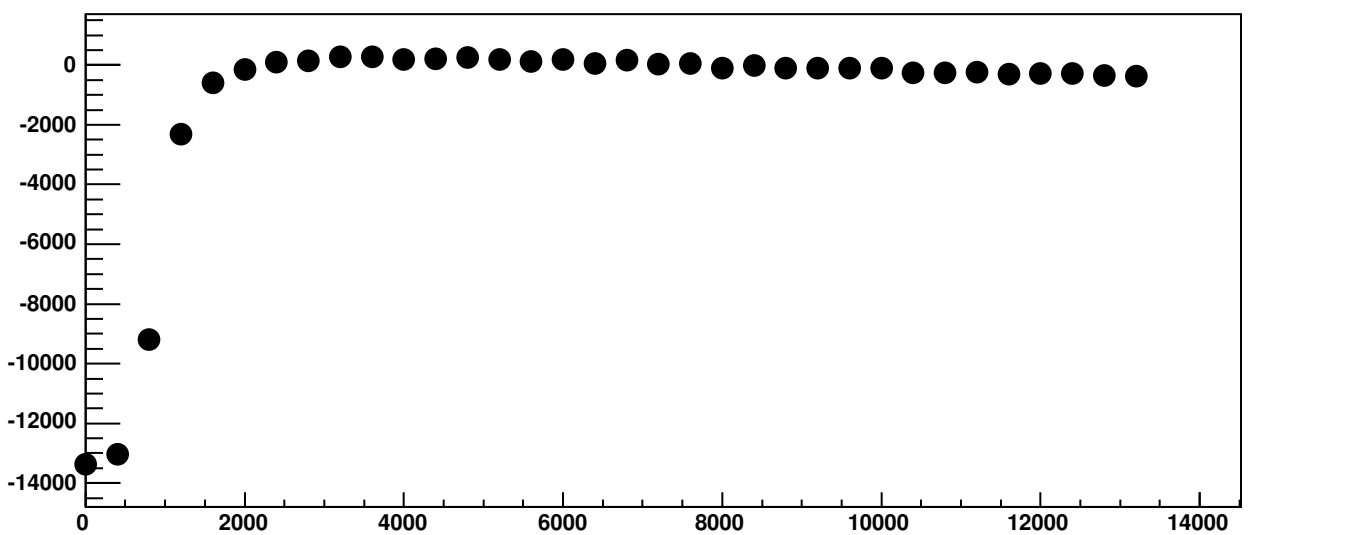
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC

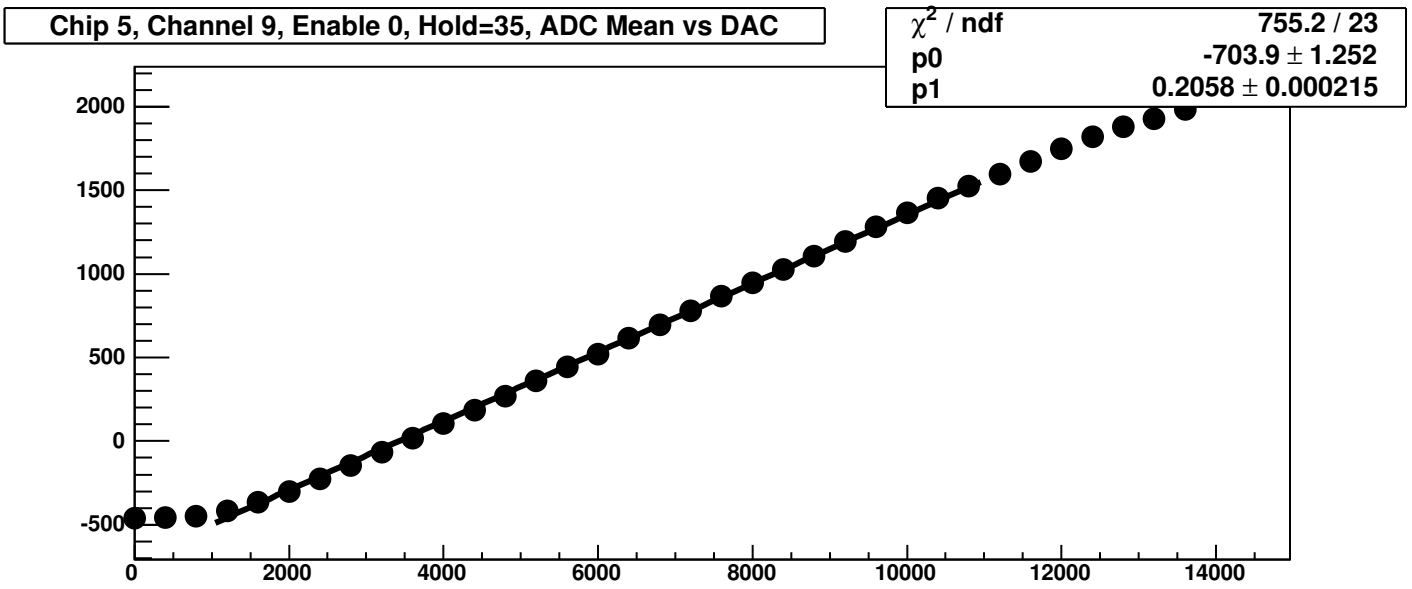


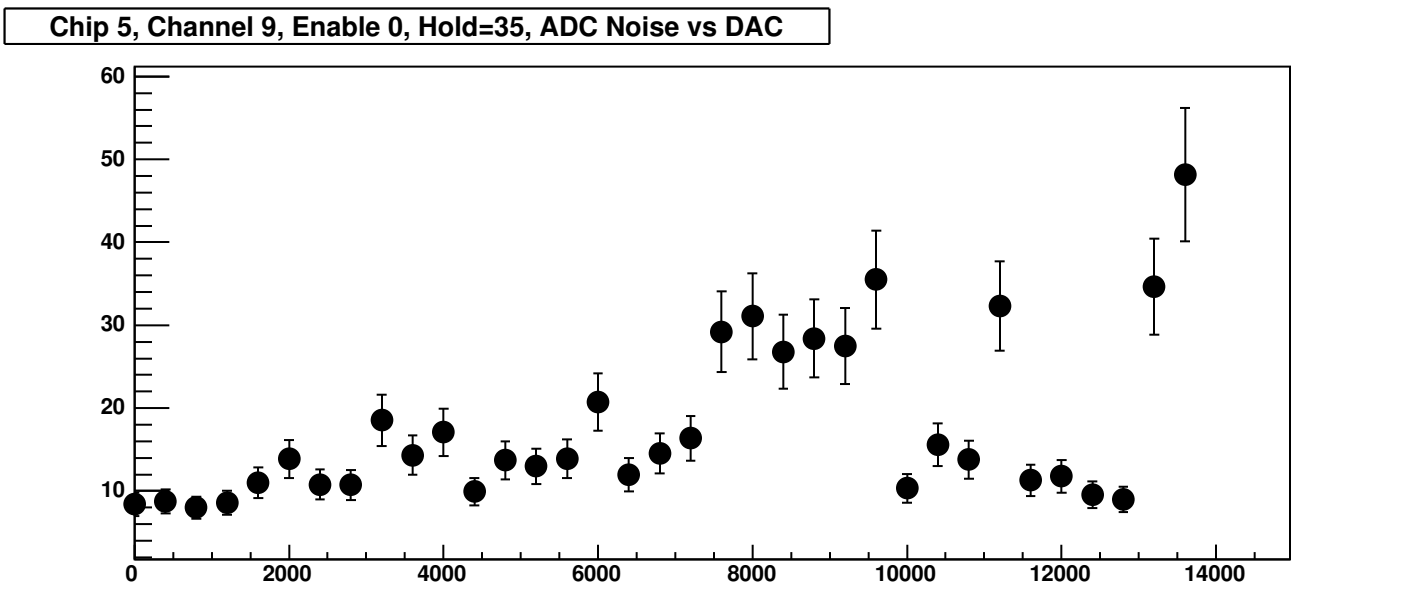
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC

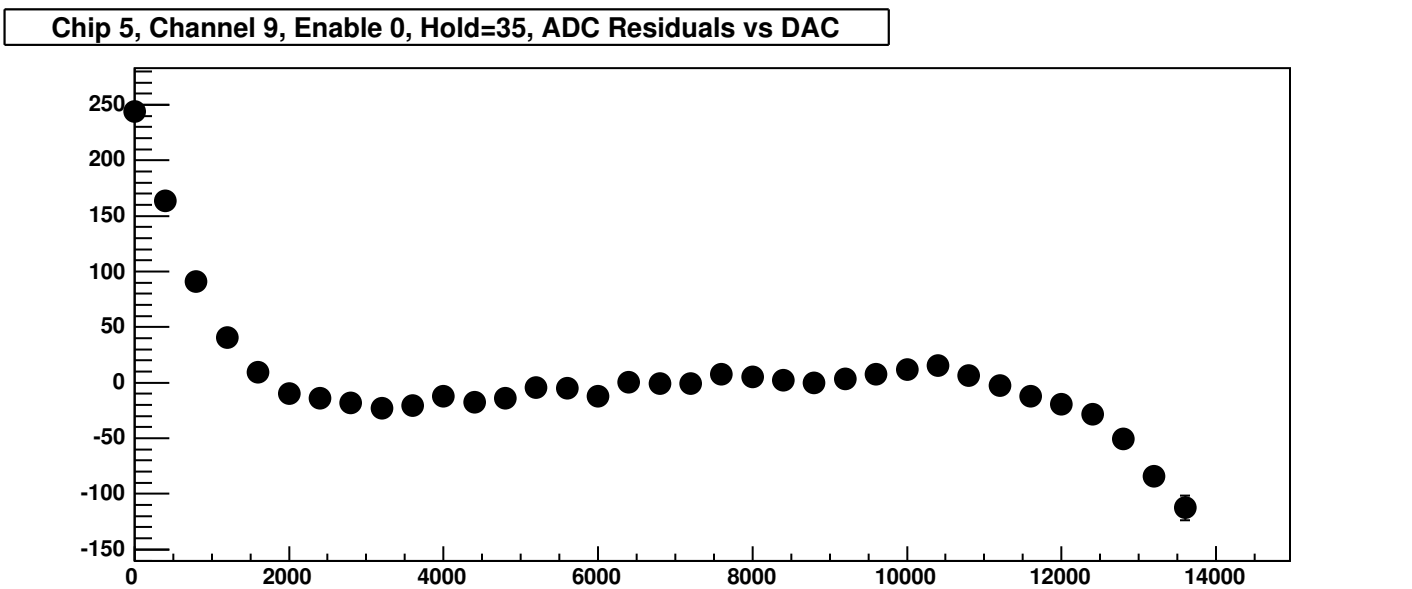


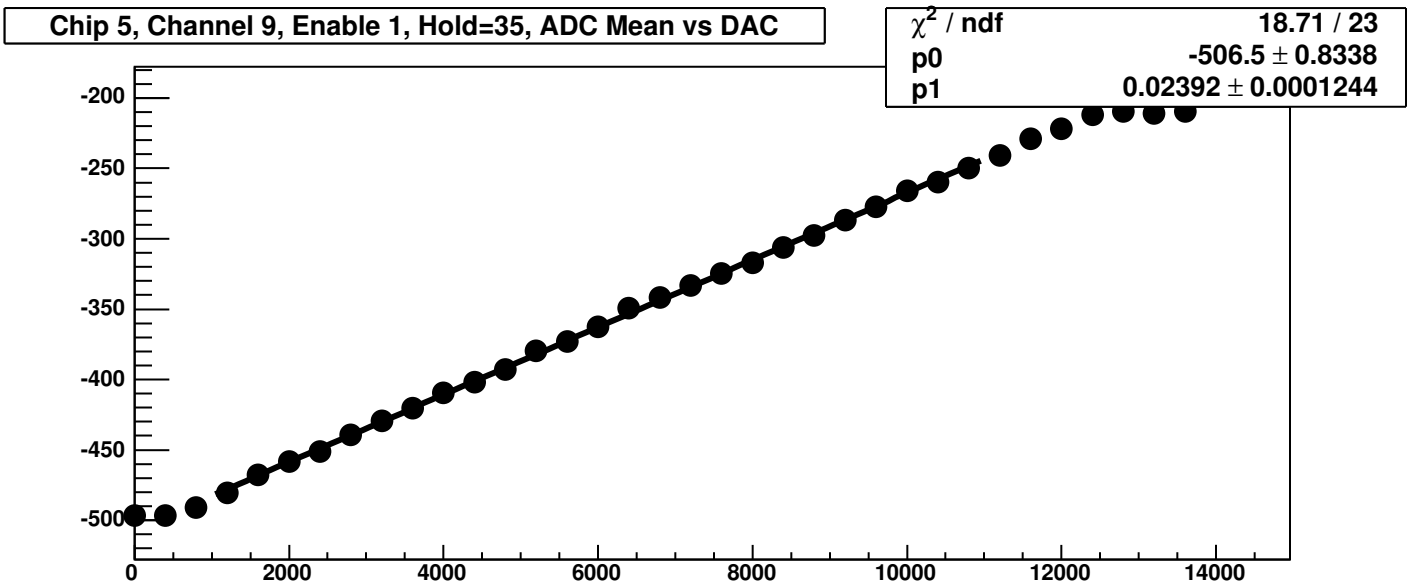
Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC

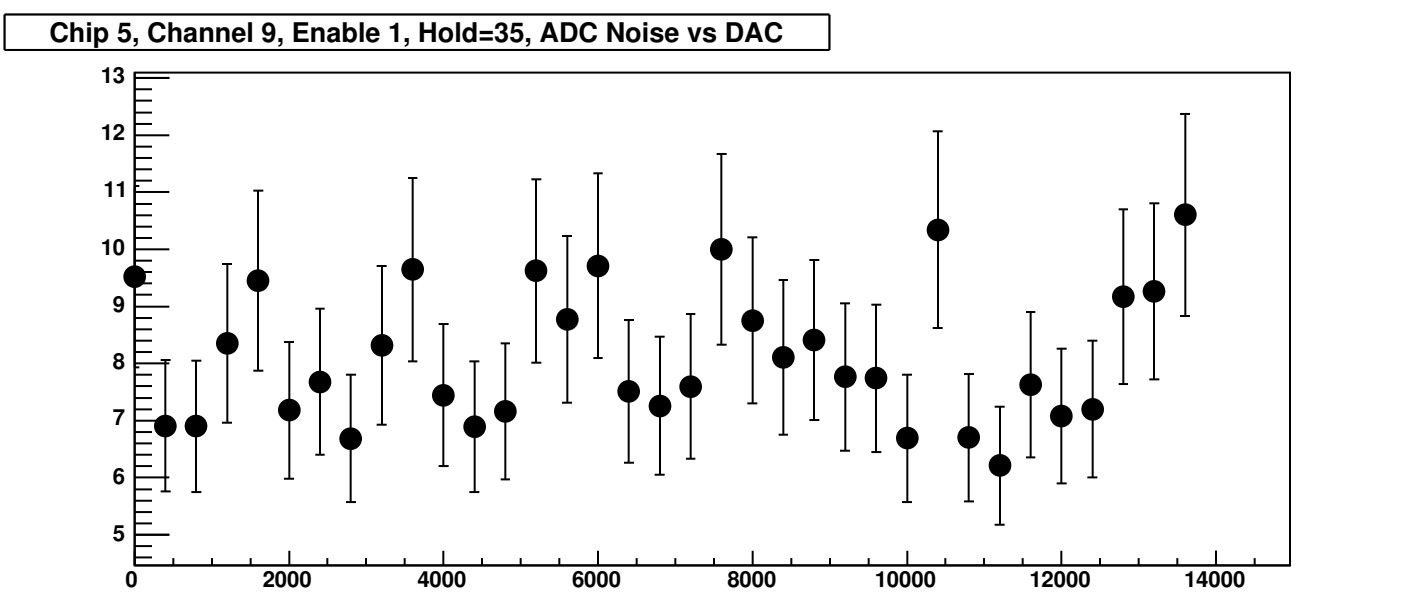


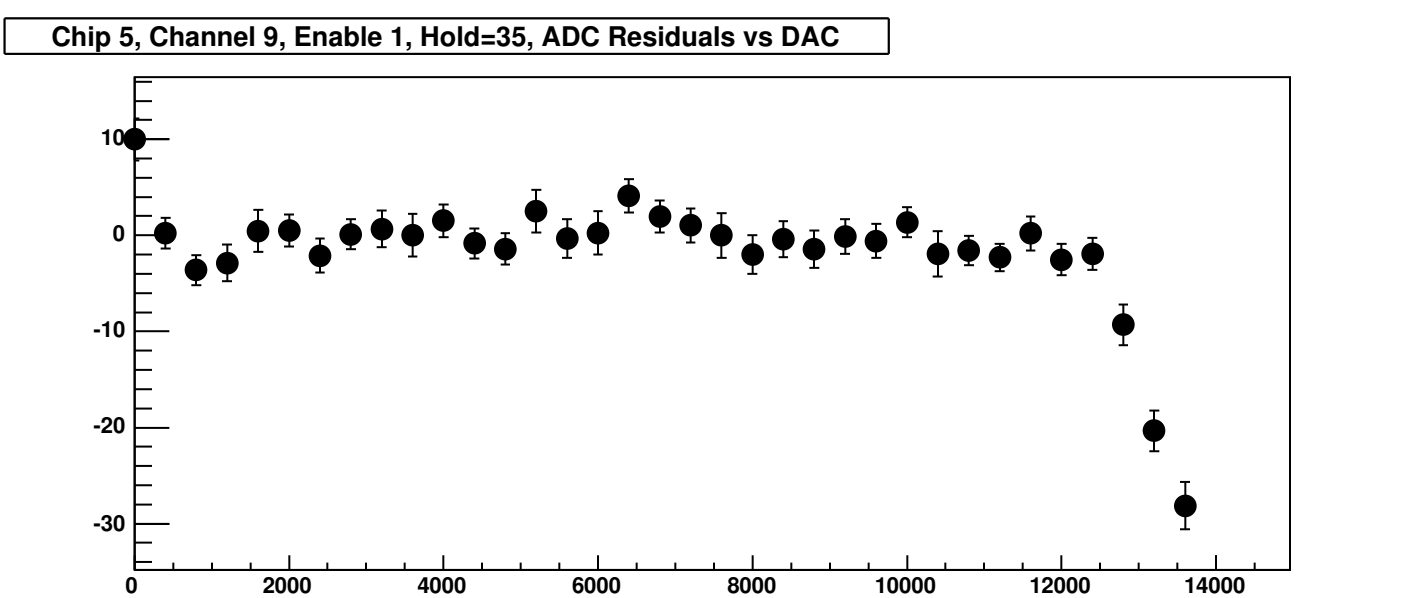

Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 755.2 / 23
p0 -703.9 ± 1.252
p1 0.2058 ± 0.000215
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

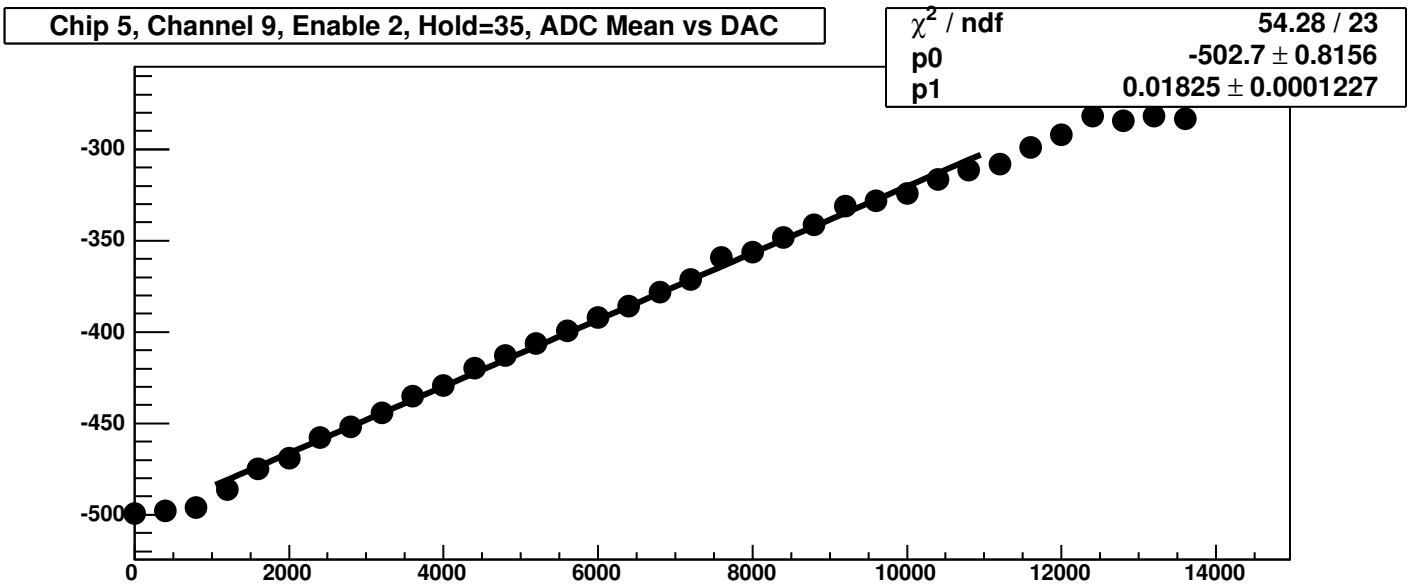

Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

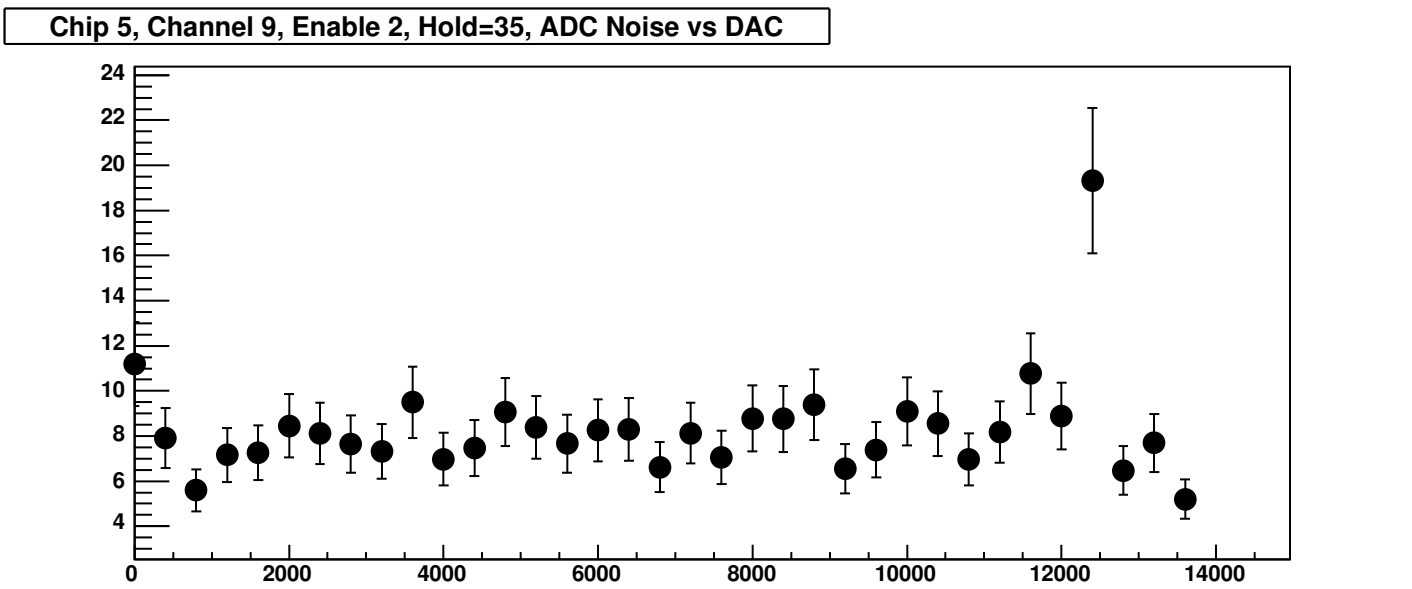

Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

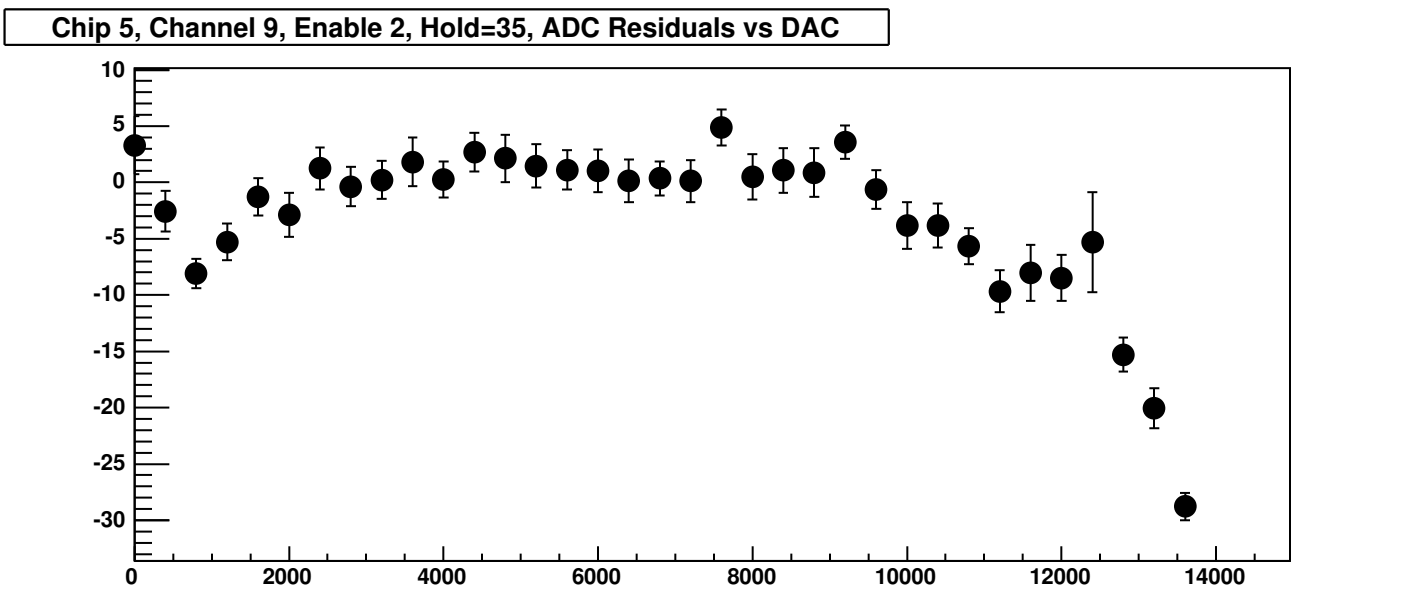

Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.71 / 23
p0 -506.5 ± 0.8338
p1 0.02392 ± 0.0001244
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

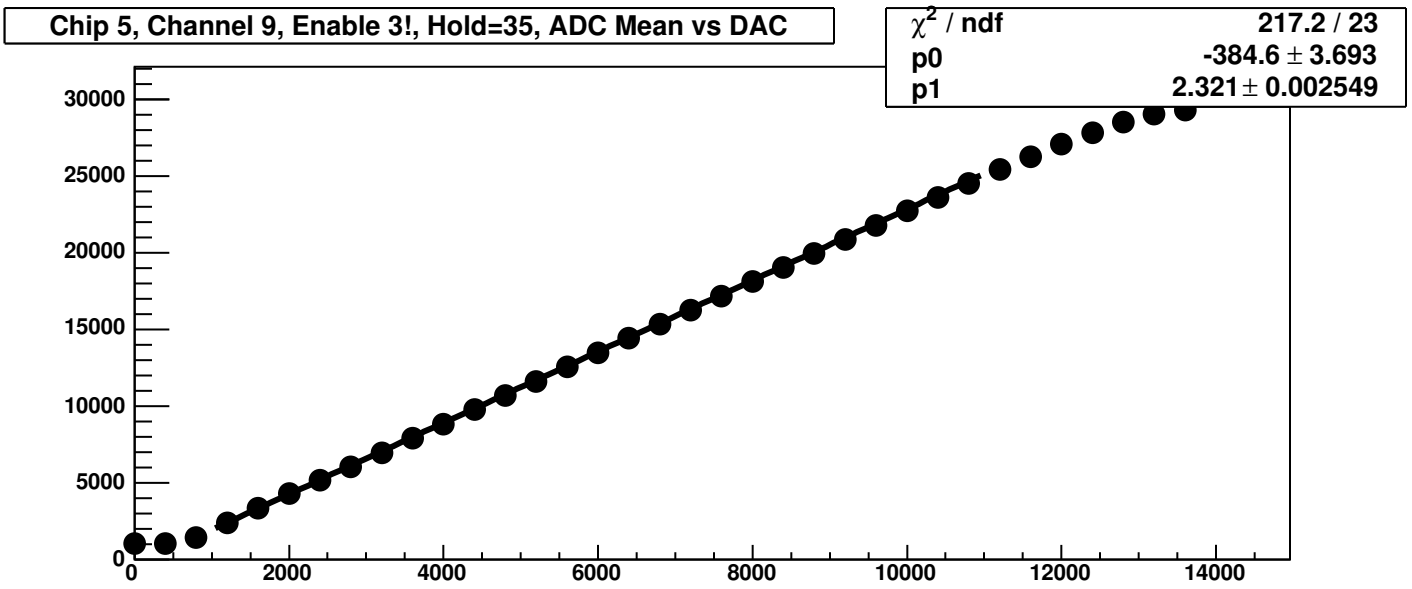

Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

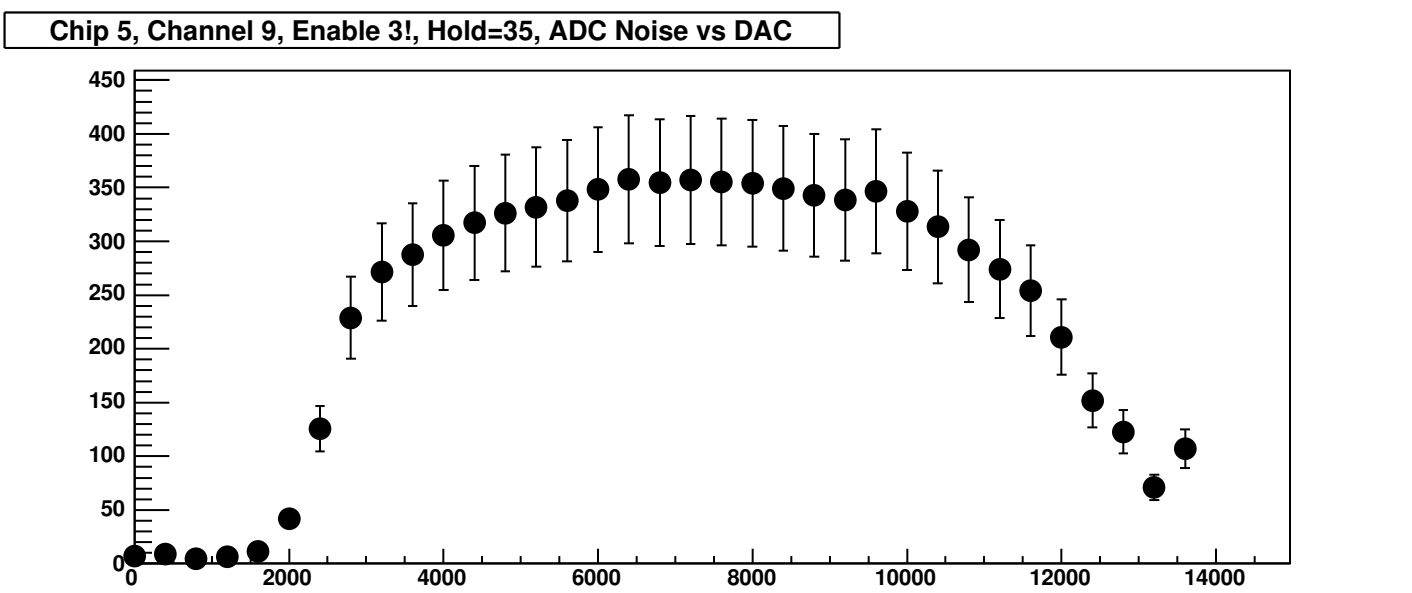

Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

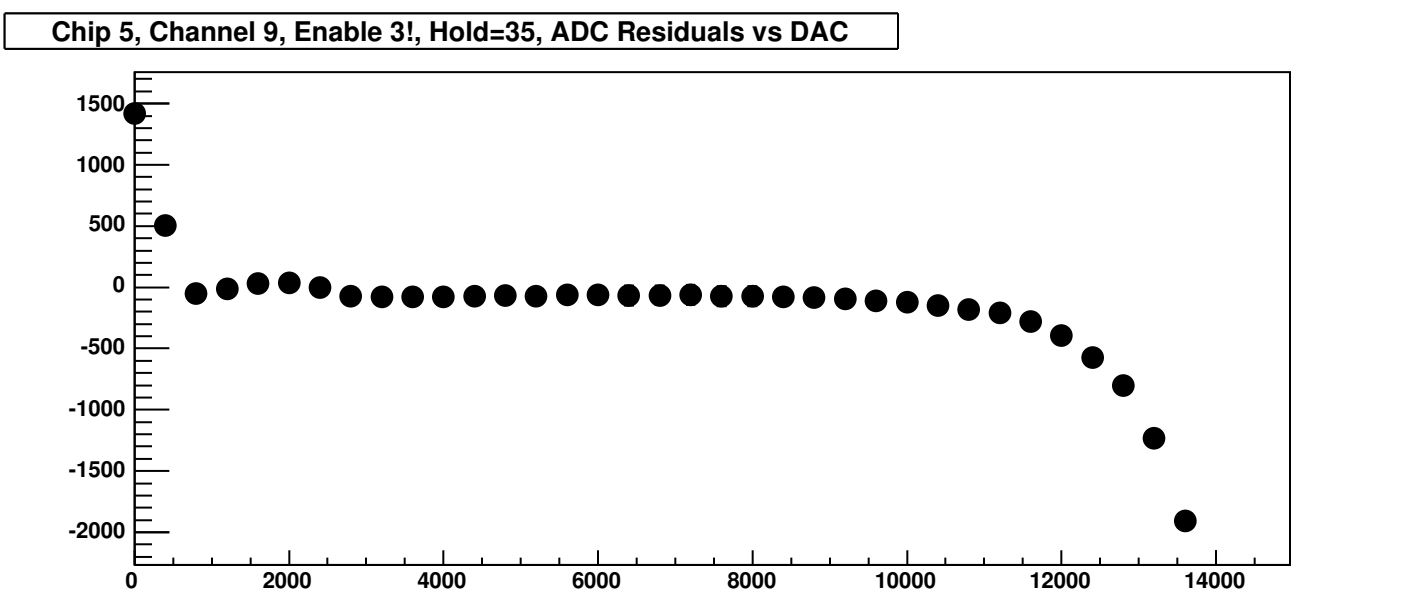

Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 54.28 / 23
p0 -502.7 ± 0.8156
p1 0.01825 ± 0.0001227
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

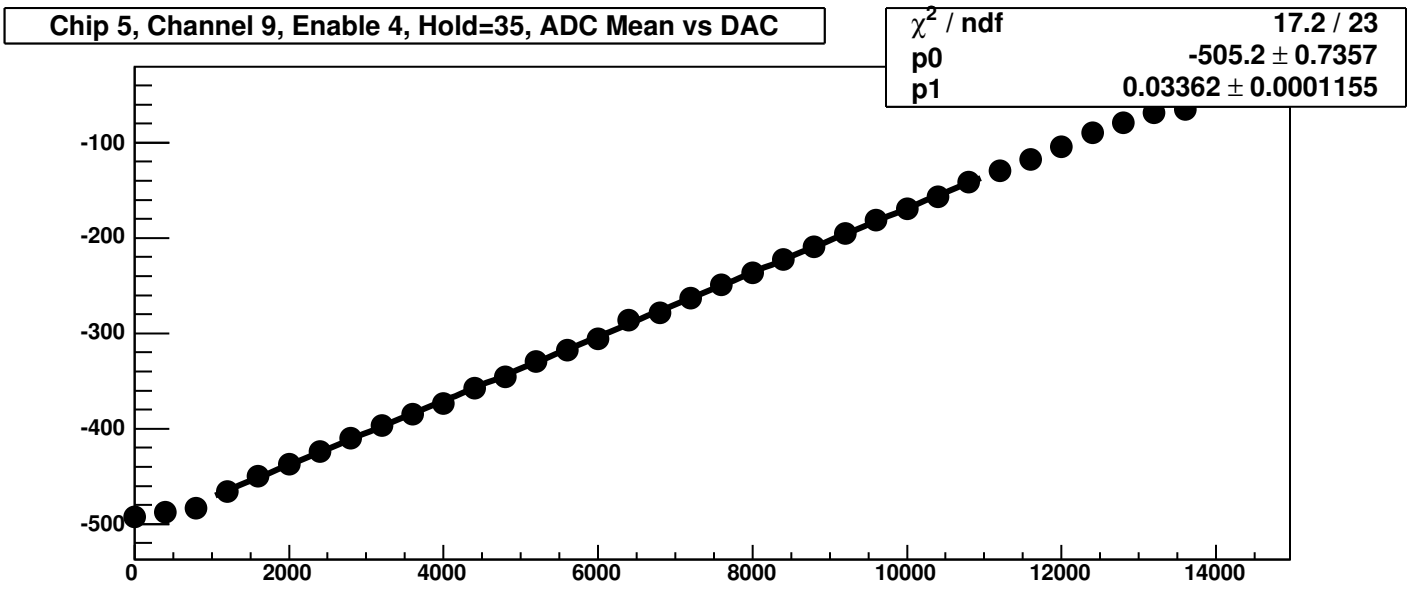

Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

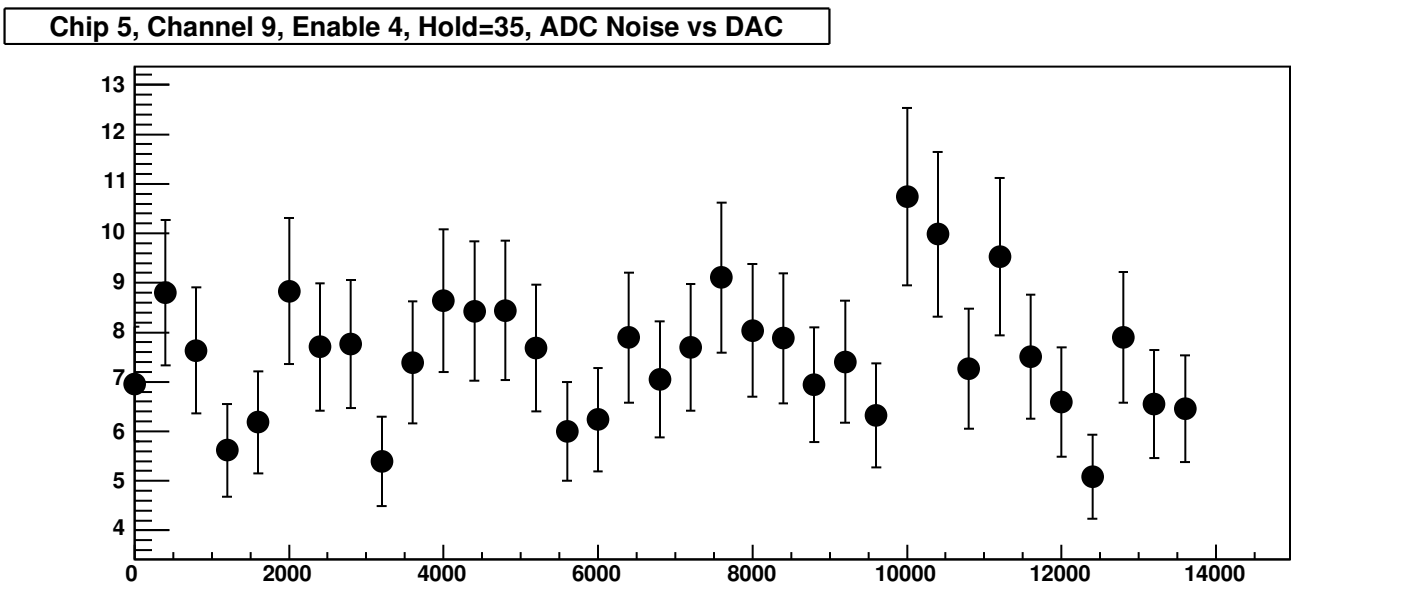

Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

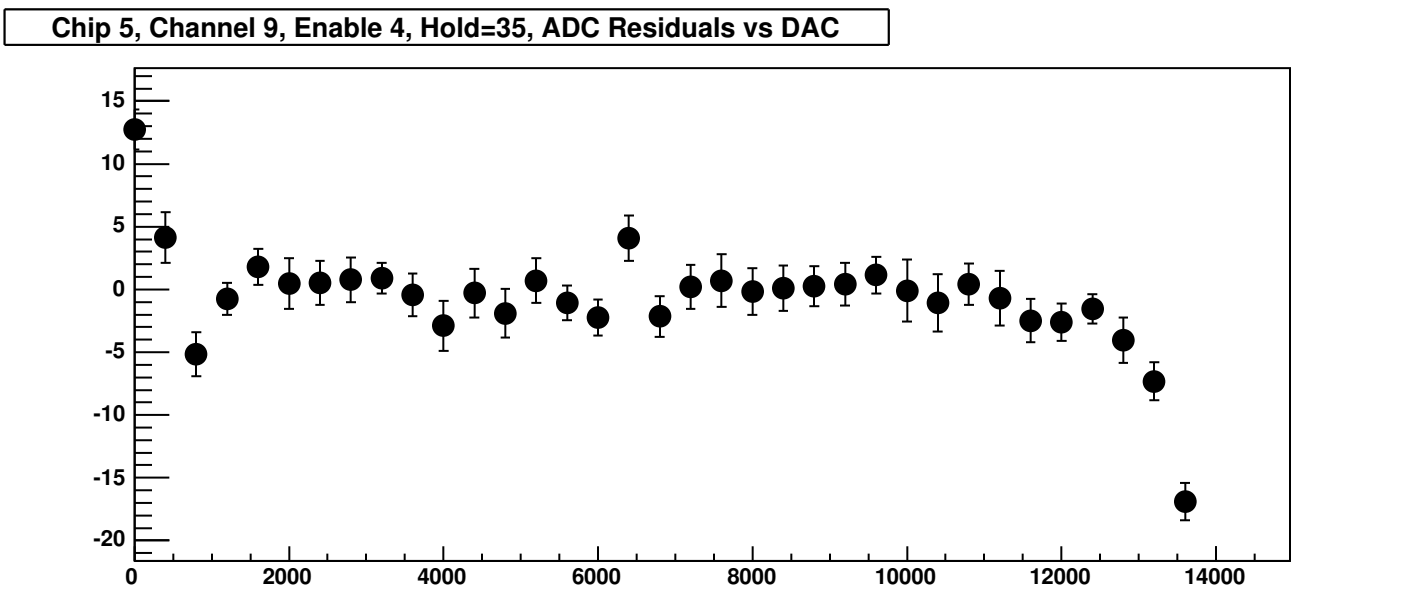

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 217.2 / 23
p0 -384.6 ± 3.693
p1 2.321 ± 0.002549
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

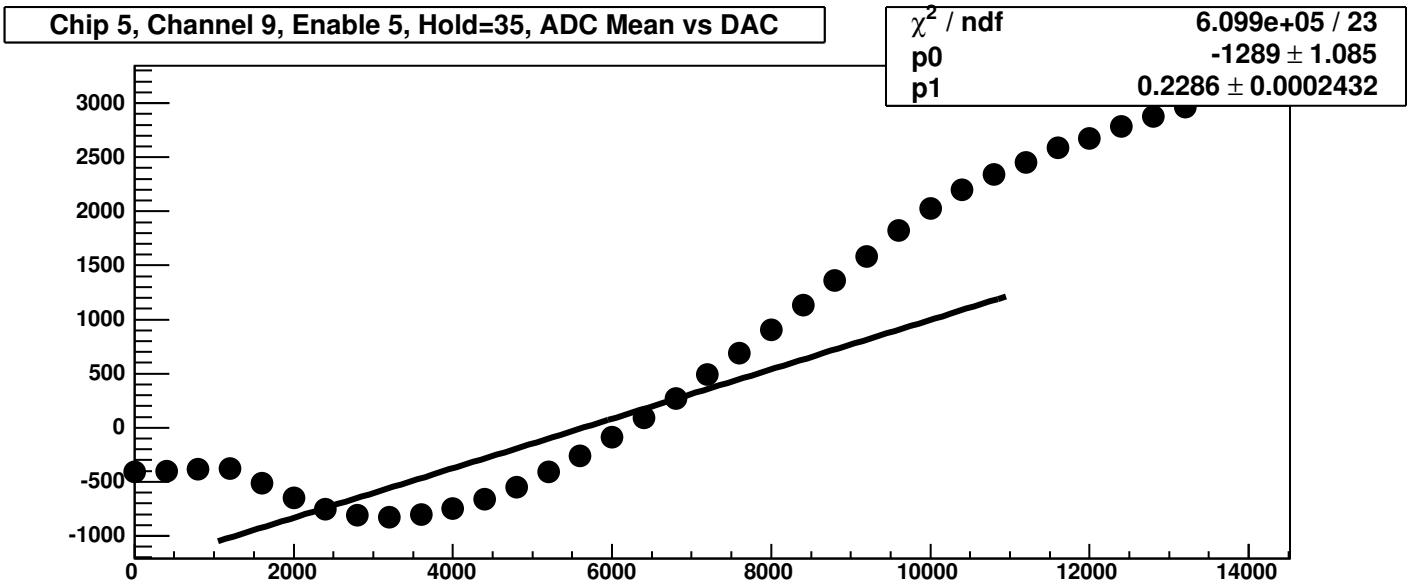

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

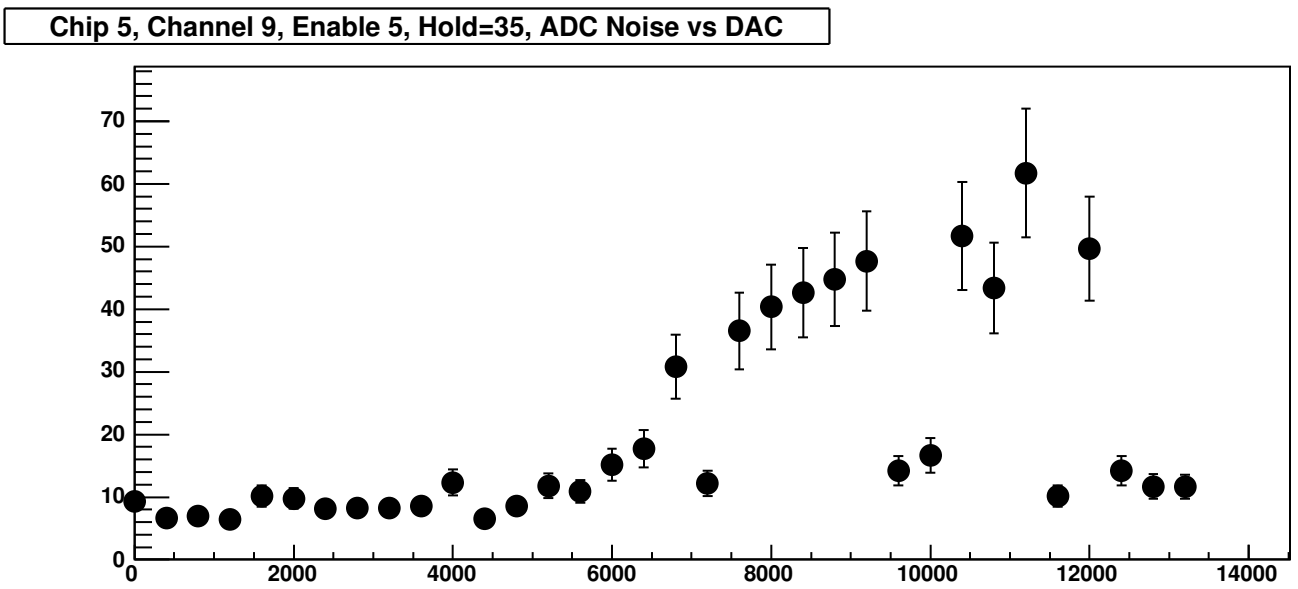

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

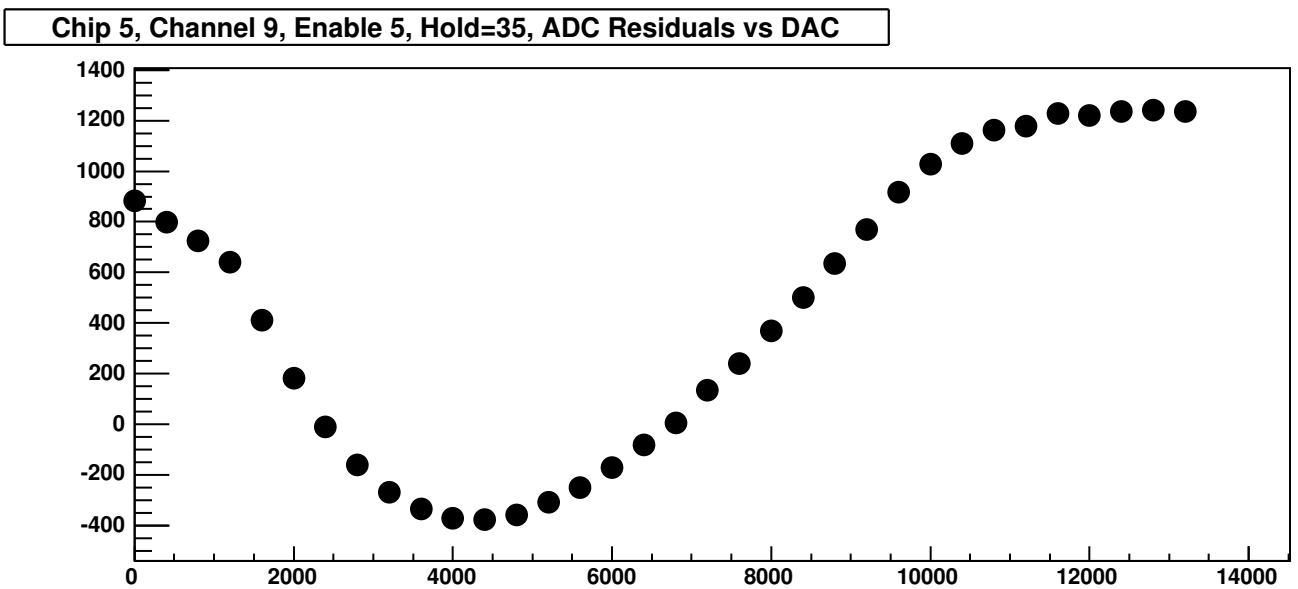

Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.2 / 23
p0 -505.2 ± 0.7357
p1 0.03362 ± 0.0001155
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

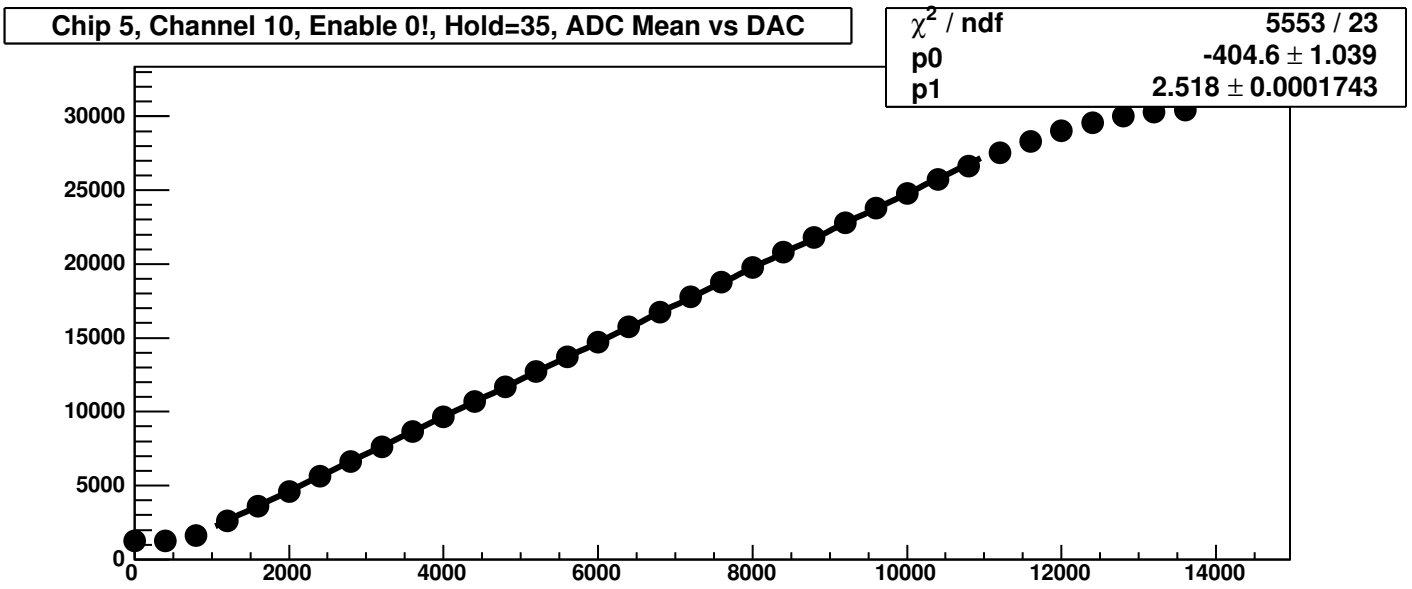

Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

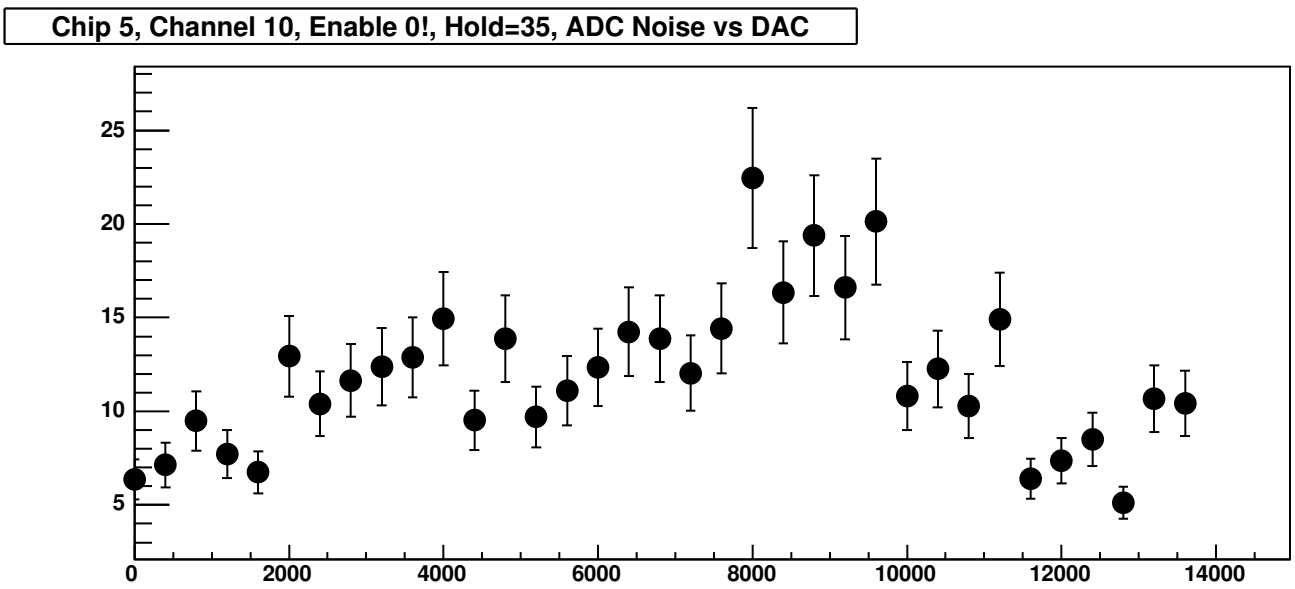

Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

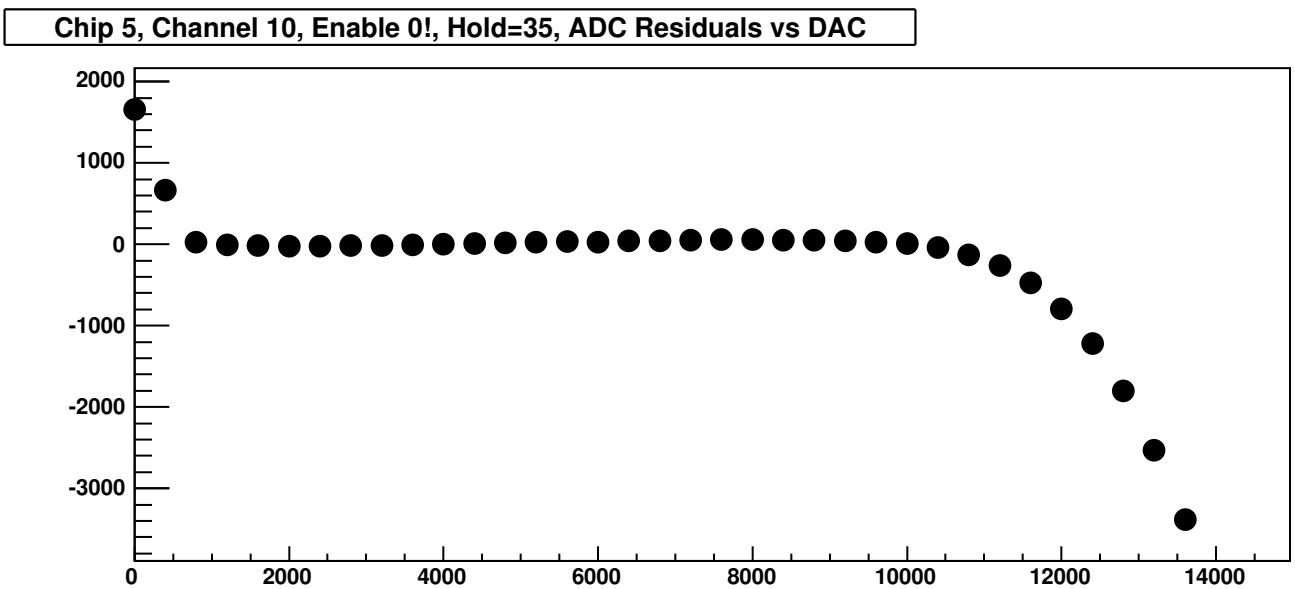

Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.099e+05 / 23
p0 -1289 ± 1.085
p1 0.2286 ± 0.0002432
3000
2500
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

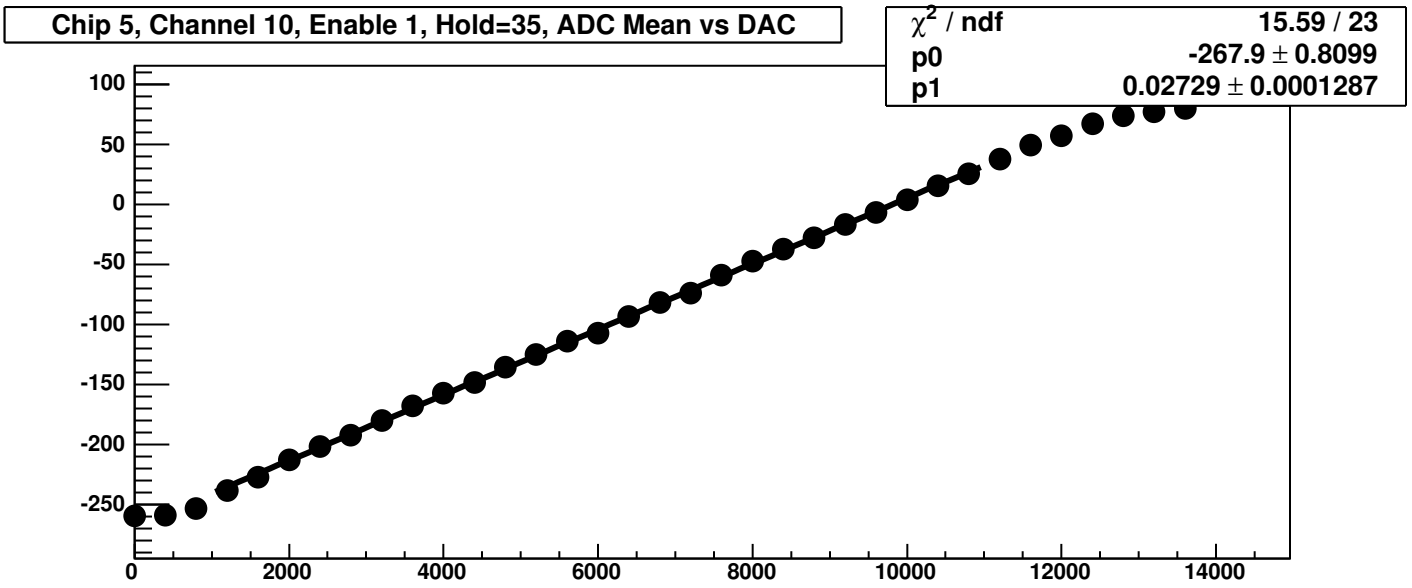

Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

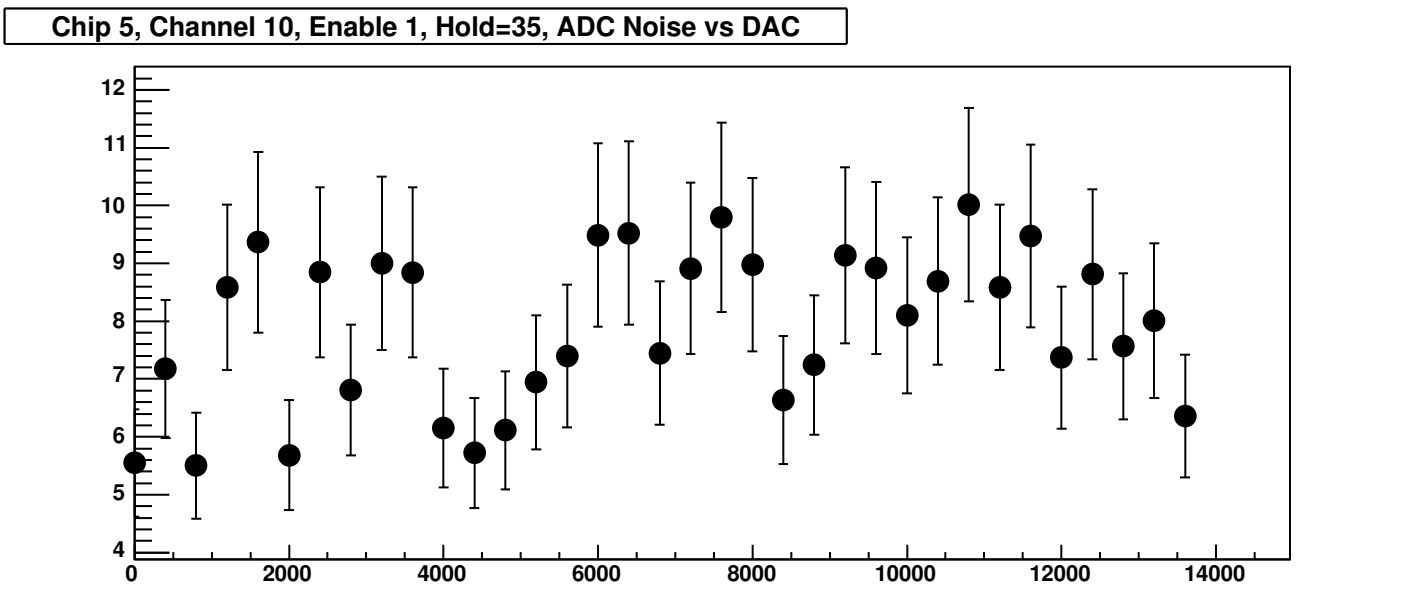

Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

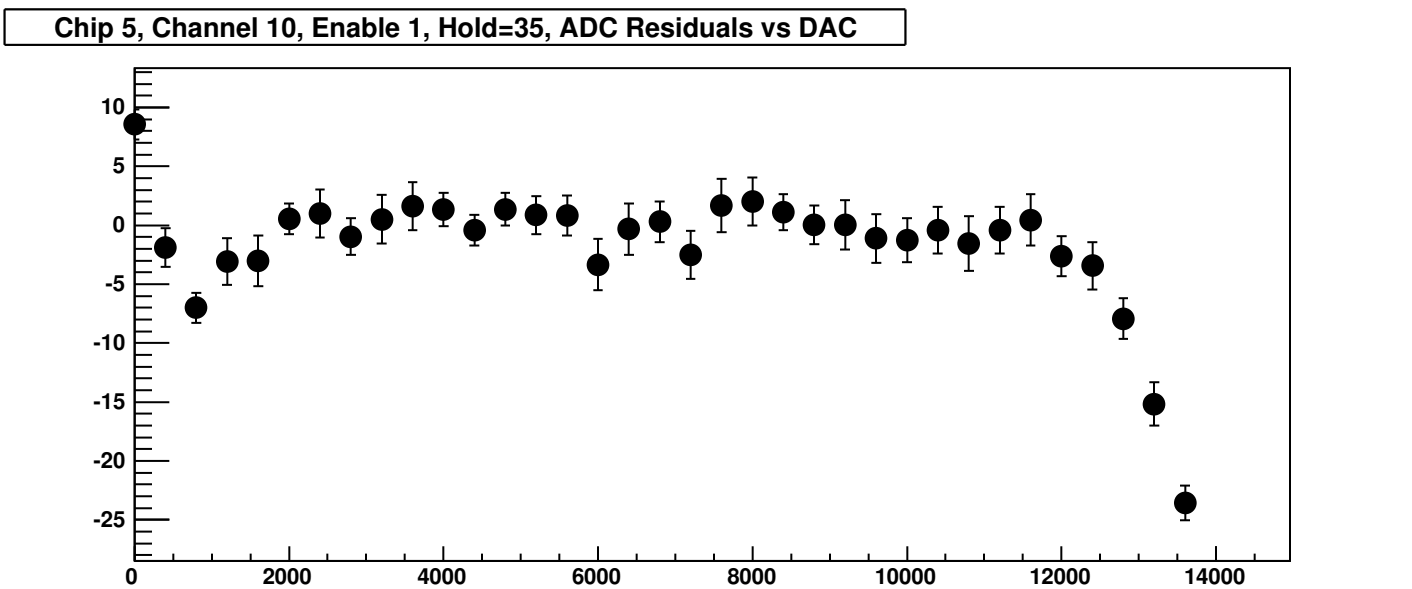

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 5553 / 23
p0 -404.6 ± 1.039
p1 2.518 ± 0.0001743
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

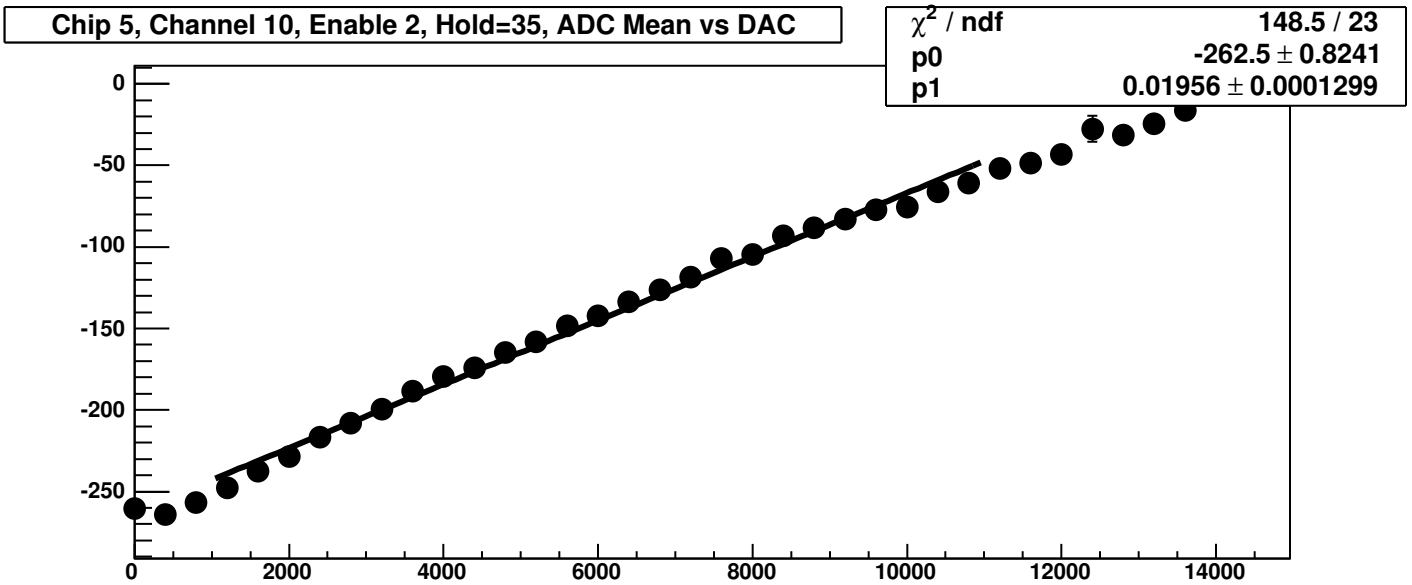

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

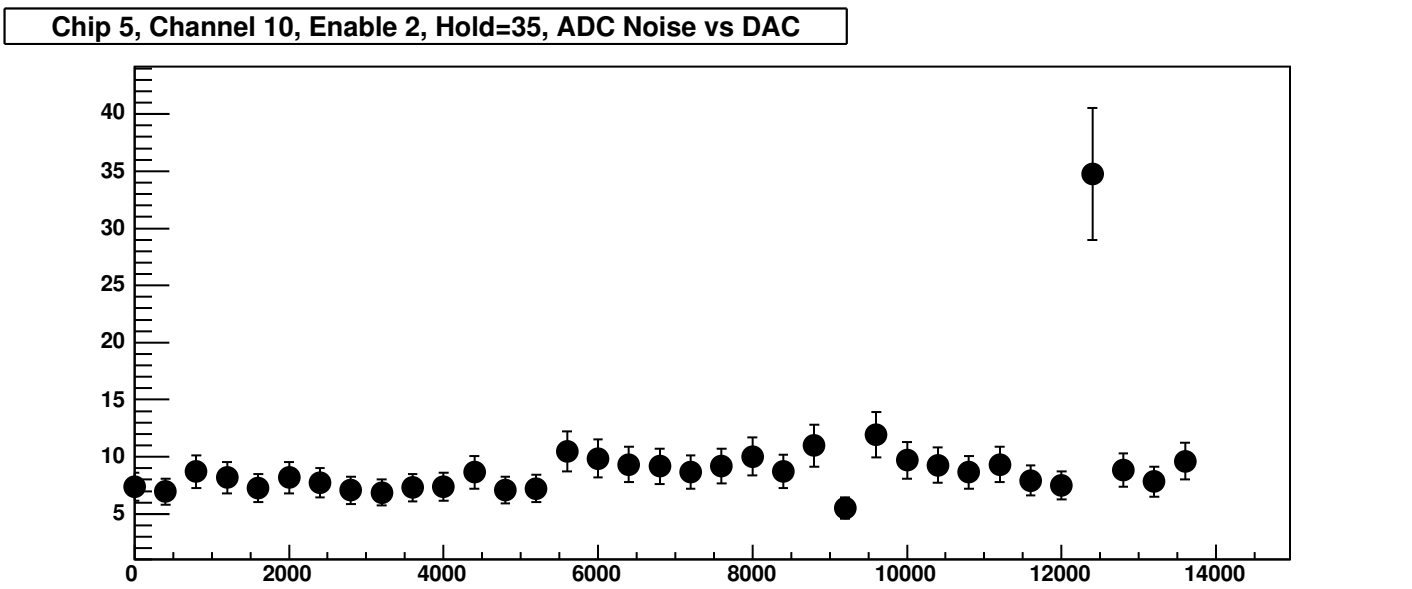

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

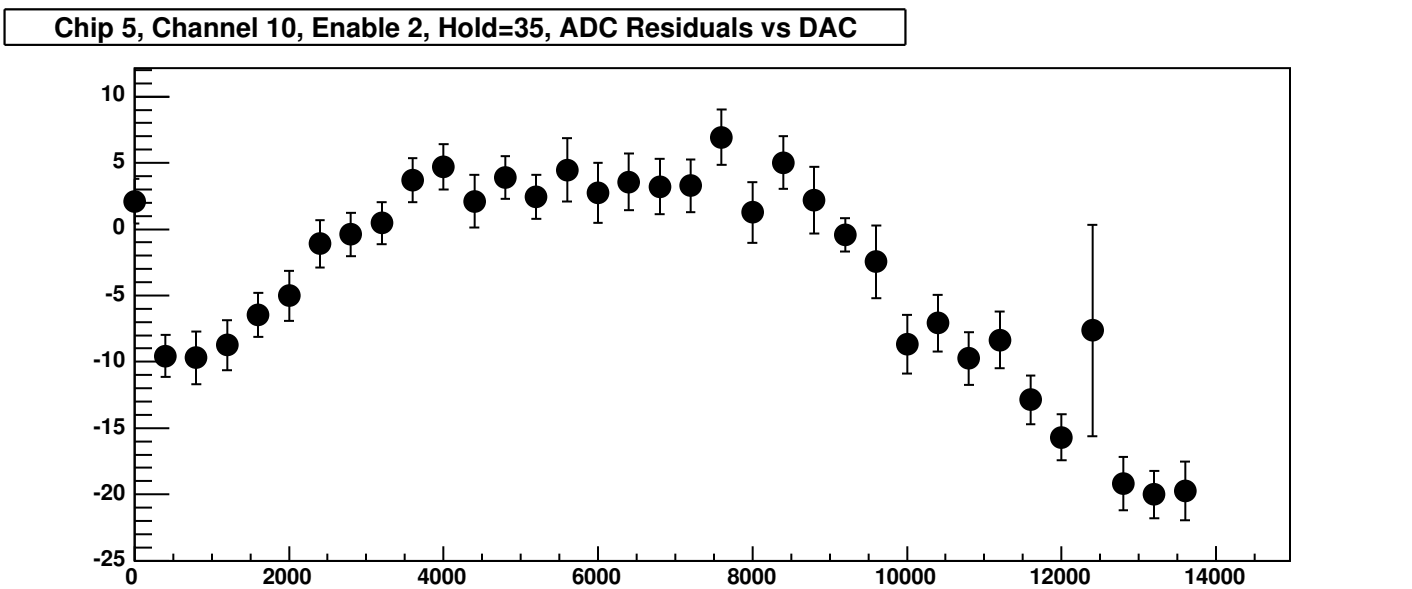

Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 15.59 / 23
p0 -267.9 ± 0.8099
p1 0.02729 ± 0.0001287
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

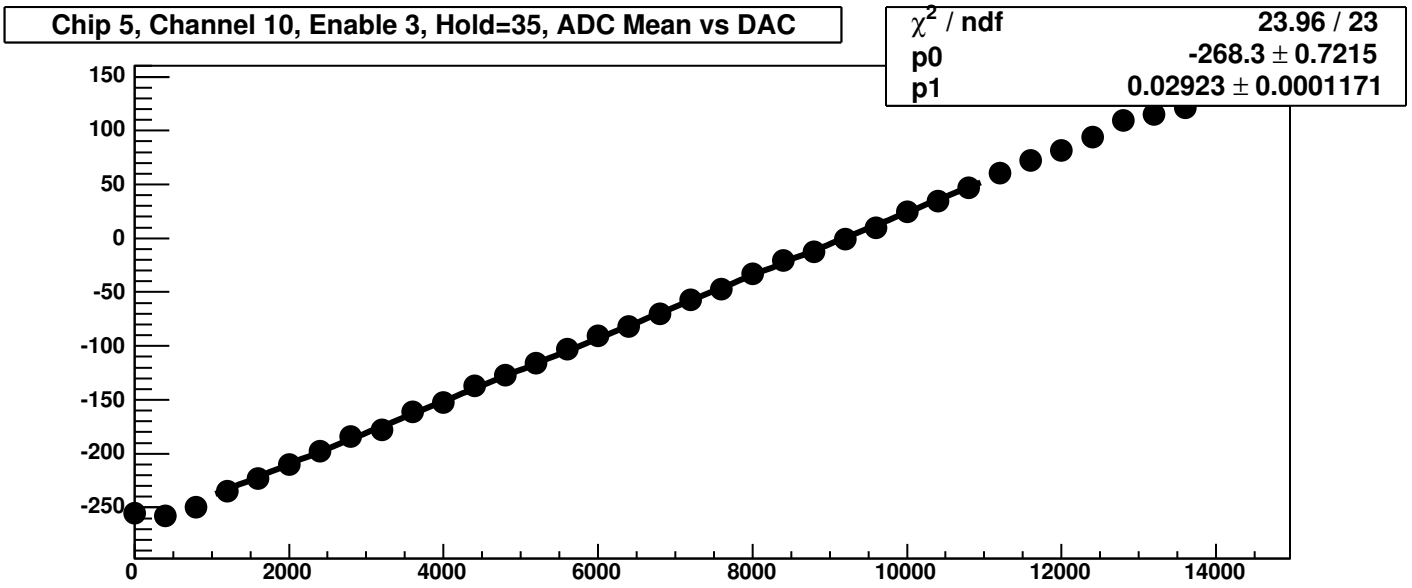

Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

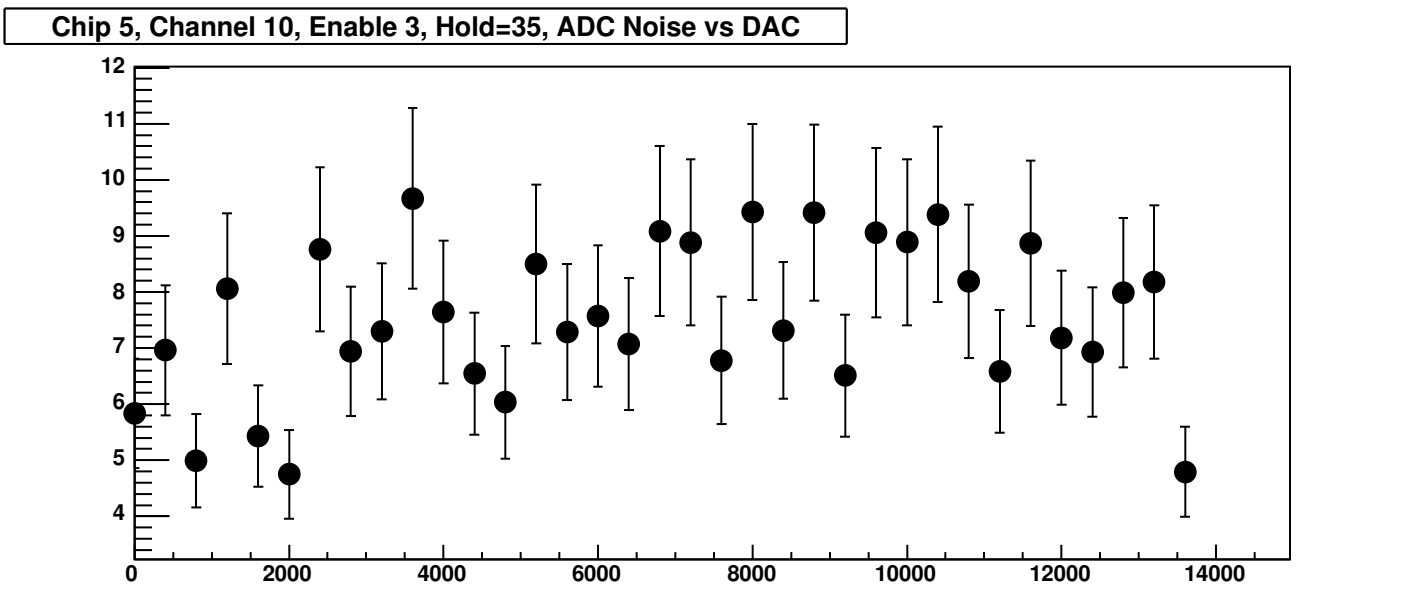

Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

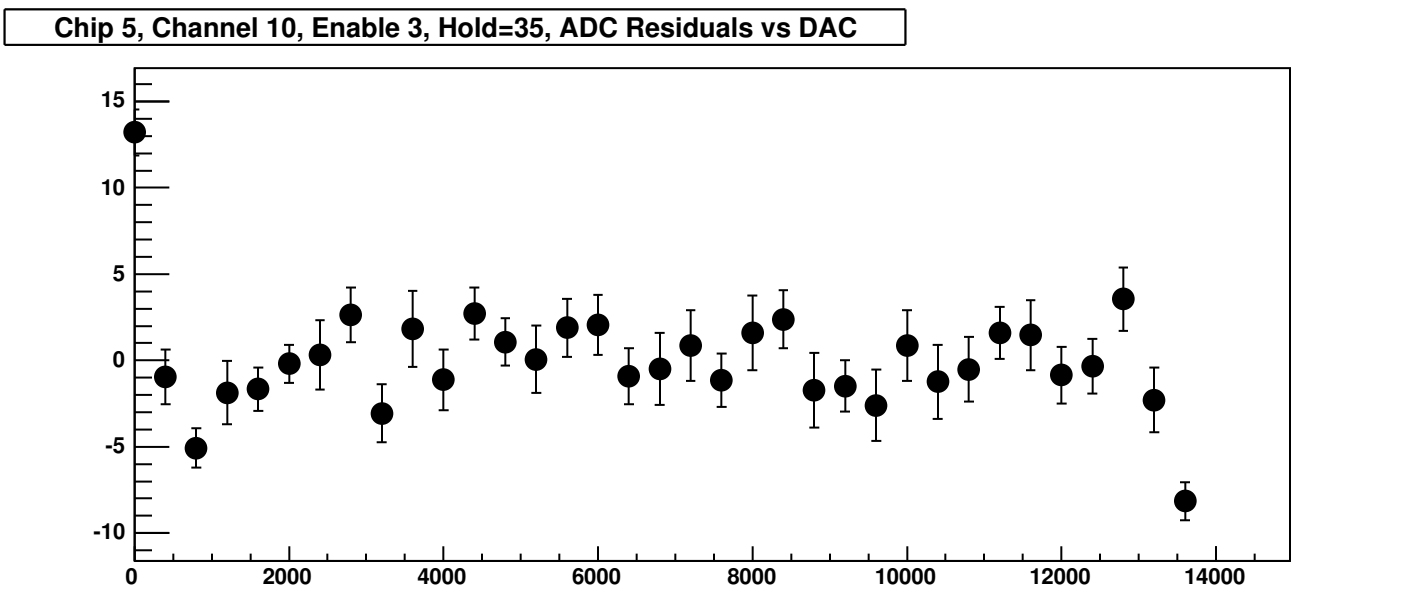

Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 148.5 / 23
p0 -262.5 ± 0.8241
p1 0.01956 ± 0.0001299
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

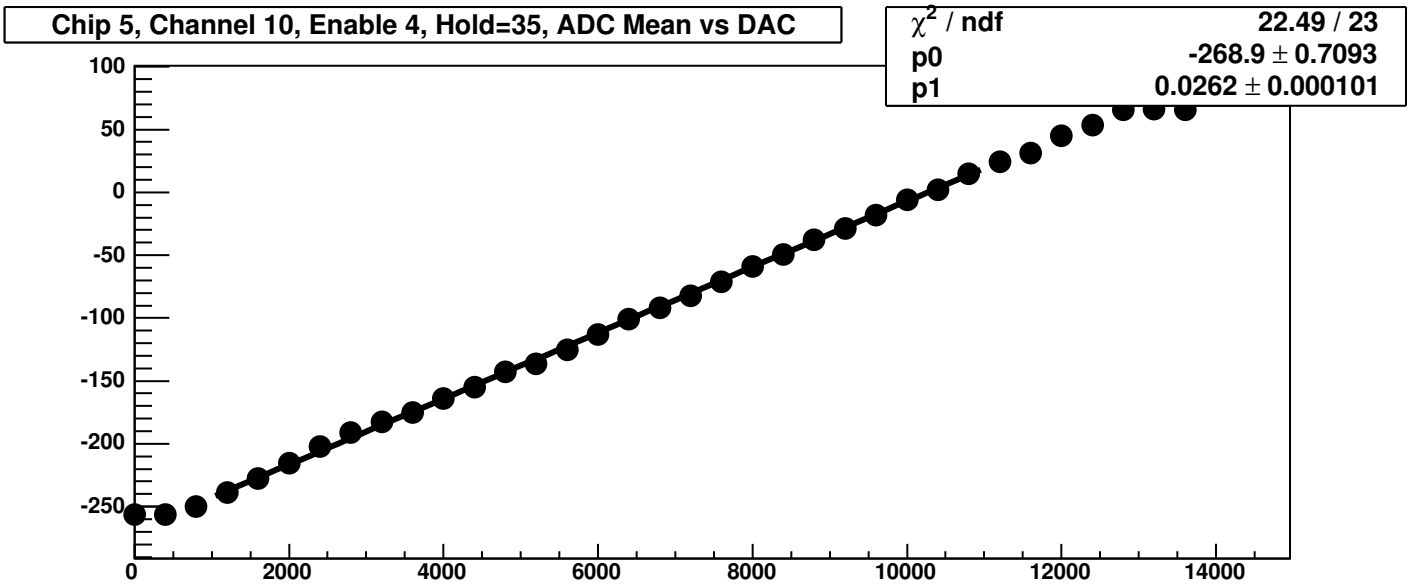

Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

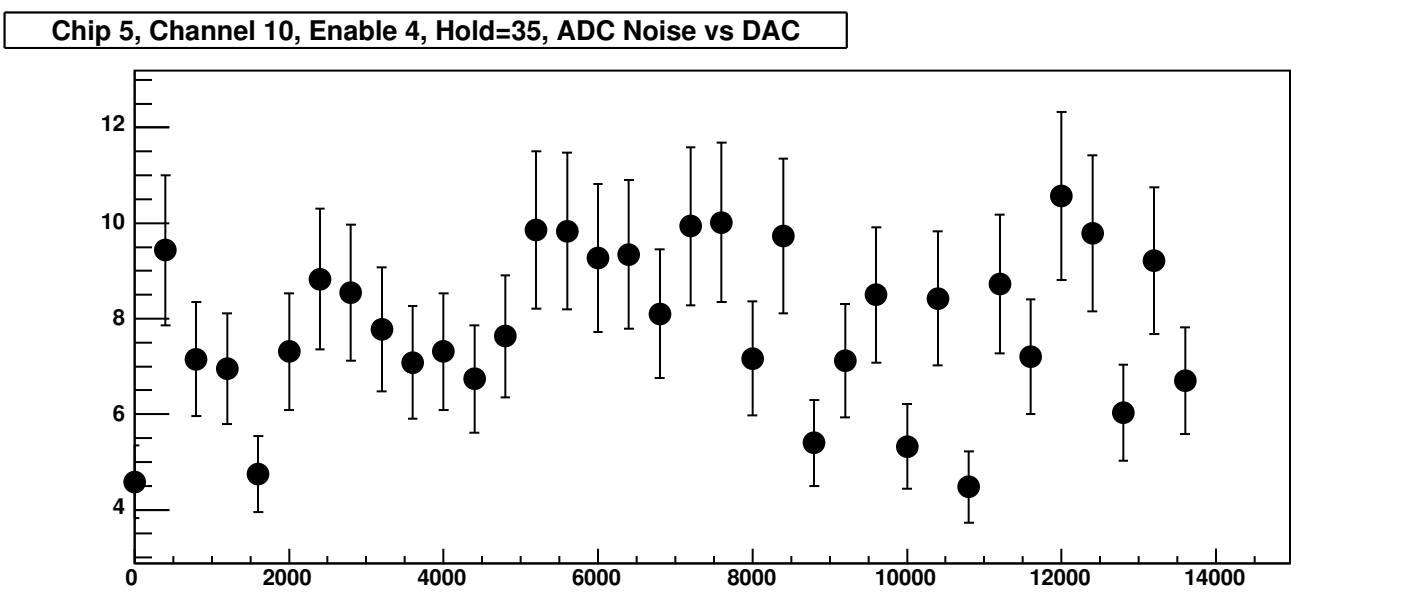

Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

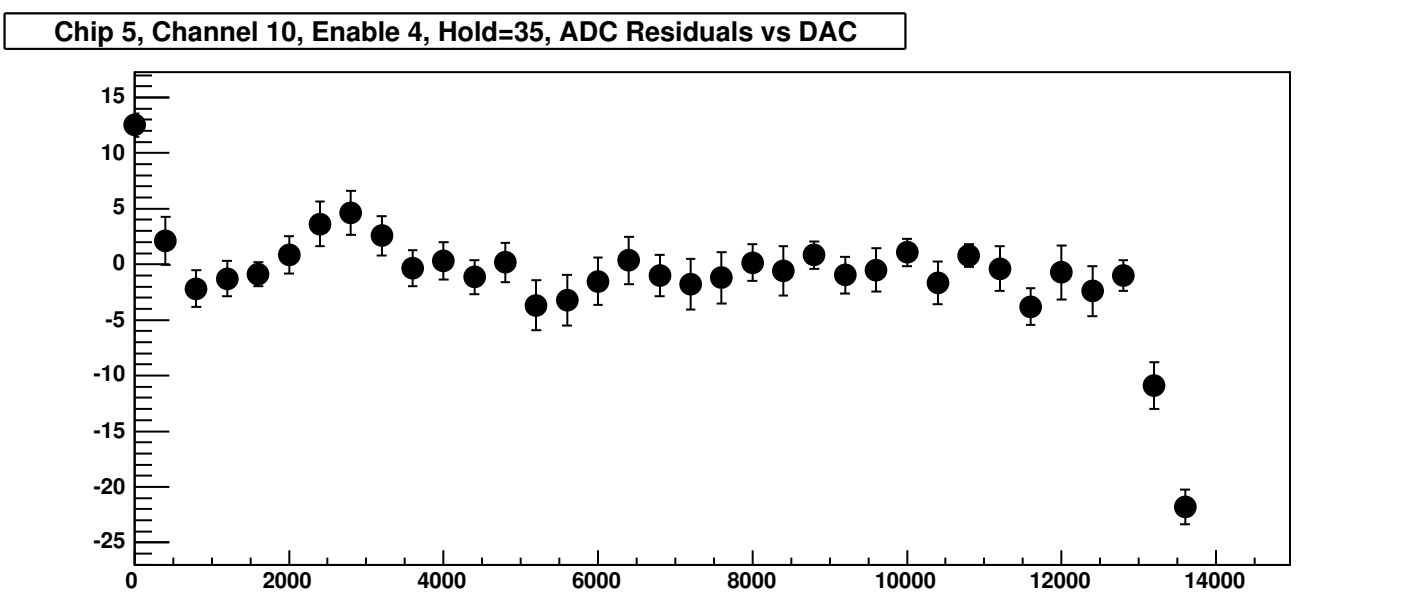

Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.96 / 23
p0 -268.3 ± 0.7215
p1 0.02923 ± 0.0001171
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

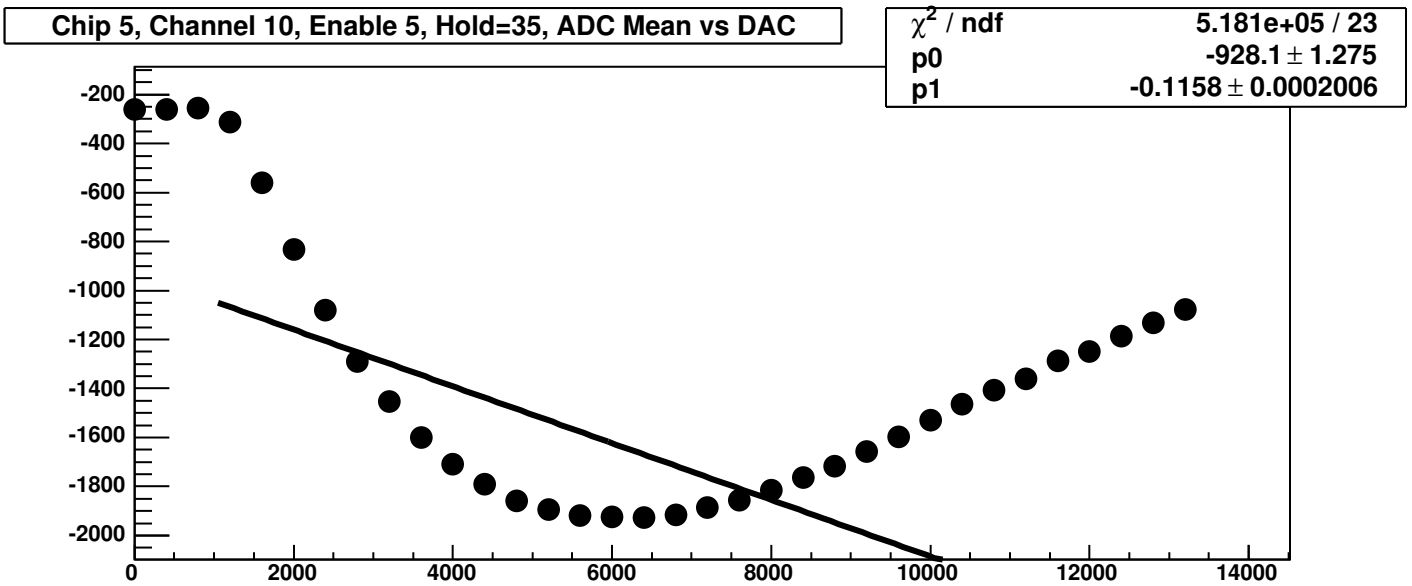

Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

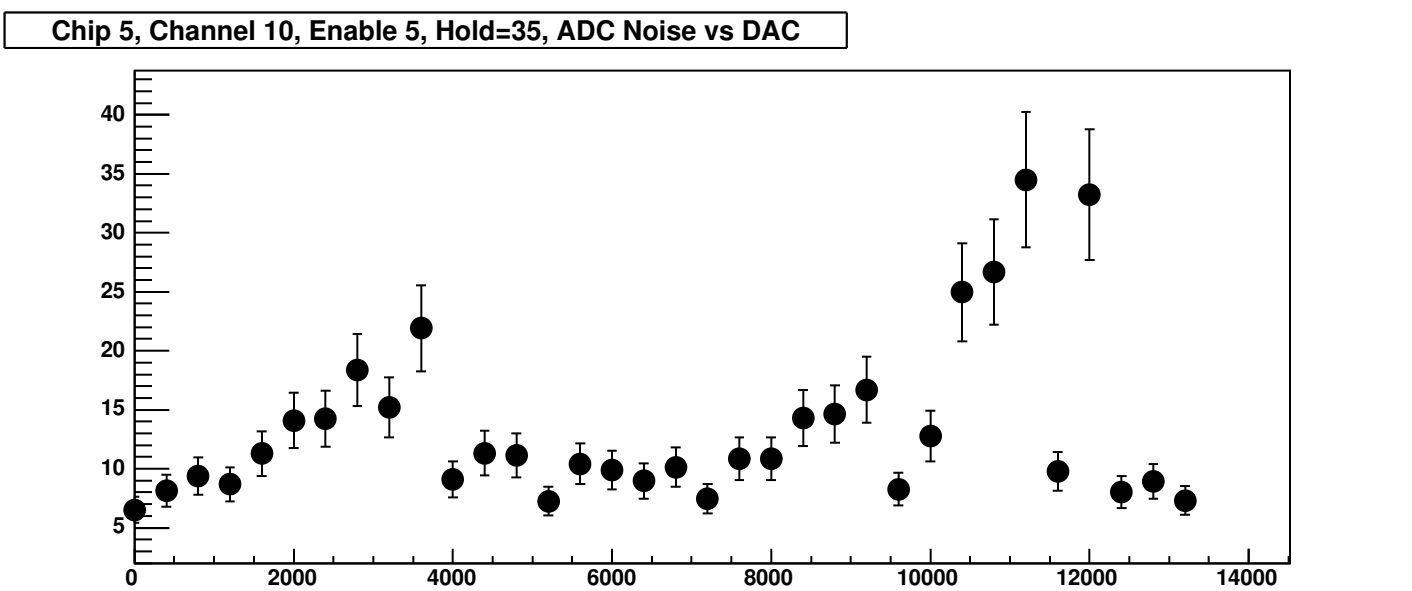

Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

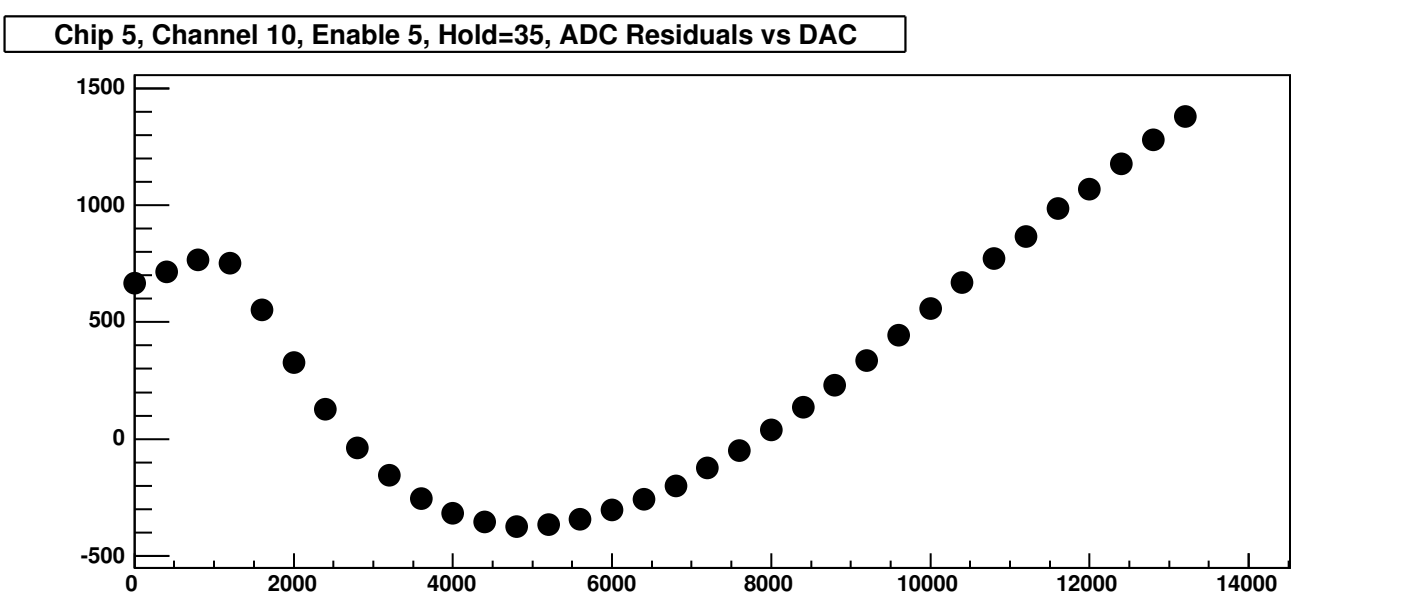

Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.49 / 23
p0 -268.9 ± 0.7093
p1 0.0262 ± 0.000101
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

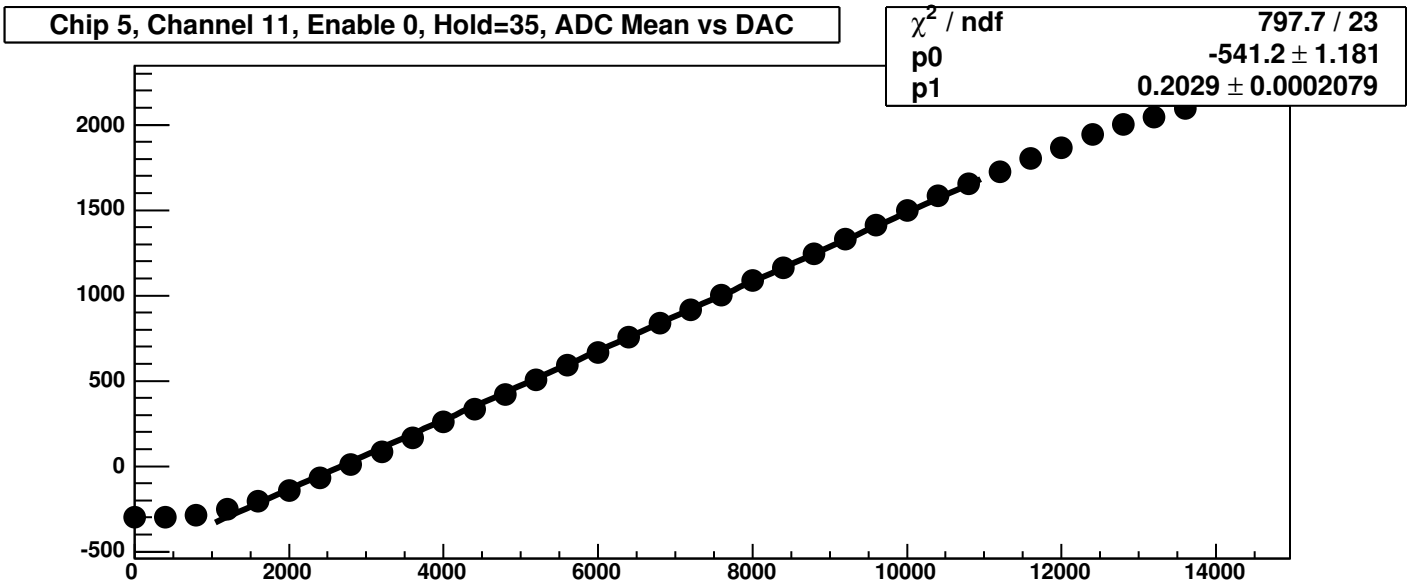

Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

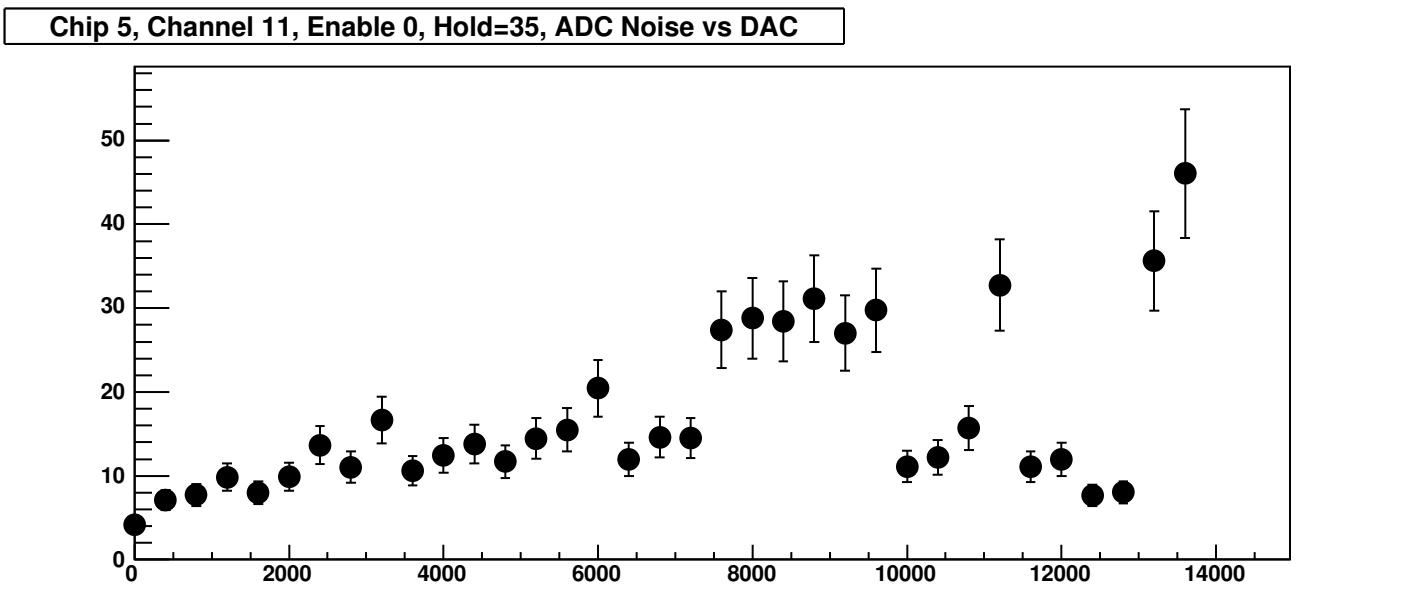

Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

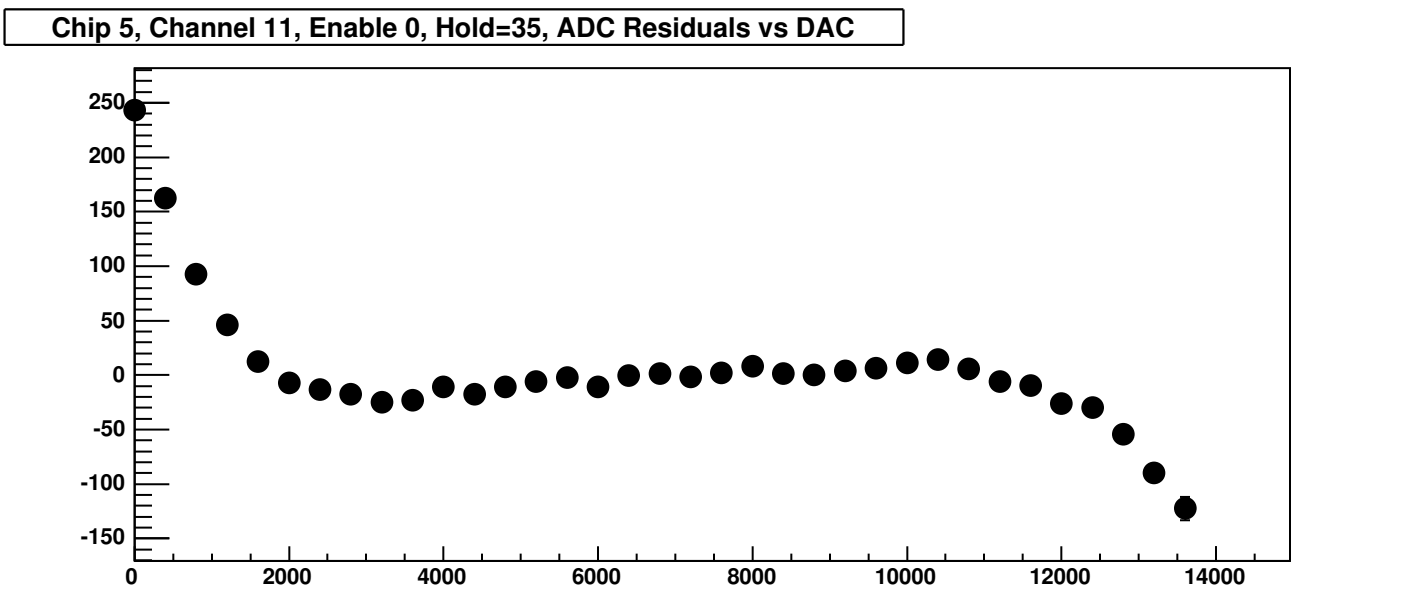

Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.181e+05 / 23
p0 -928.1 ± 1.275
p1 -0.1158 ± 0.0002006
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
2000
4000
6000
8000
10000
12000
14000

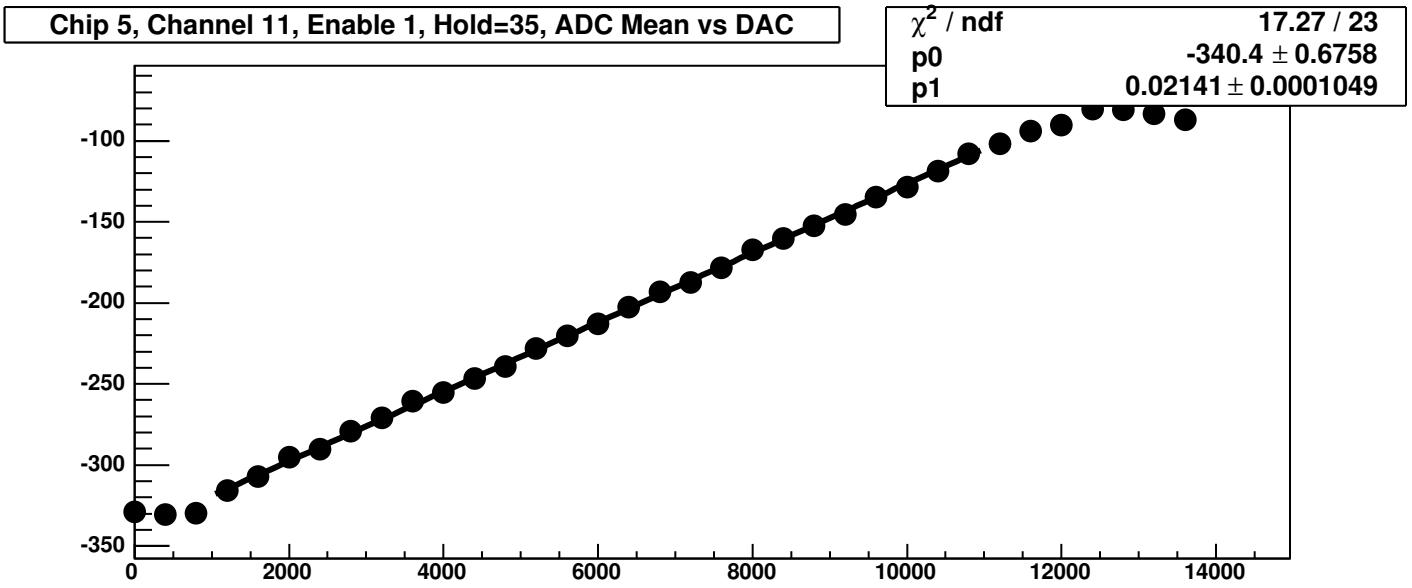

Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

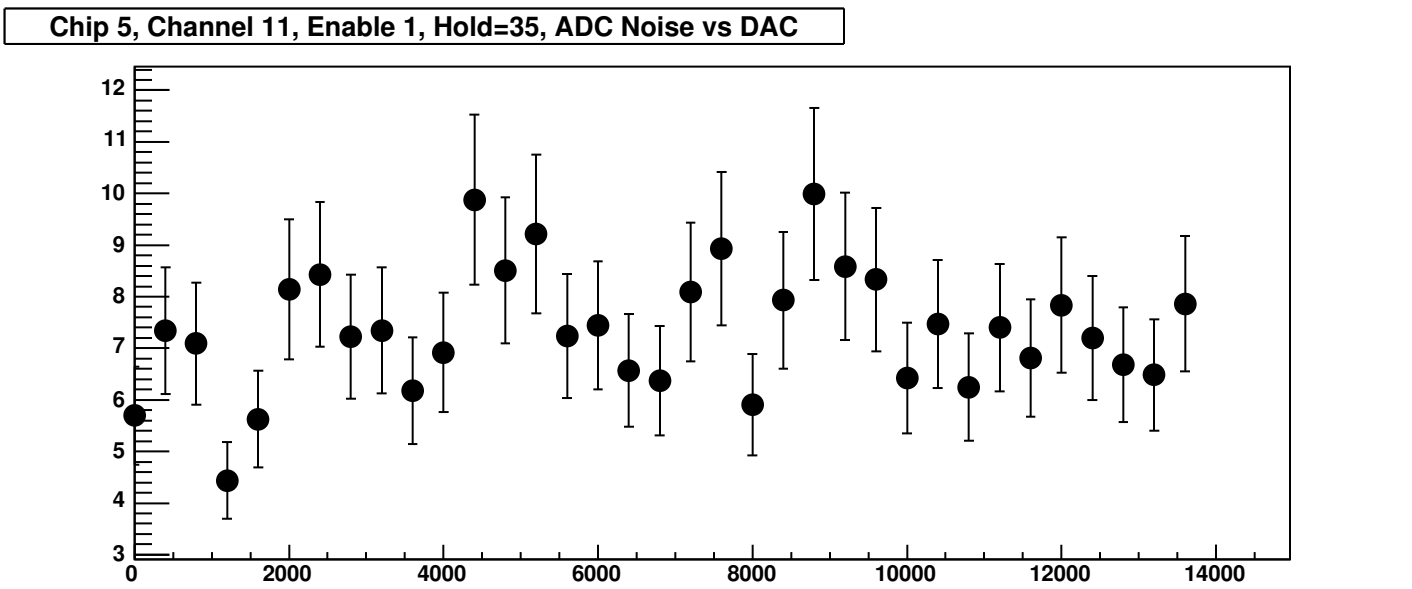

Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

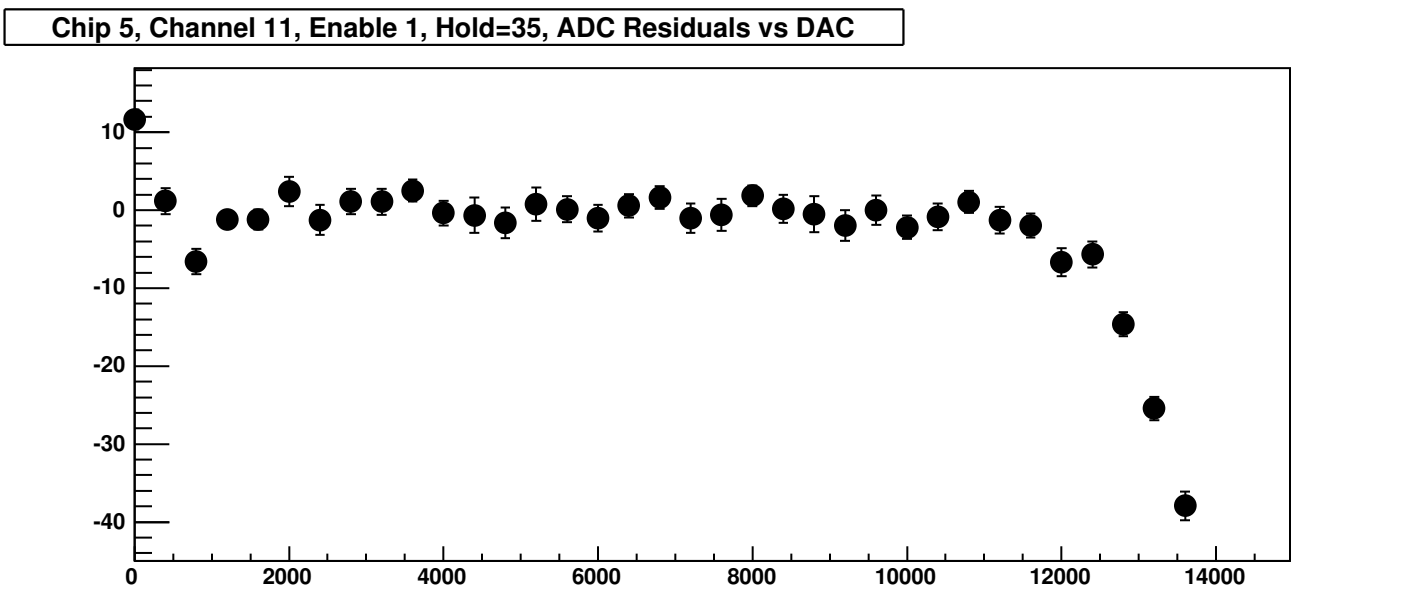

Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 797.7 / 23
p0 -541.2 ± 1.181
p1 0.2029 ± 0.0002079
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

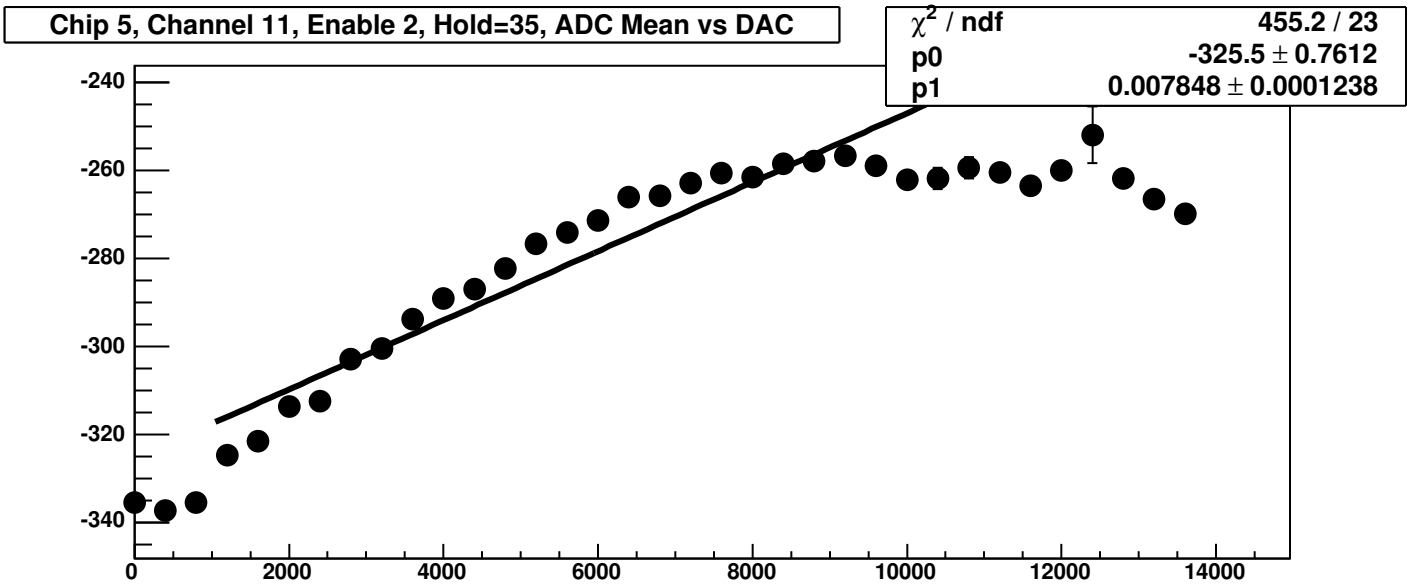

Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

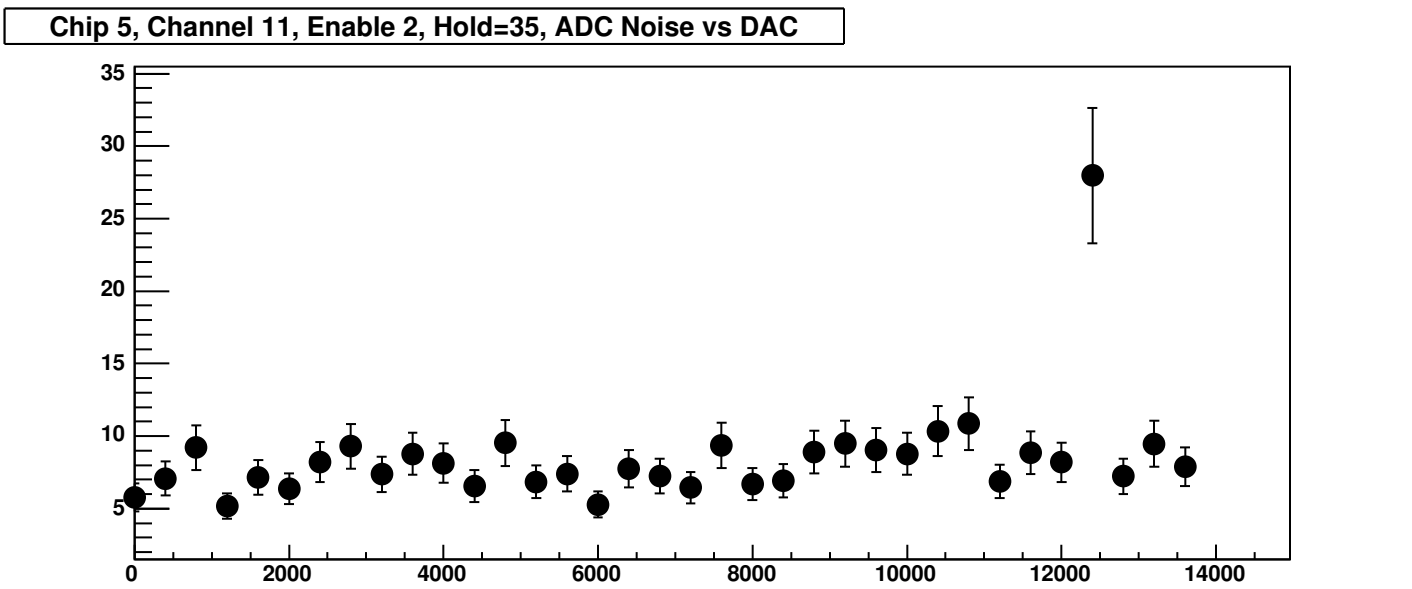

Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

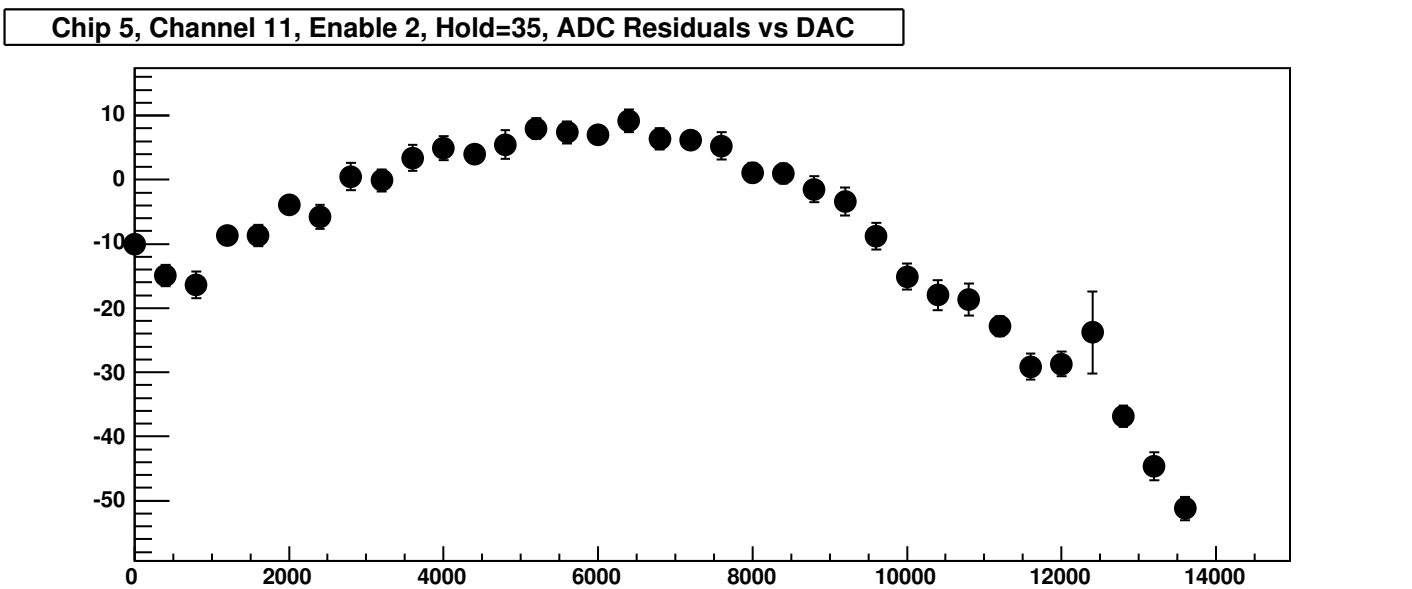

Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 17.27 / 23
p0 -340.4 ± 0.6758
p1 0.02141 ± 0.0001049
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

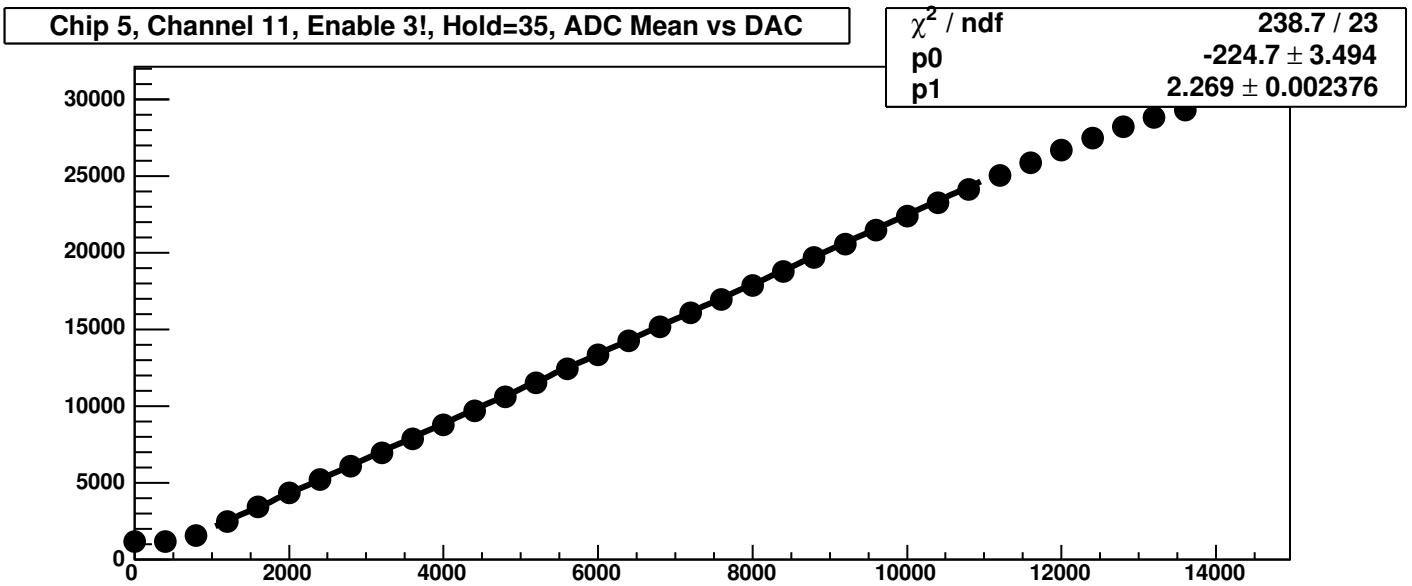

Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

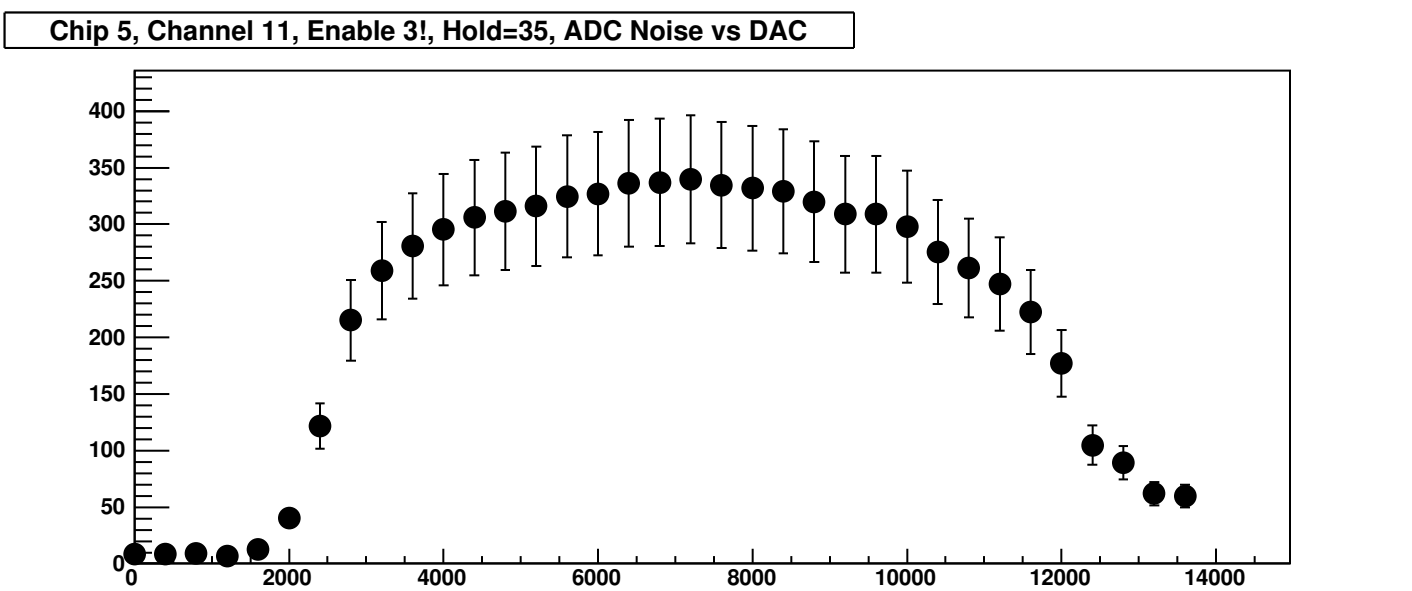

Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

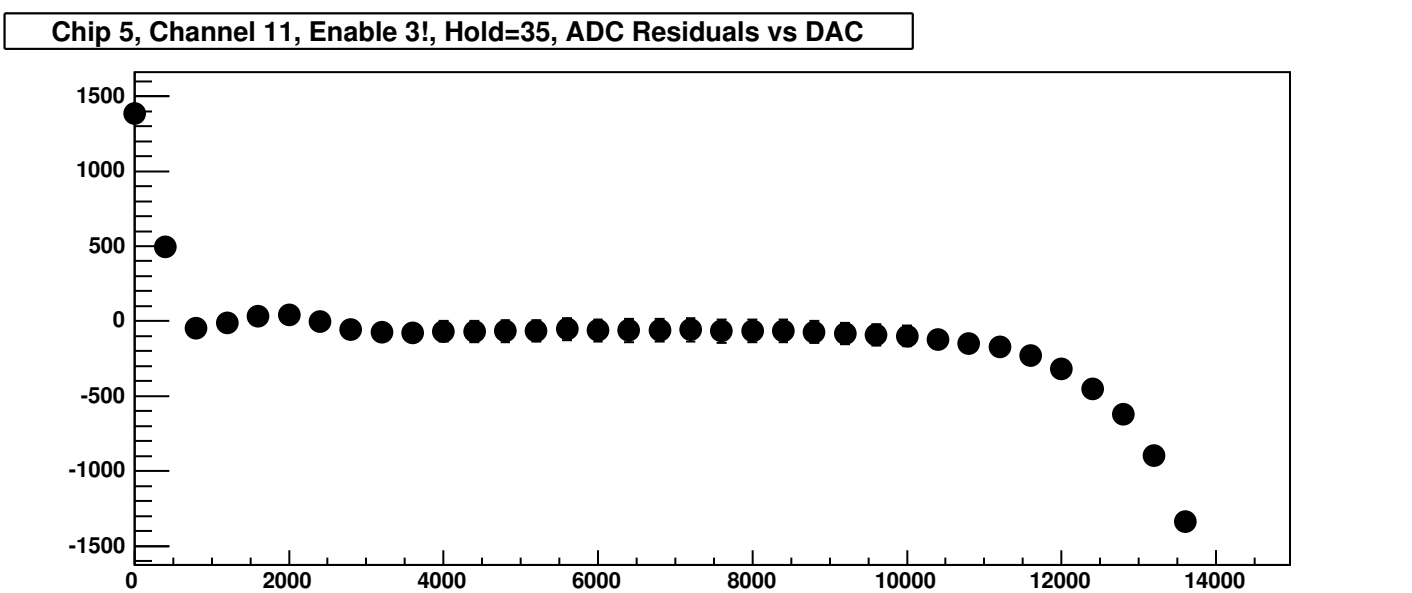

Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 455.2 / 23
p0 -325.5 ± 0.7612
p1 0.007848 ± 0.0001238
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

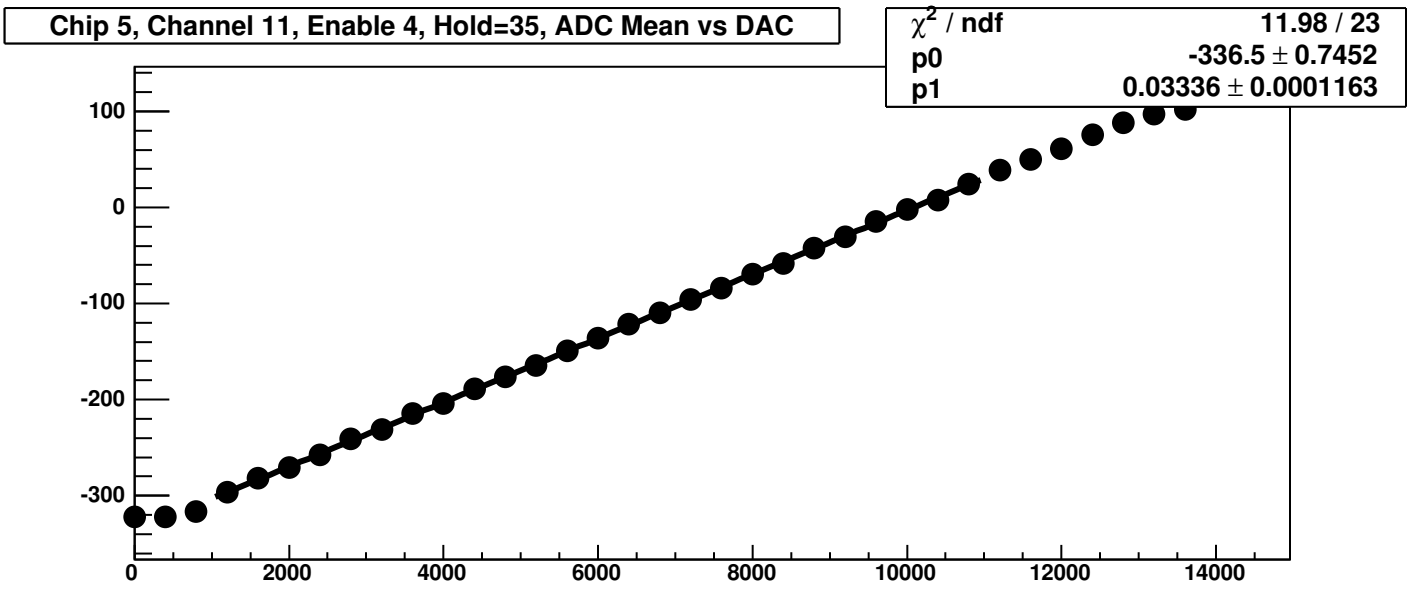

Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

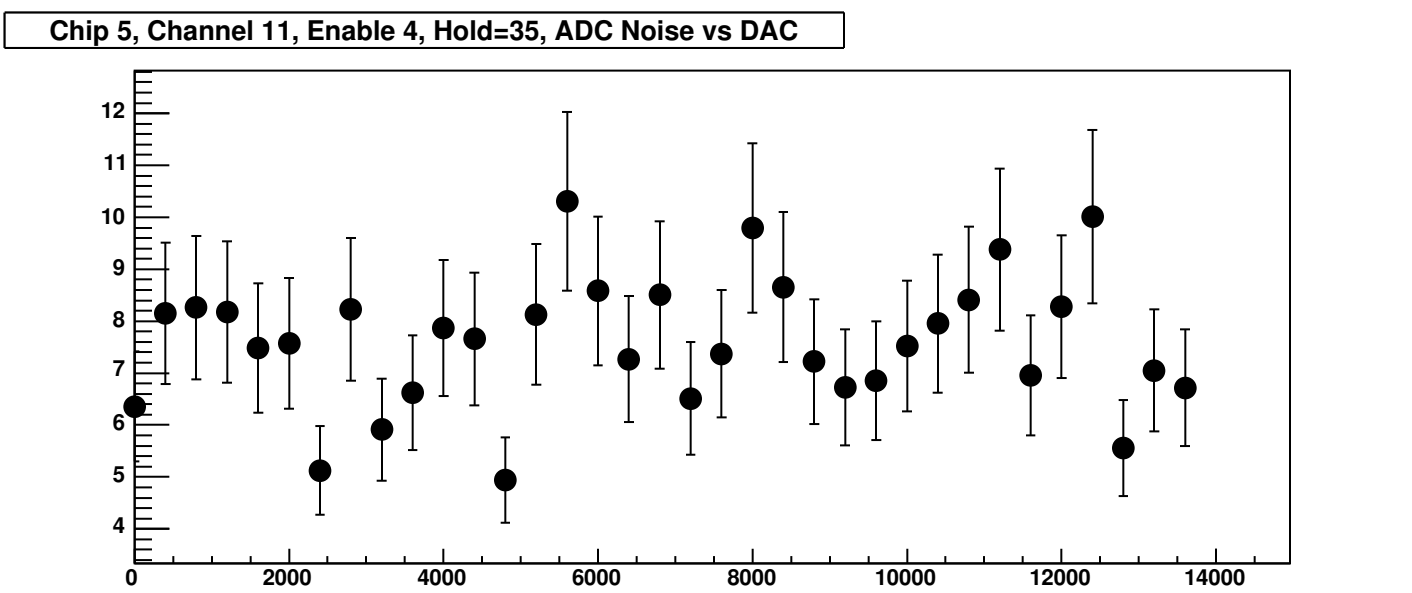

Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

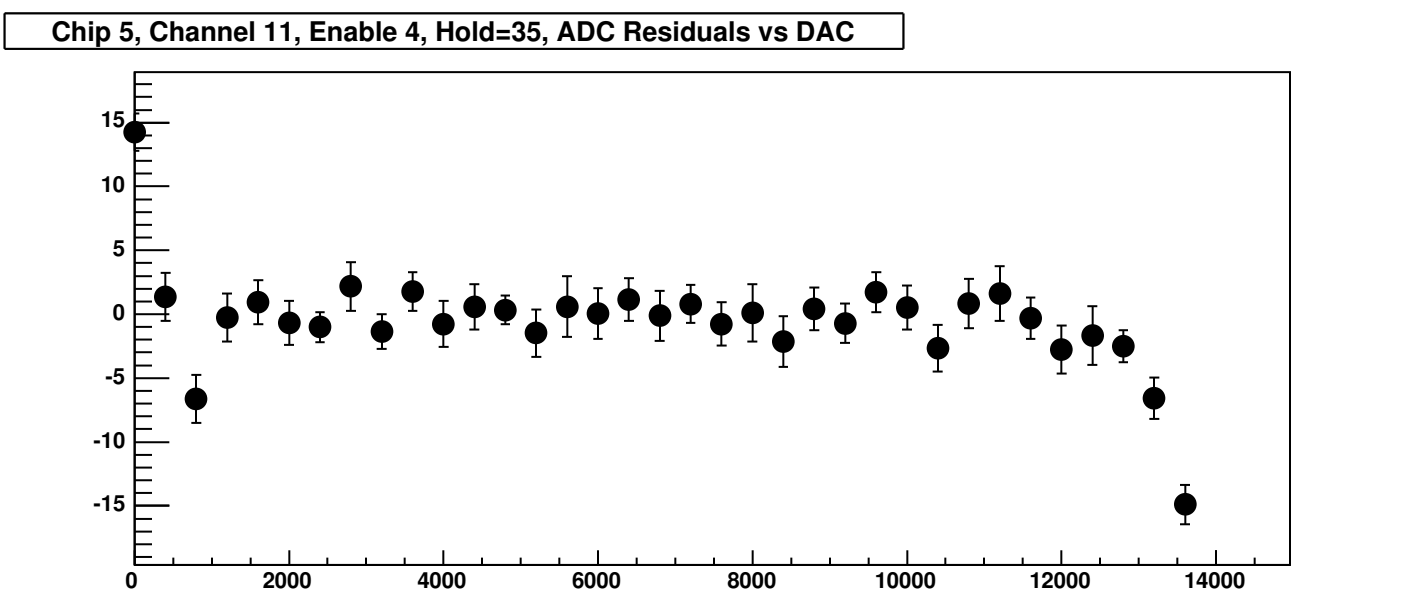

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 238.7 / 23
p0 -224.7 ± 3.494
p1 2.269 ± 0.002376
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

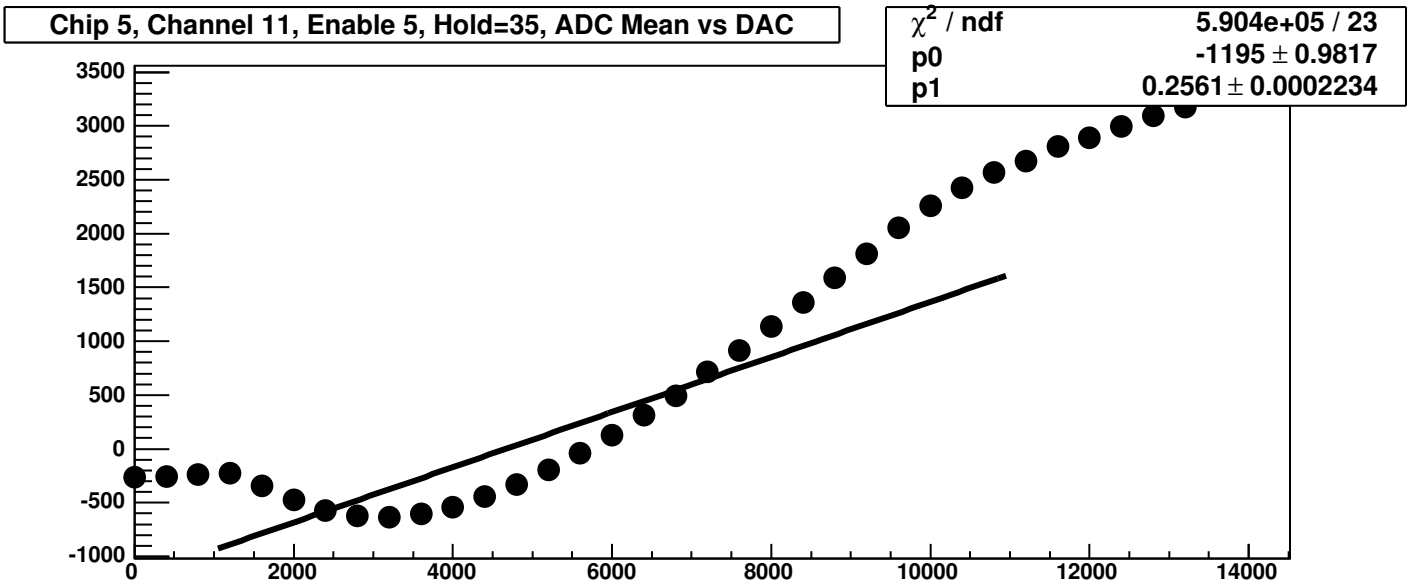

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

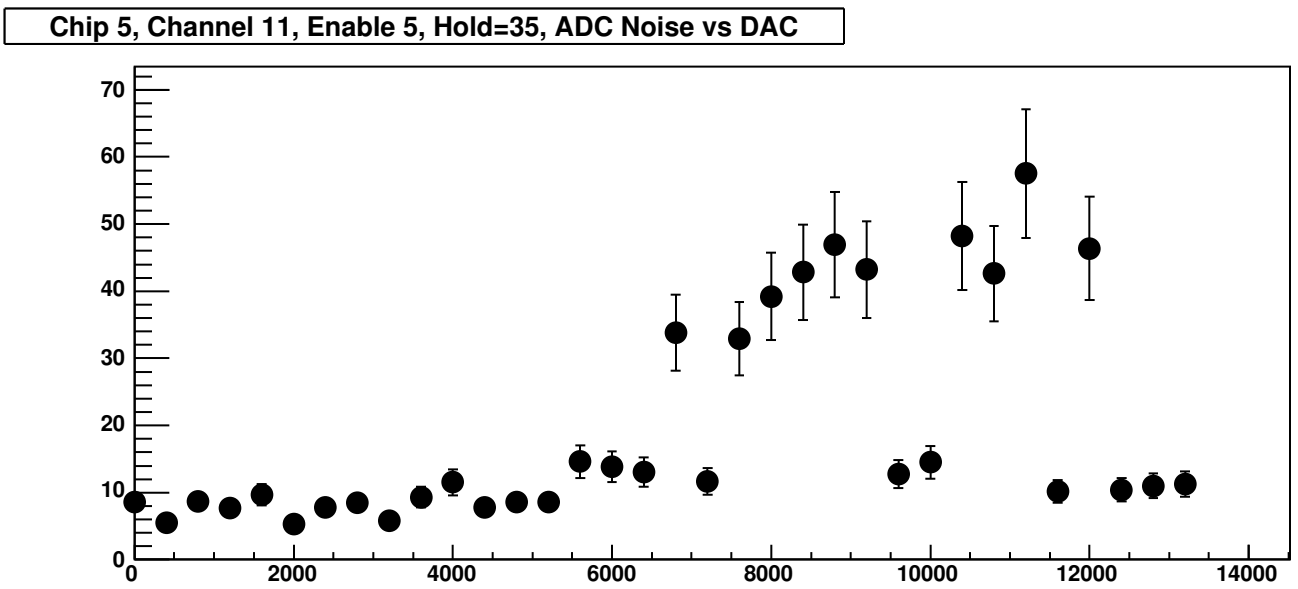

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

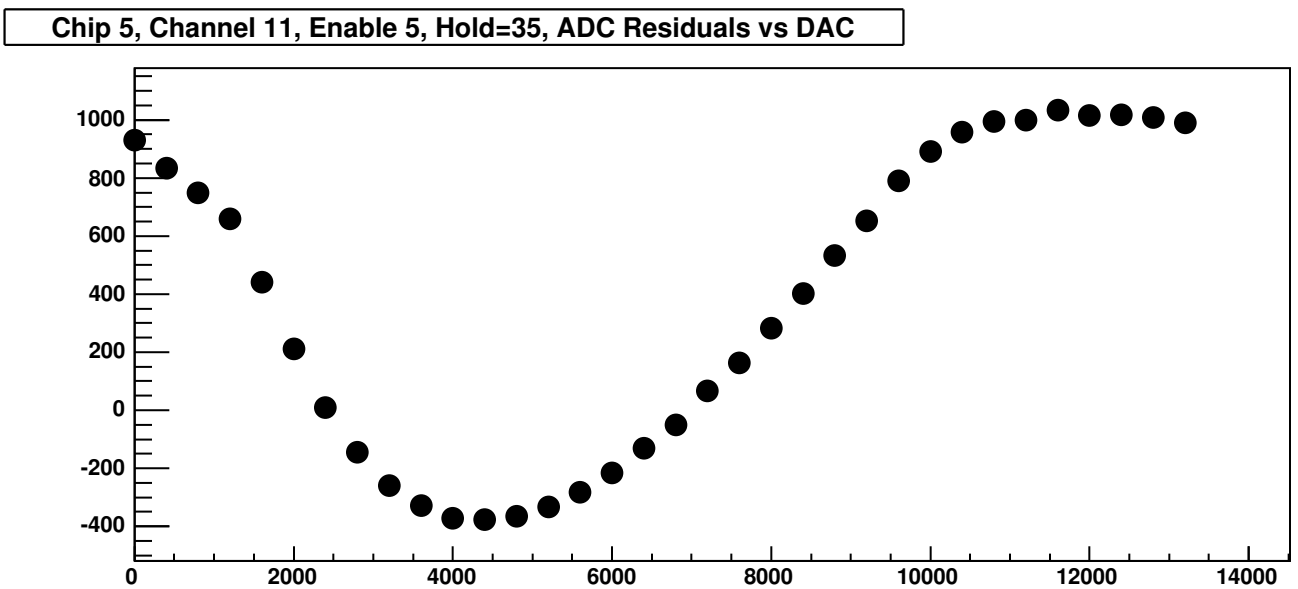

Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.98 / 23
p0 -336.5 ± 0.7452
p1 0.03336 ± 0.0001163
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

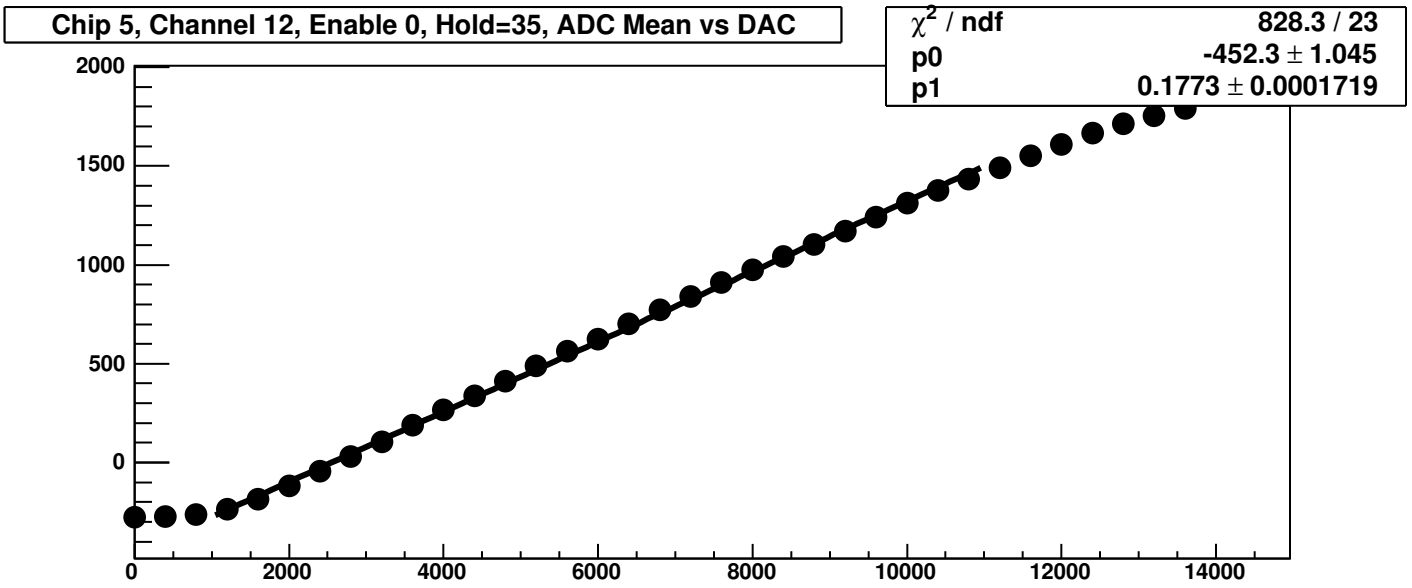

Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

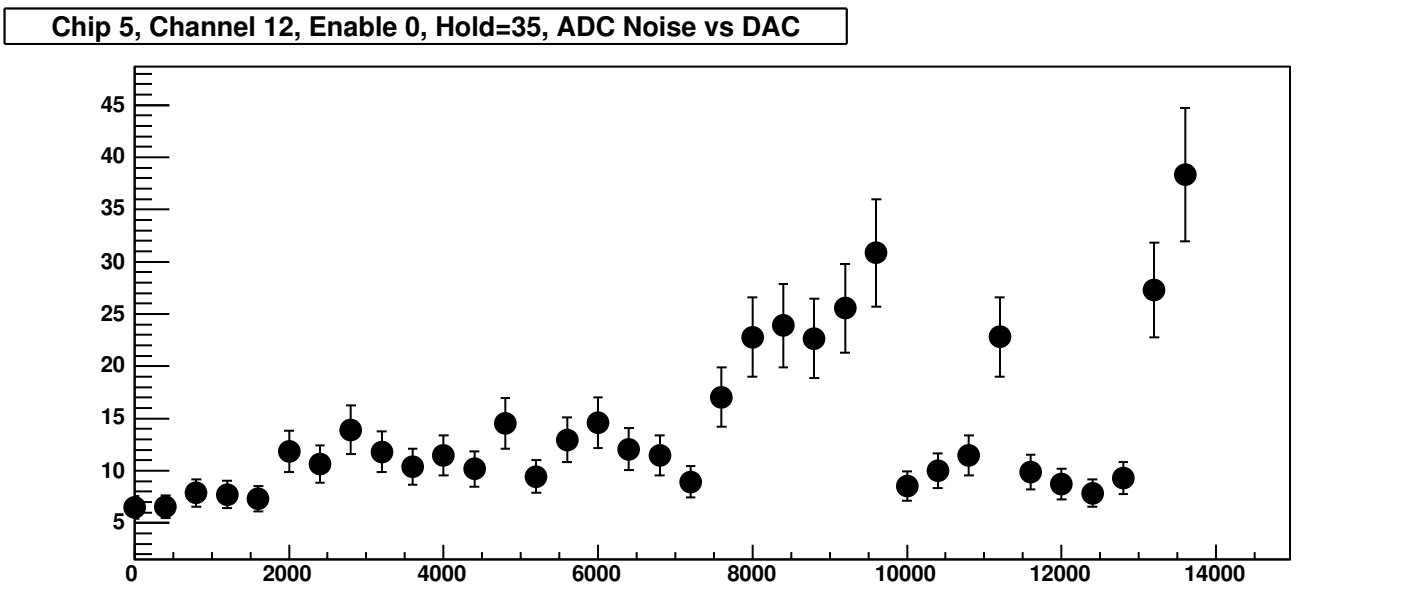

Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

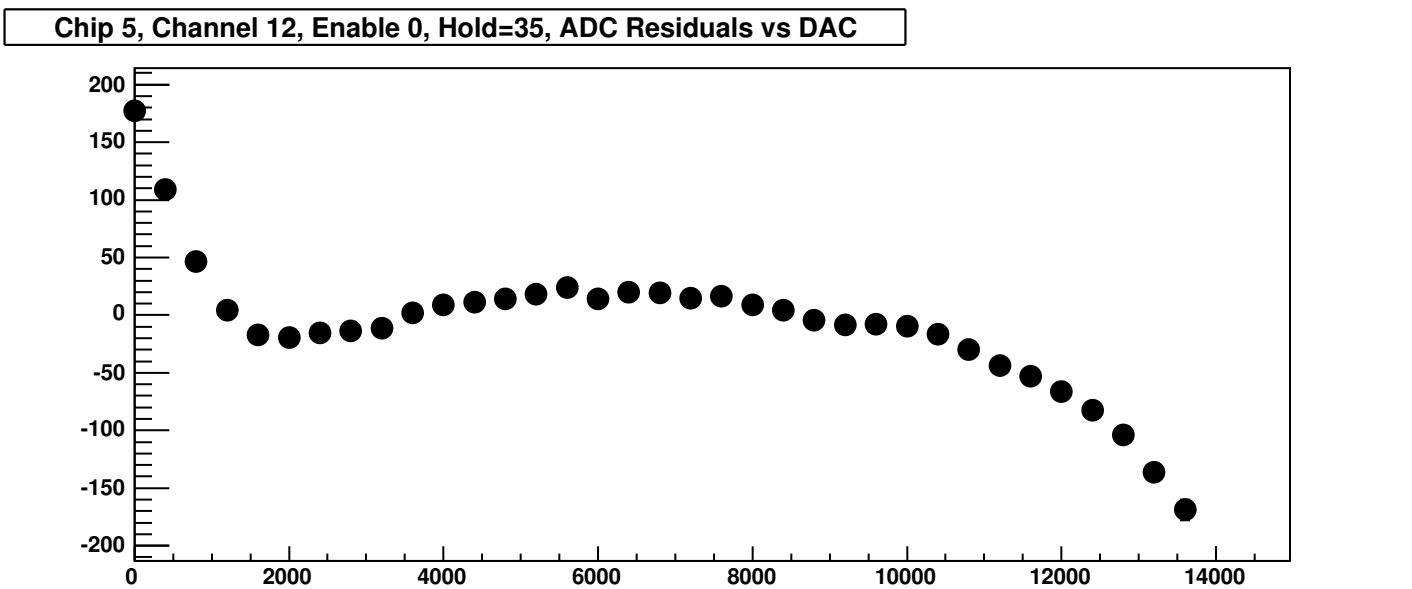

Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.904e+05 / 23
p0 -1195 ± 0.9817
p1 0.2561 ± 0.0002234
3500
3000
2500
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

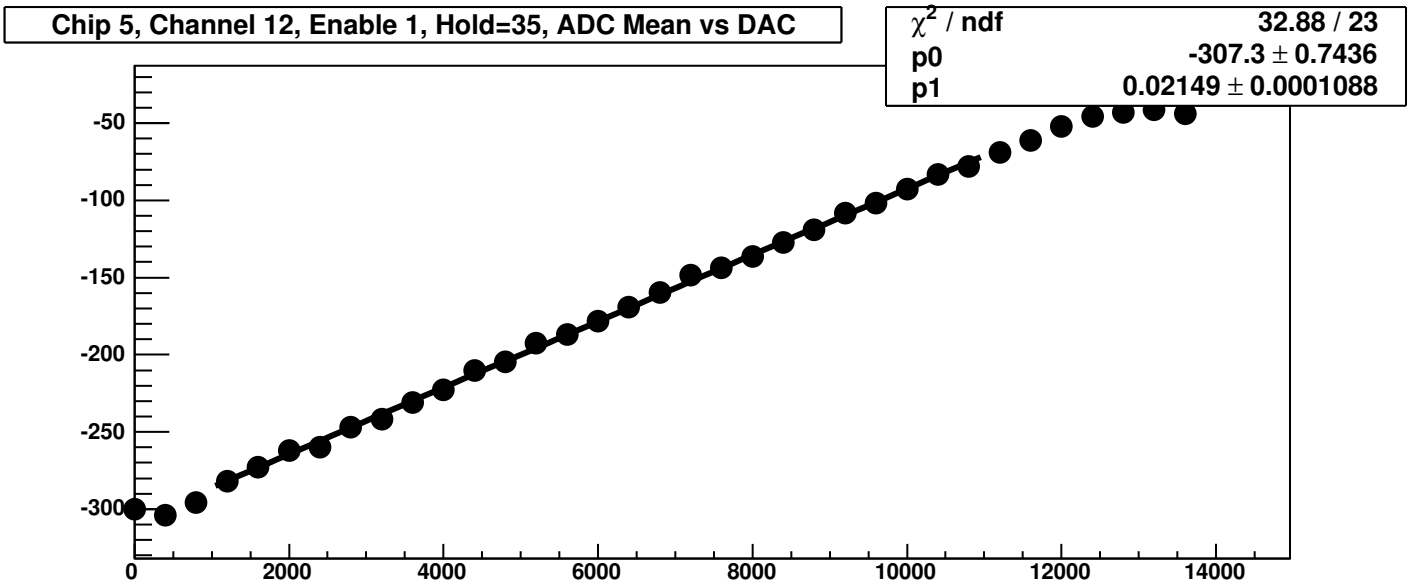

Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

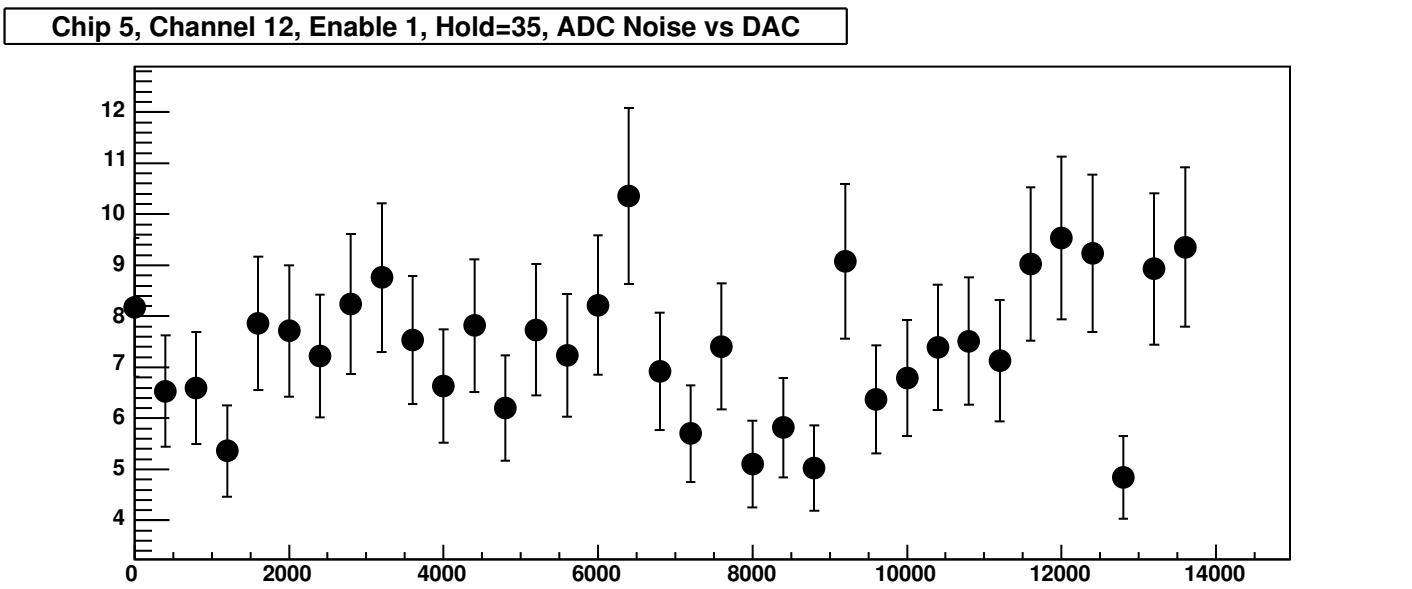

Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

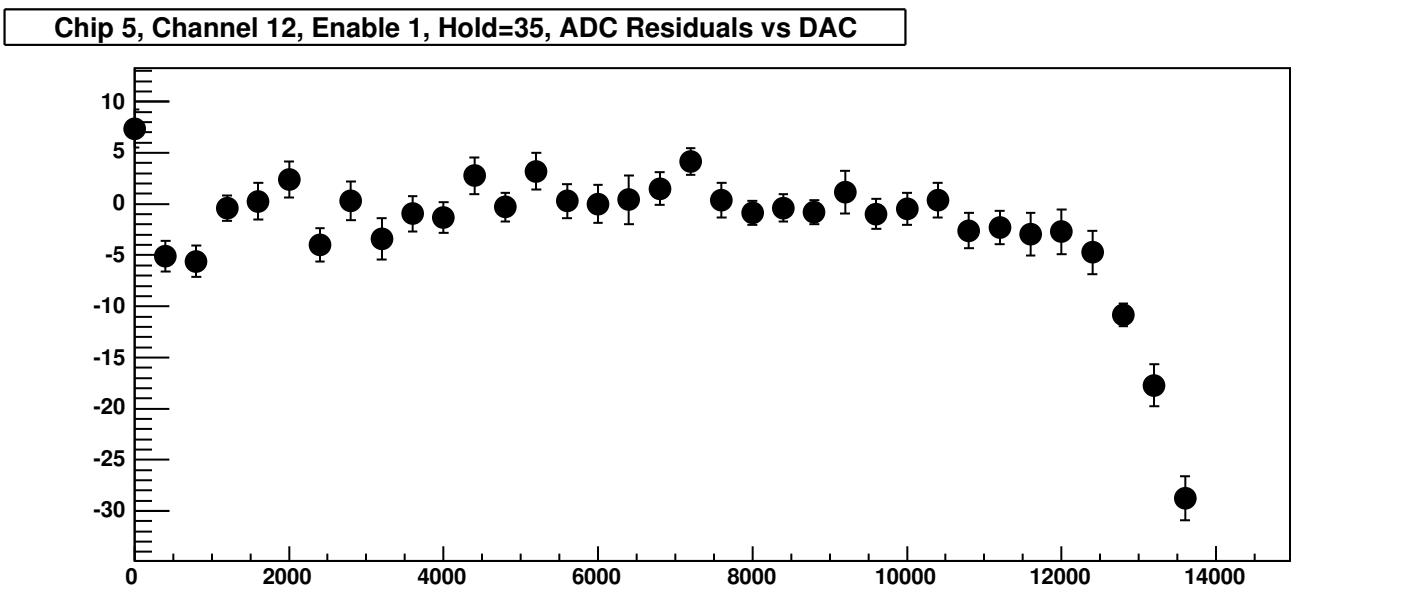

Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 828.3 / 23
p0 -452.3 ± 1.045
p1 0.1773 ± 0.0001719
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

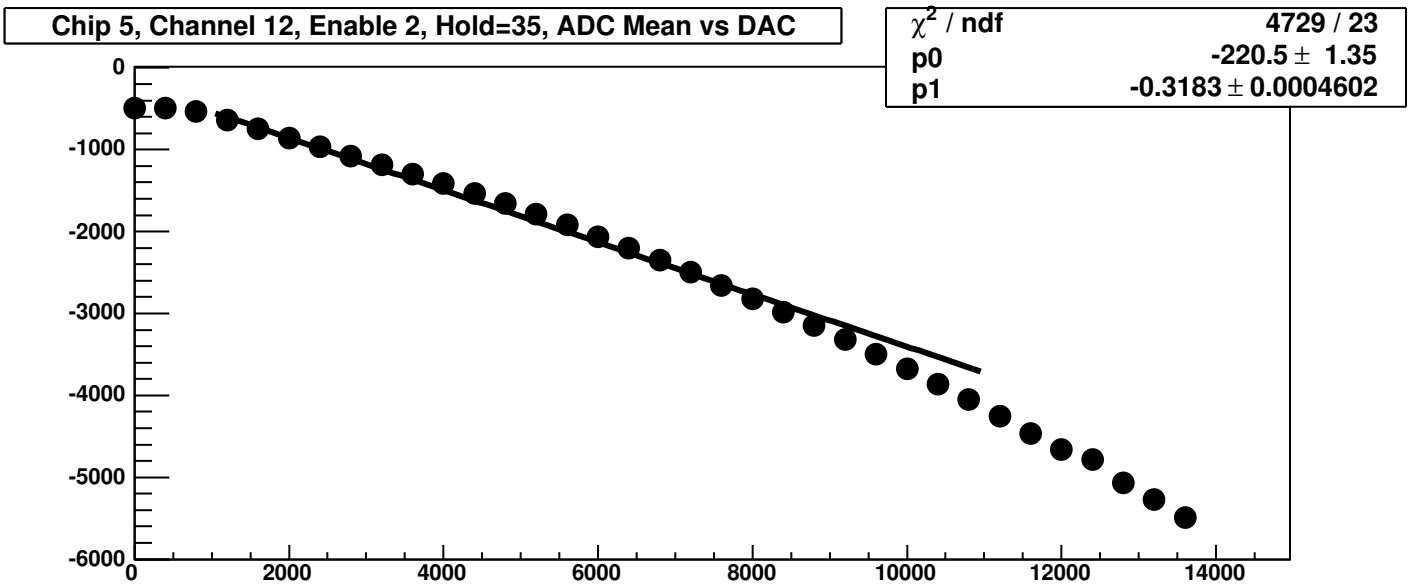

Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

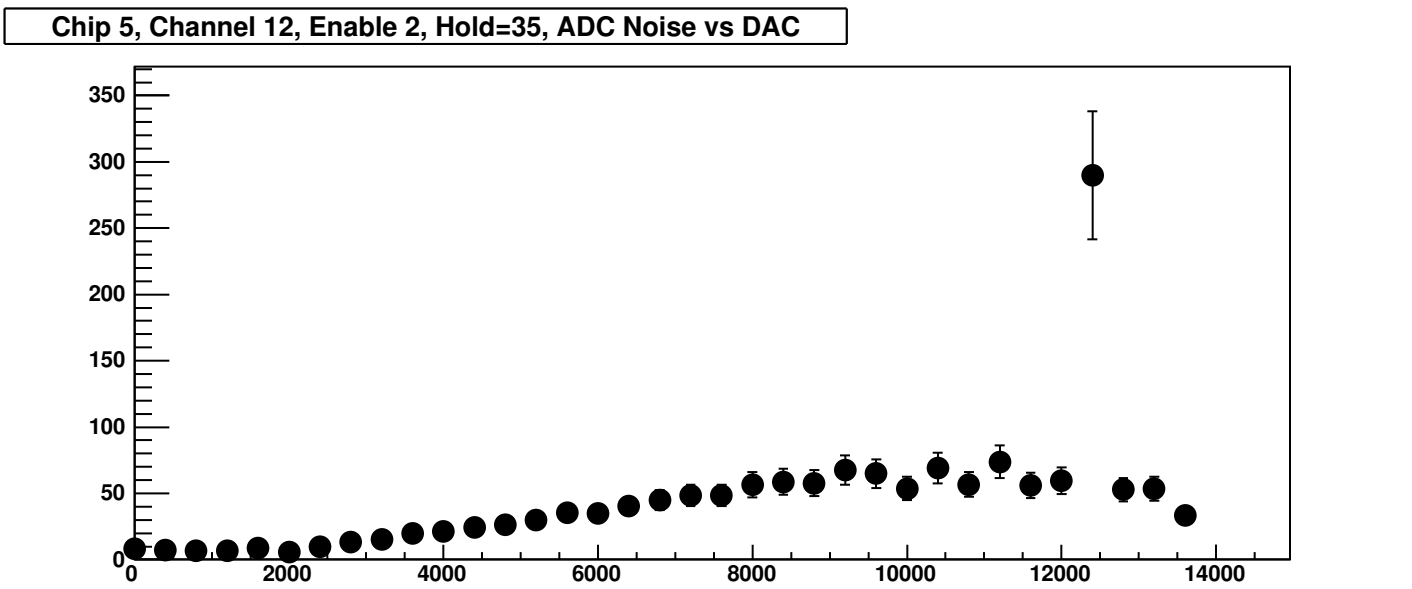

Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

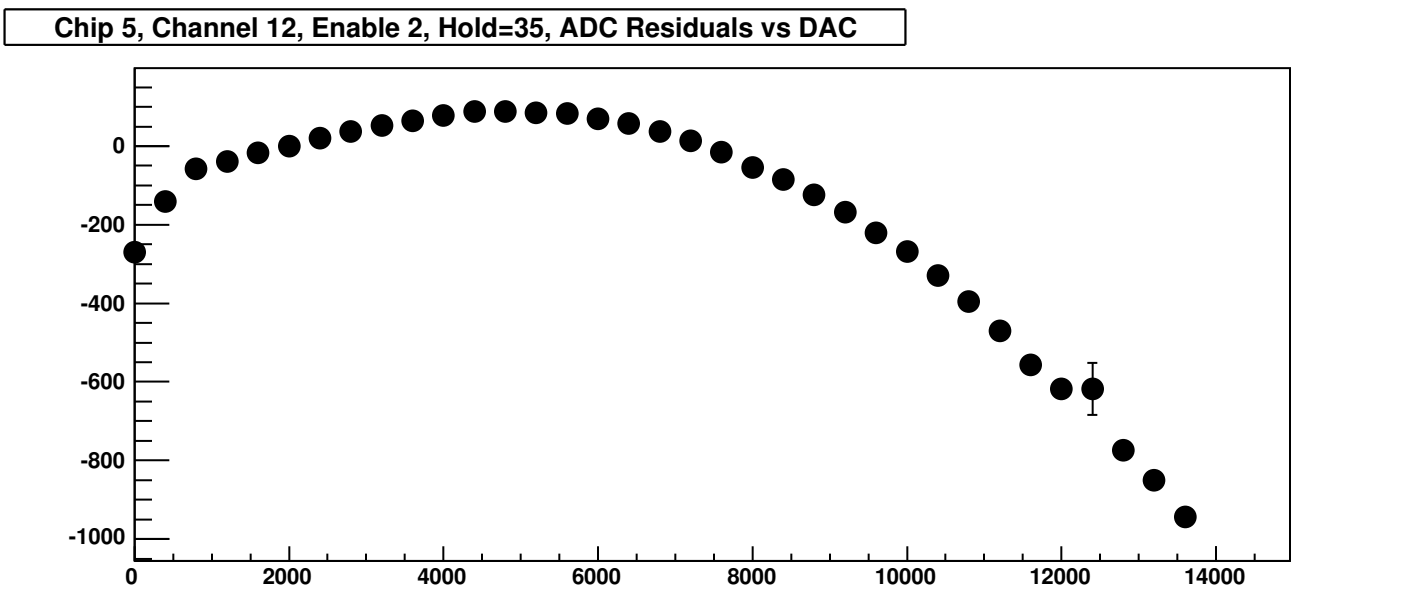

Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.88 / 23
p0 -307.3 ± 0.7436
p1 0.02149 ± 0.0001088
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

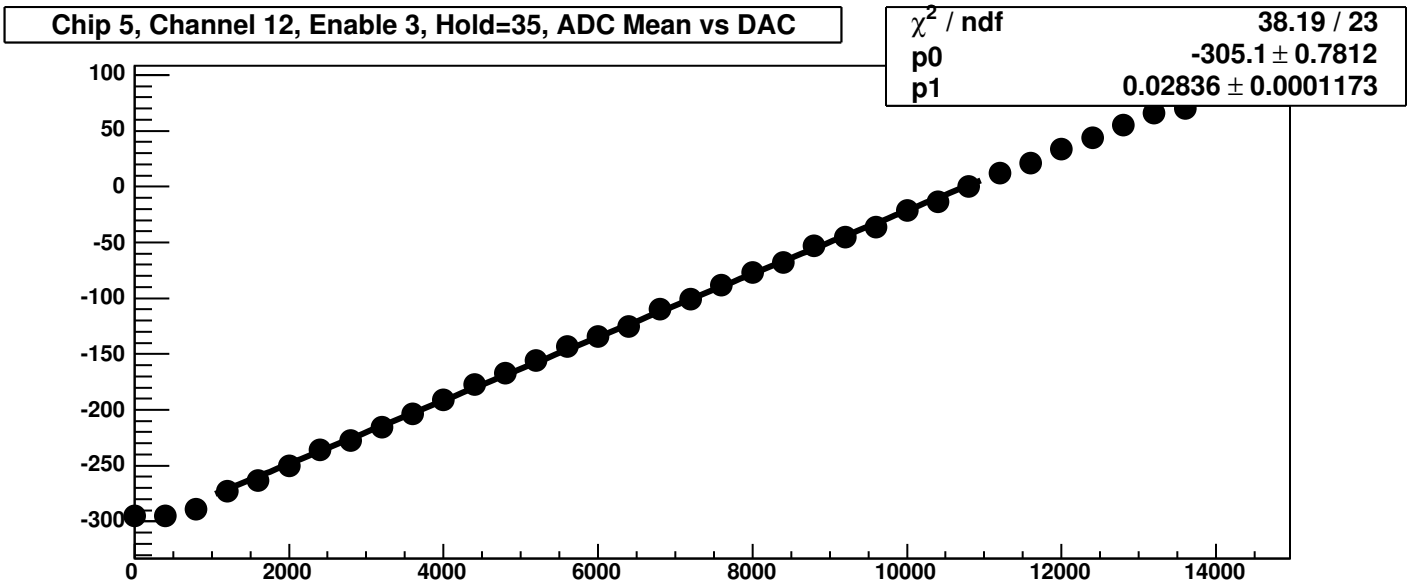

Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

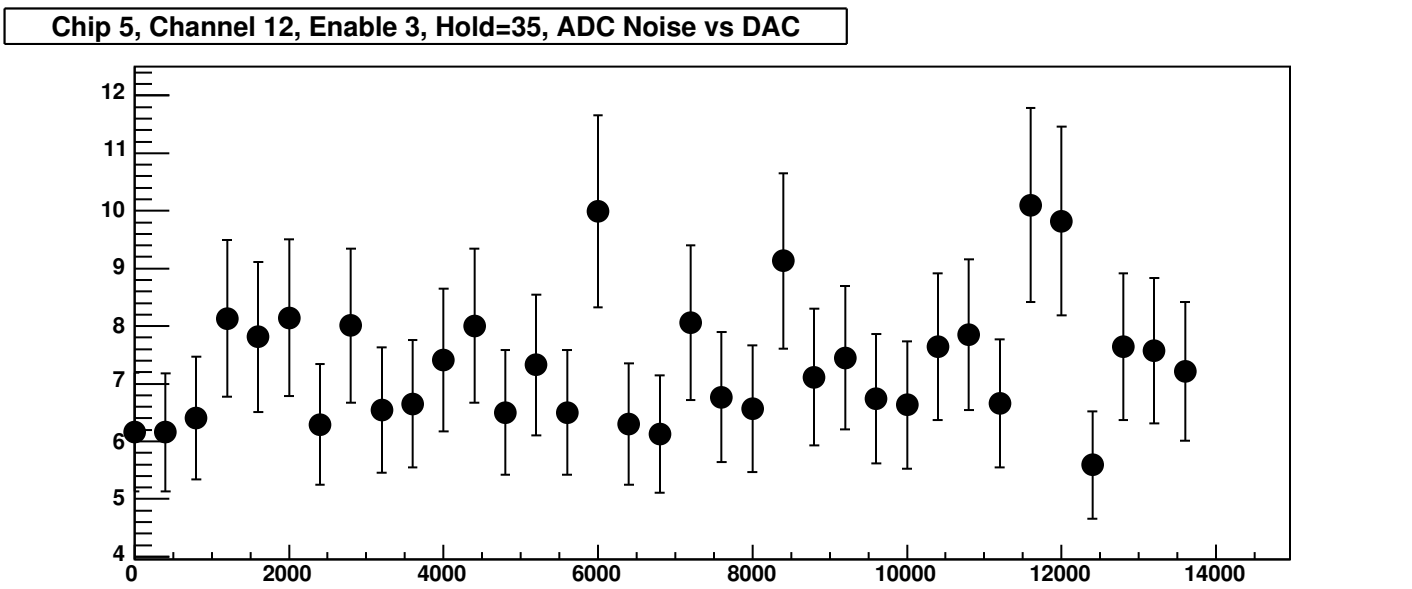

Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

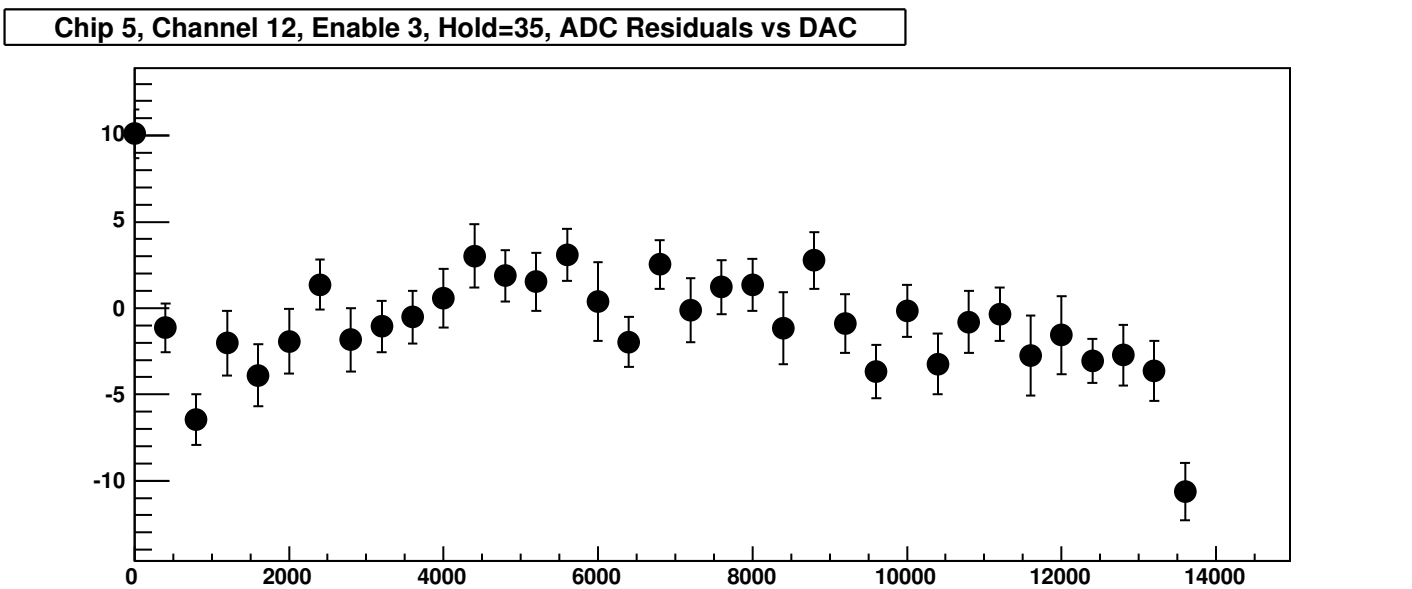

Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4729 / 23
p0 -220.5 ± 1.35
p1 -0.3183 ± 0.0004602
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

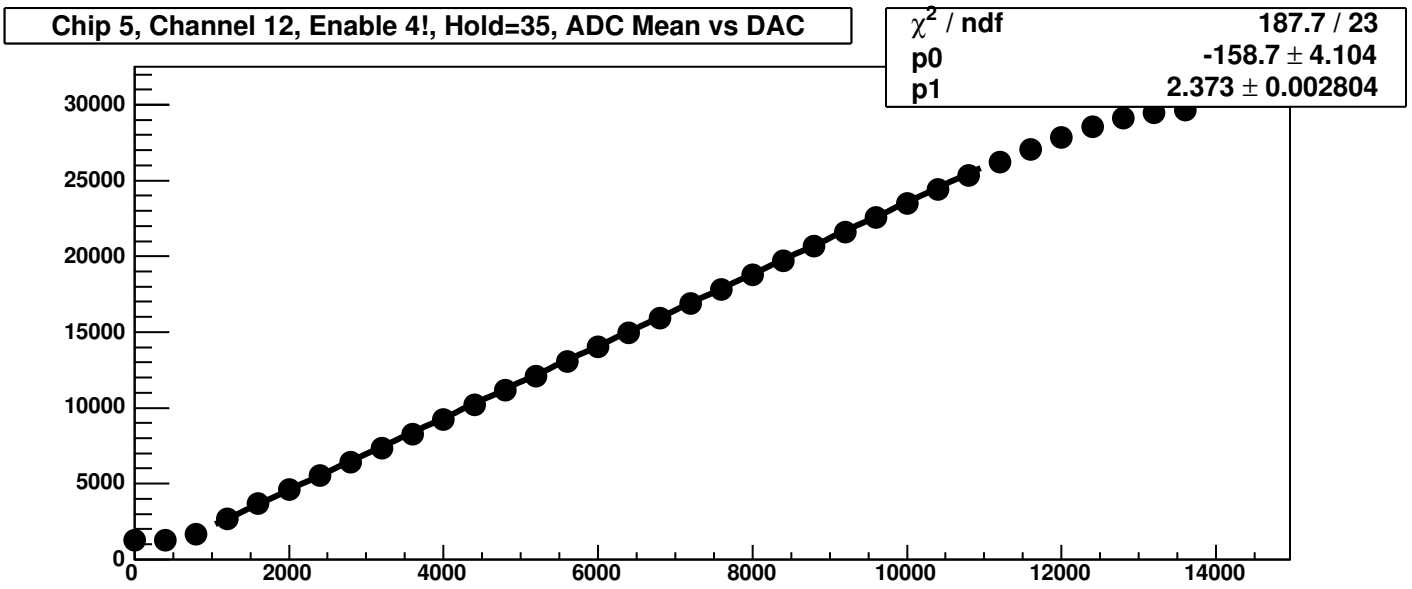

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

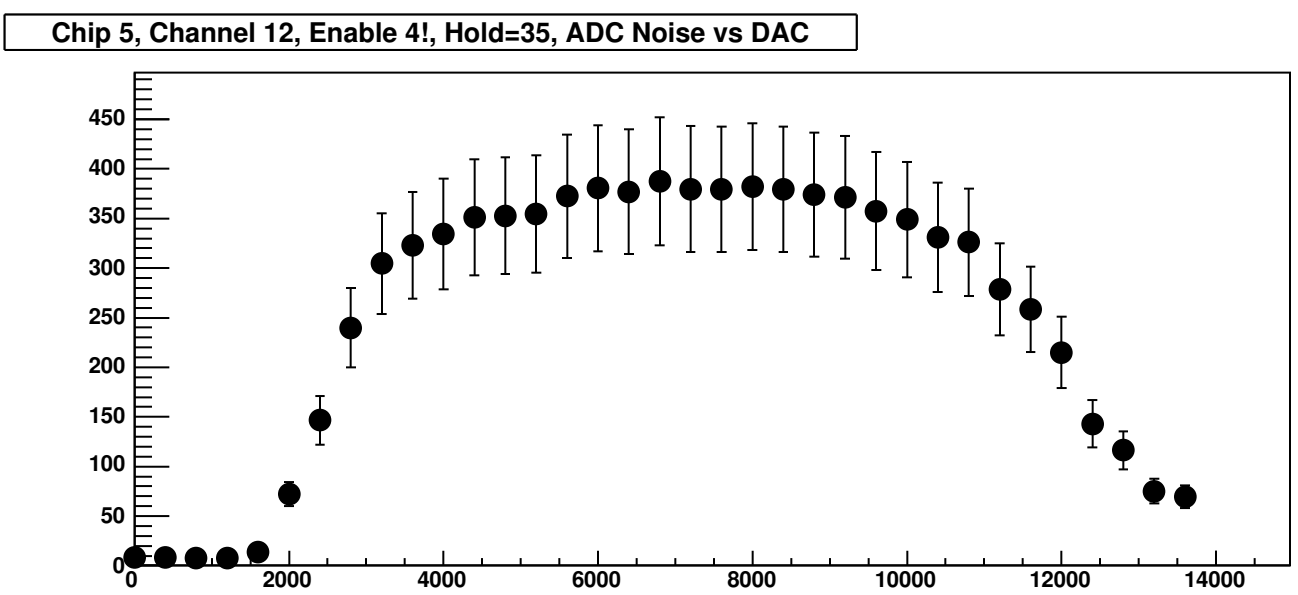

Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

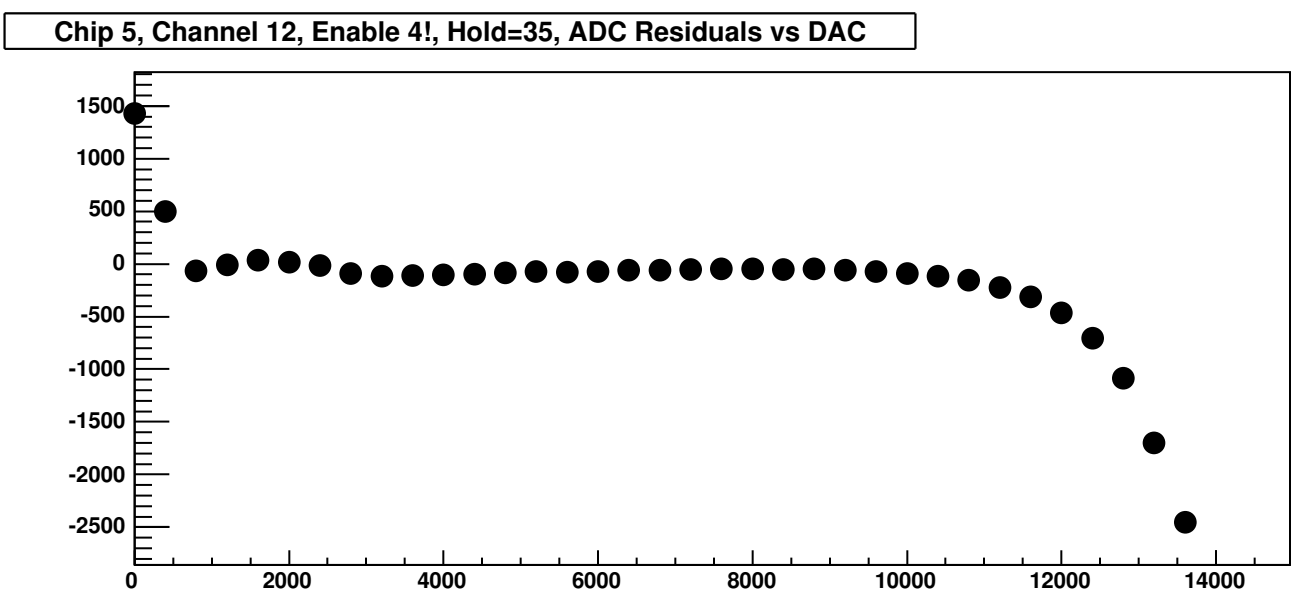

Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.19 / 23
p0 -305.1 ± 0.7812
p1 0.02836 ± 0.0001173
100
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

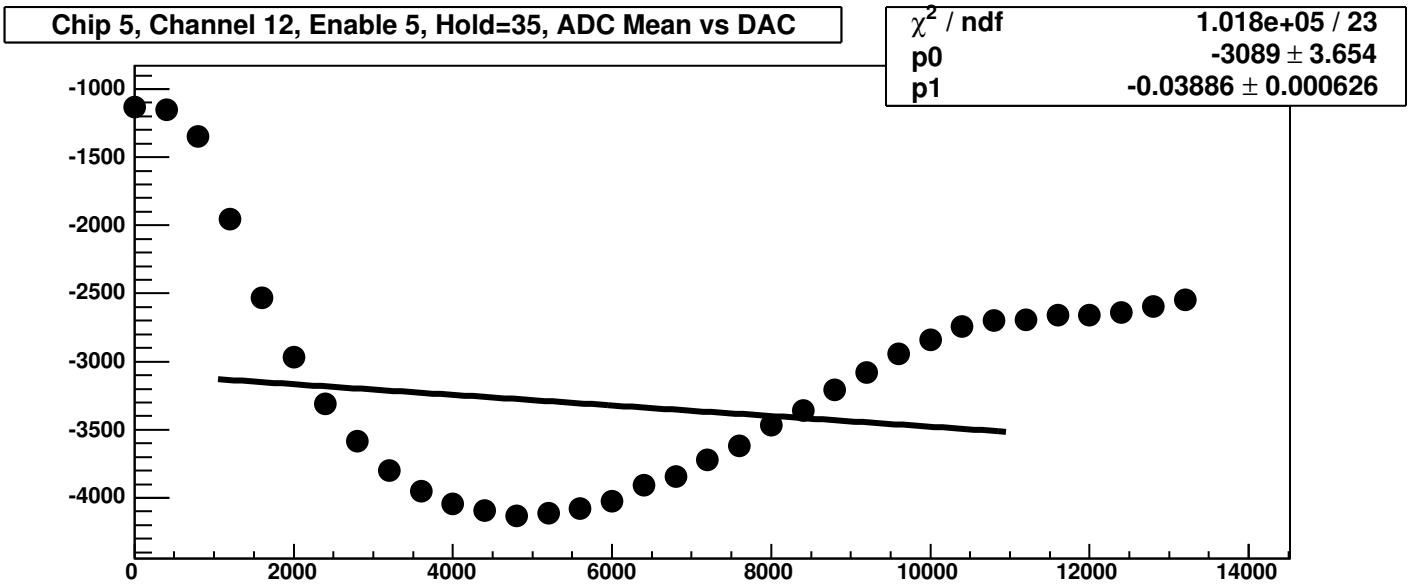

Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

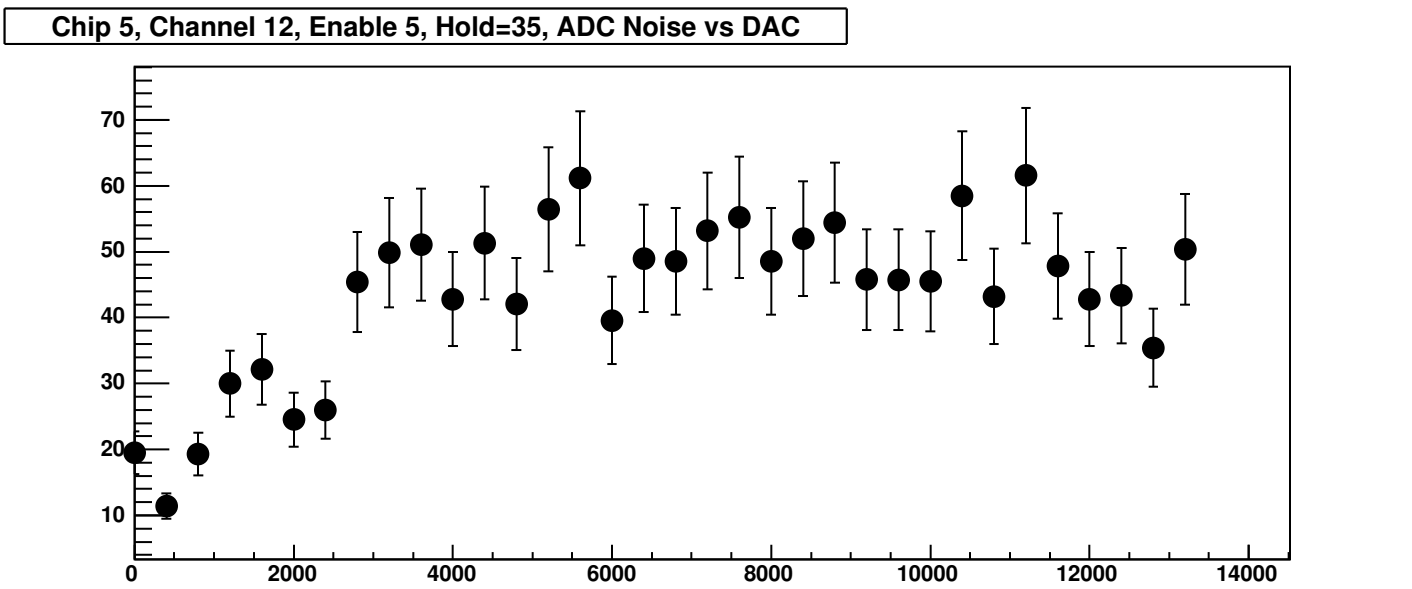

Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

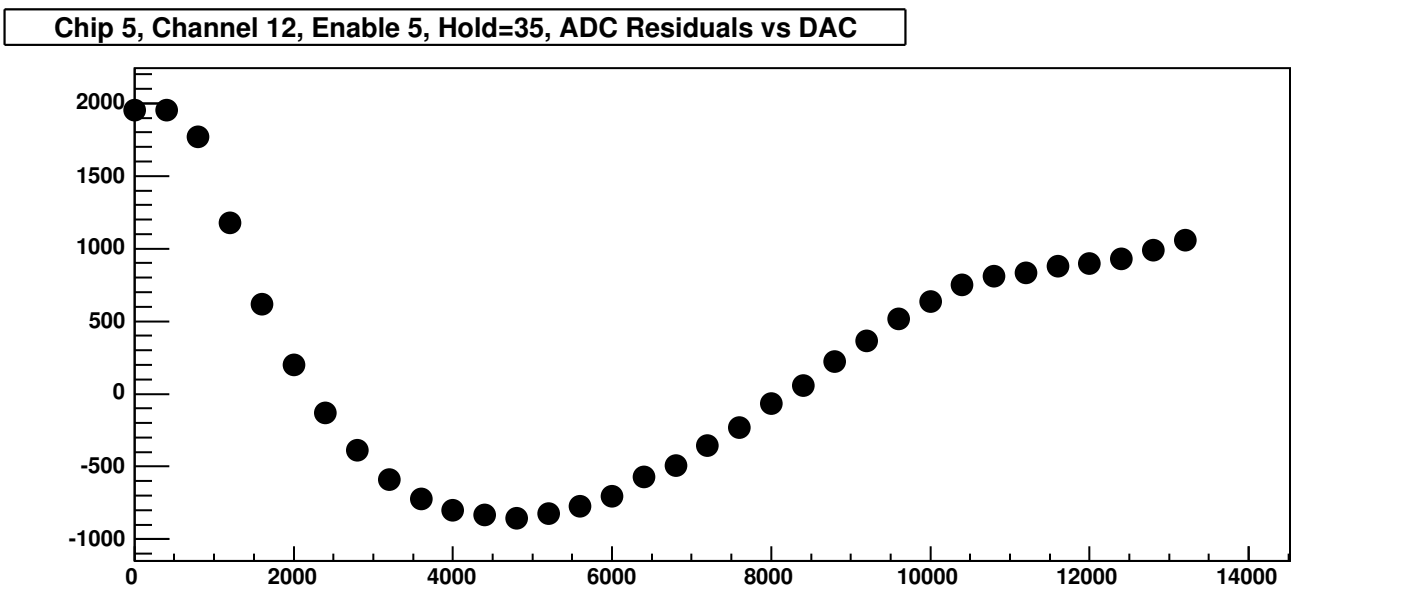

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 187.7 / 23
p0 -158.7 ± 4.104
p1 2.373 ± 0.002804
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

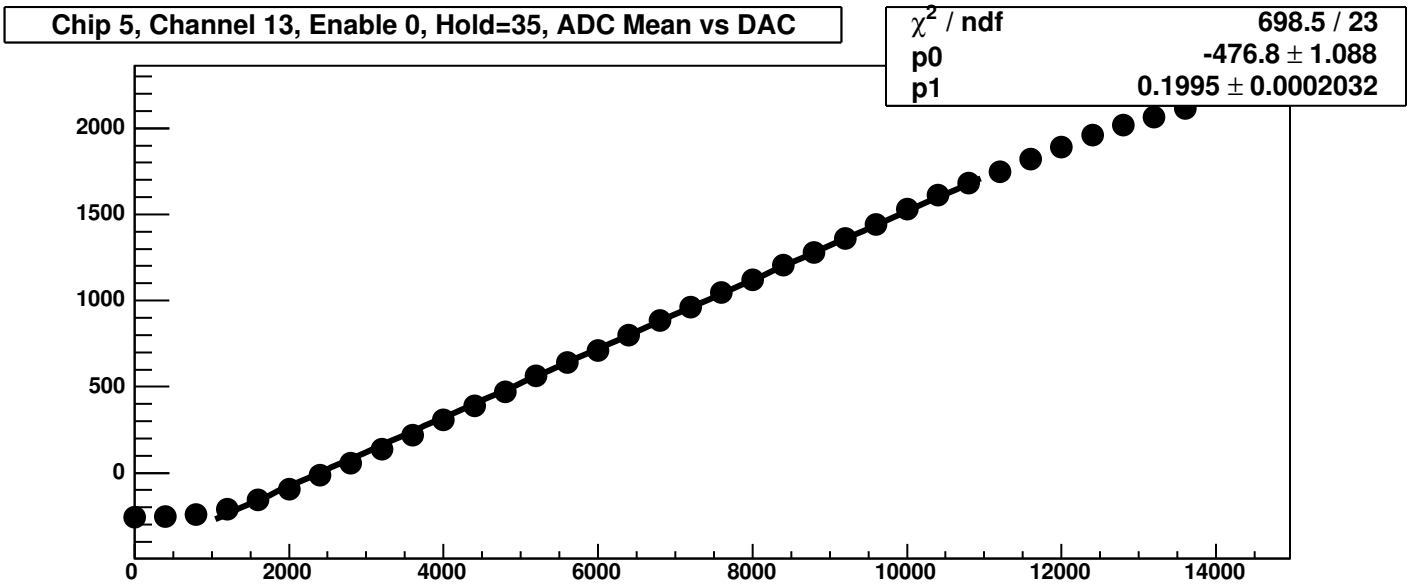

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

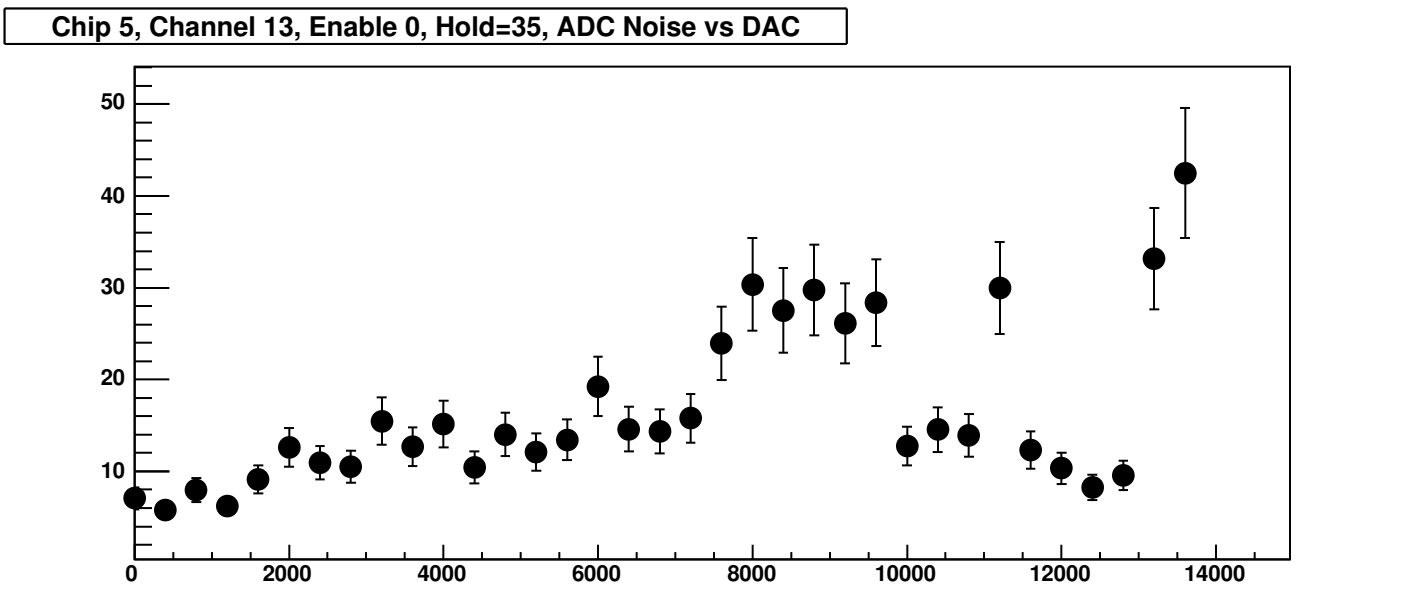

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

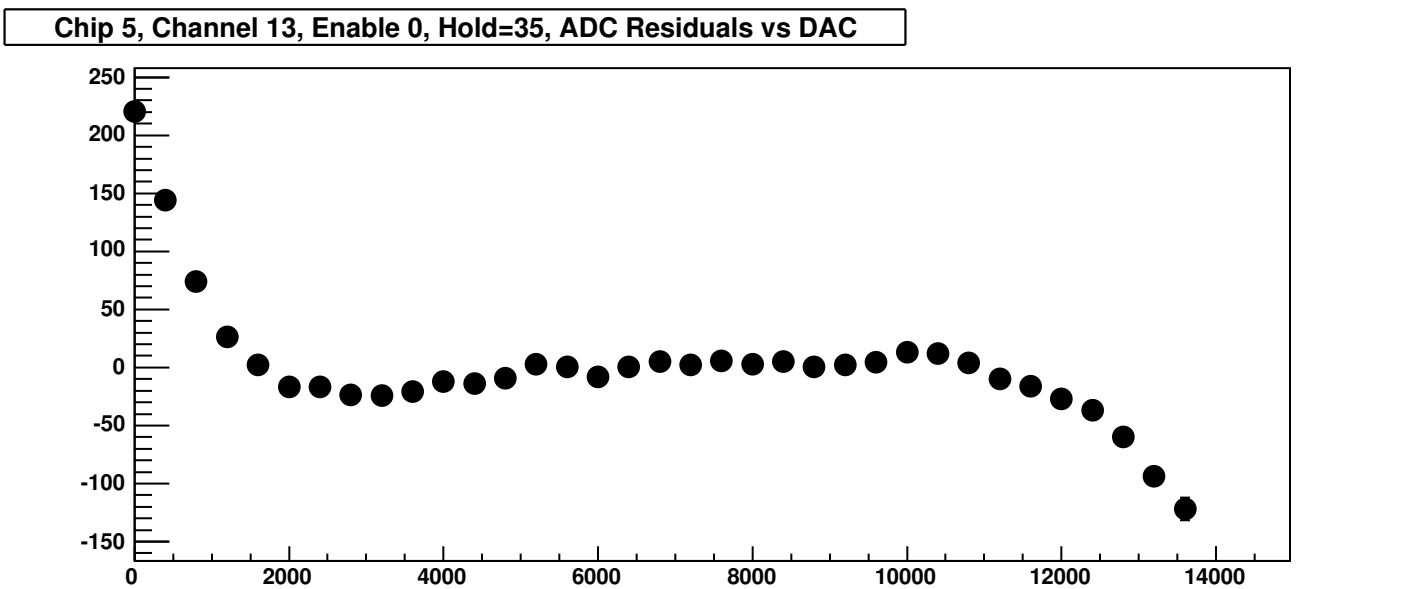

Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.018e+05 / 23
p0 -3089 ± 3.654
p1 -0.03886 ± 0.000626
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
2000
4000
6000
8000
10000
12000
14000

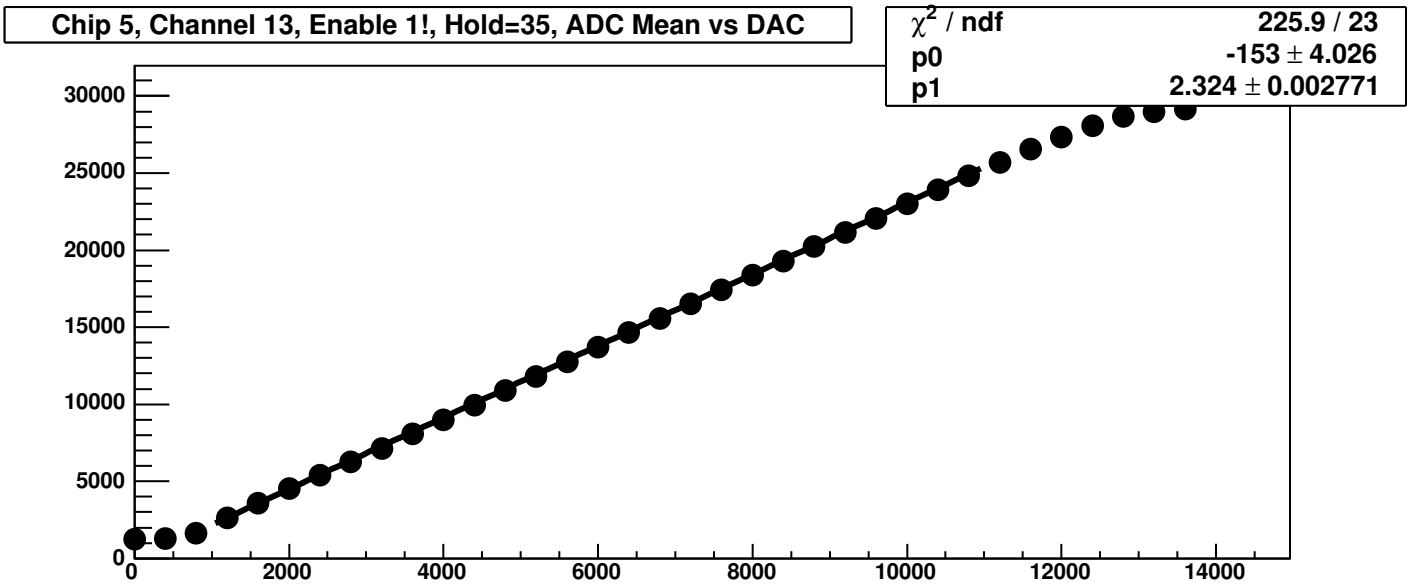

Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

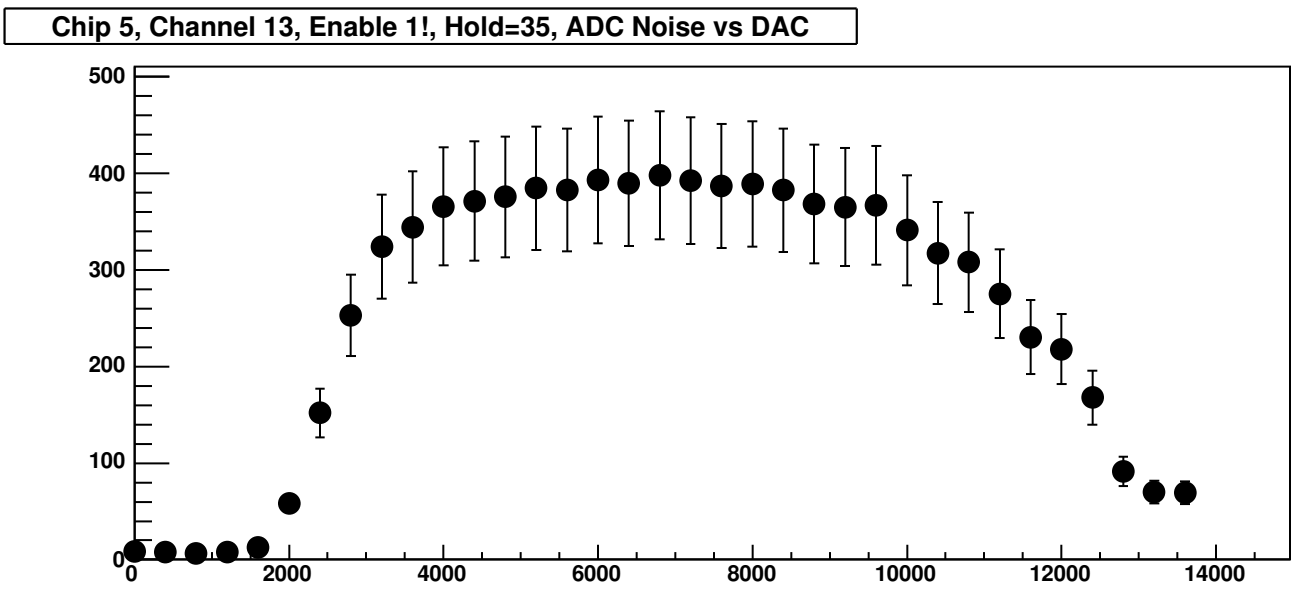

Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

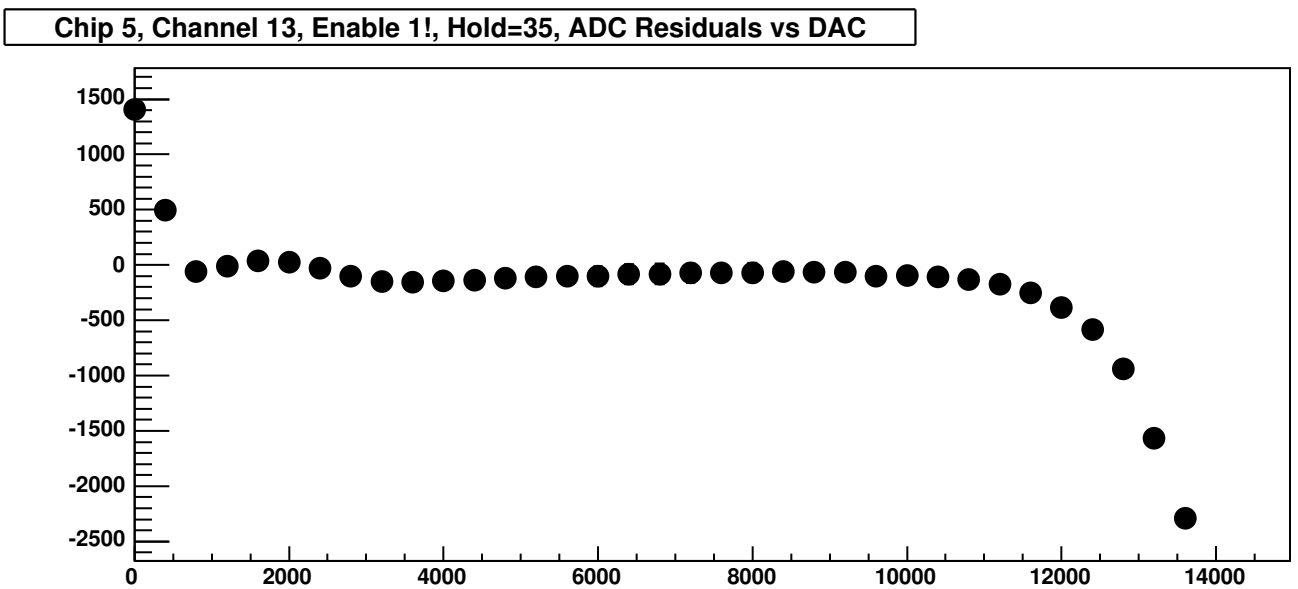

Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 698.5 / 23
p0 -476.8 ± 1.088
p1 0.1995 ± 0.0002032
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

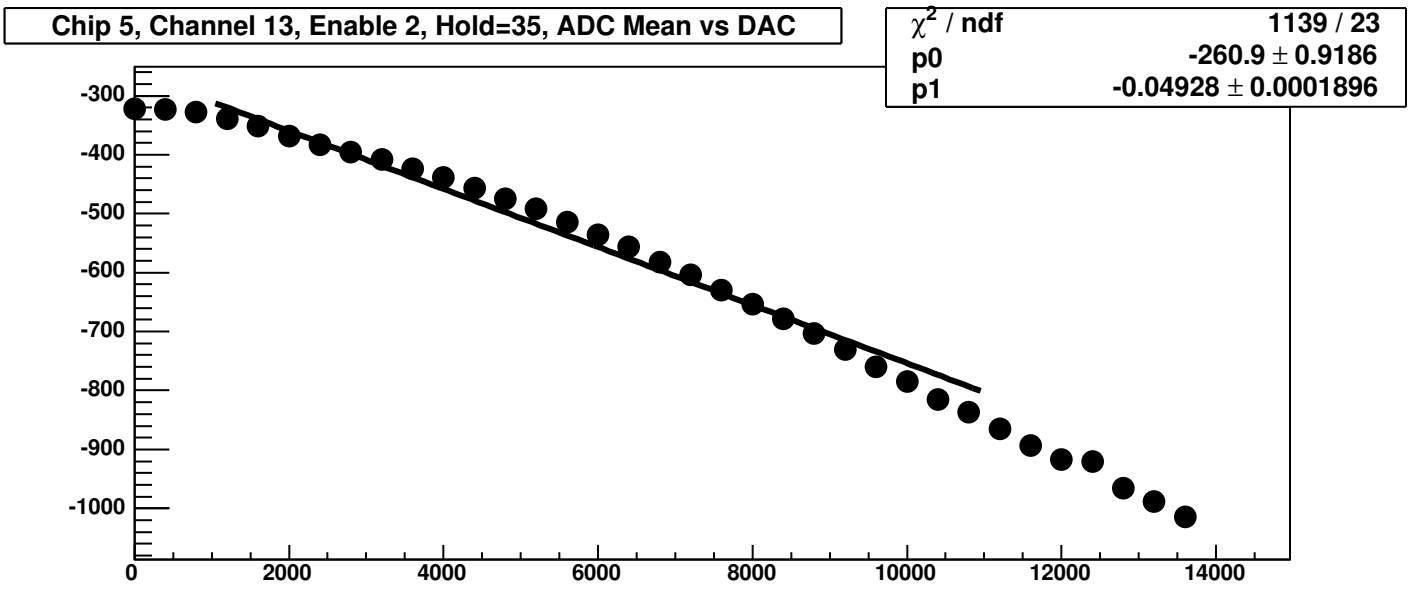

Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

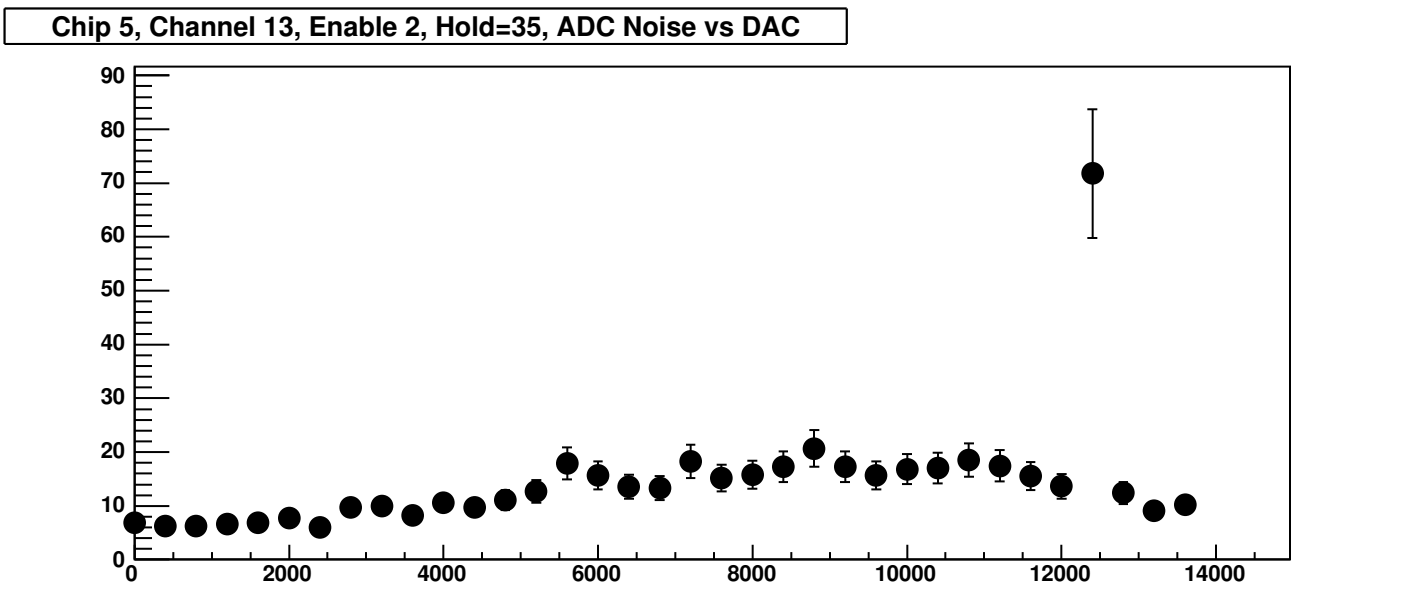

Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

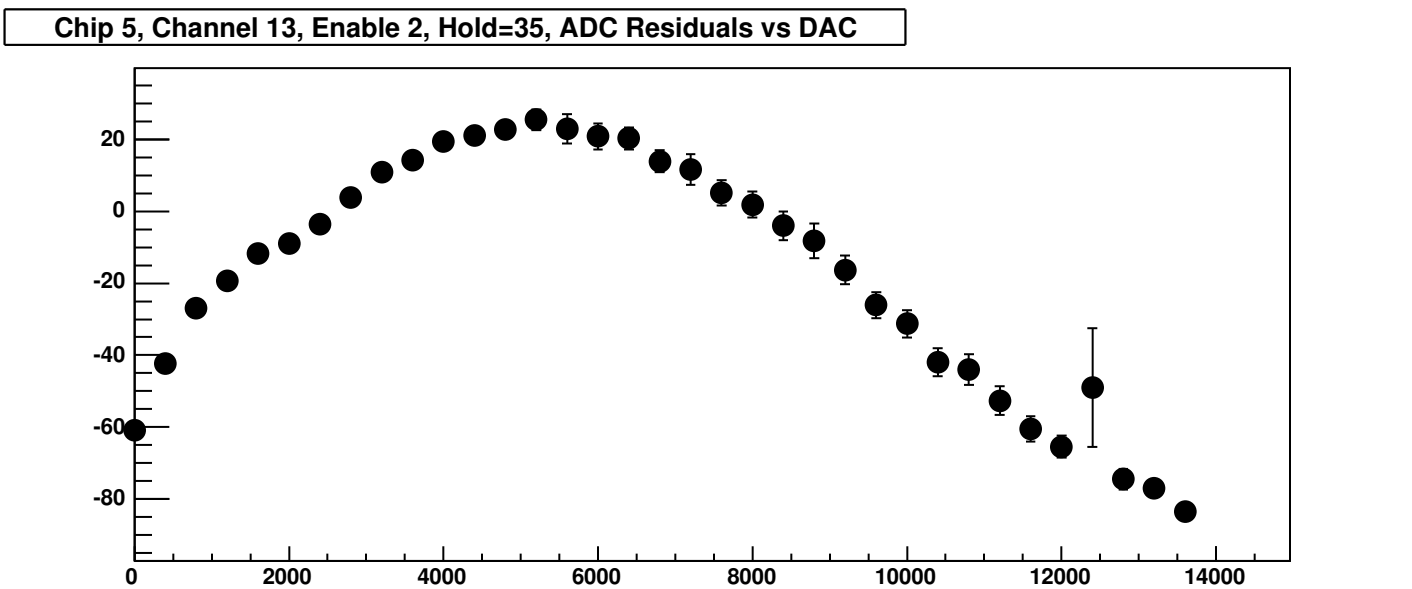

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 225.9 / 23
p0 -153 ± 4.026
p1 2.324 ± 0.002771
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

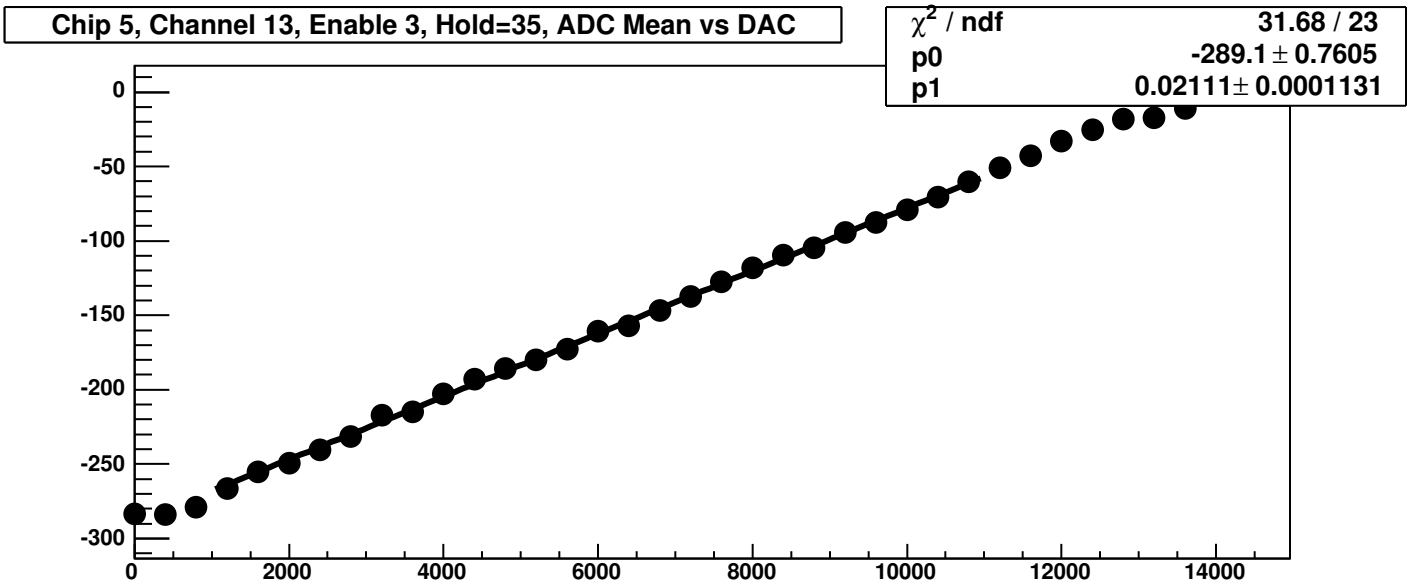

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

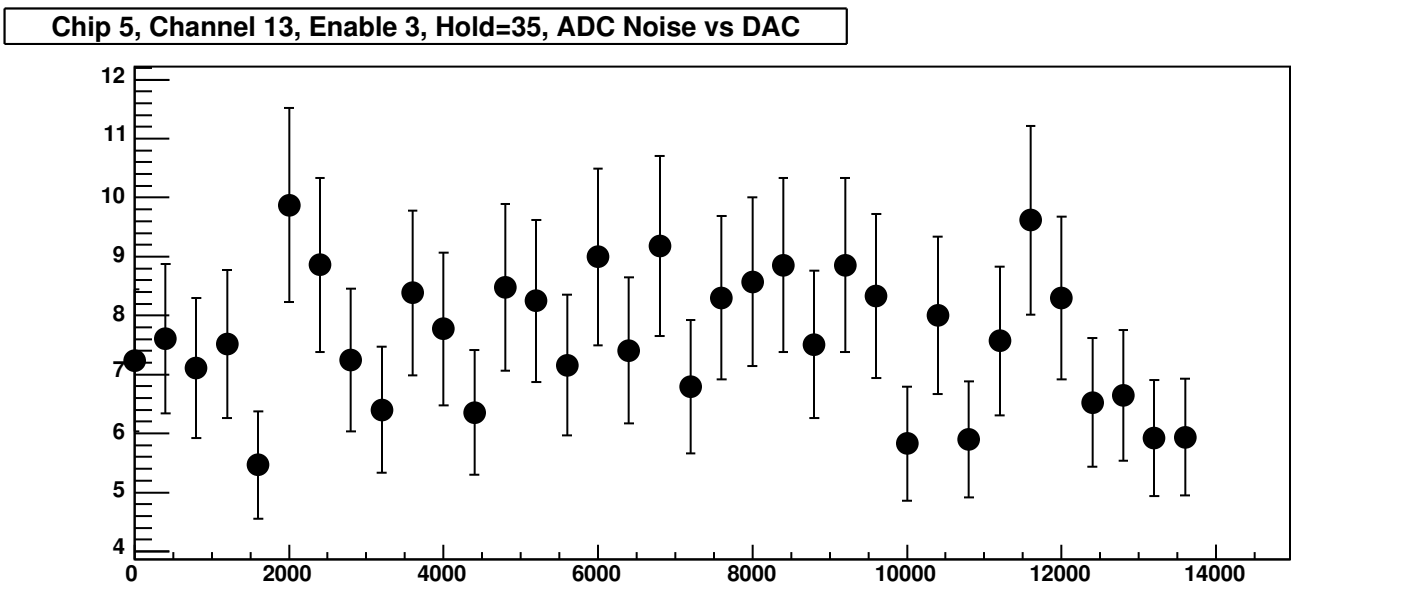

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

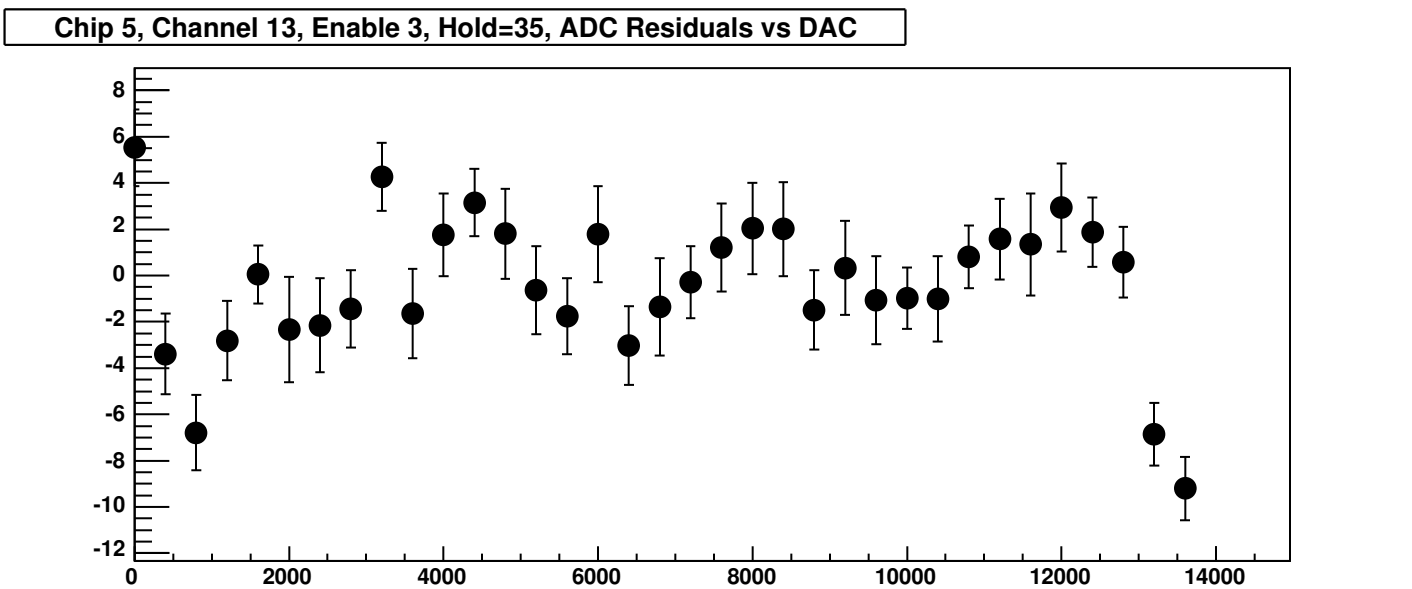

Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1139 / 23
p0 -260.9 ± 0.9186
p1 -0.04928 ± 0.0001896
-300
-400
-500
-600
-700
-800
-900
-1000
0
2000
4000
6000
8000
10000
12000
14000

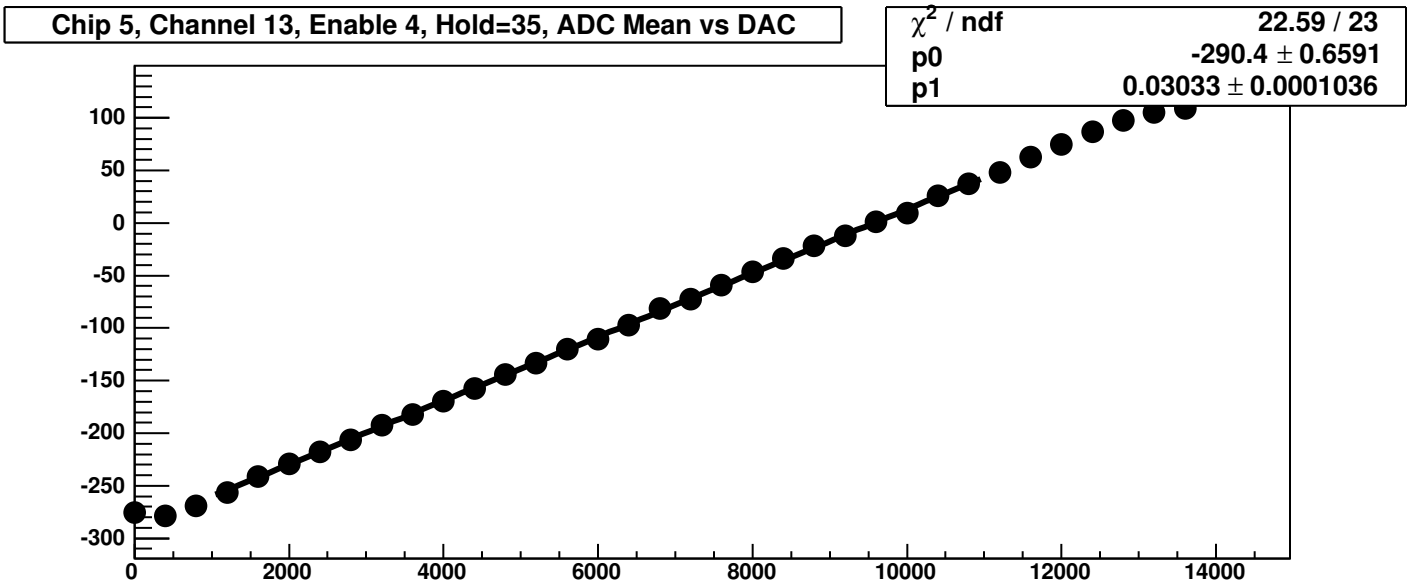

Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

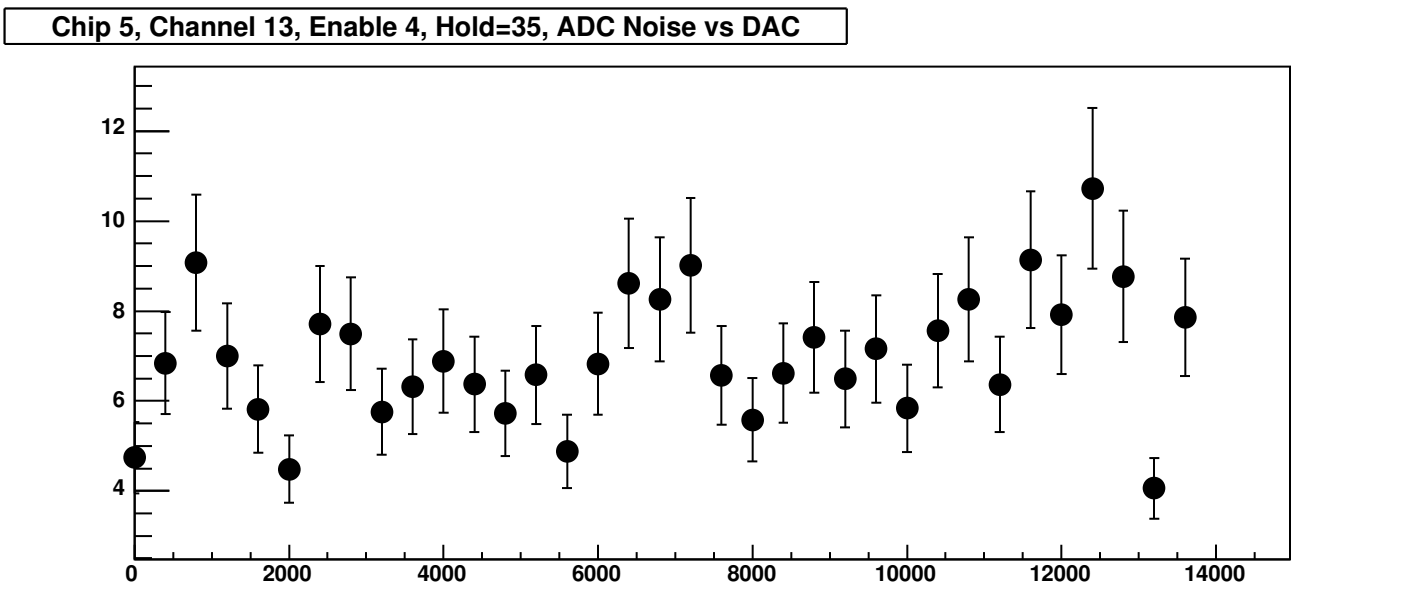

Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

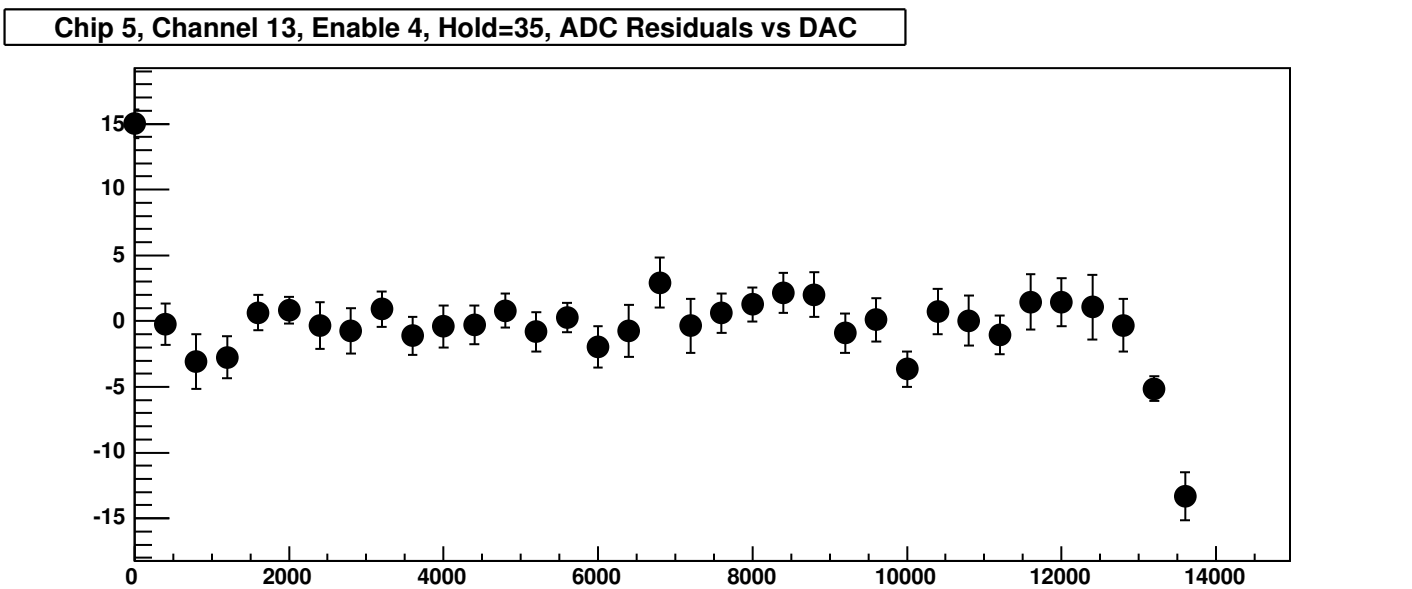

Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.68 / 23
p0 -289.1 ± 0.7605
p1 0.02111 ± 0.0001131
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

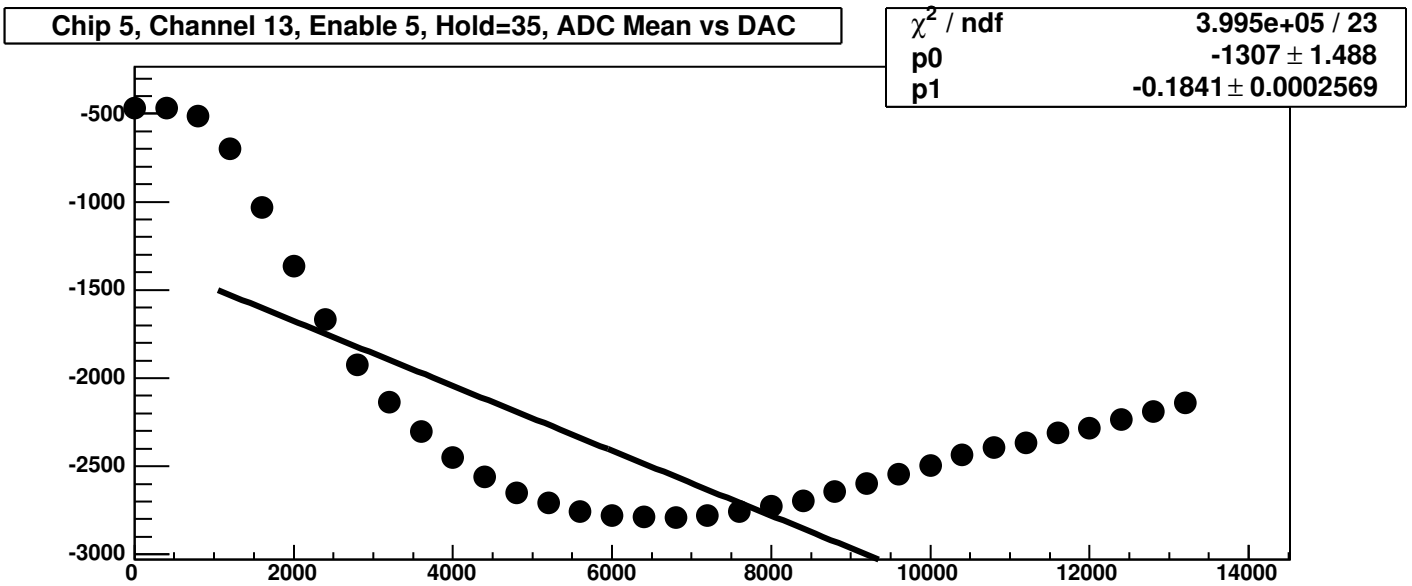

Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

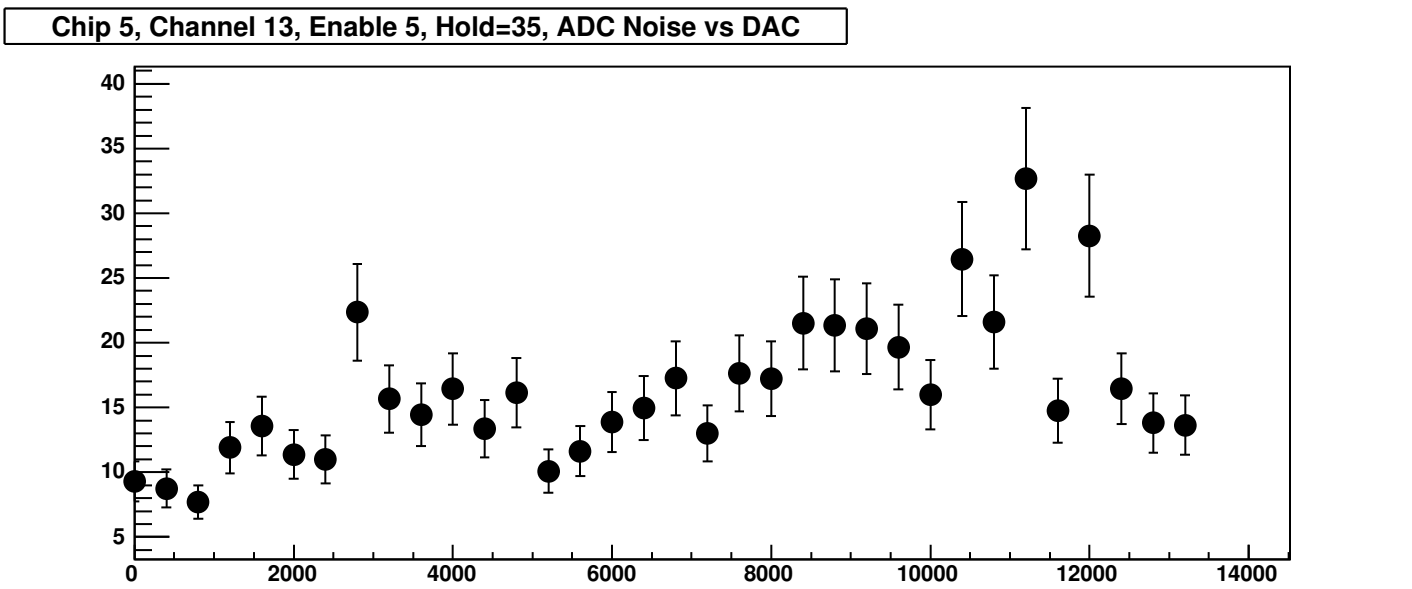

Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

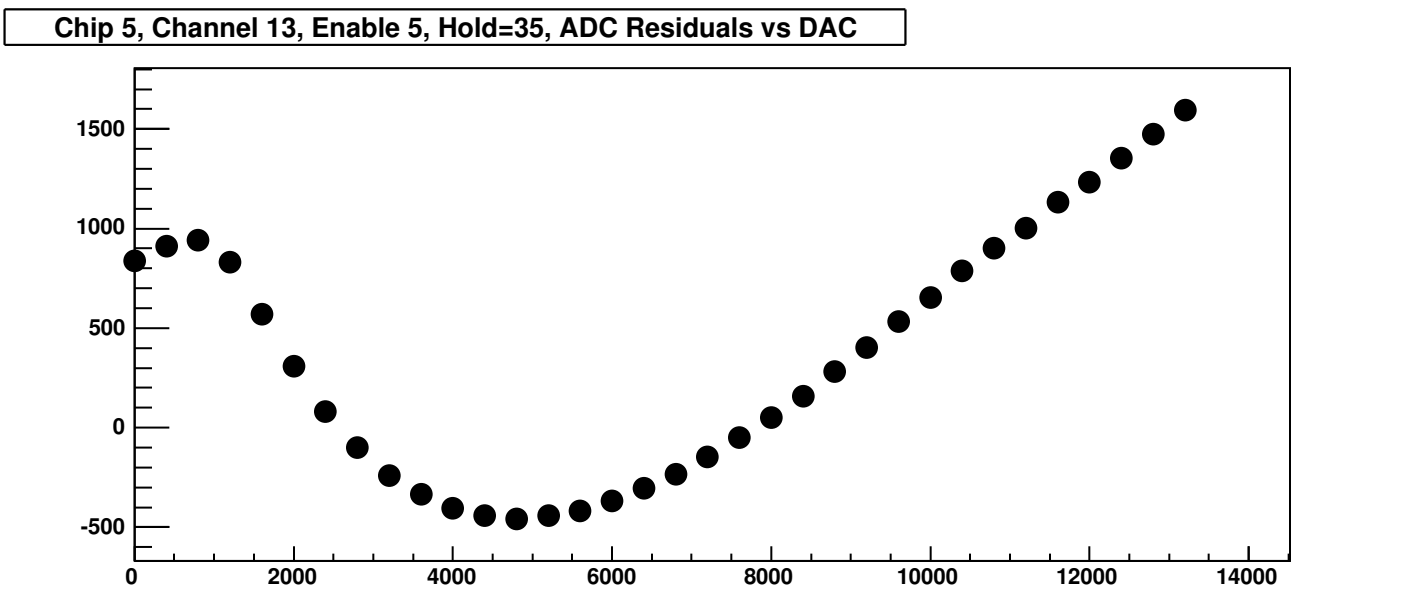

Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.59 / 23
p0 -290.4 ± 0.6591
p1 0.03033 ± 0.0001036
100
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

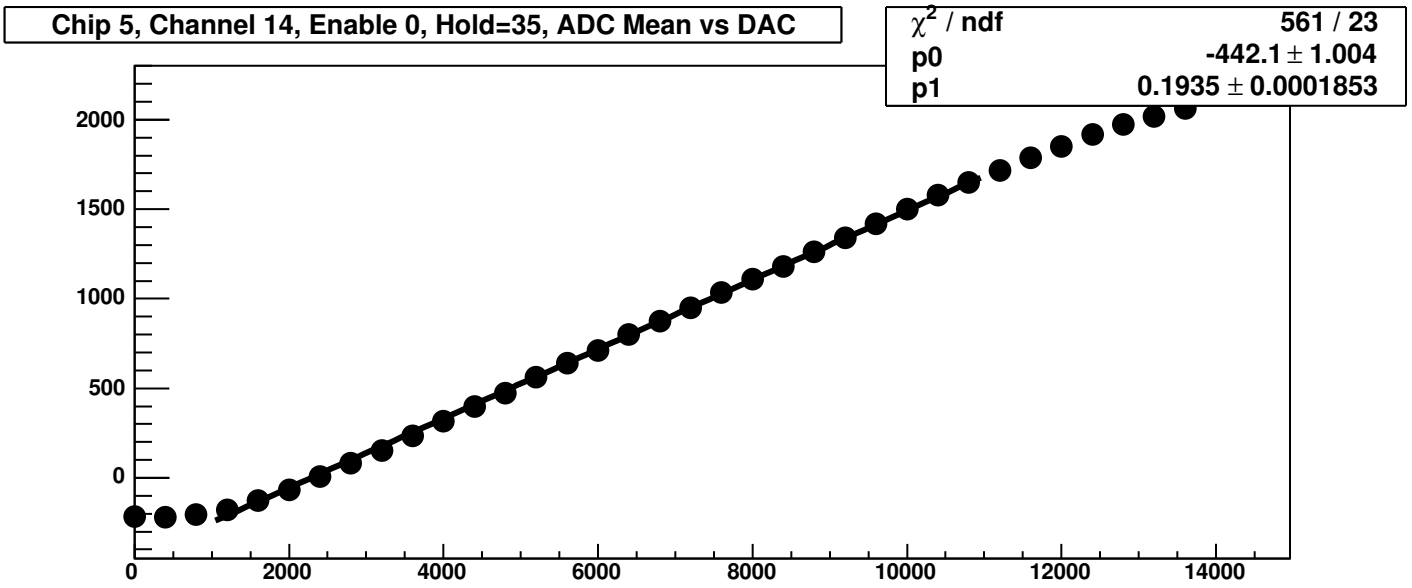

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

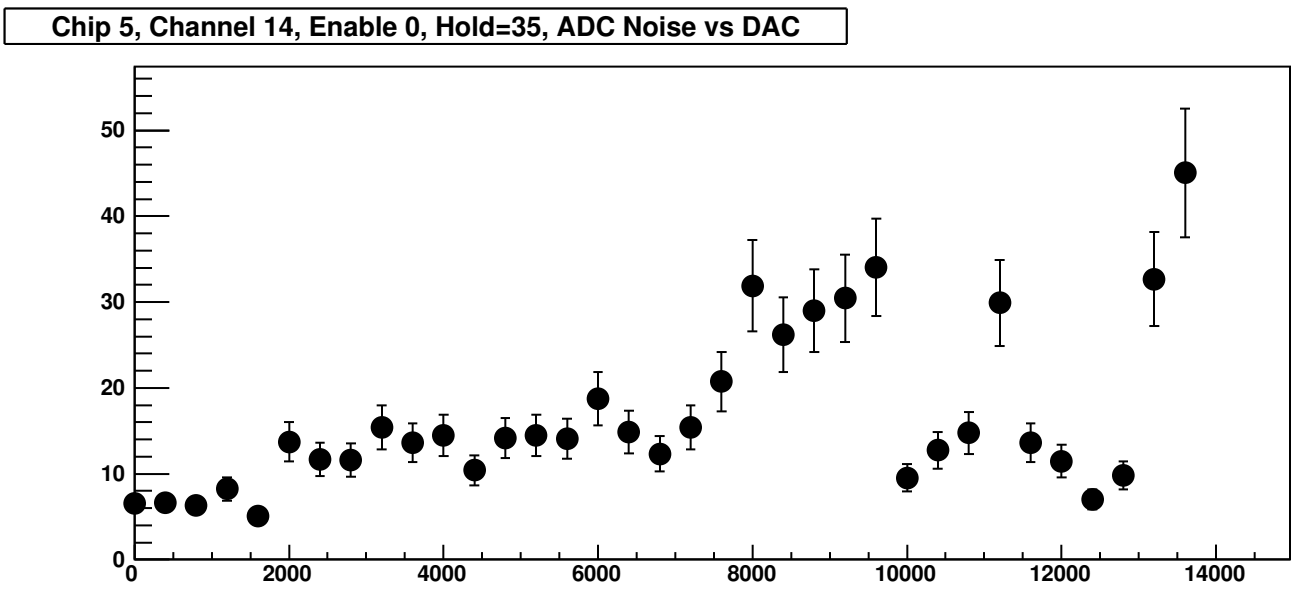

Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

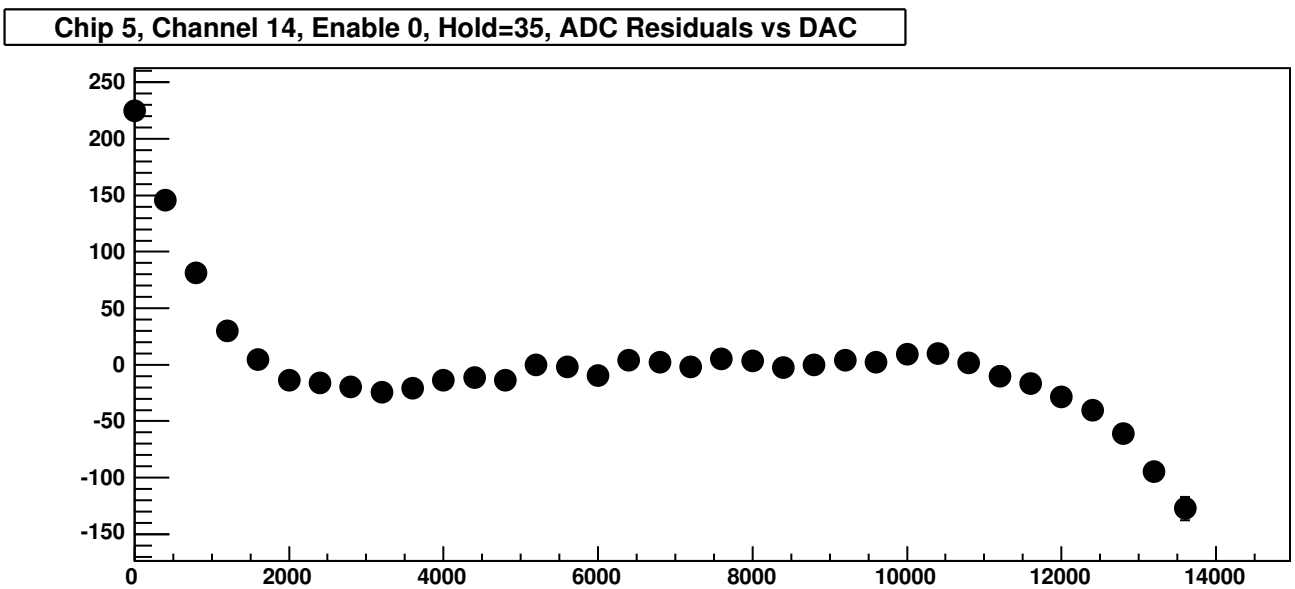

Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.995e+05 / 23
p0 -1307 ± 1.488
p1 -0.1841 ± 0.0002569
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

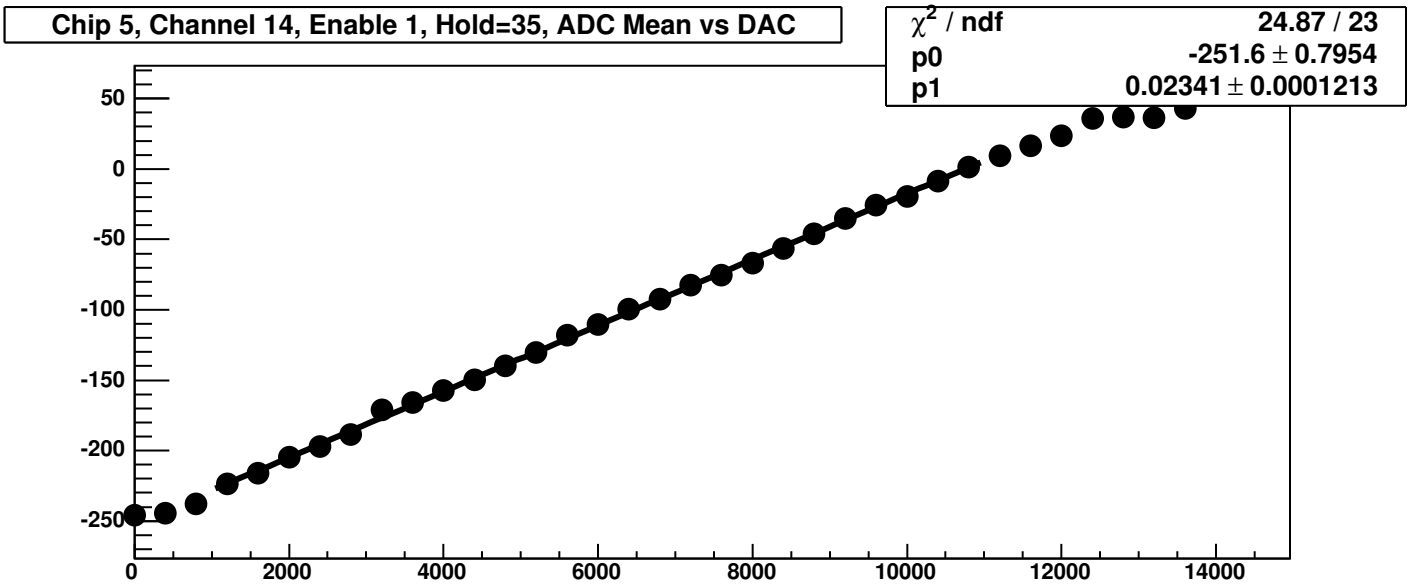

Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

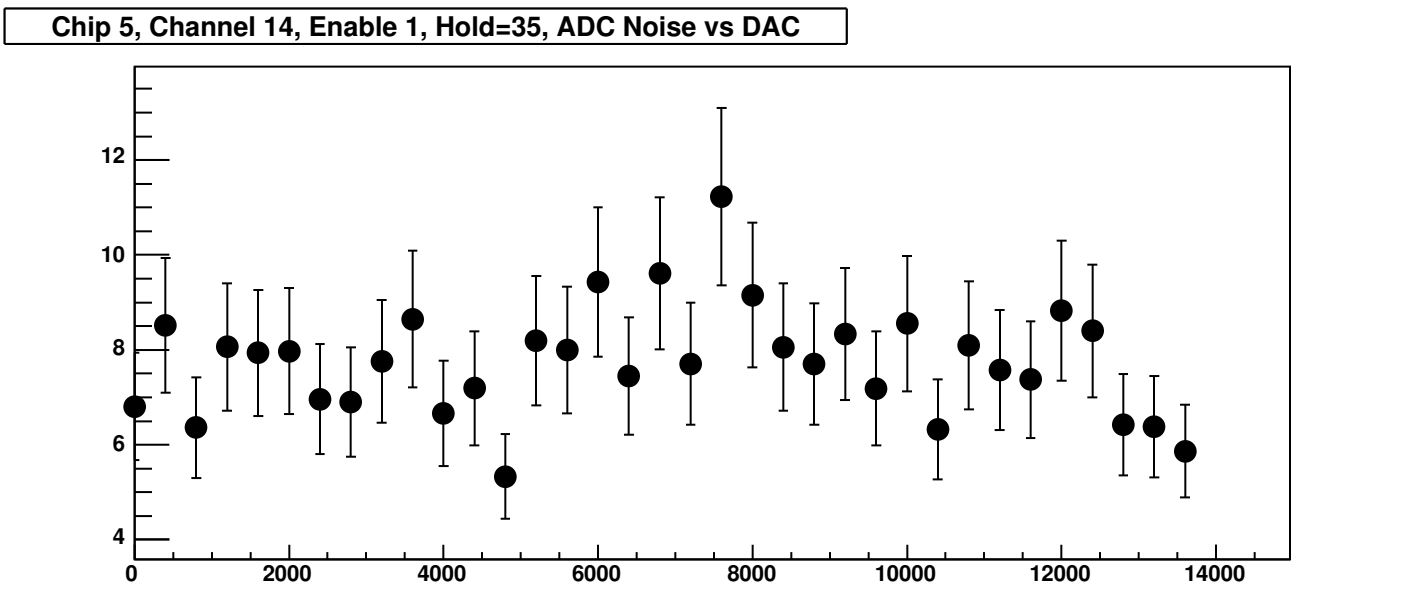

Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

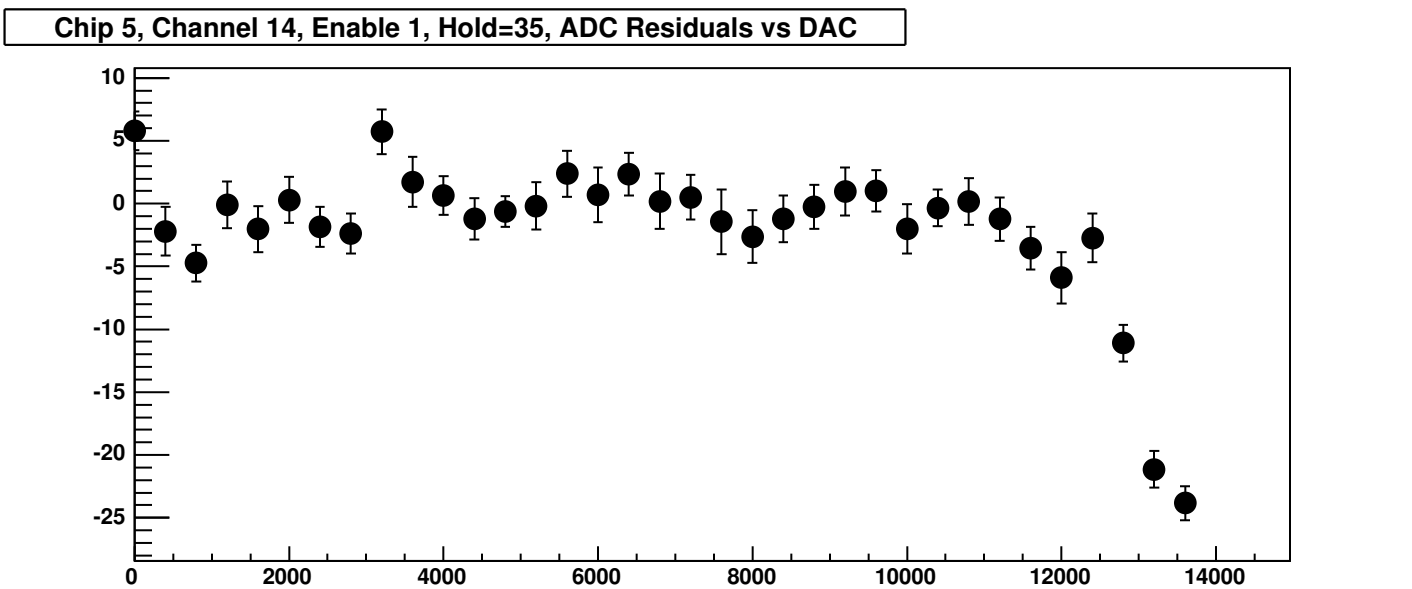

Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 561 / 23
p0 -442.1 ± 1.004
p1 0.1935 ± 0.0001853
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

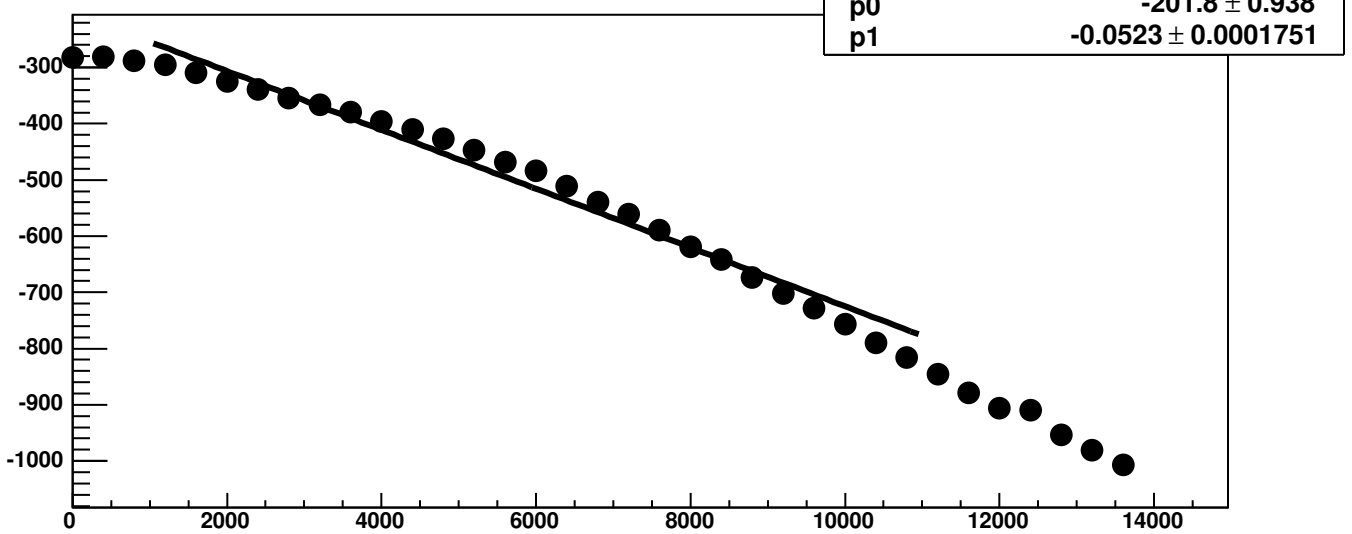

Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.87 / 23
p0 -251.6 ± 0.7954
p1 0.02341 ± 0.0001213
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

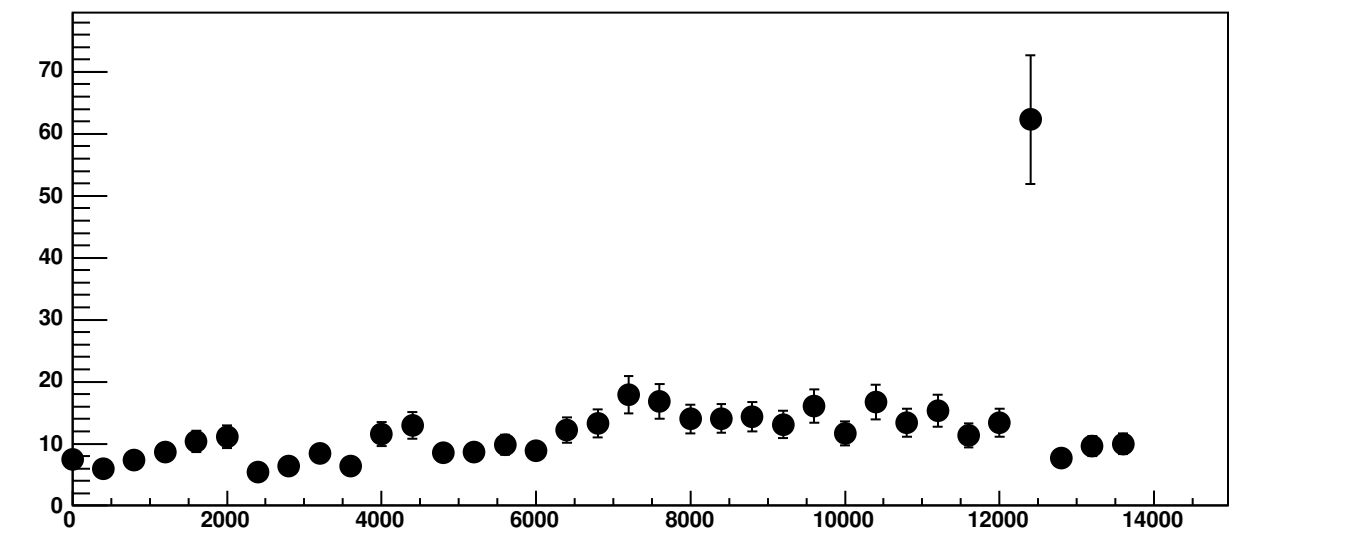

Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

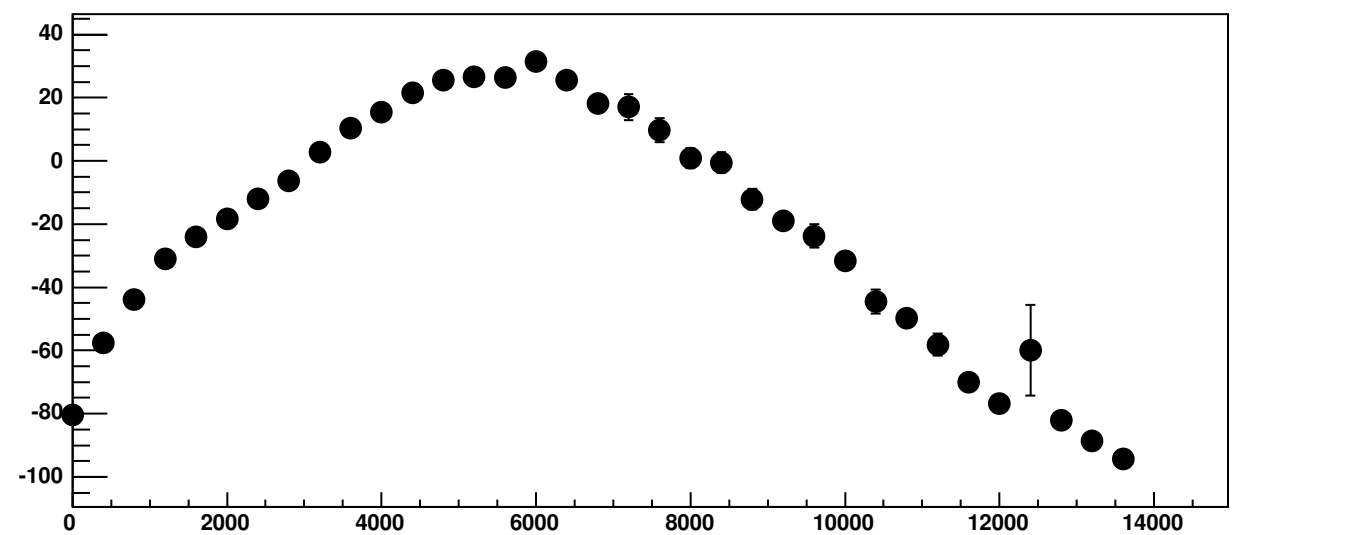
Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

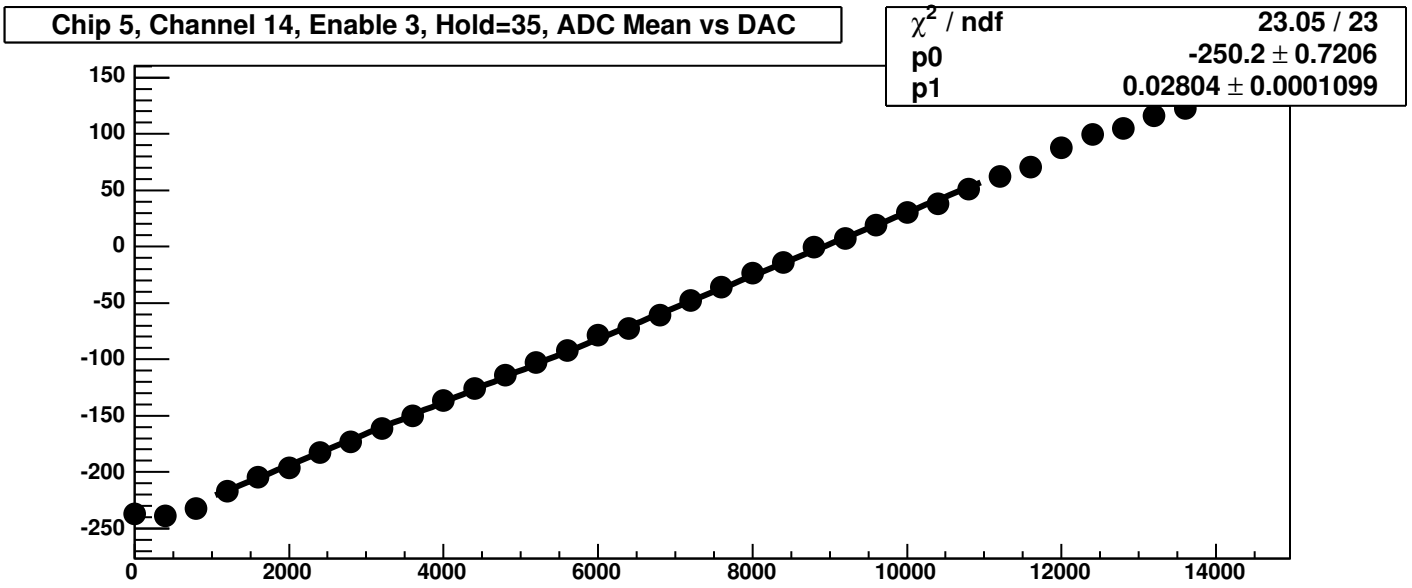


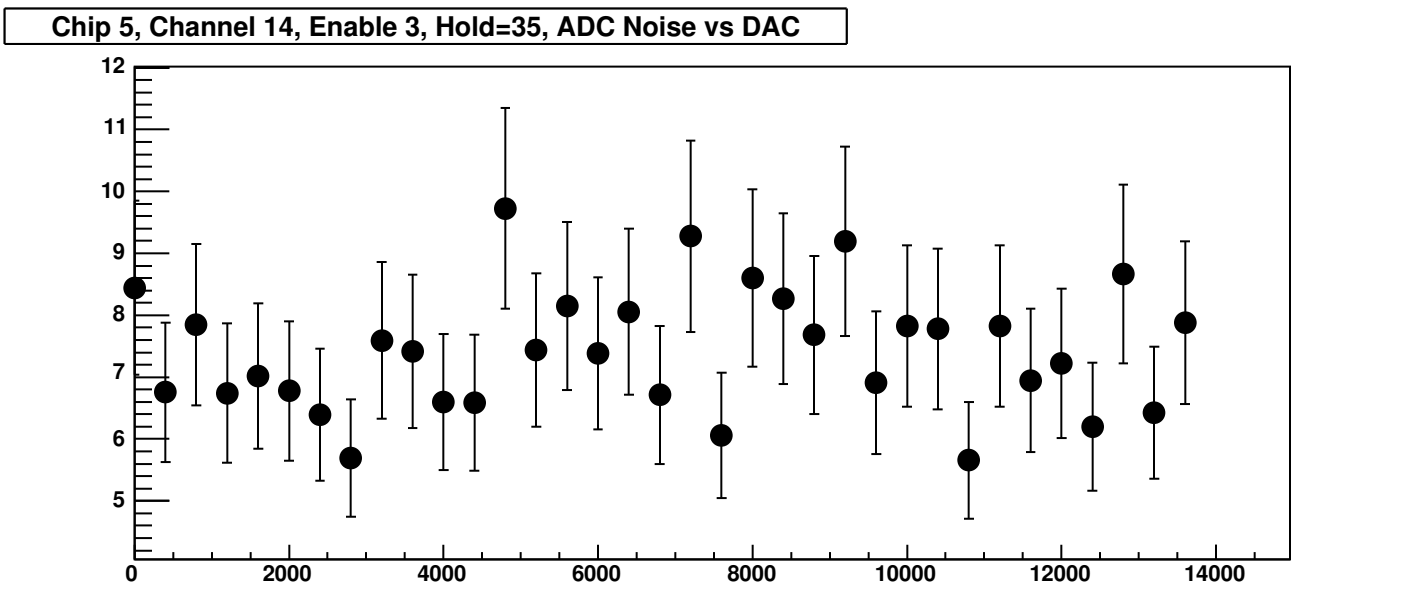
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

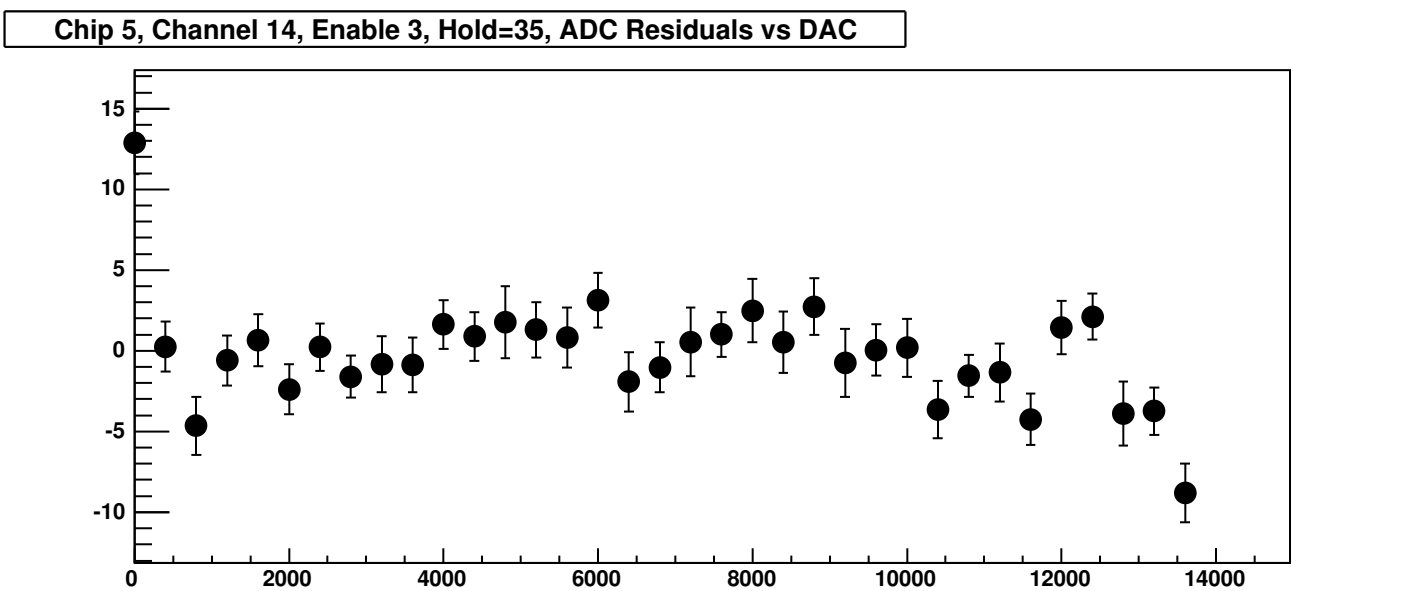


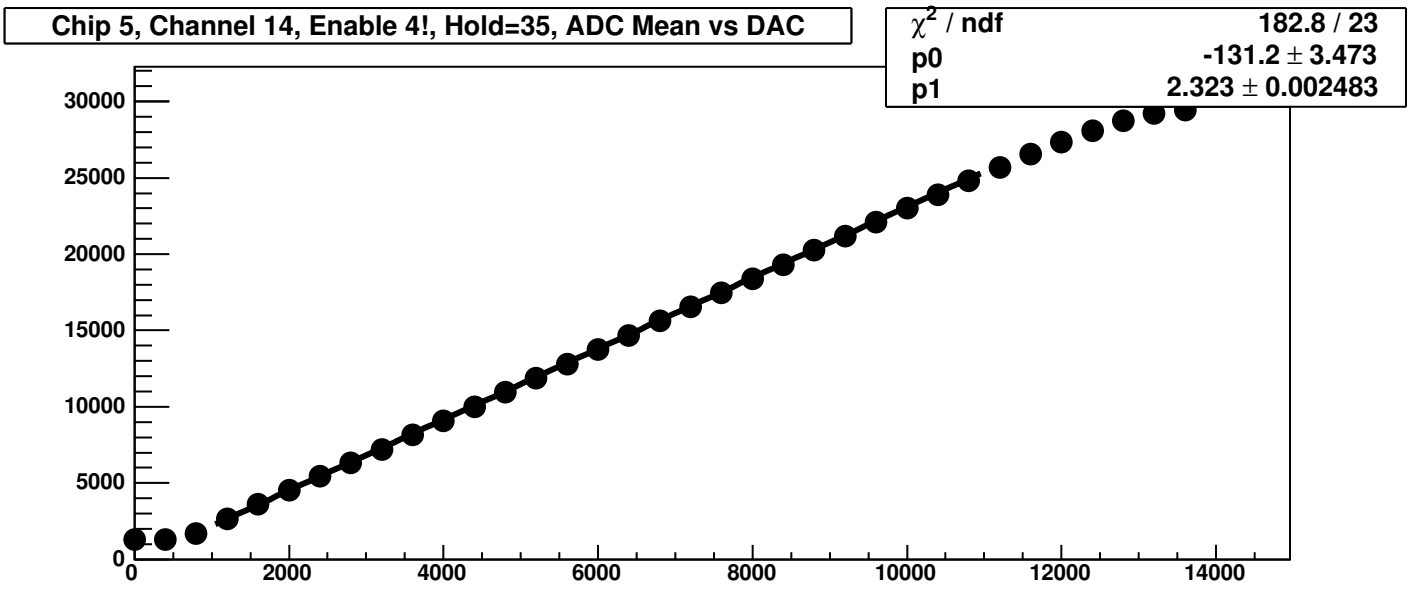
Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

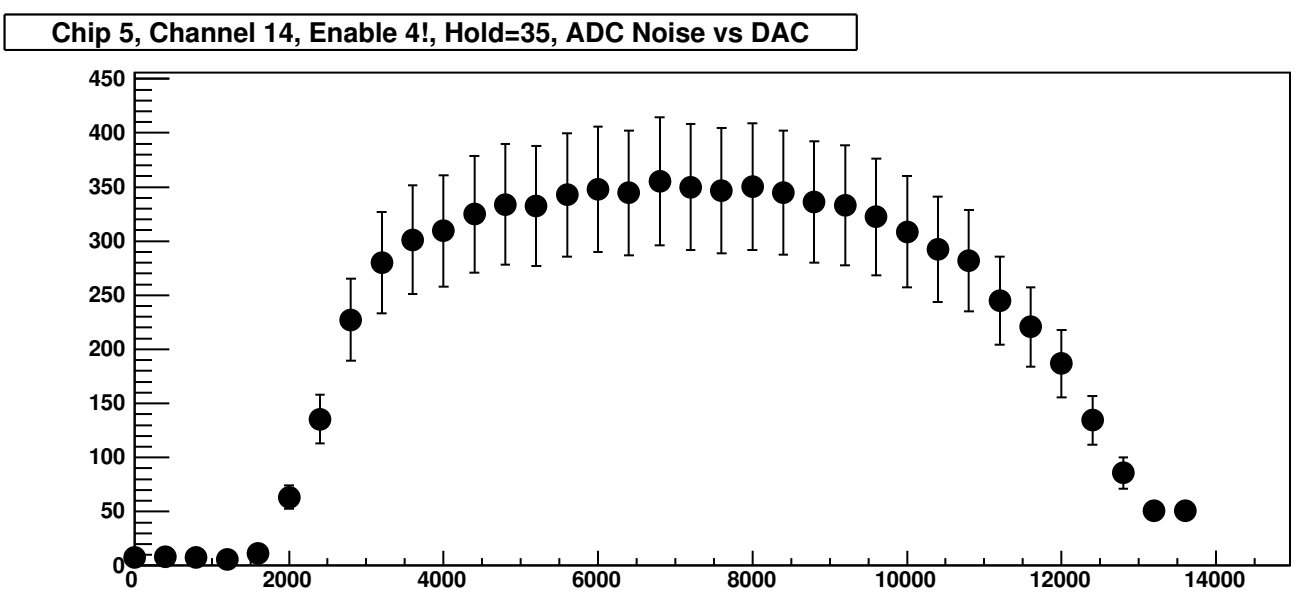


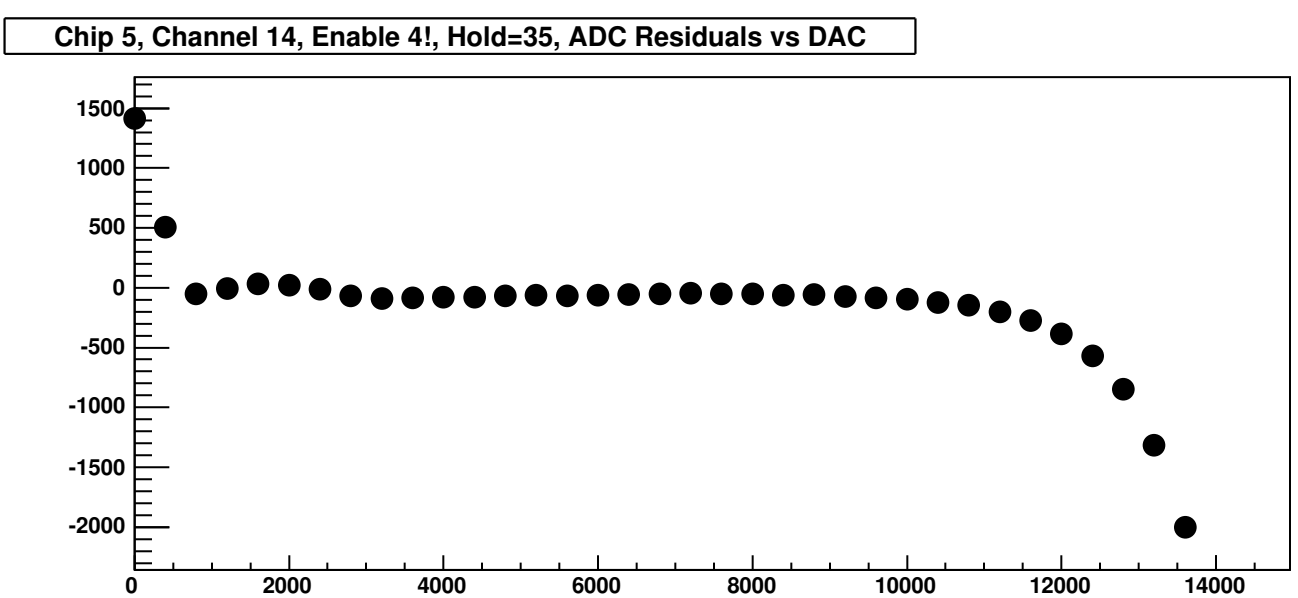

Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.05 / 23
p0 -250.2 ± 0.7206
p1 0.02804 ± 0.0001099
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

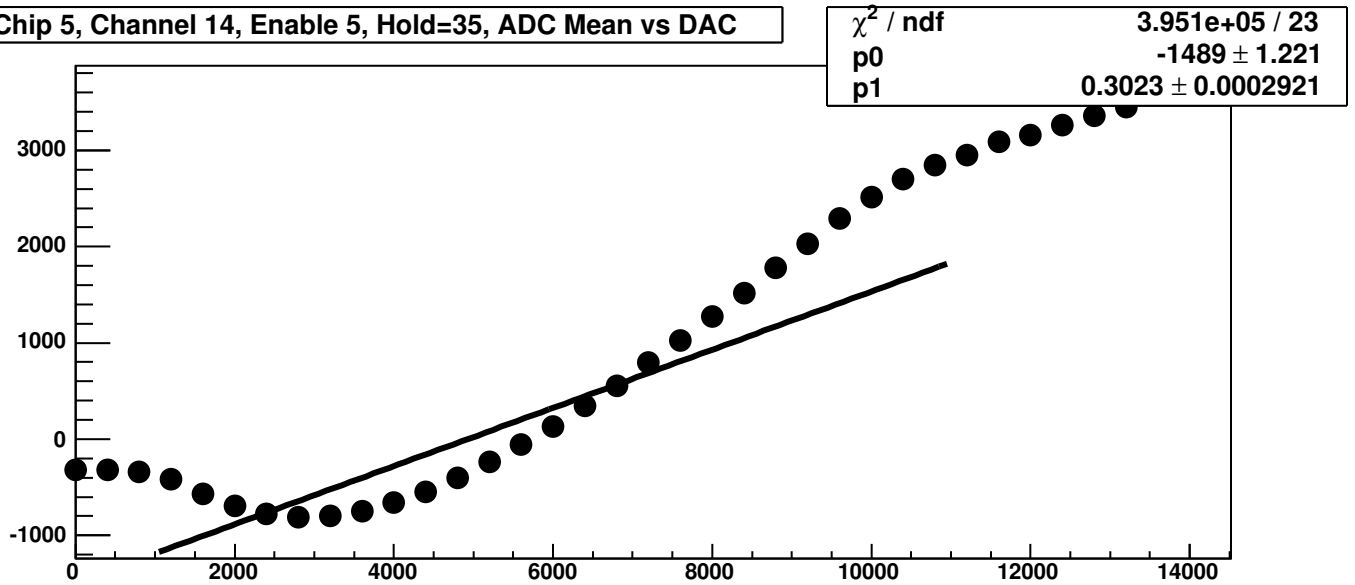

Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 182.8 / 23
p0 -131.2 ± 3.473
p1 2.323 ± 0.002483
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

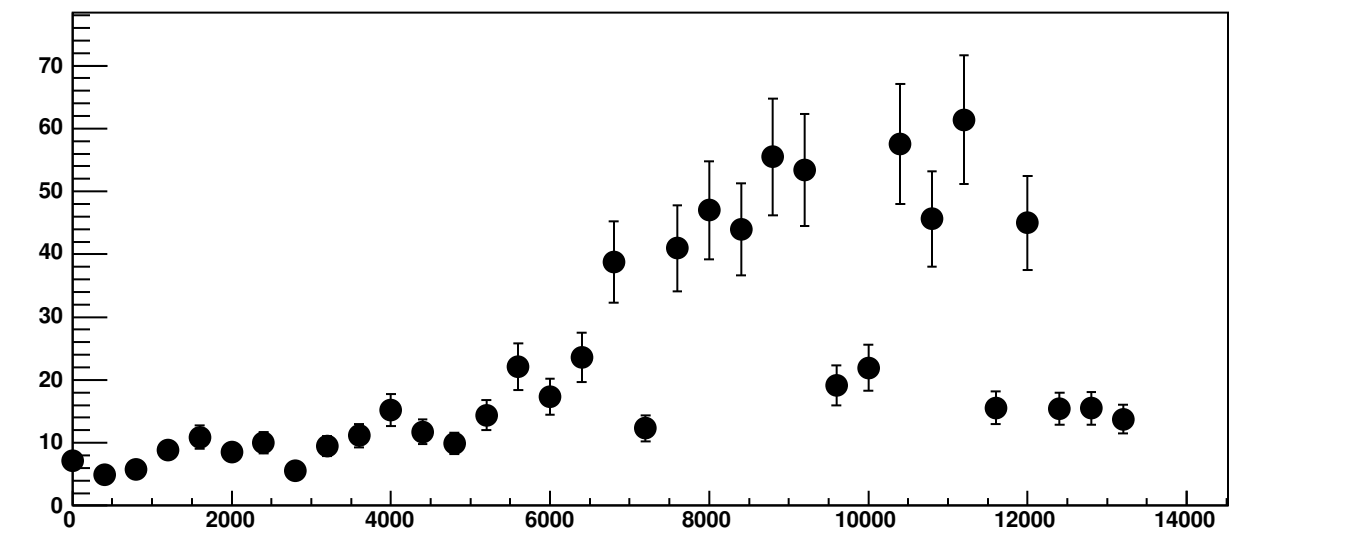

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

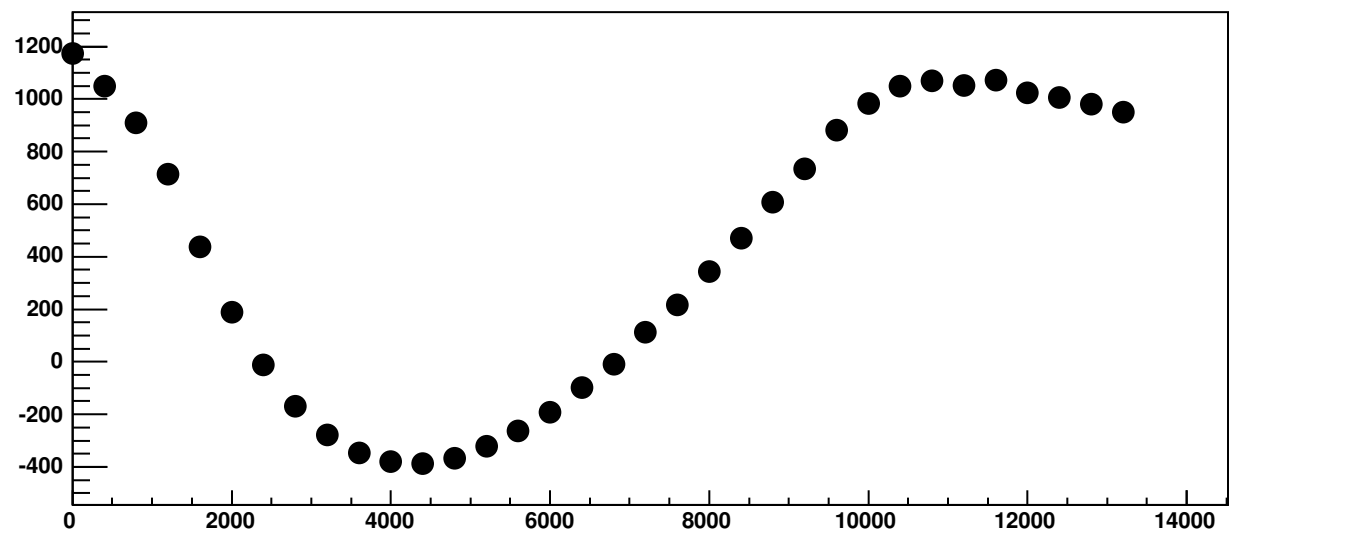
Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

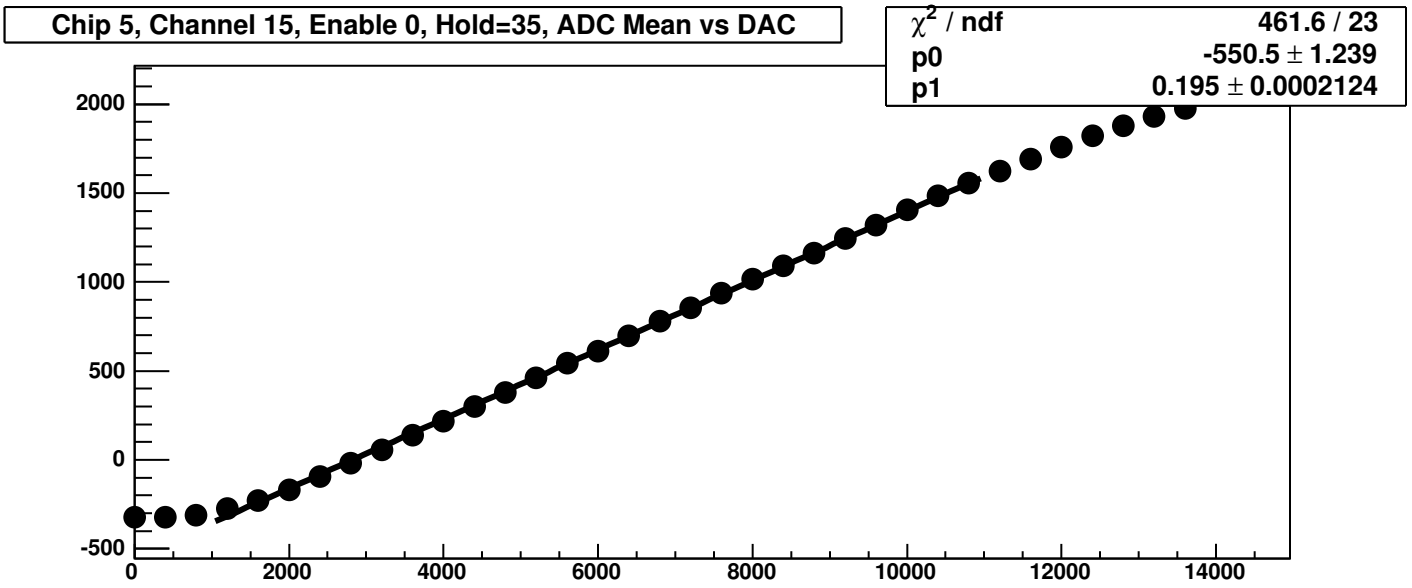


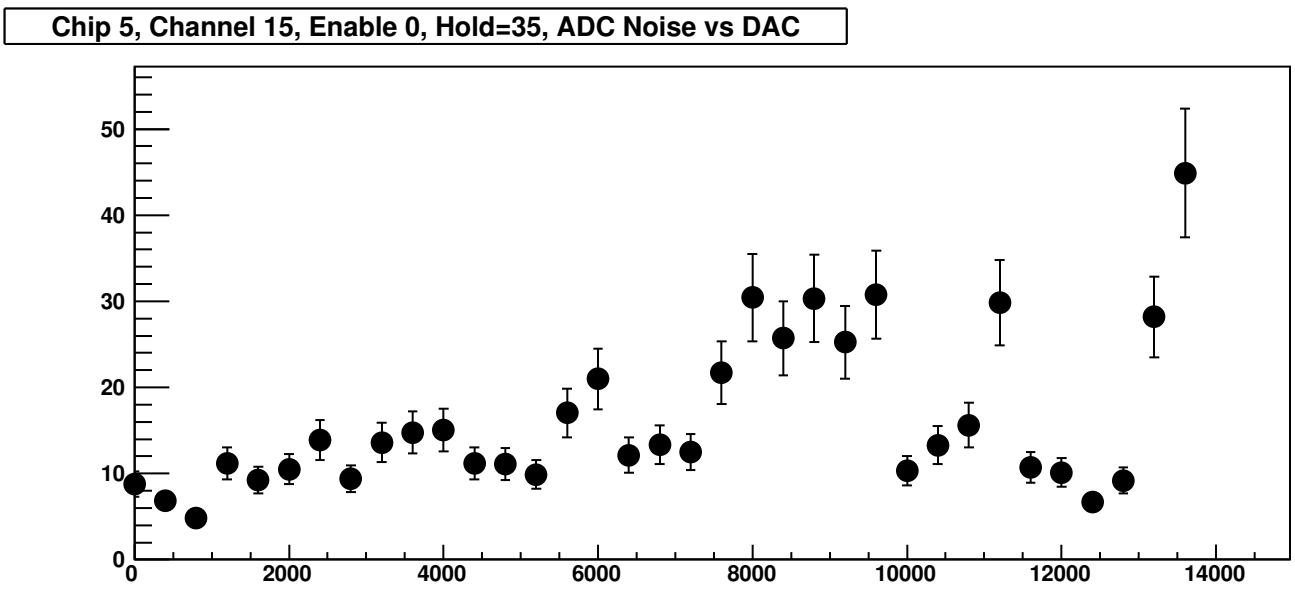
Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

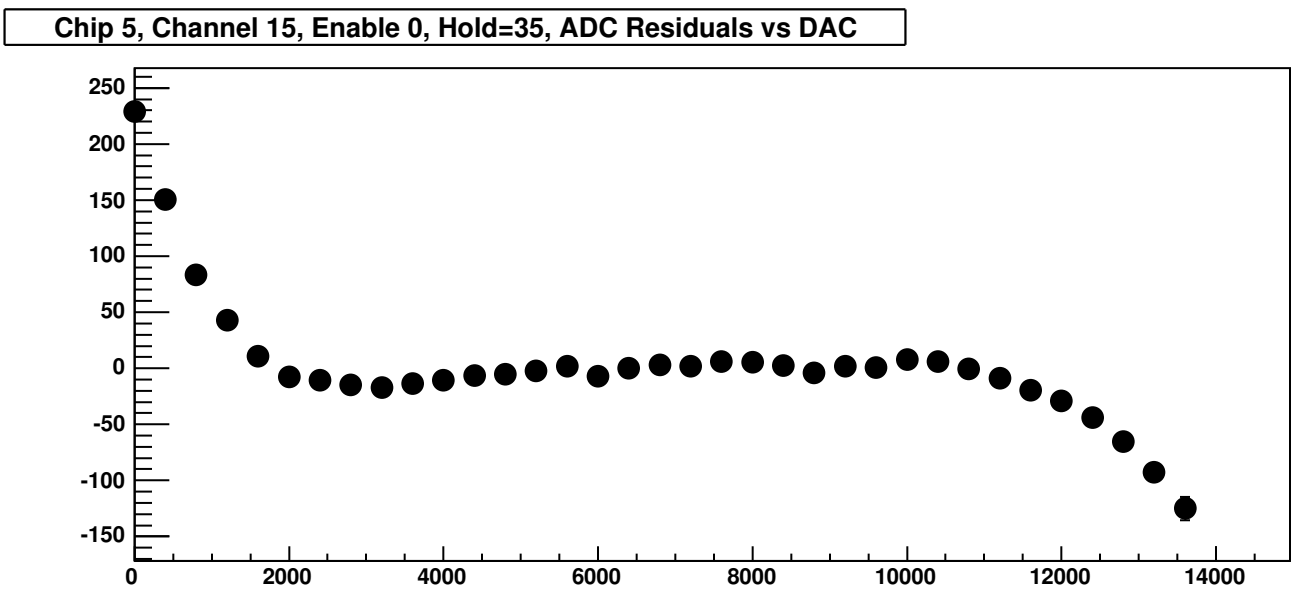


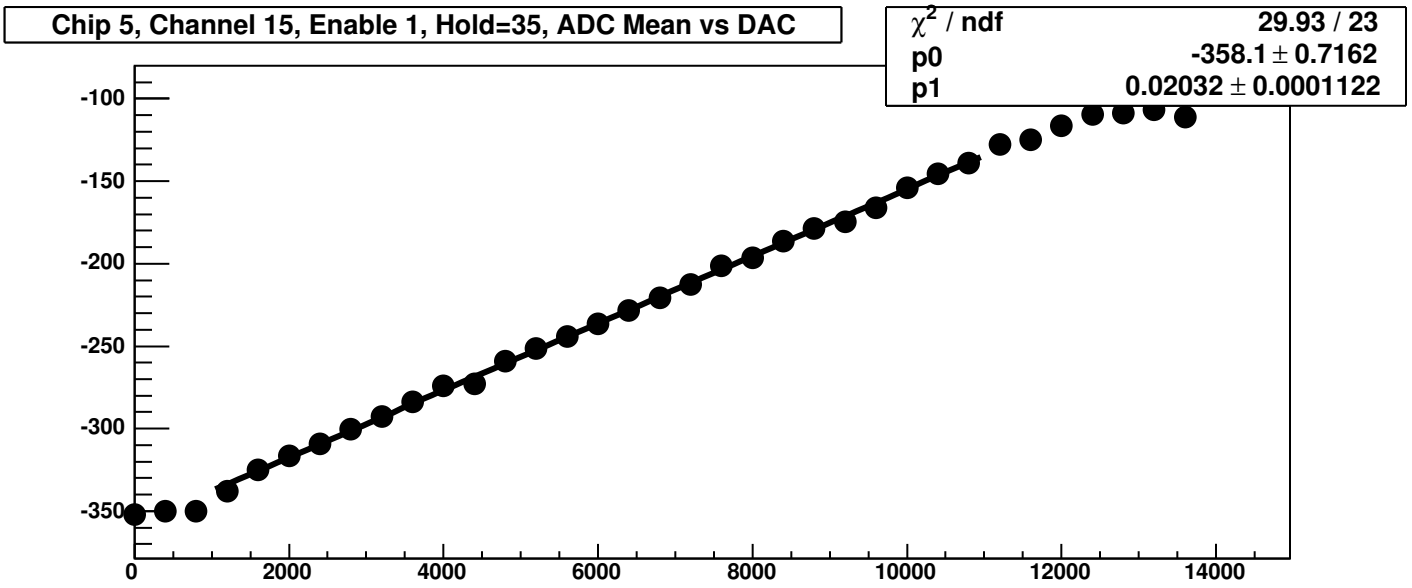
Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

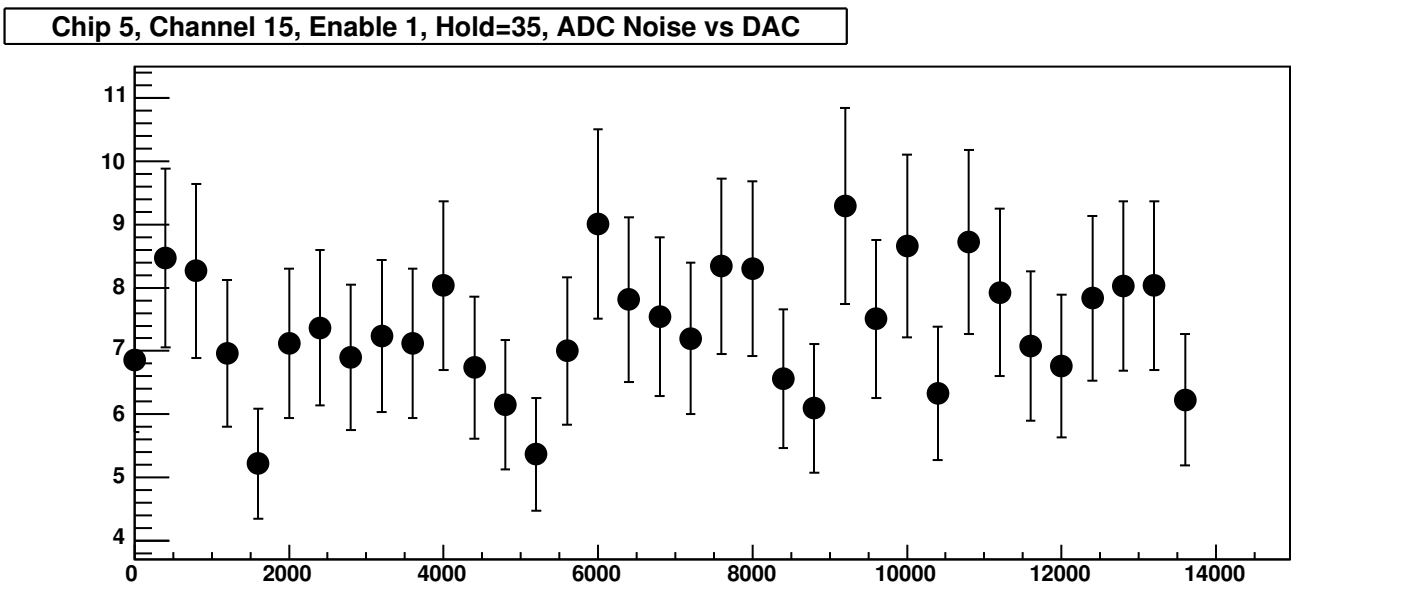


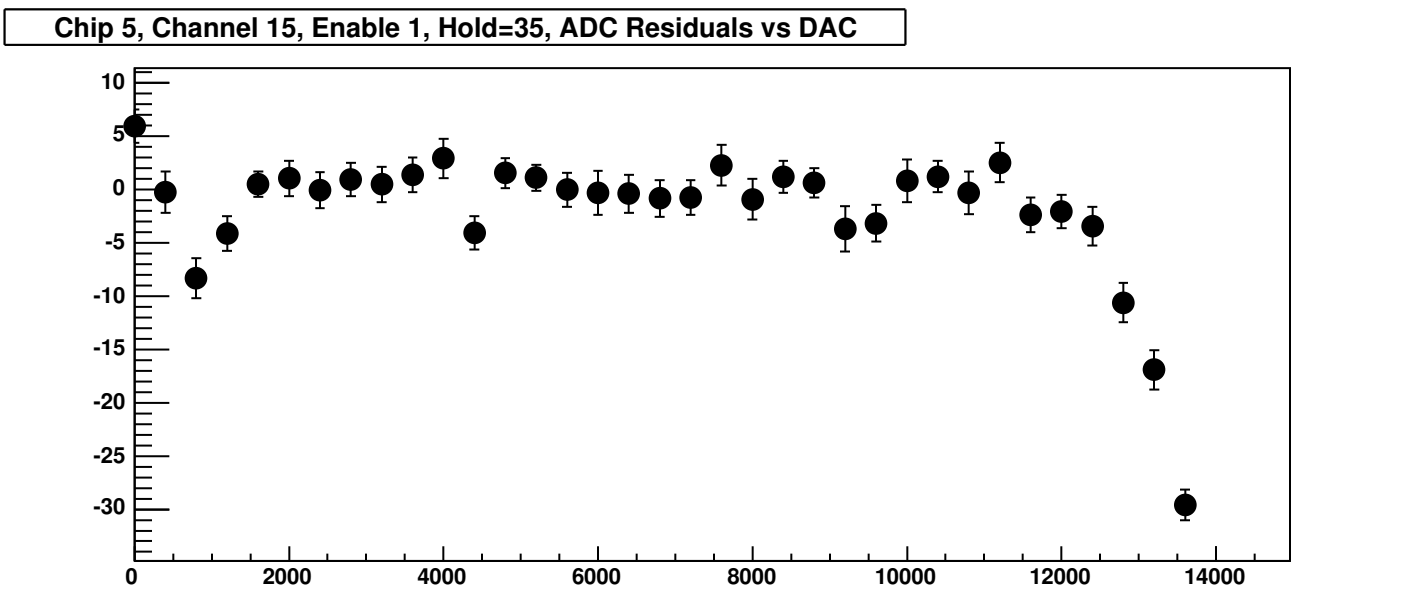

Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 461.6 / 23
p0 -550.5 ± 1.239
p1 0.195 ± 0.0002124
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

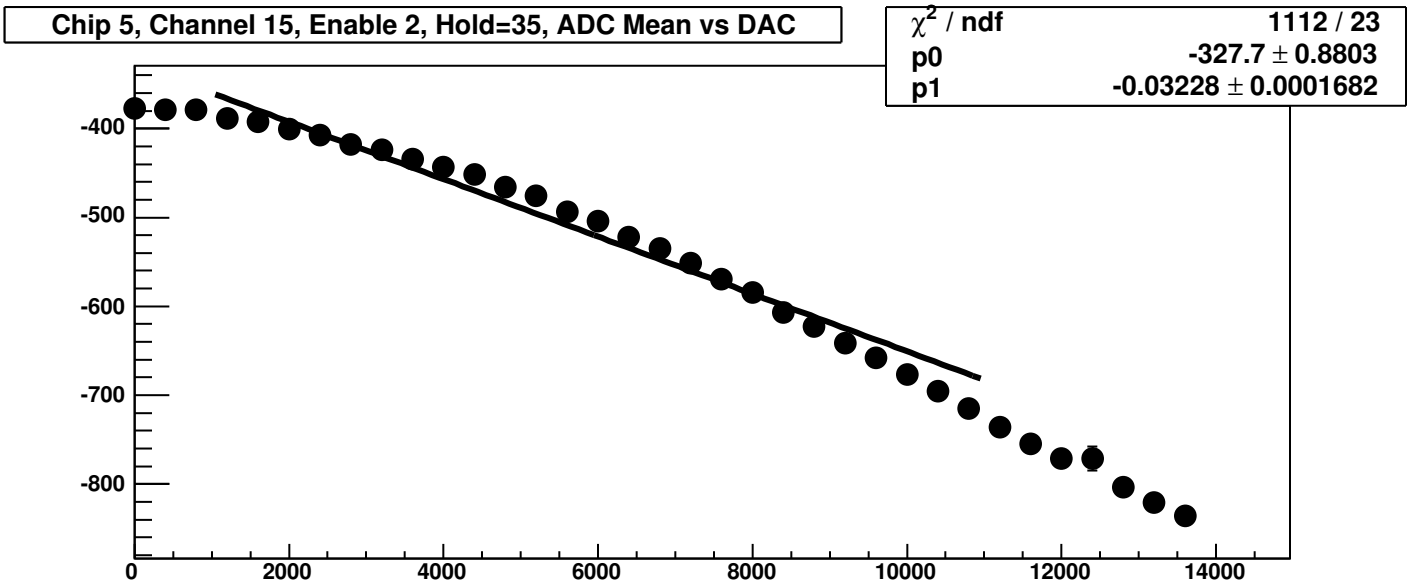

Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

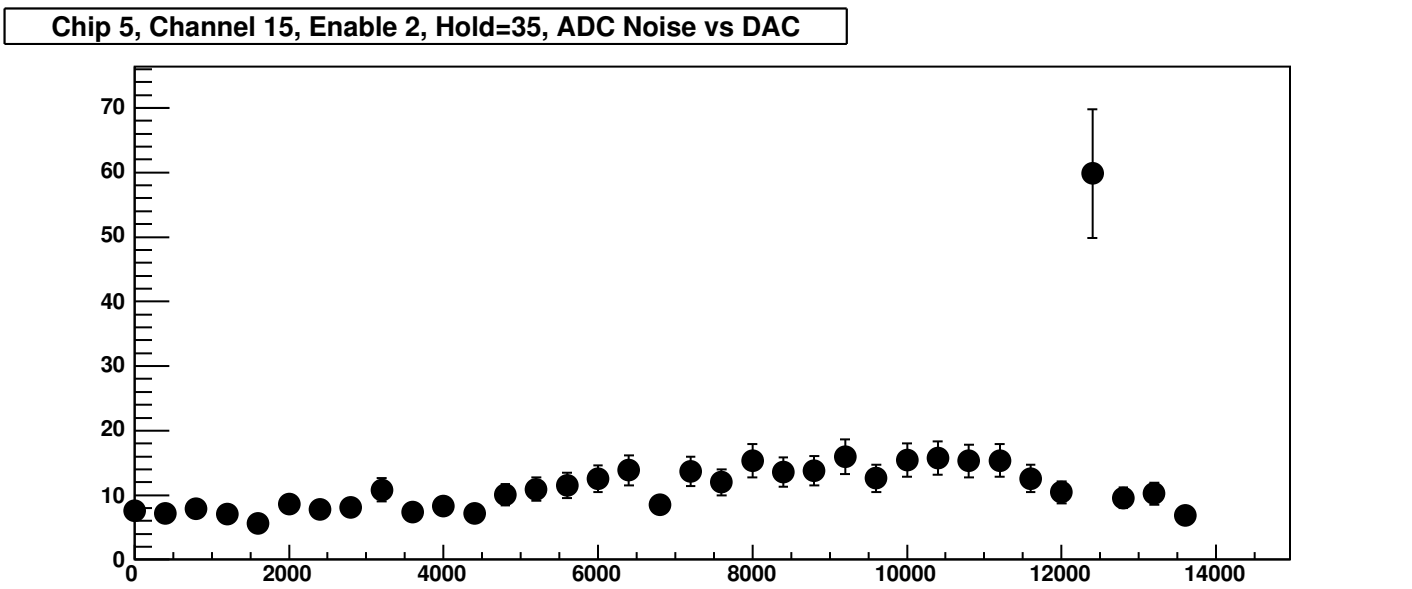

Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

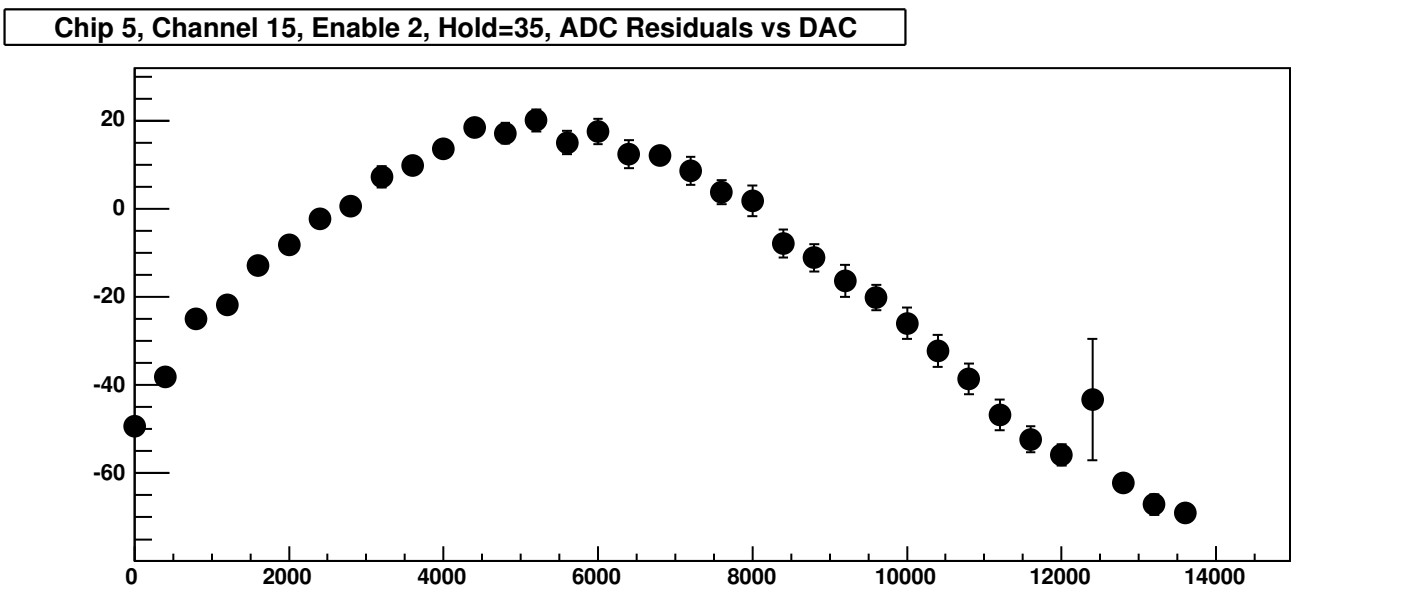

Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.93 / 23
p0 -358.1 ± 0.7162
p1 0.02032 ± 0.0001122
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

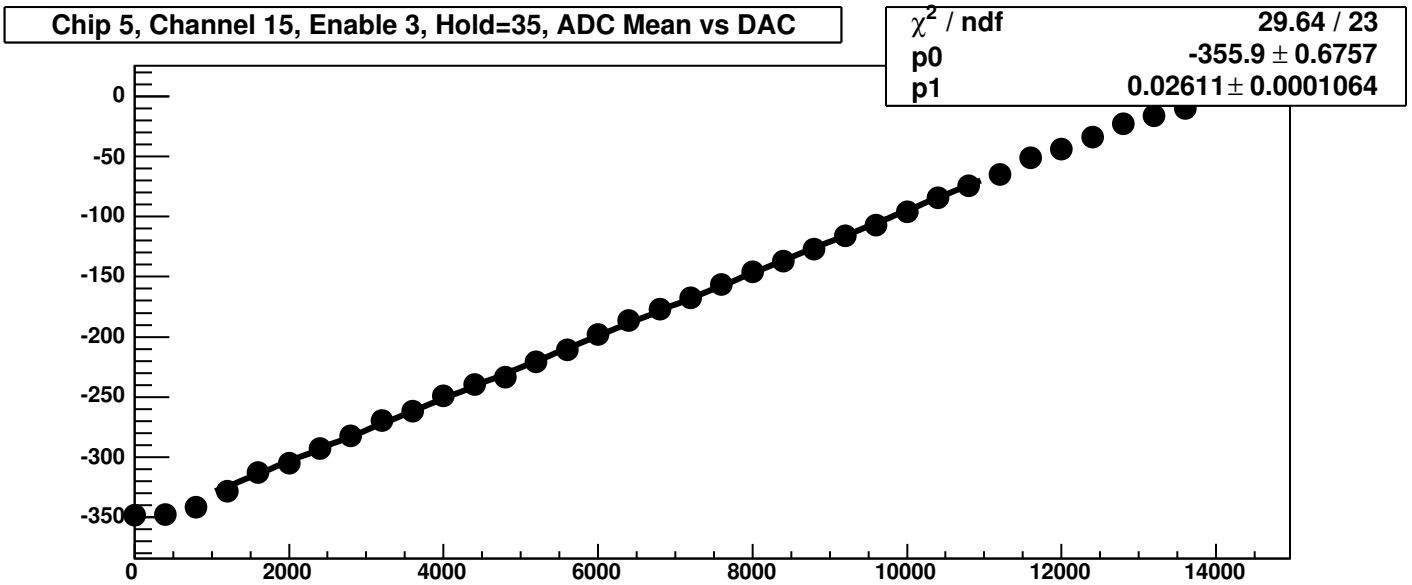

Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

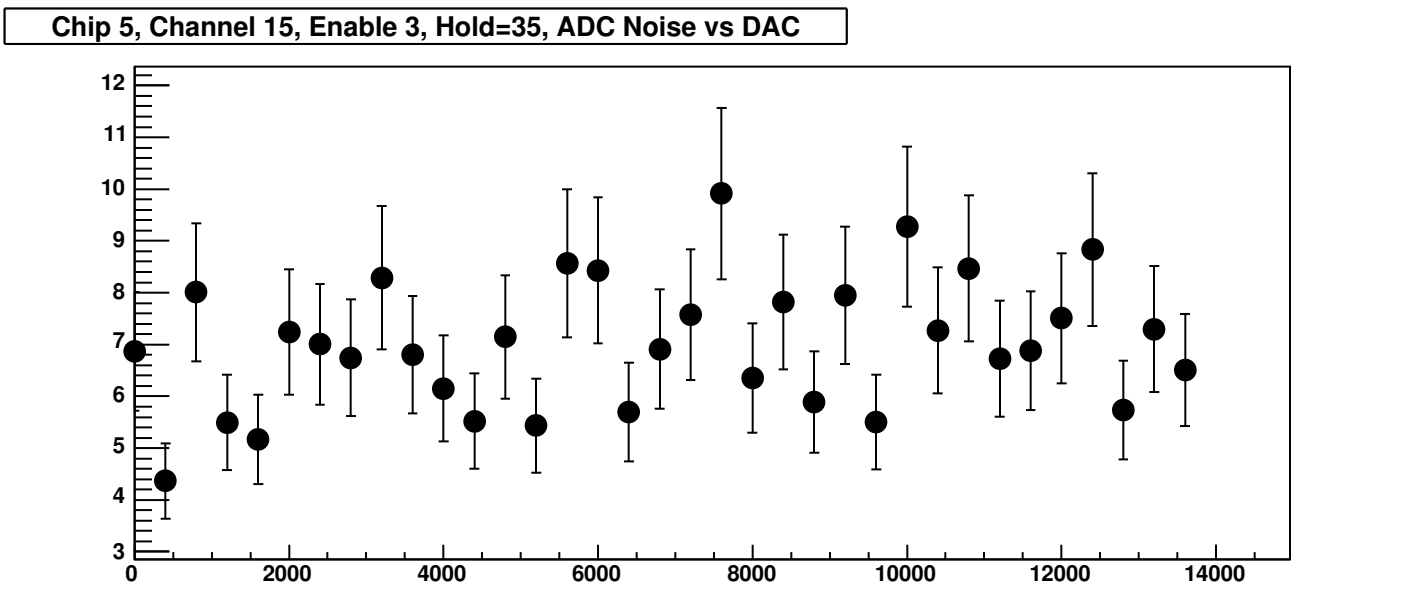

Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

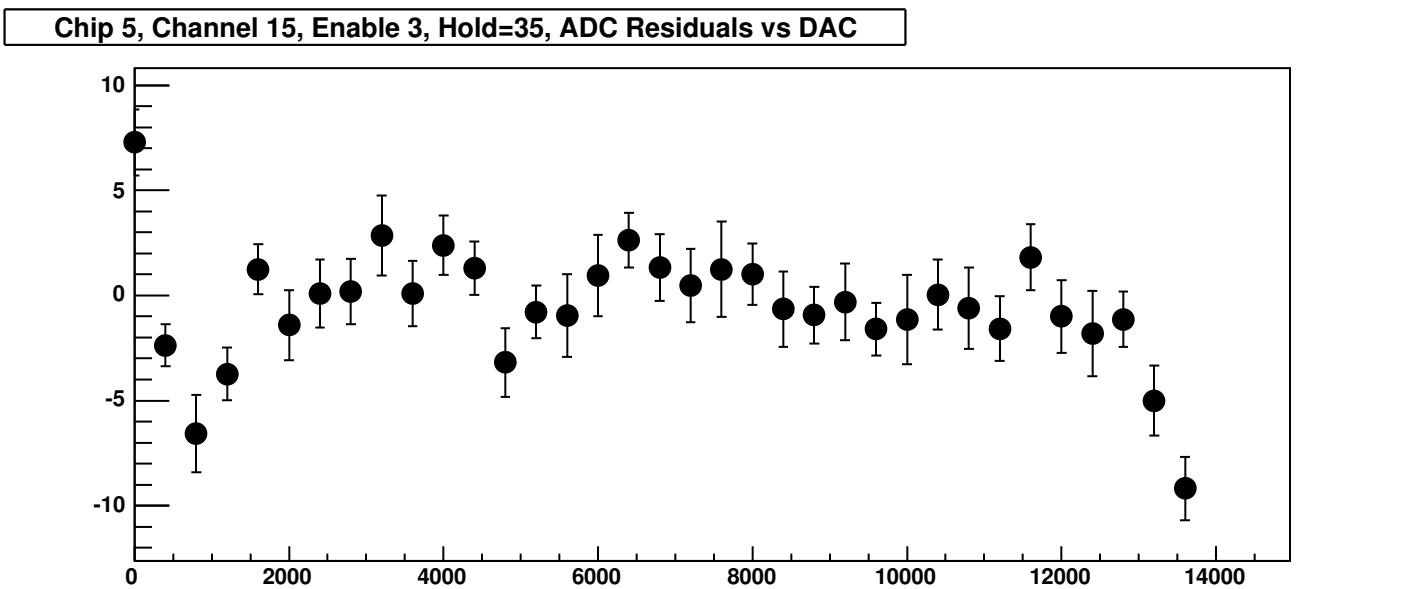

Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1112 / 23
p0 -327.7 ± 0.8803
p1 -0.03228 ± 0.0001682
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

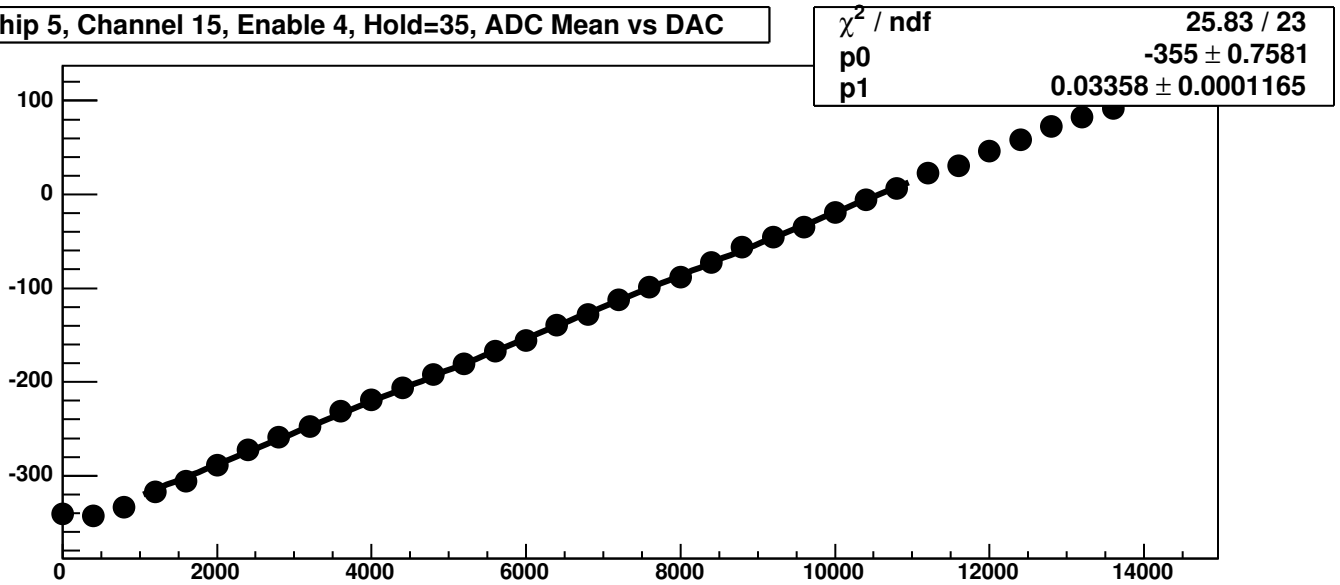

Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.64 / 23
p0 -355.9 ± 0.6757
p1 0.02611 ± 0.0001064
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

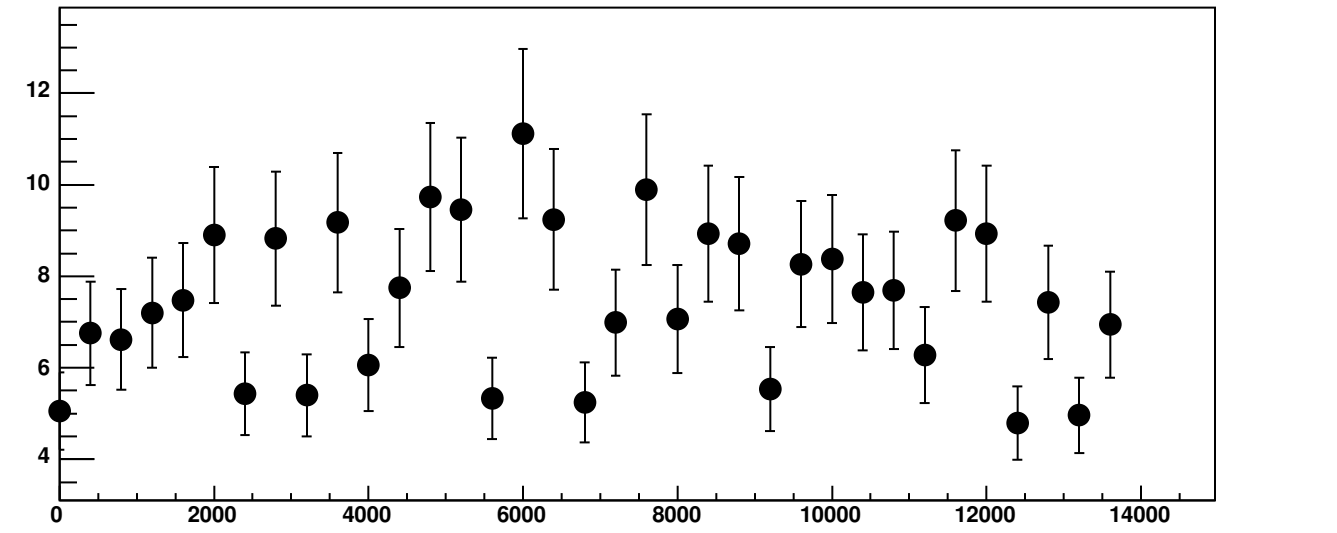

Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

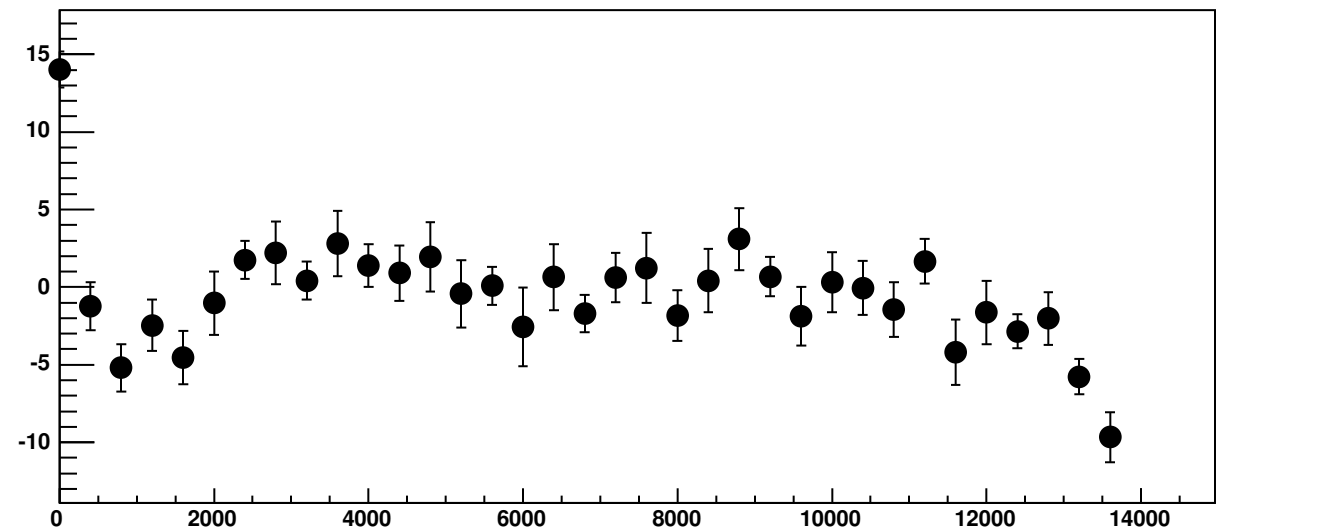
Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

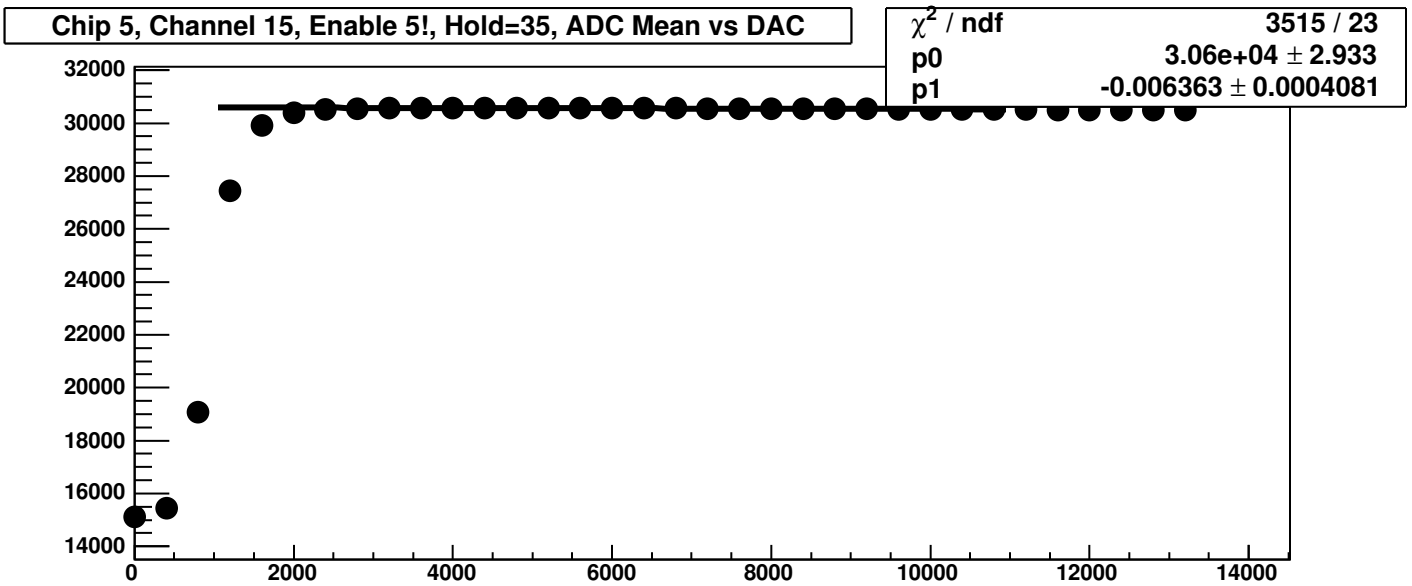


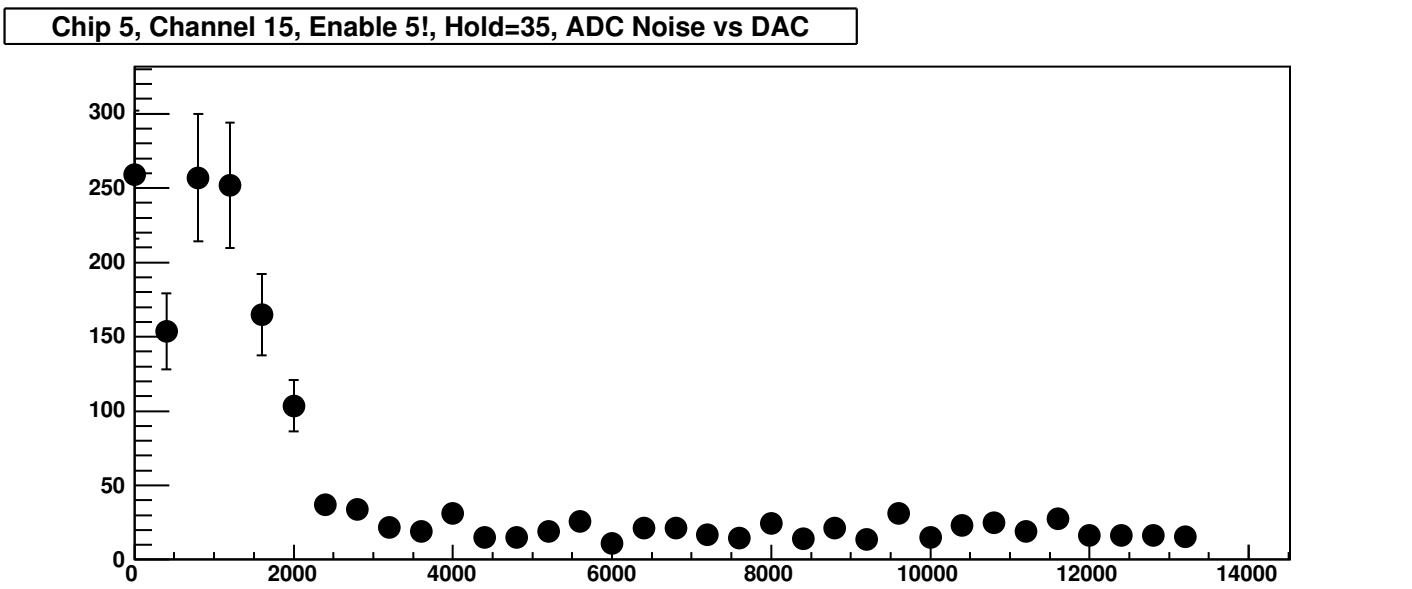
Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

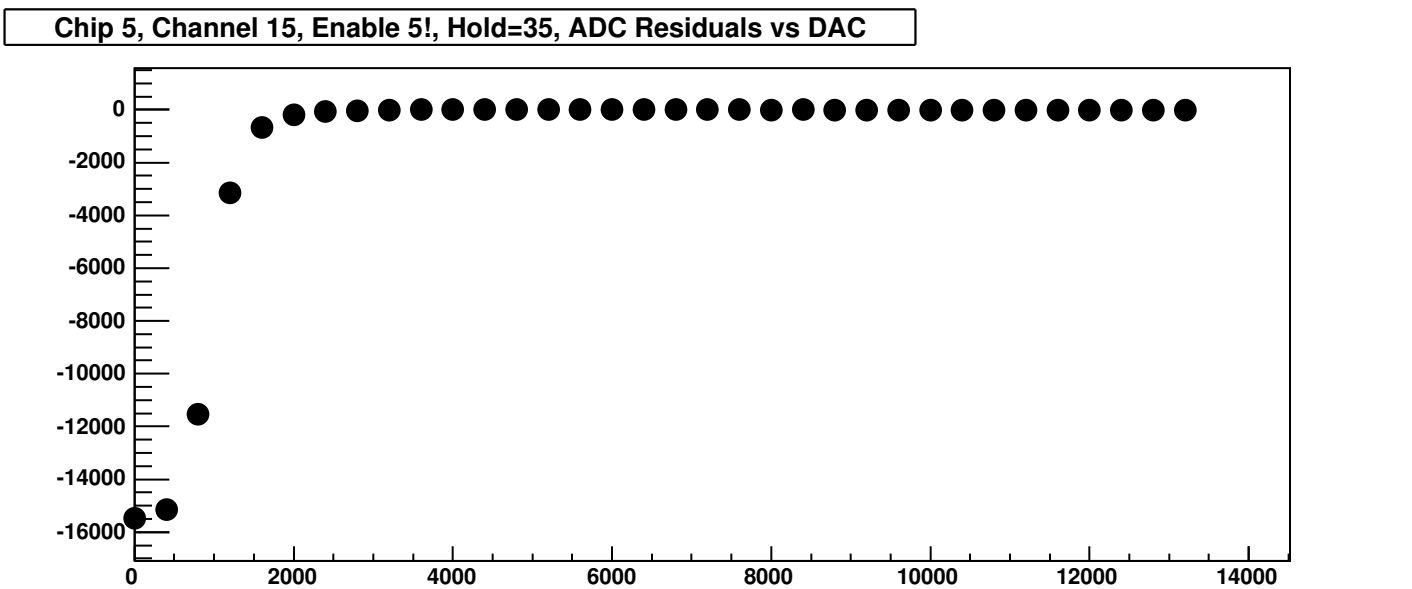


Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC



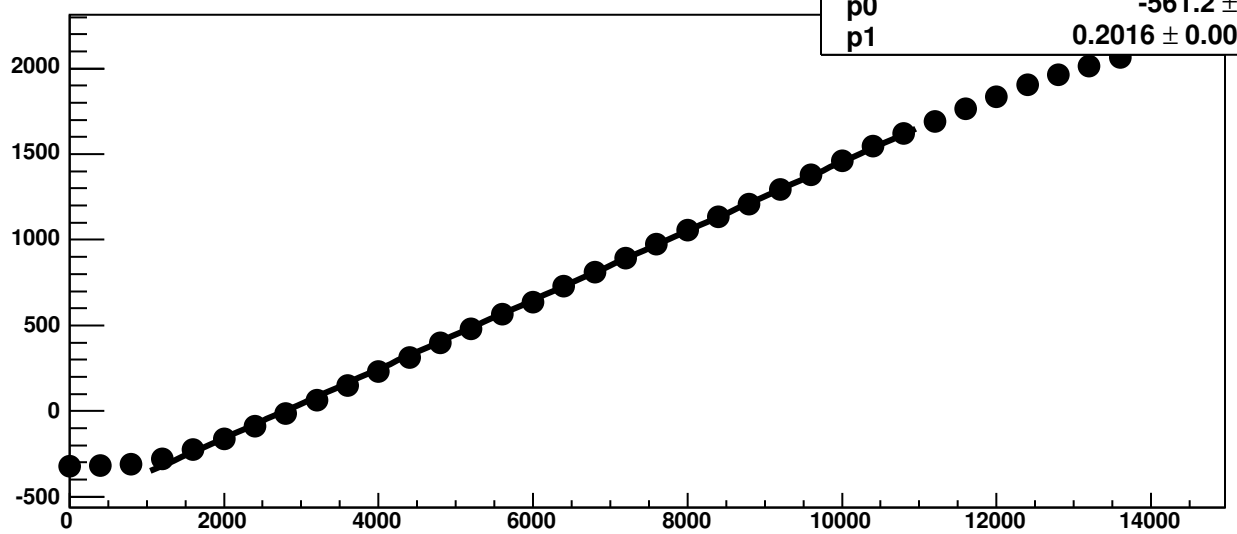

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3515 / 23
p0 3.06e+04 ± 2.933
p1 -0.006363 ± 0.0004081
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

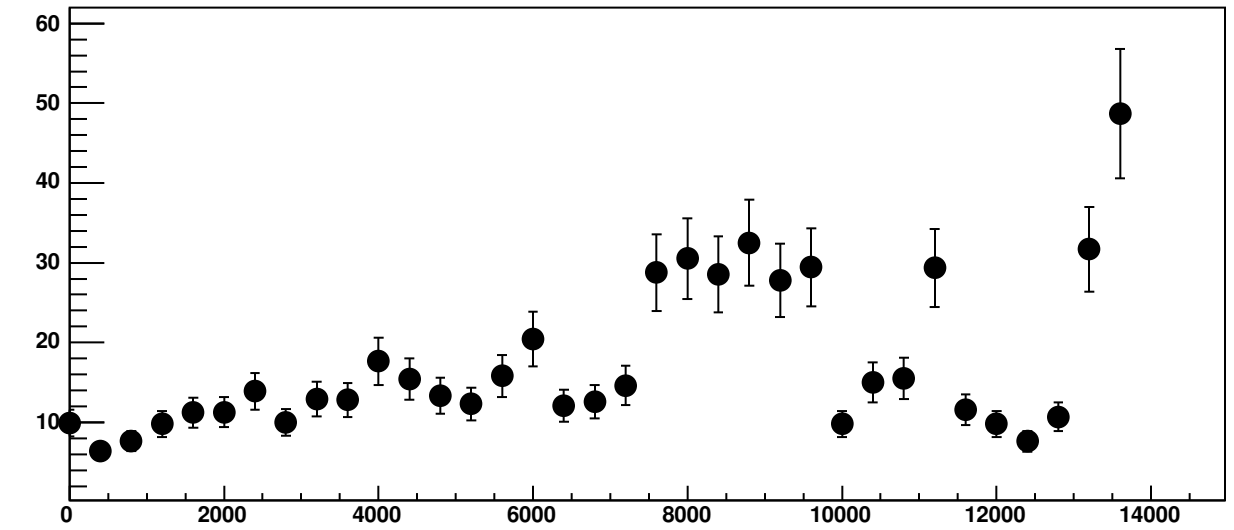

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

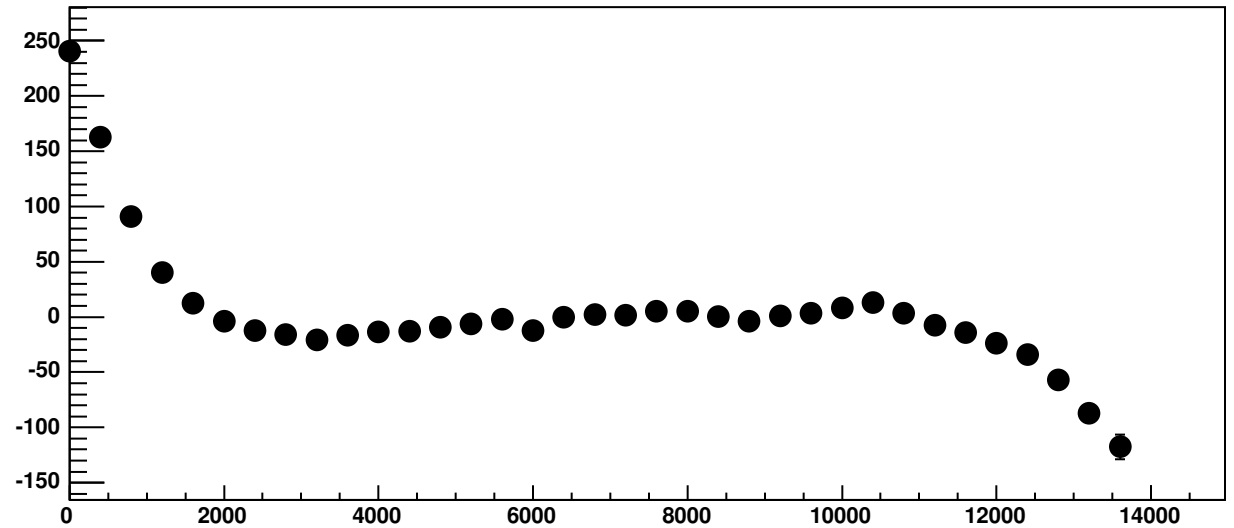
χ^2 / ndf	563.8 / 23
p0	-561.2 ± 1.283
p1	0.2016 ± 0.0002172

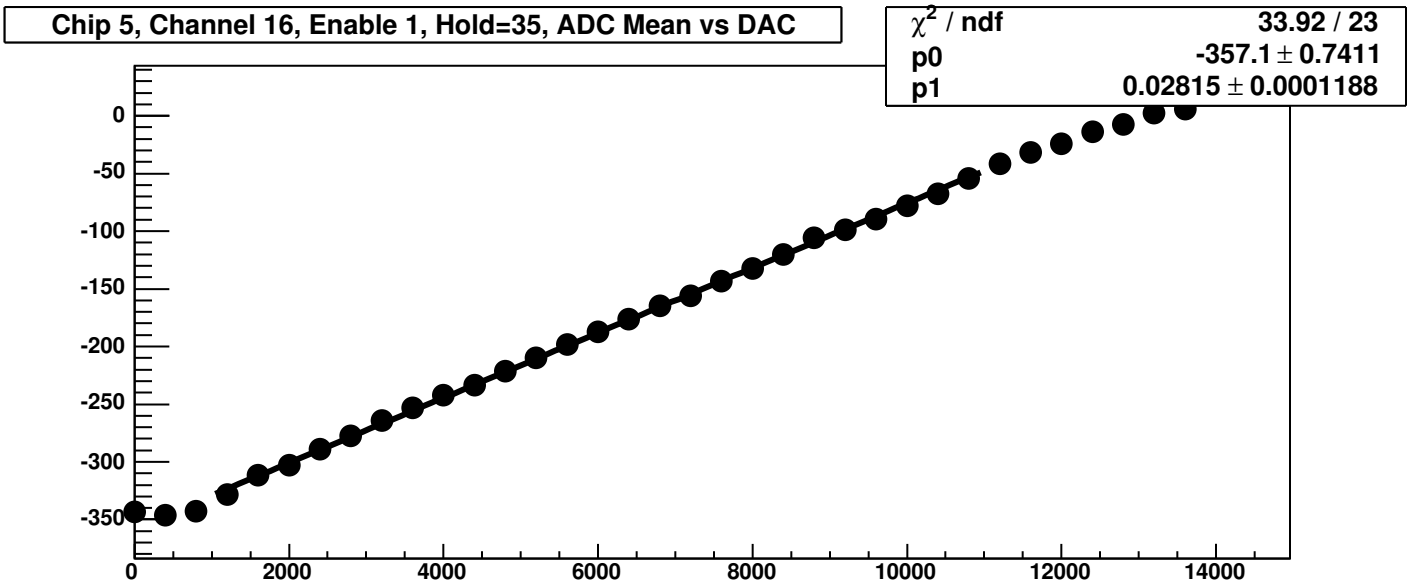


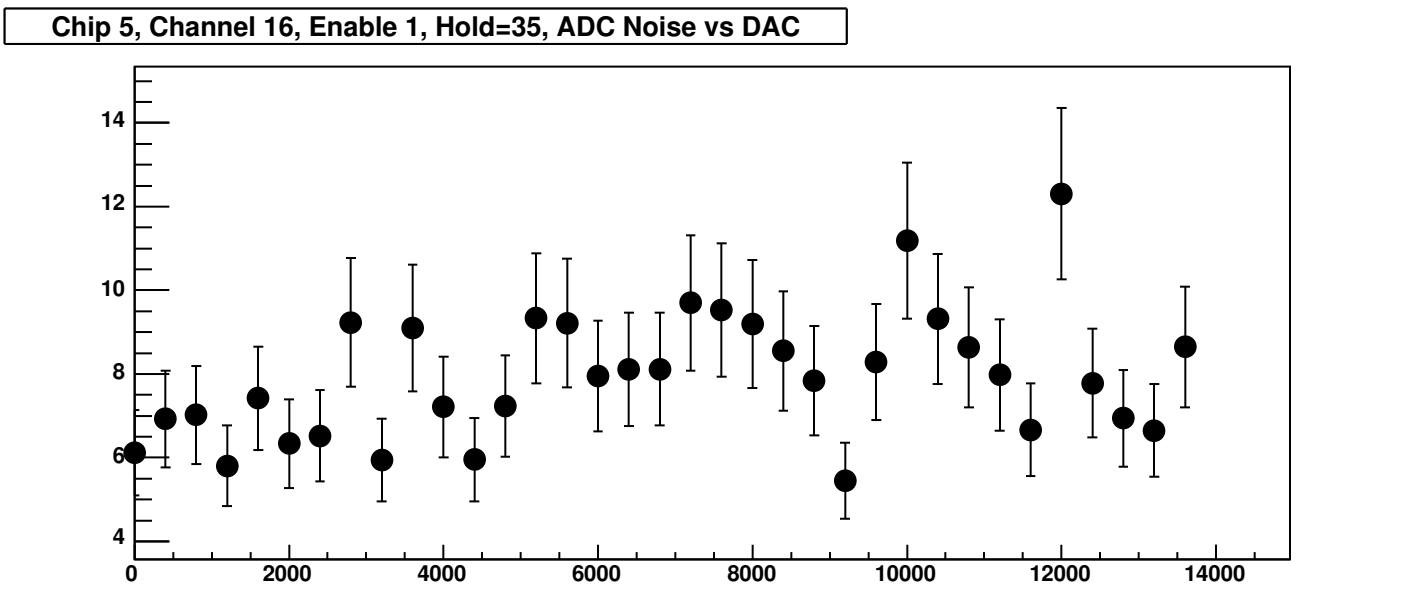
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

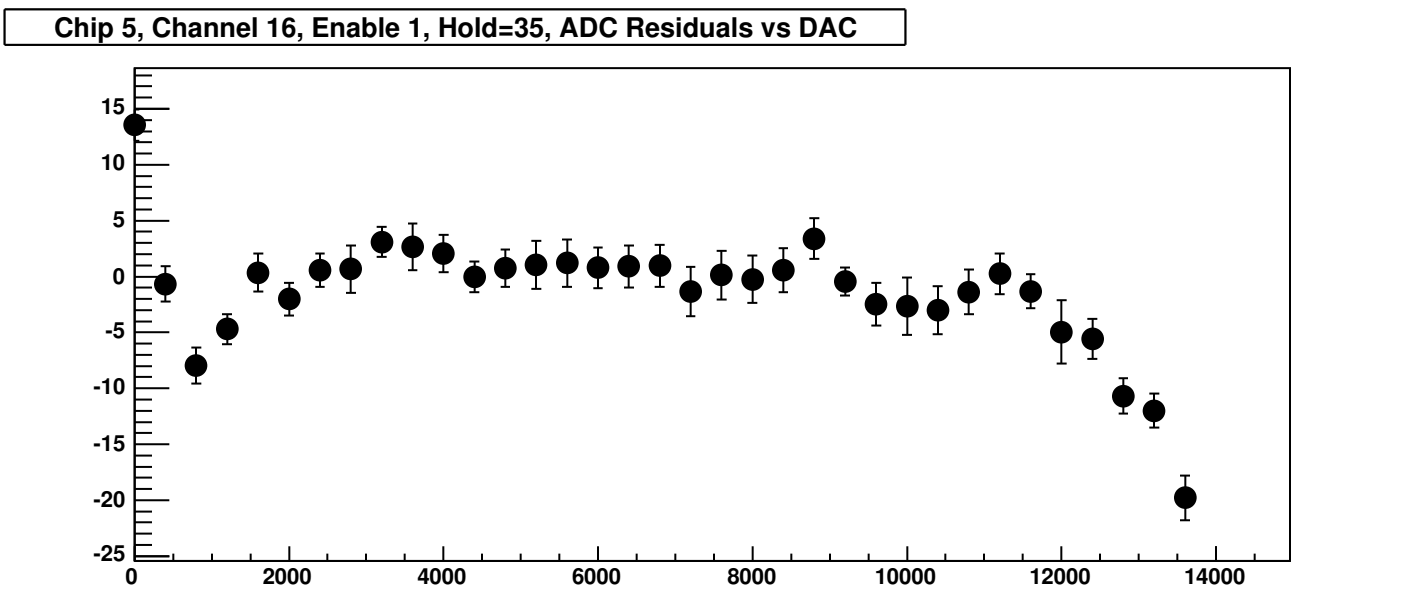


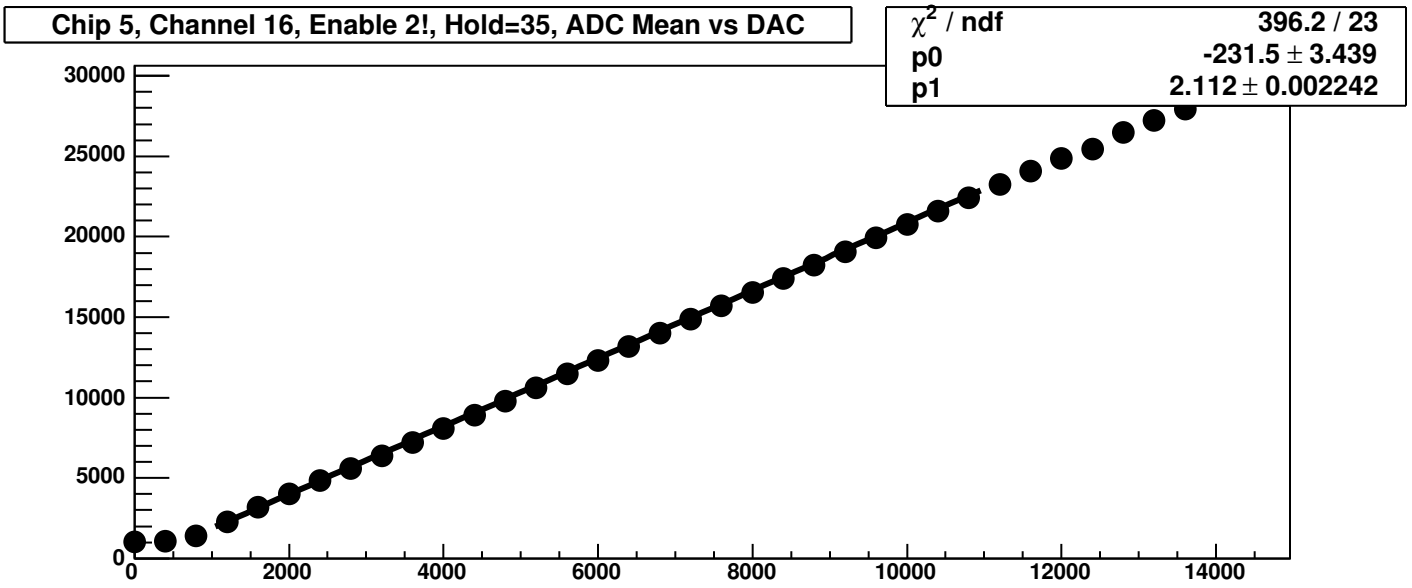
Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

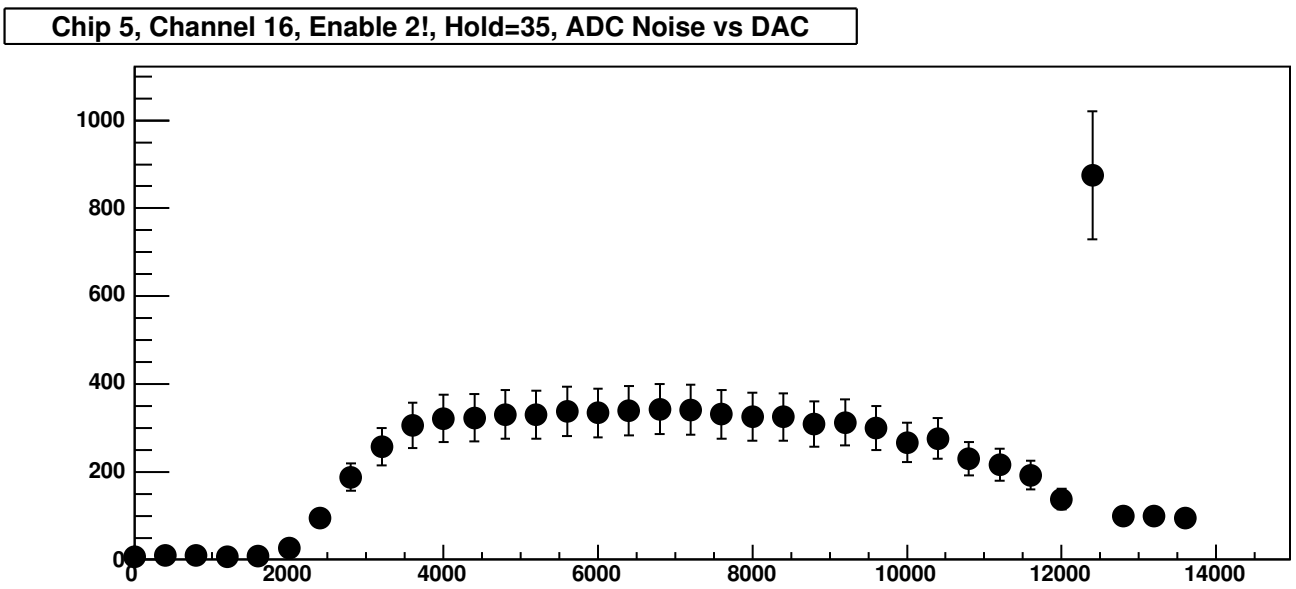


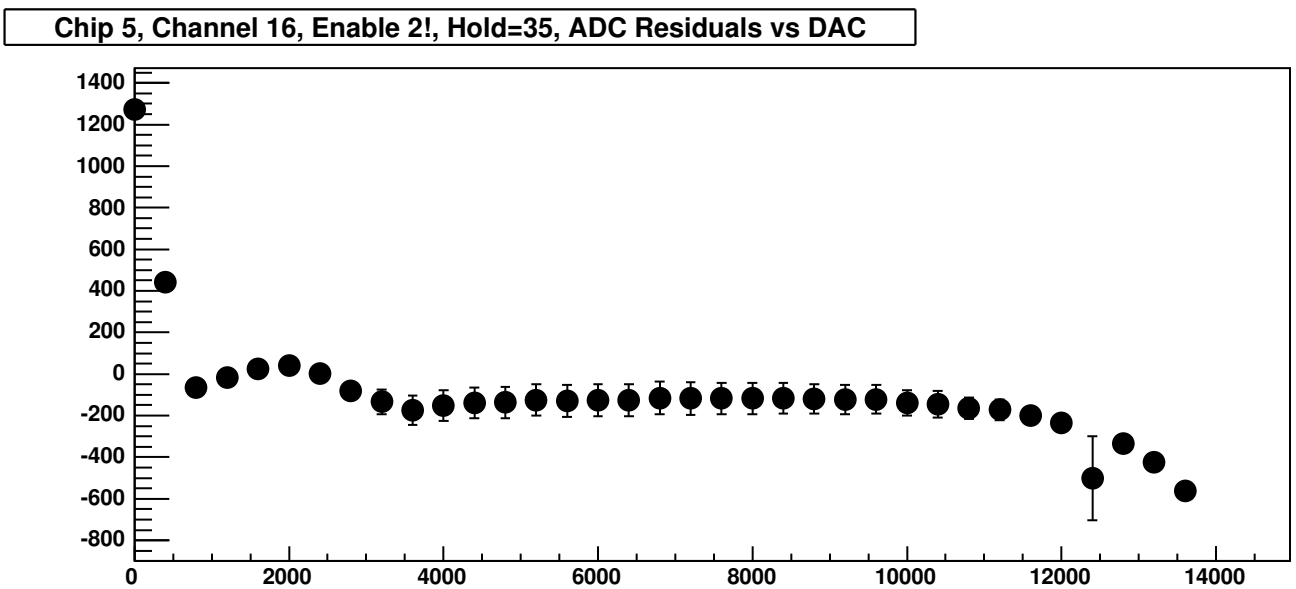

Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.92 / 23
p0 -357.1 ± 0.7411
p1 0.02815 ± 0.0001188
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

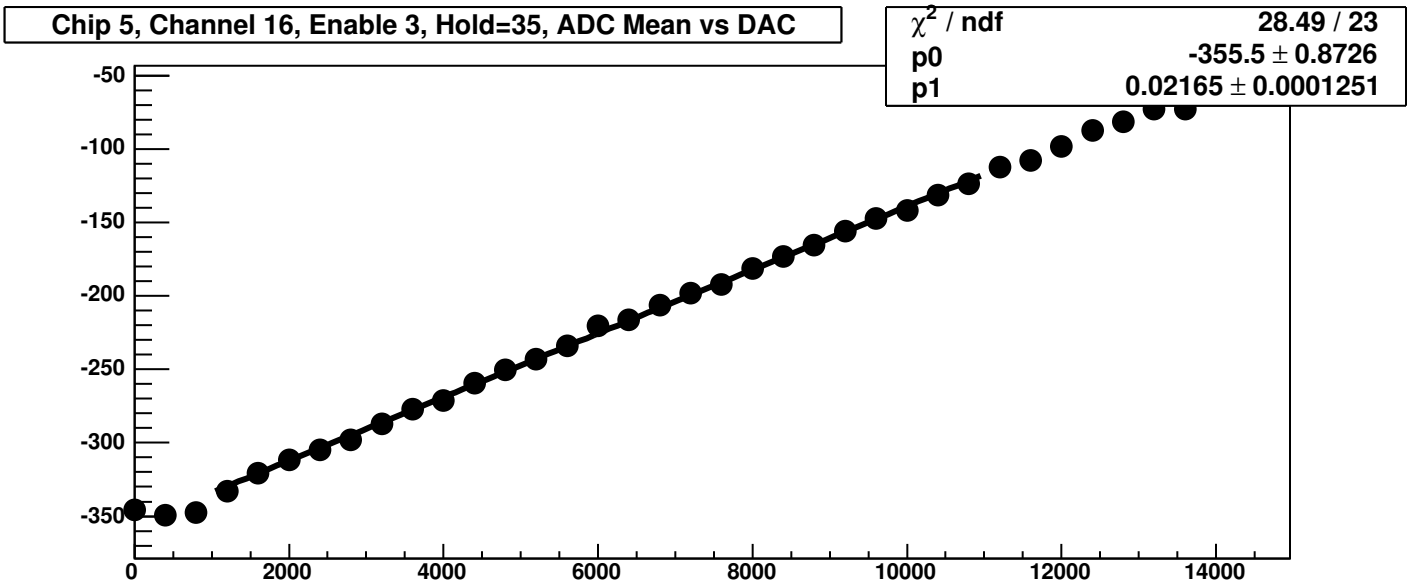

Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

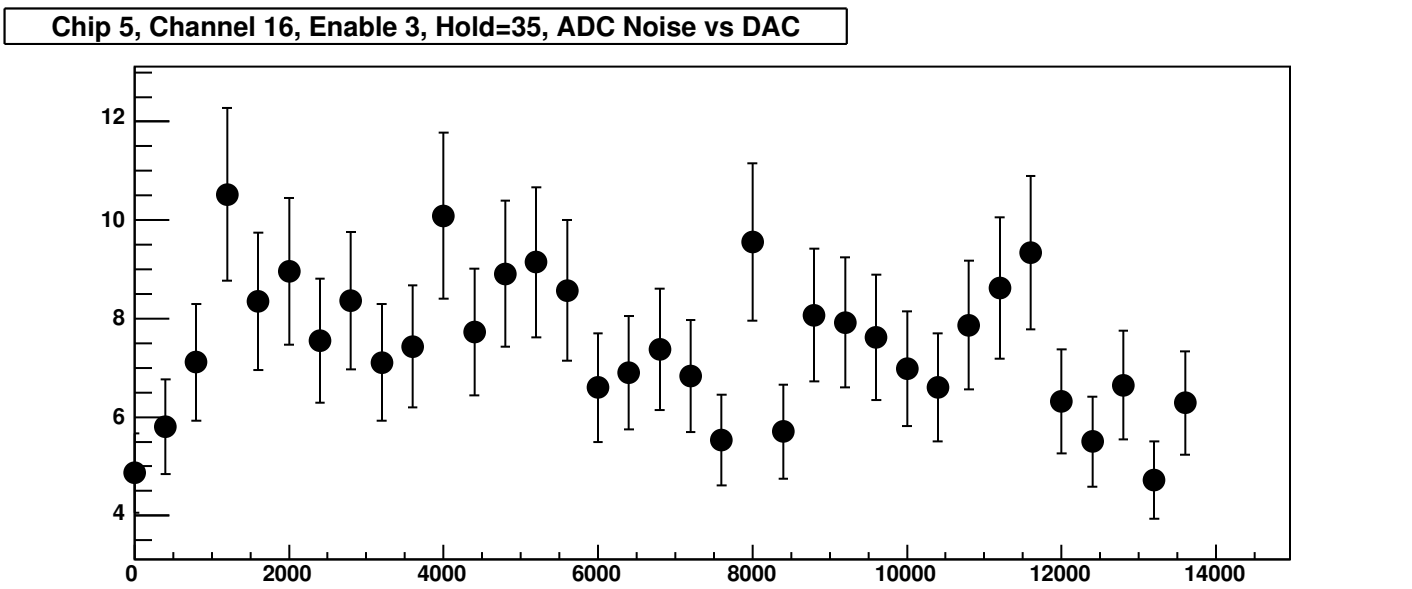

Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

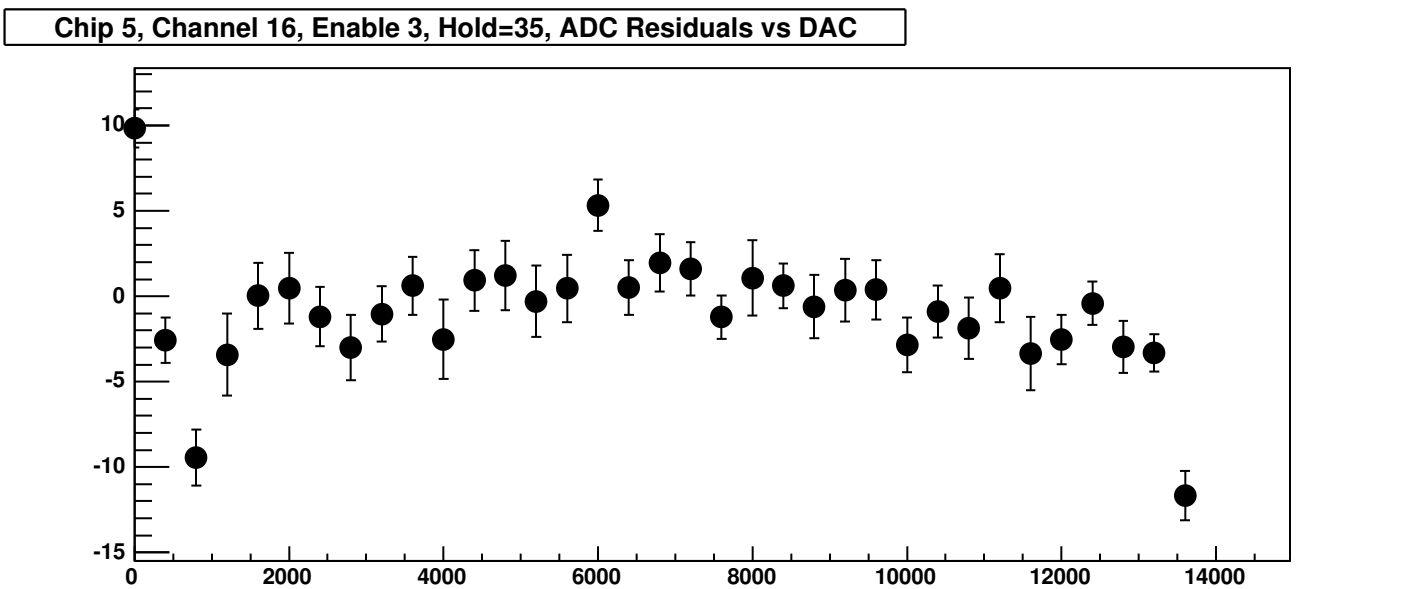

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 396.2 / 23
p0 -231.5 ± 3.439
p1 2.112 ± 0.002242
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

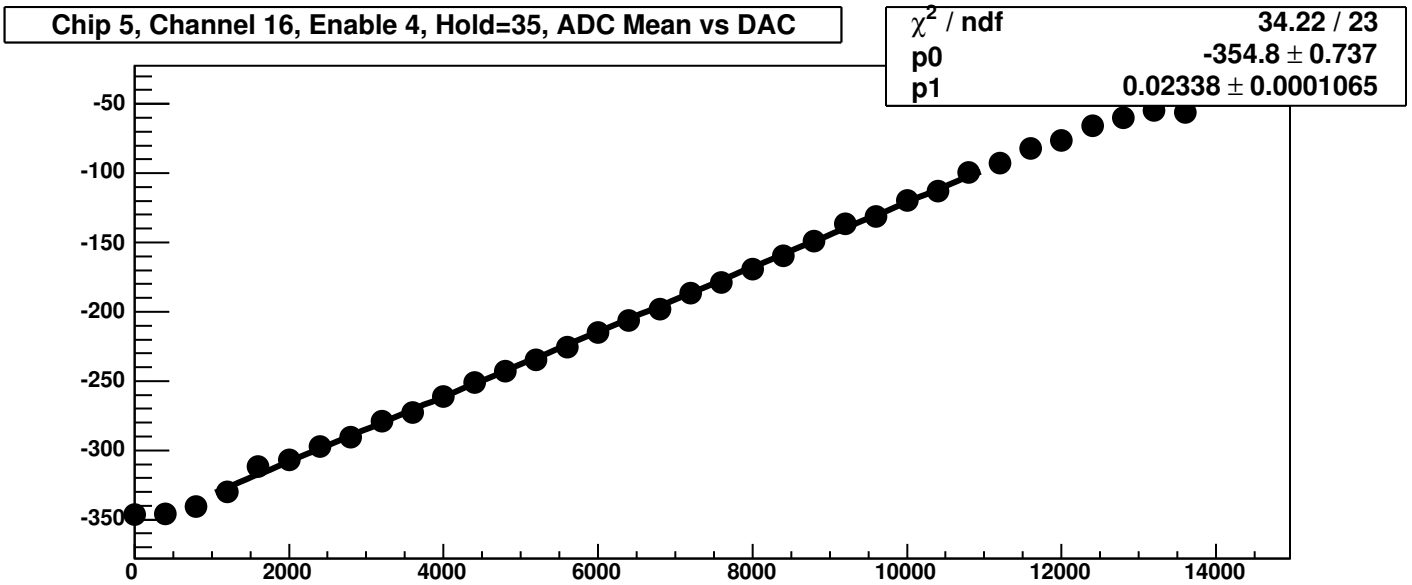

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

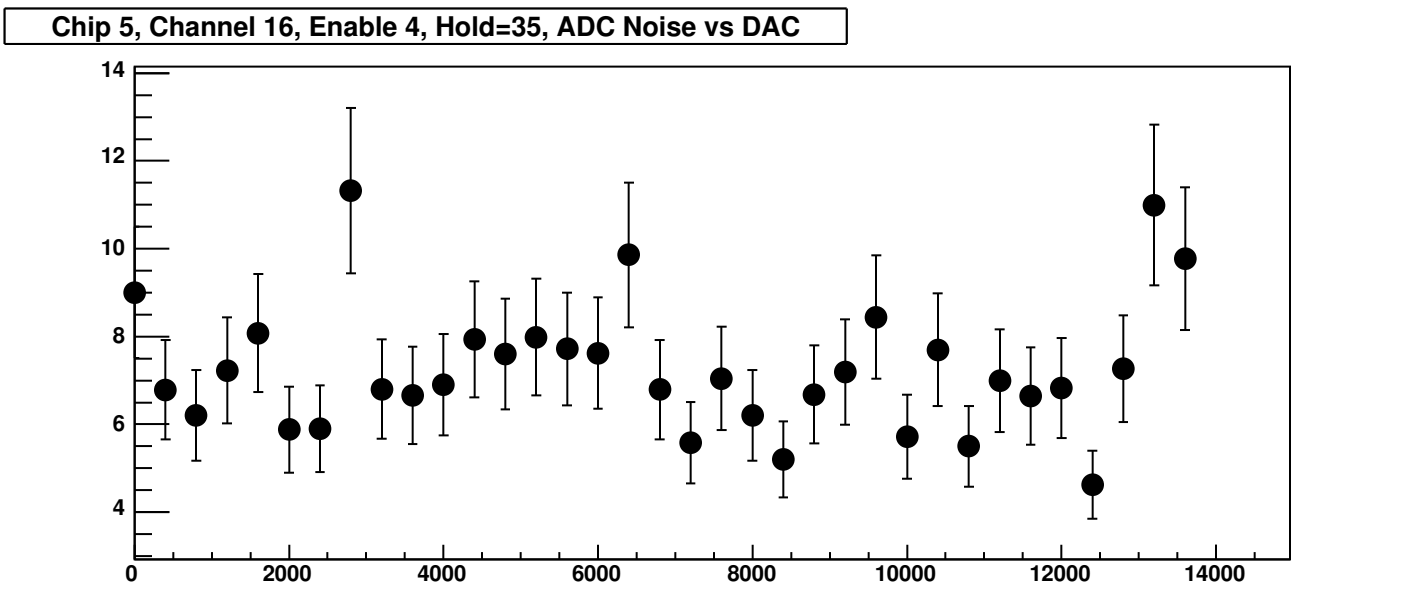

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

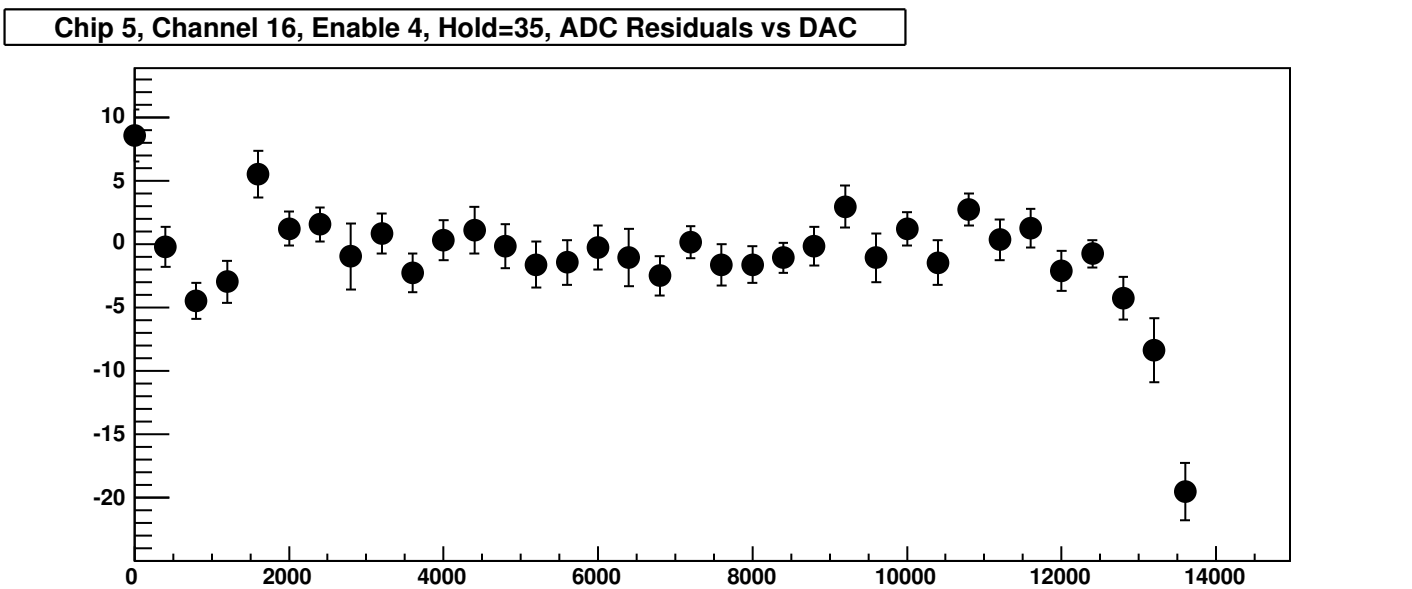

Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.49 / 23
p0 -355.5 ± 0.8726
p1 0.02165 ± 0.0001251
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

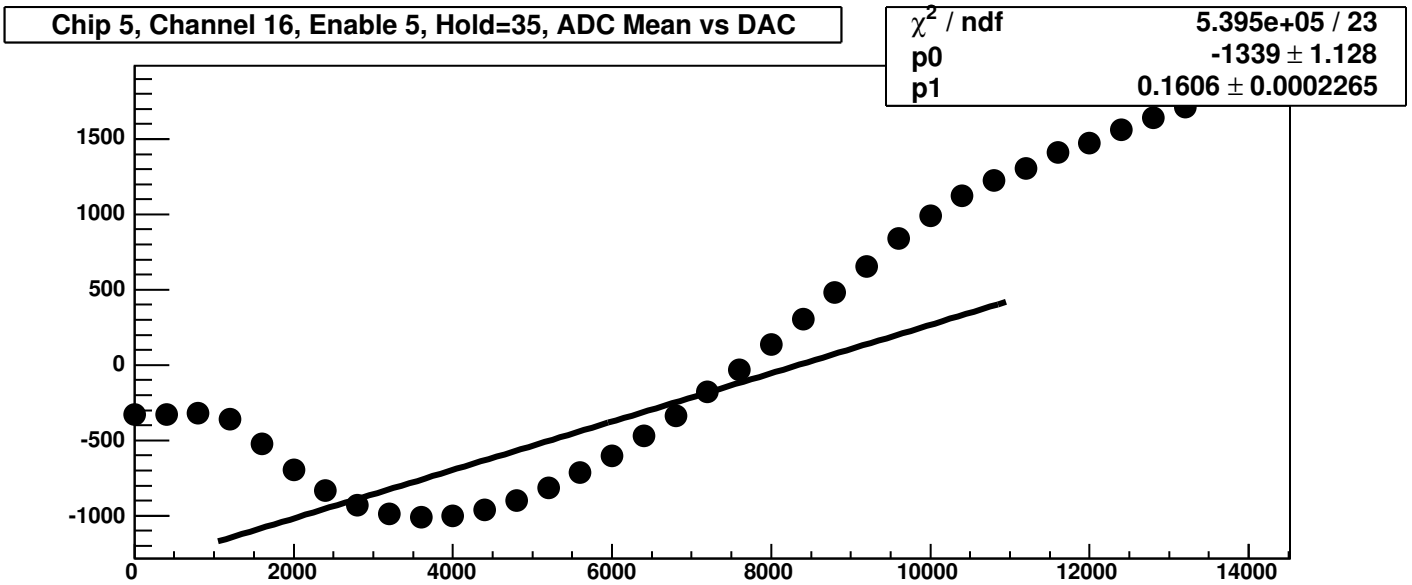

Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

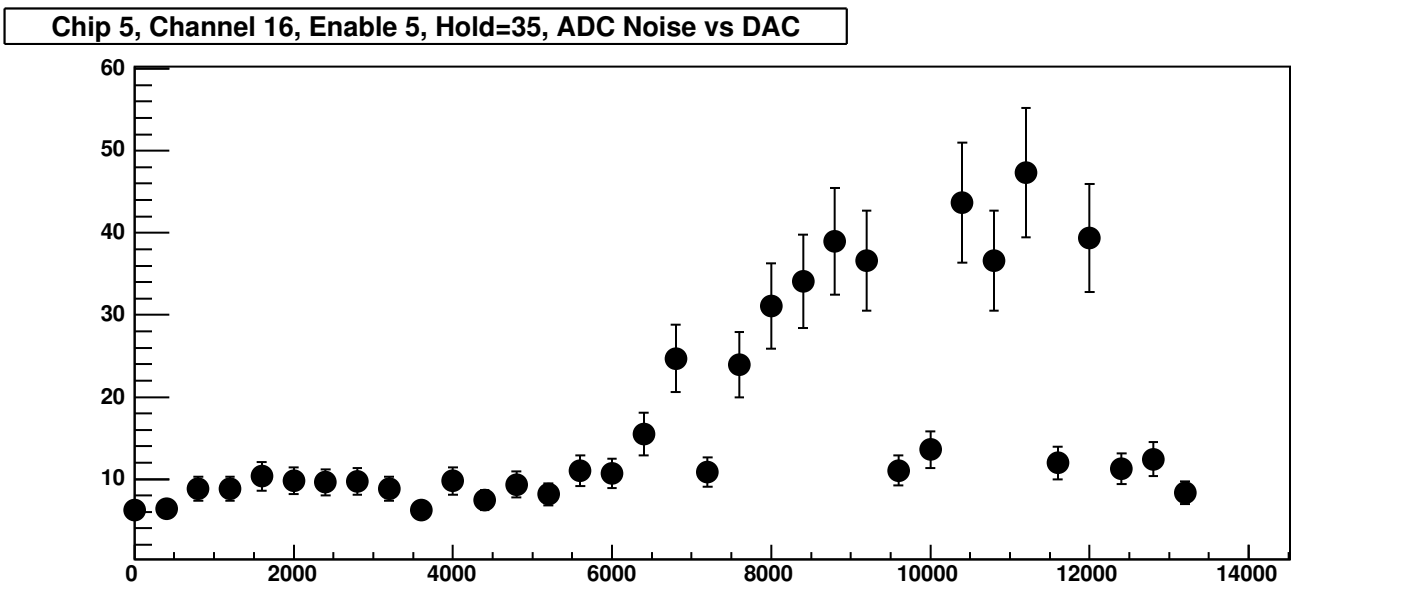

Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

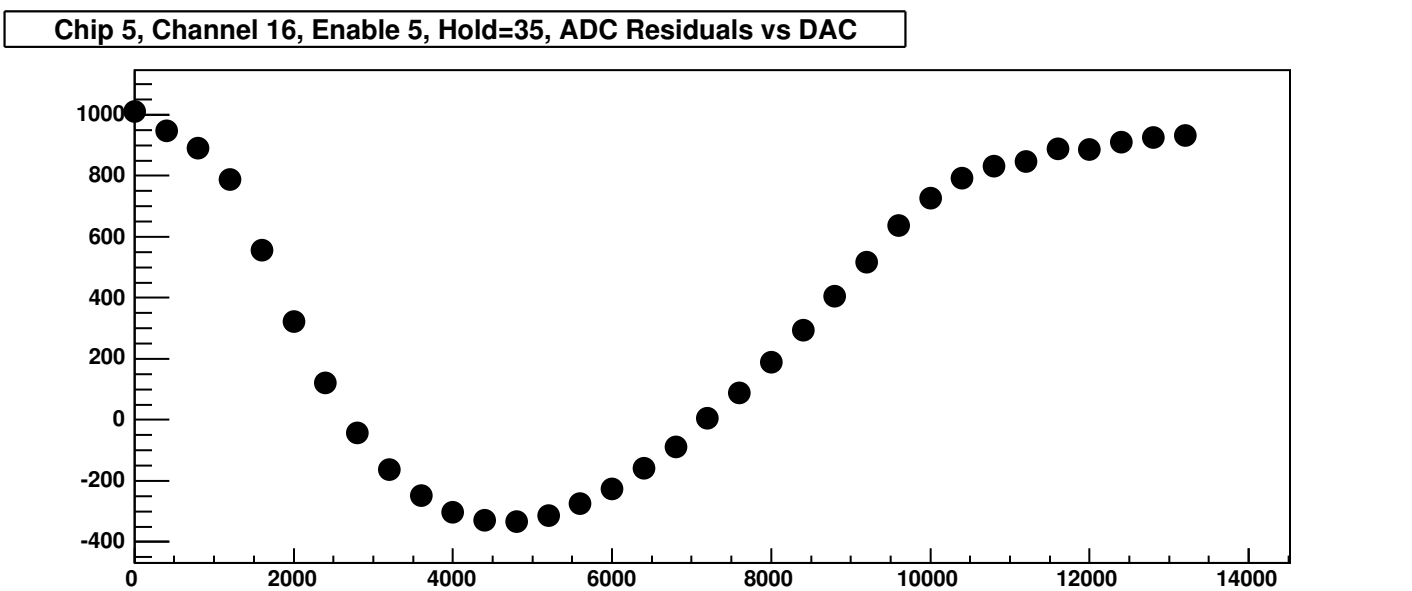

Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.22 / 23
p0 -354.8 ± 0.737
p1 0.02338 ± 0.0001065
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

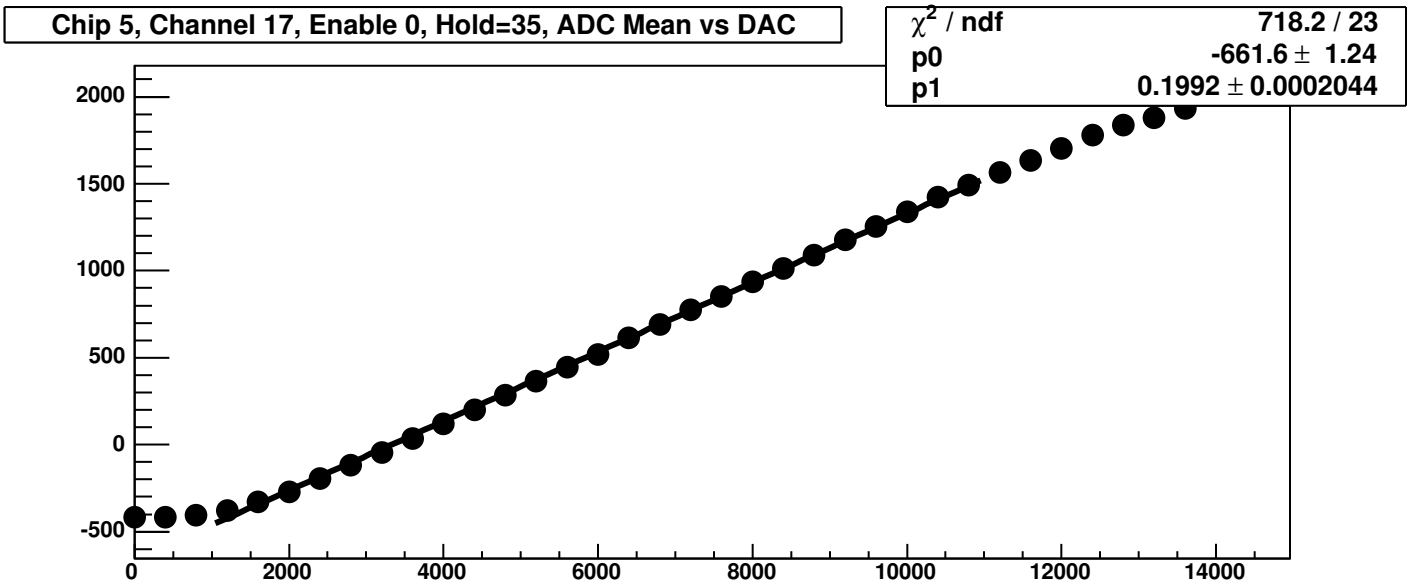

Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

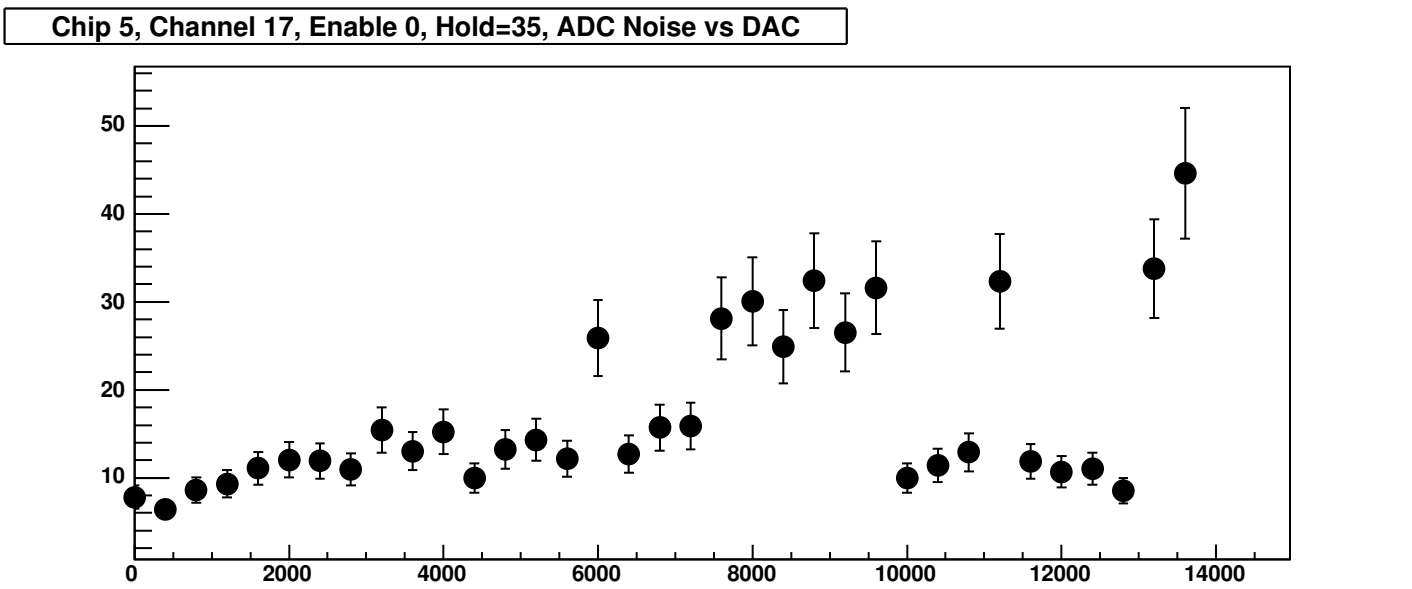

Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

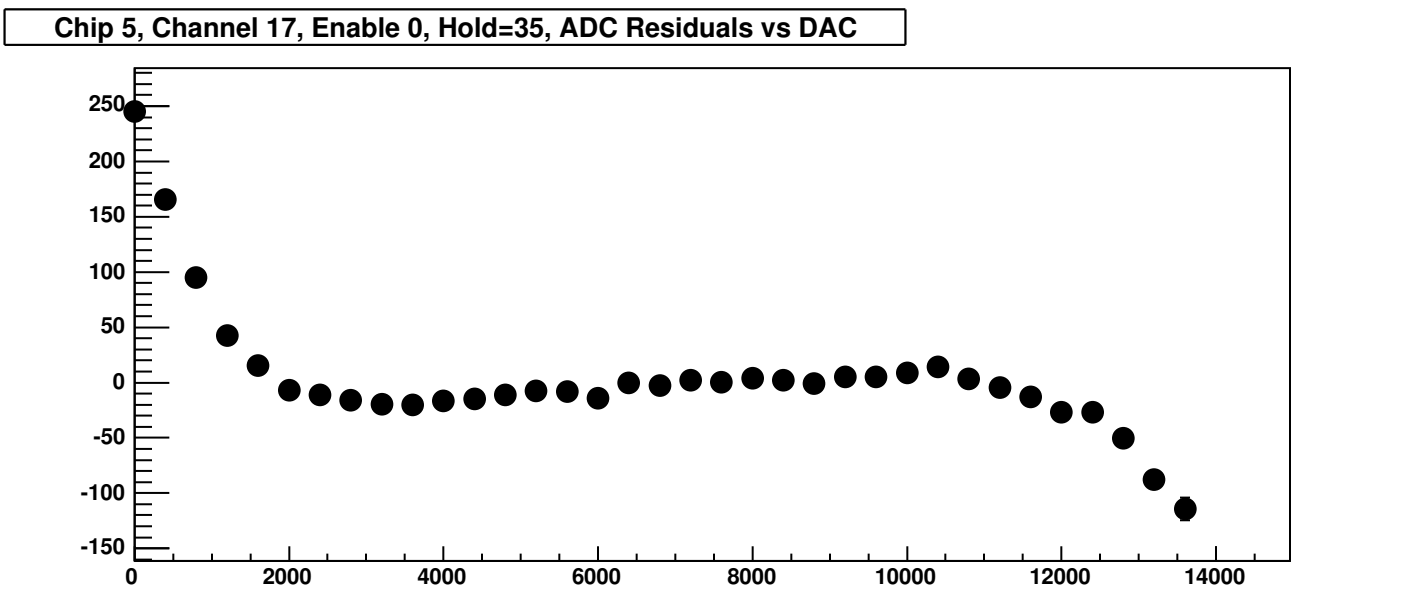

Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.395e+05 / 23
p0 -1339 ± 1.128
p1 0.1606 ± 0.0002265
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

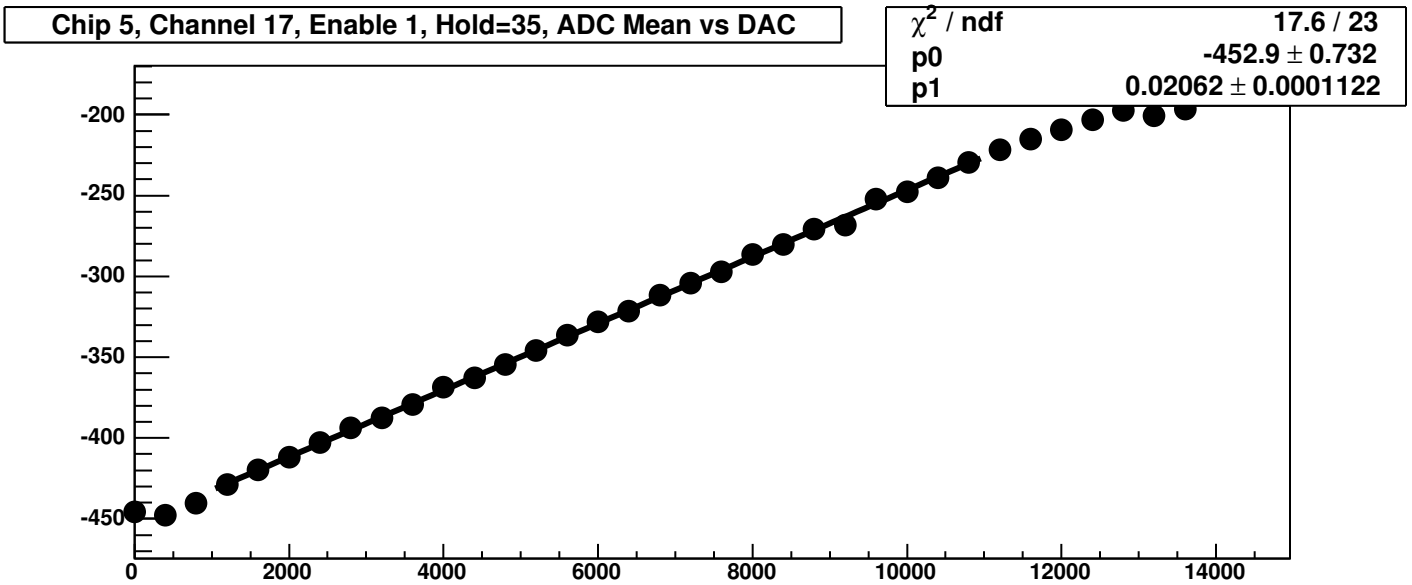

Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

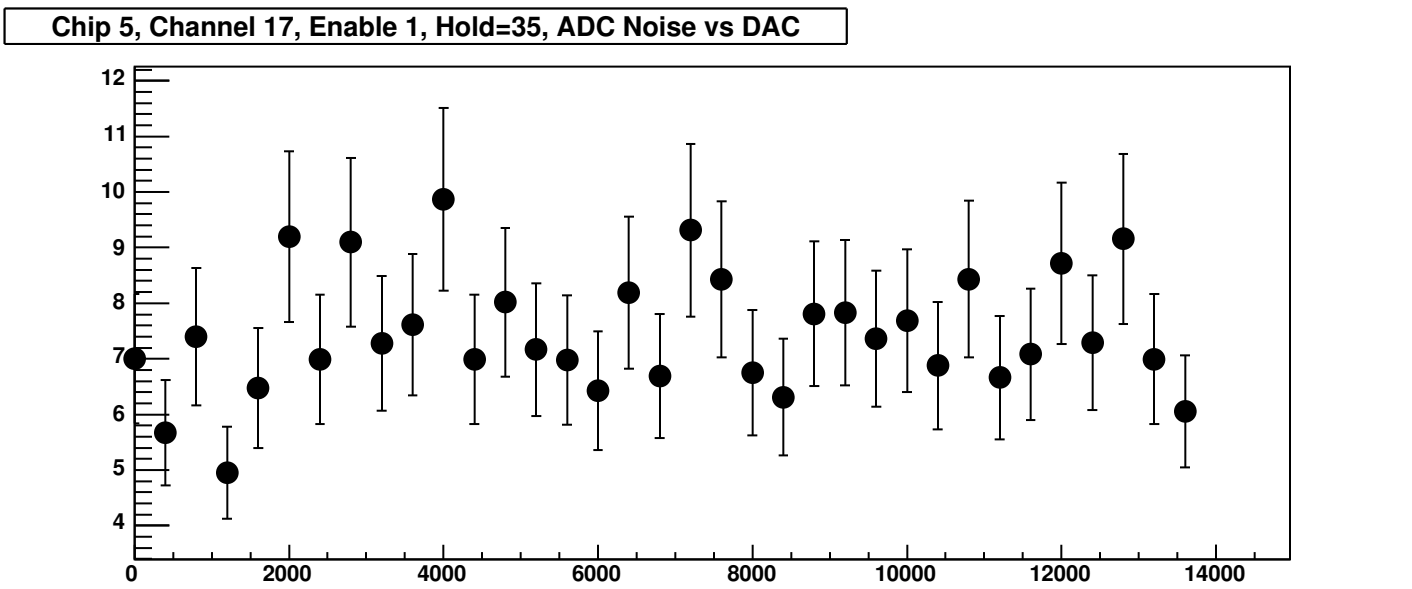

Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

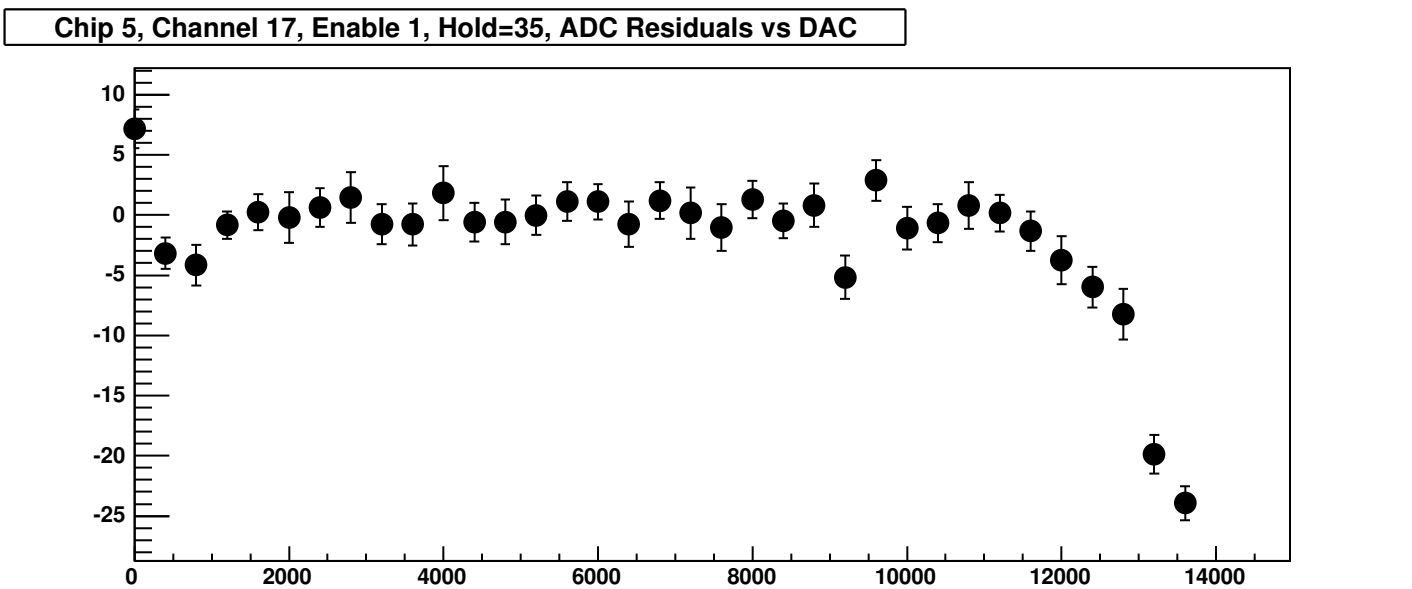

Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 718.2 / 23
p0 -661.6 ± 1.24
p1 0.1992 ± 0.0002044
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

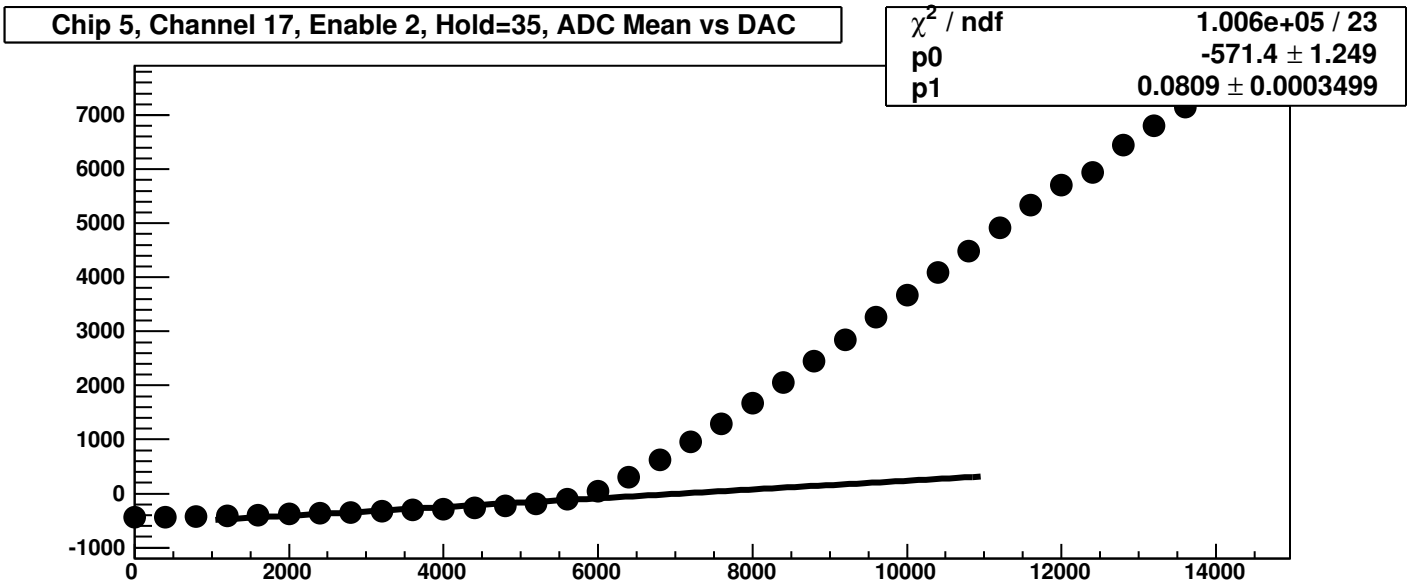

Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

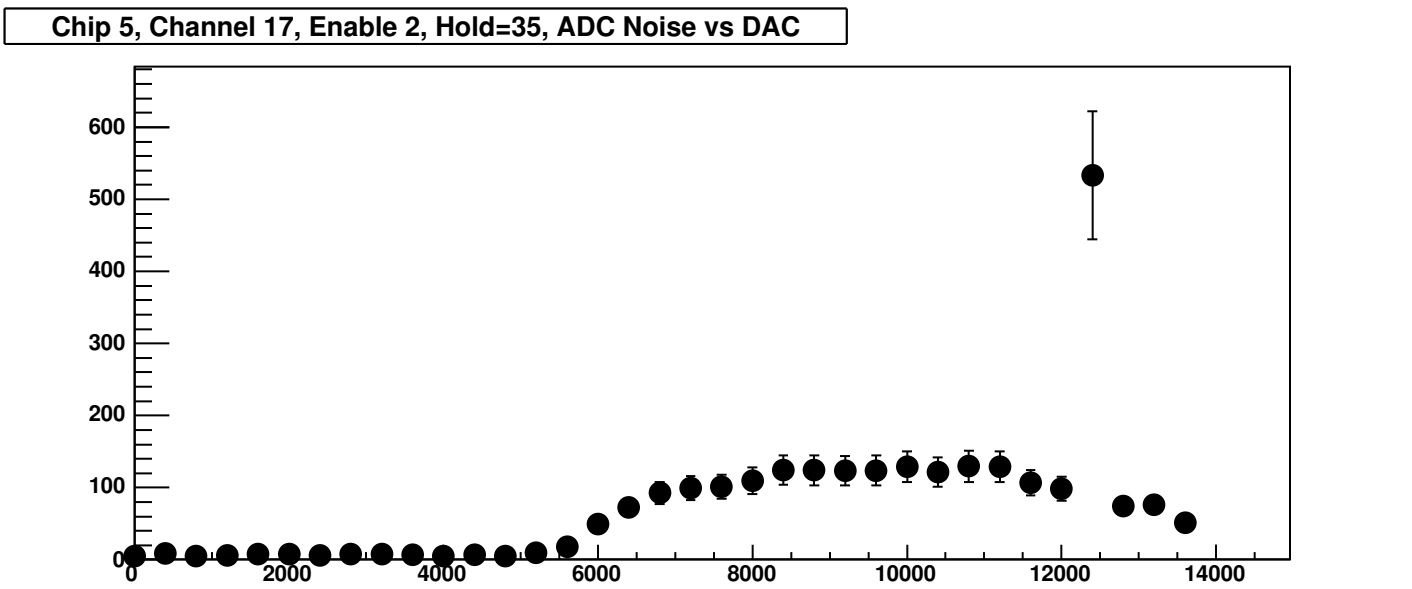

Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

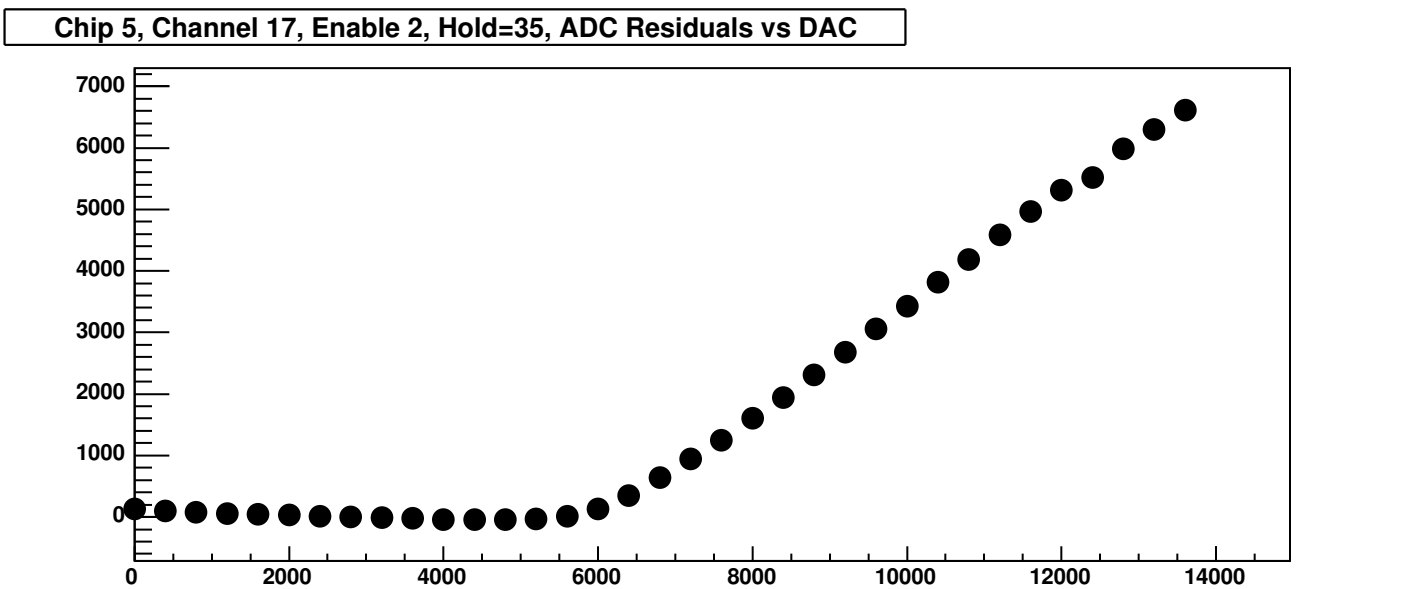

Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 17.6 / 23
p0 -452.9 ± 0.732
p1 0.02062 ± 0.0001122
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

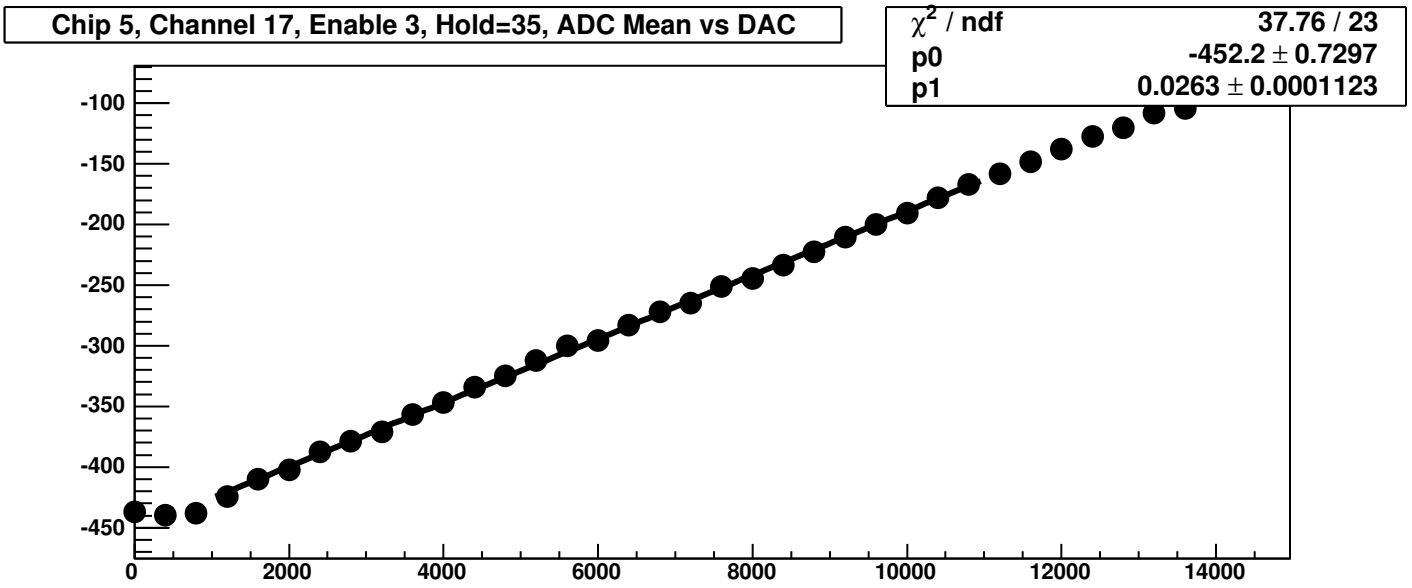

Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

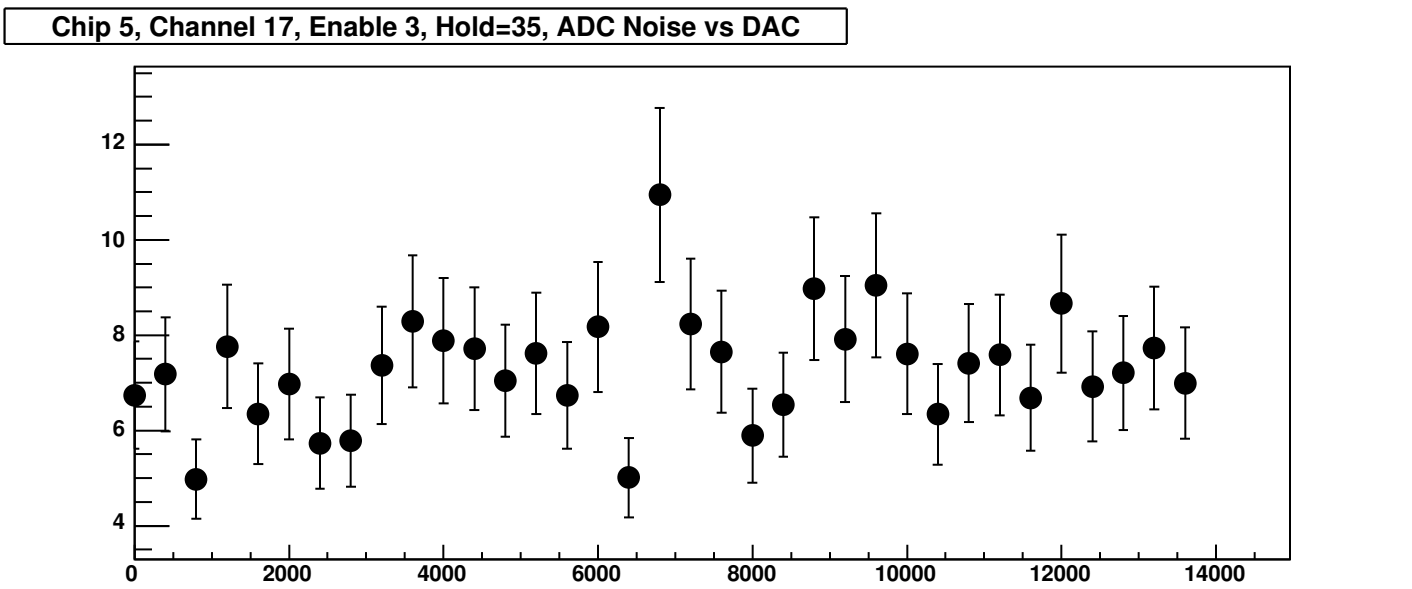

Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

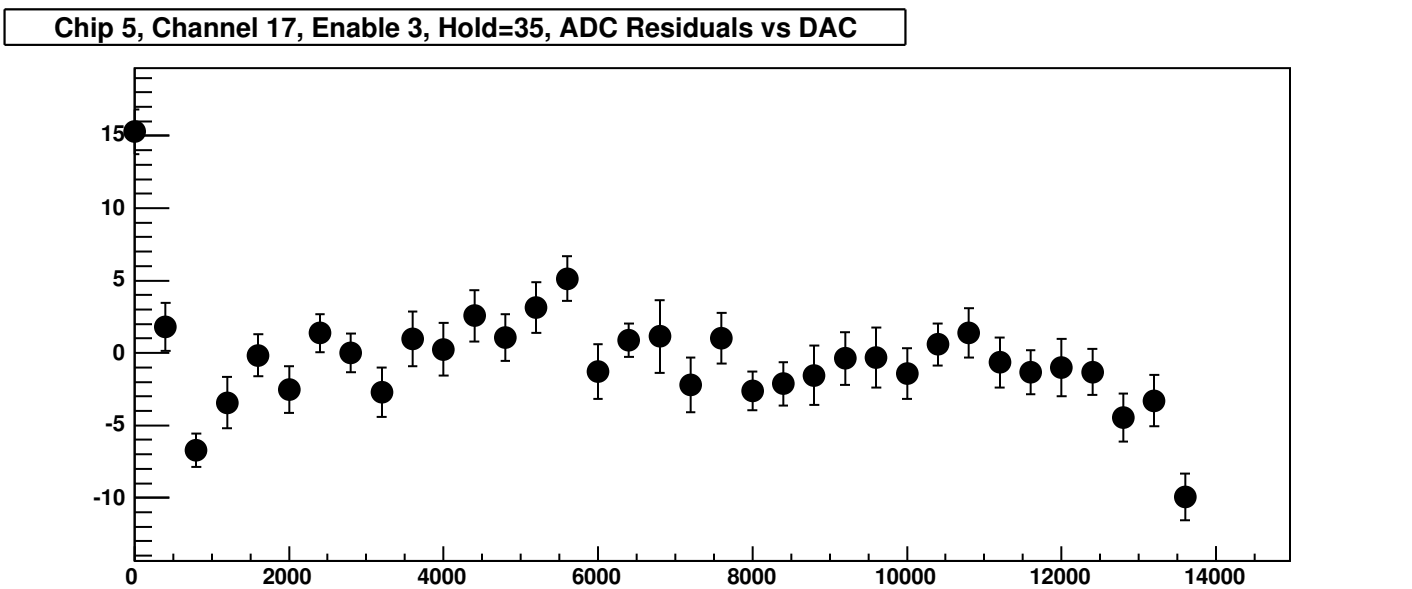

Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.006e+05 / 23
p0 -571.4 ± 1.249
p1 0.0809 ± 0.0003499
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

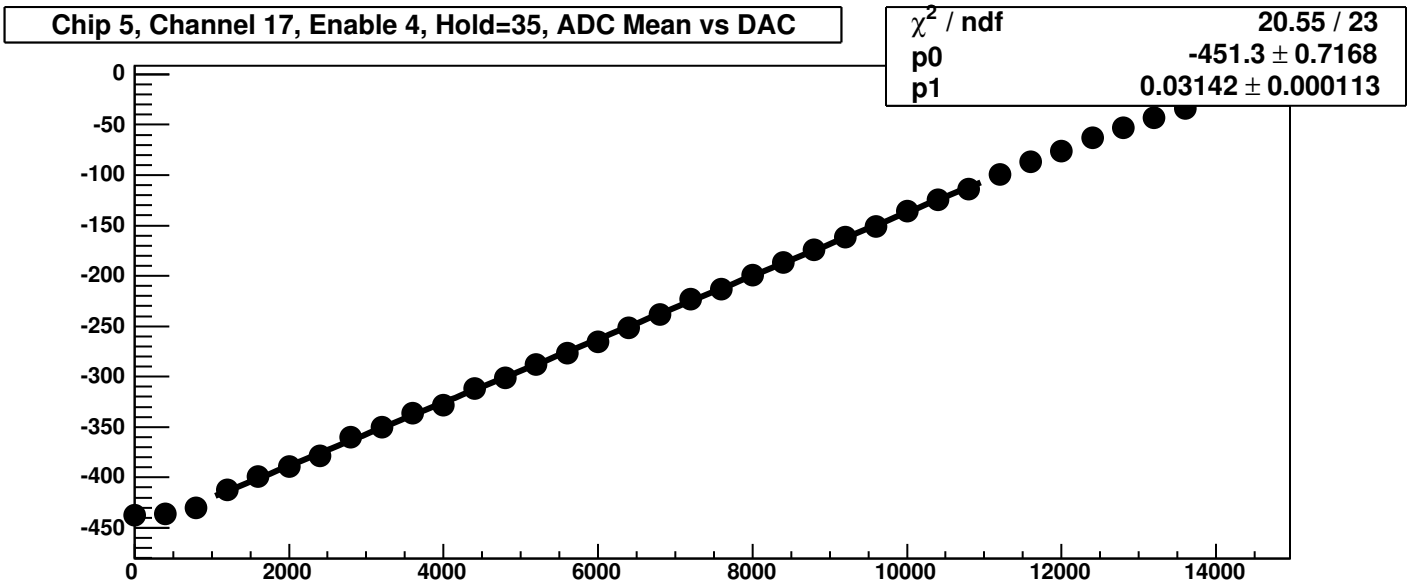

Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

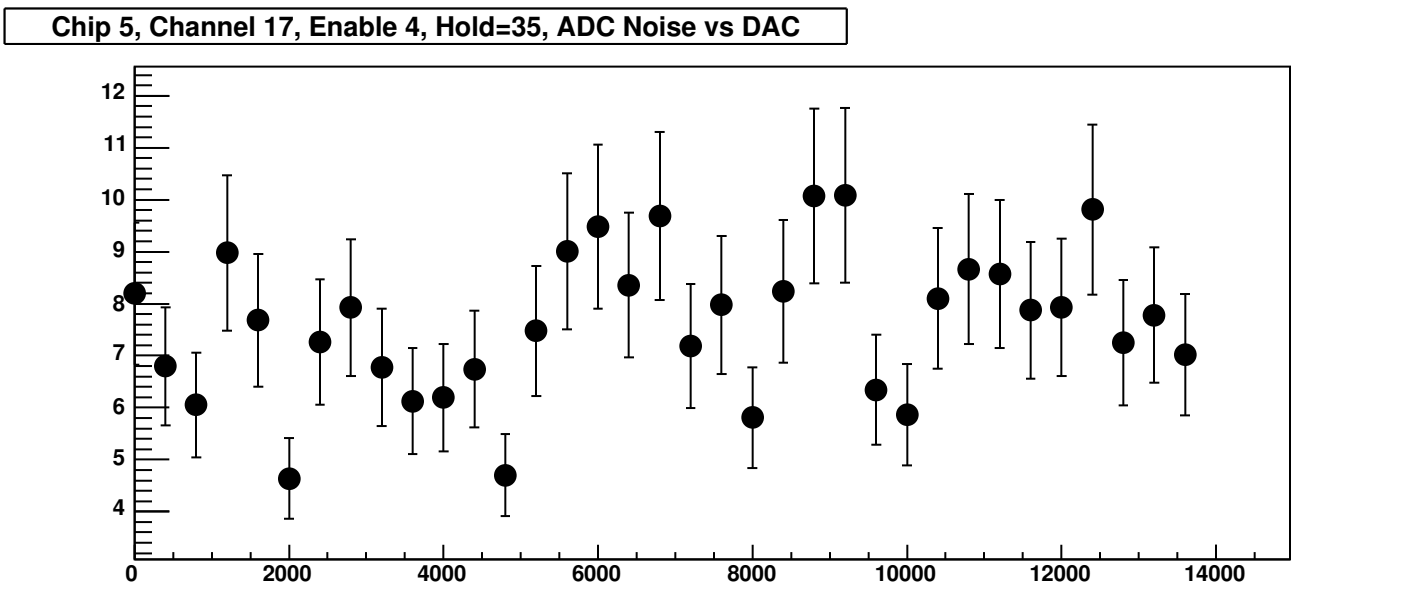

Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

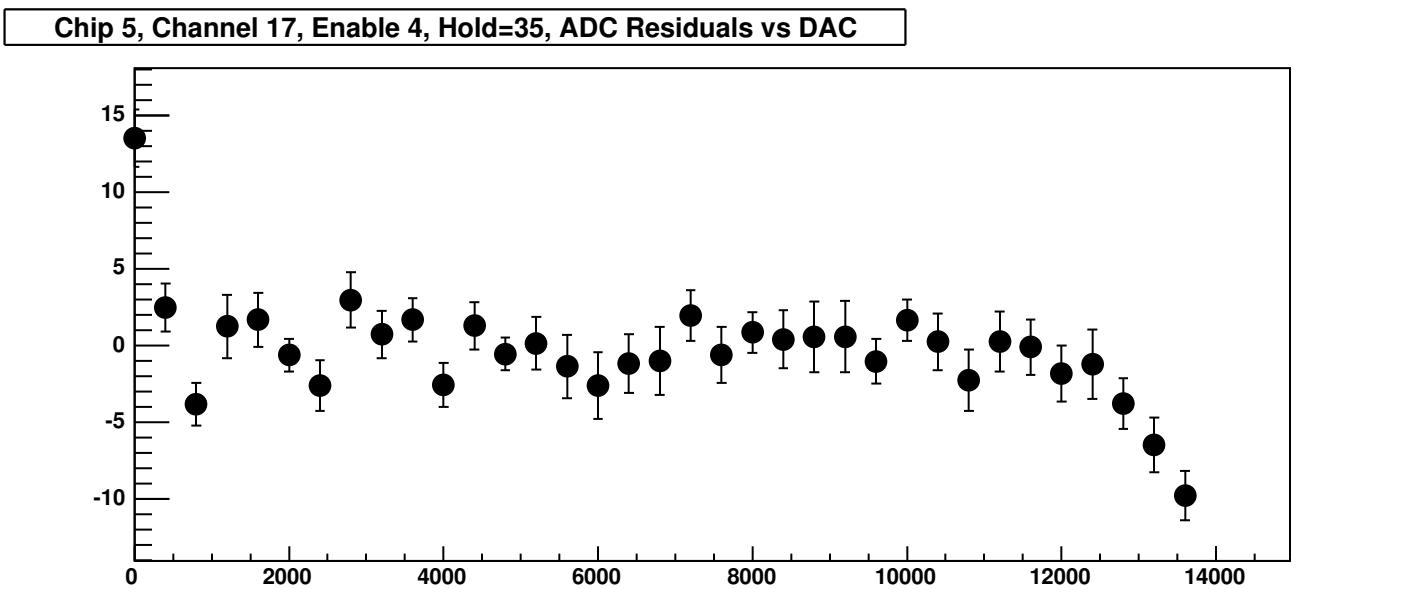

Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.76 / 23
p0 -452.2 ± 0.7297
p1 0.0263 ± 0.0001123
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

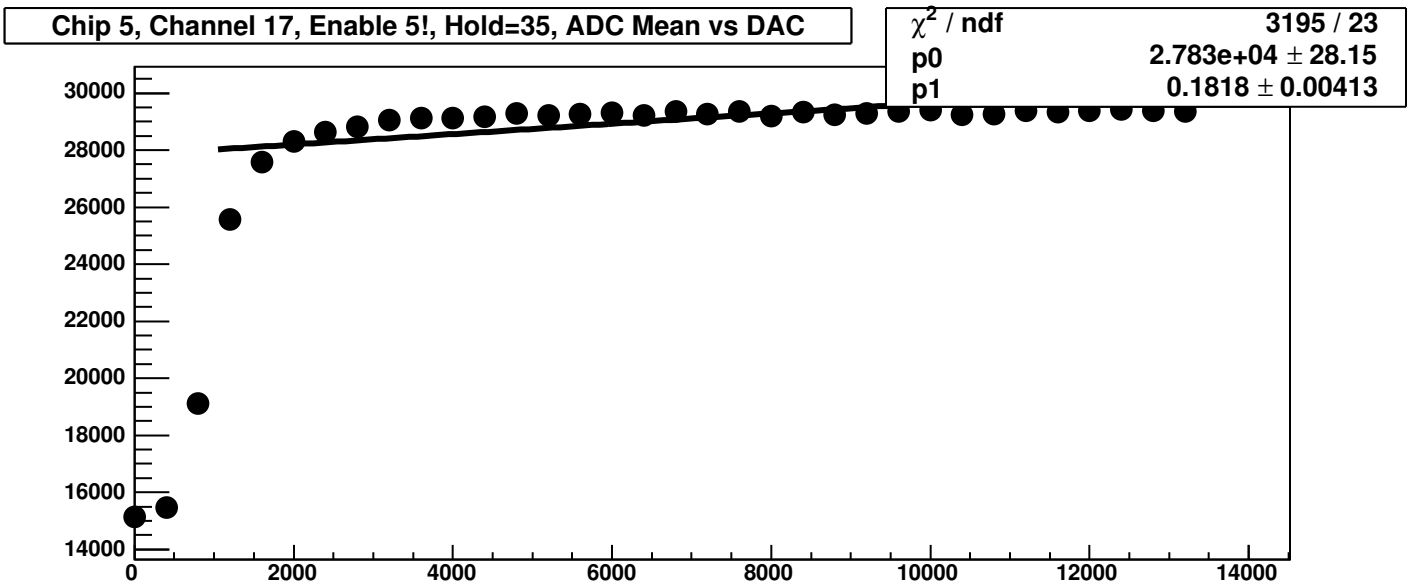

Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

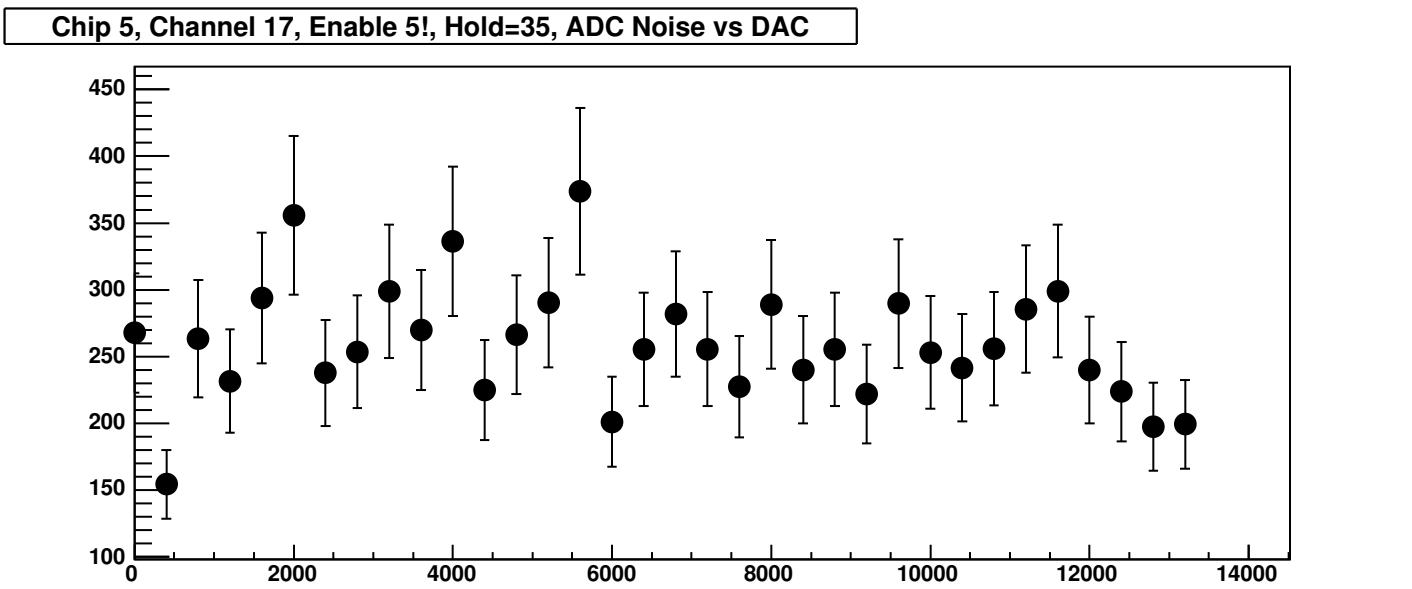

Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

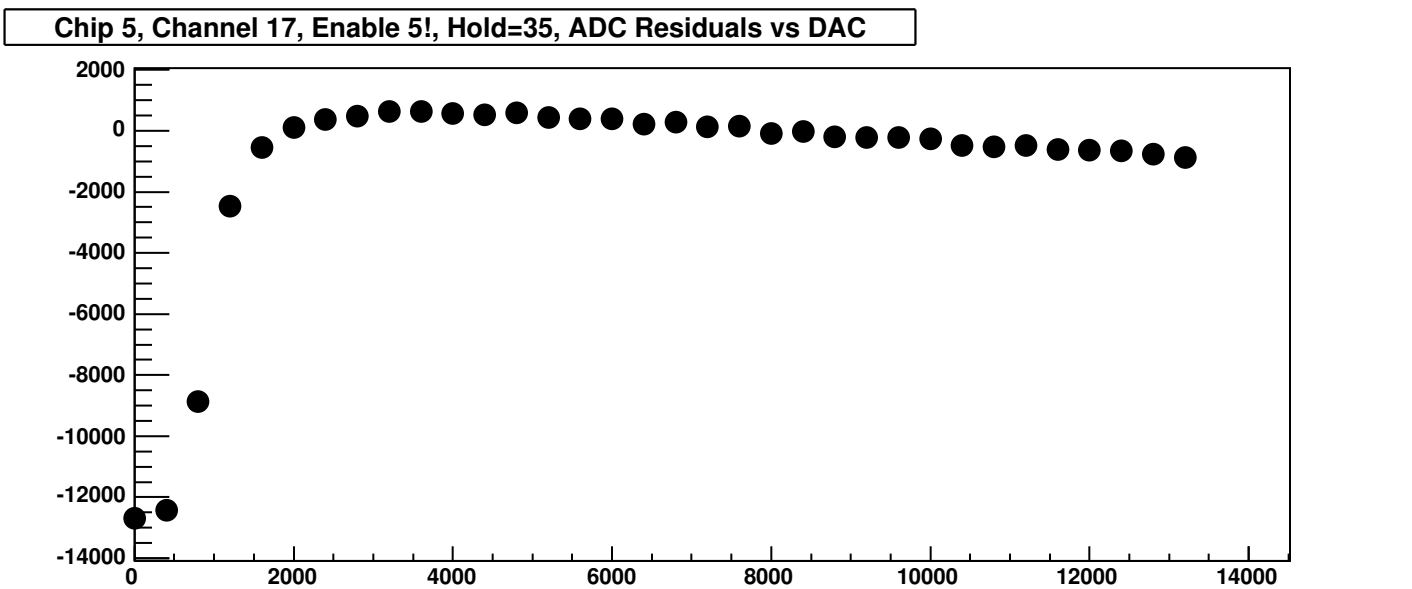

Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.55 / 23
p0 -451.3 ± 0.7168
p1 0.03142 ± 0.000113
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

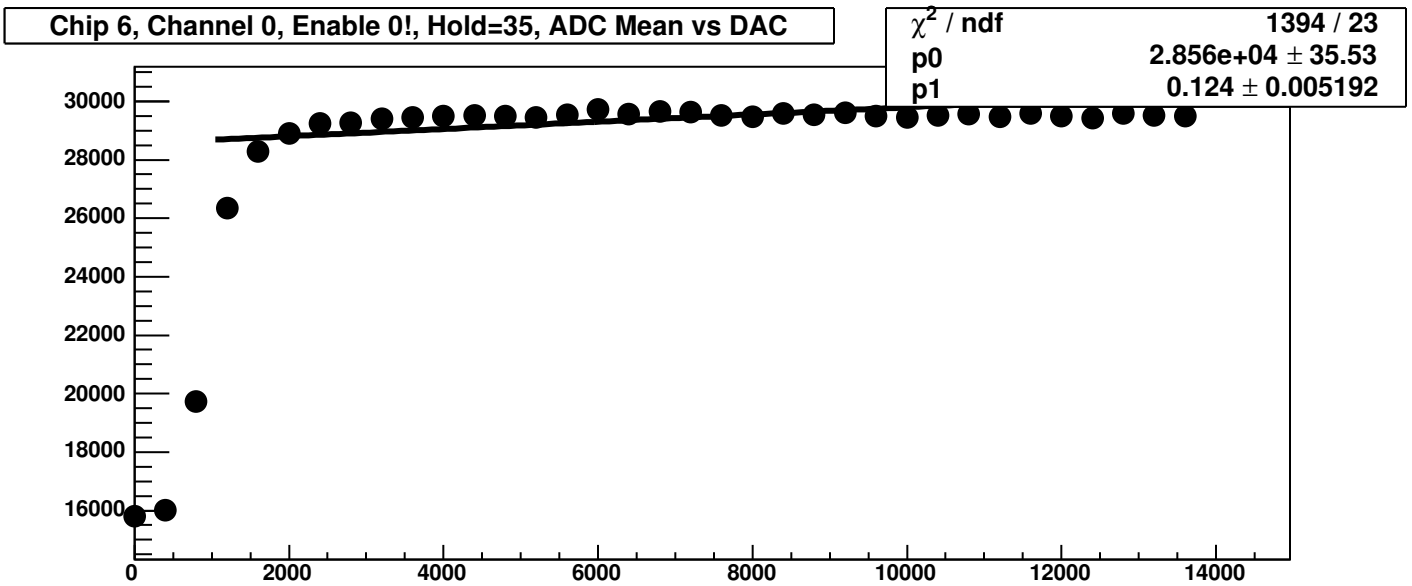

Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

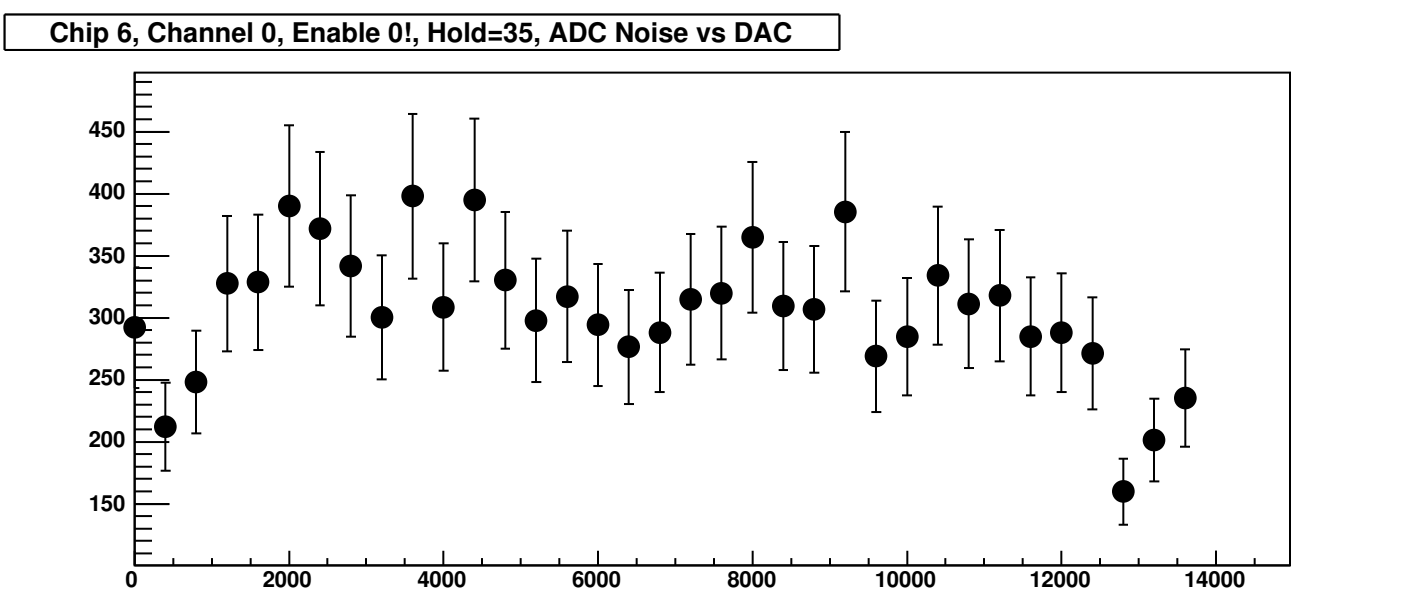

Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

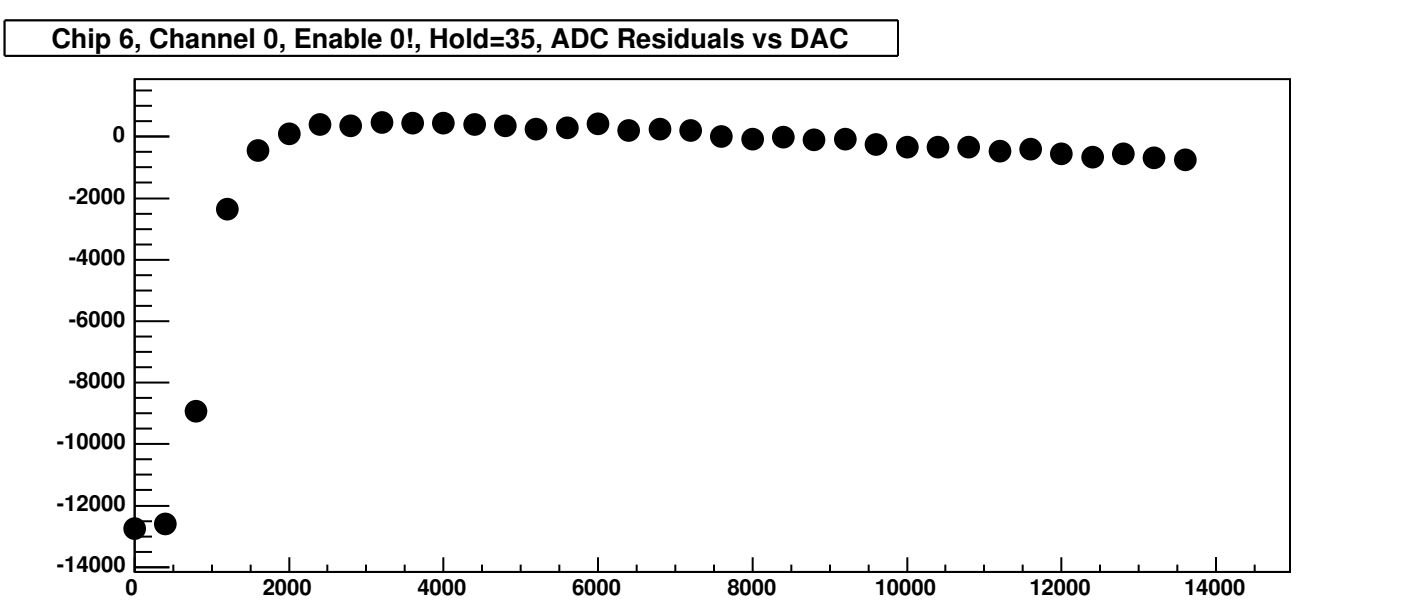

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3195 / 23
p0 2.783e+04 ± 28.15
p1 0.1818 ± 0.00413
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

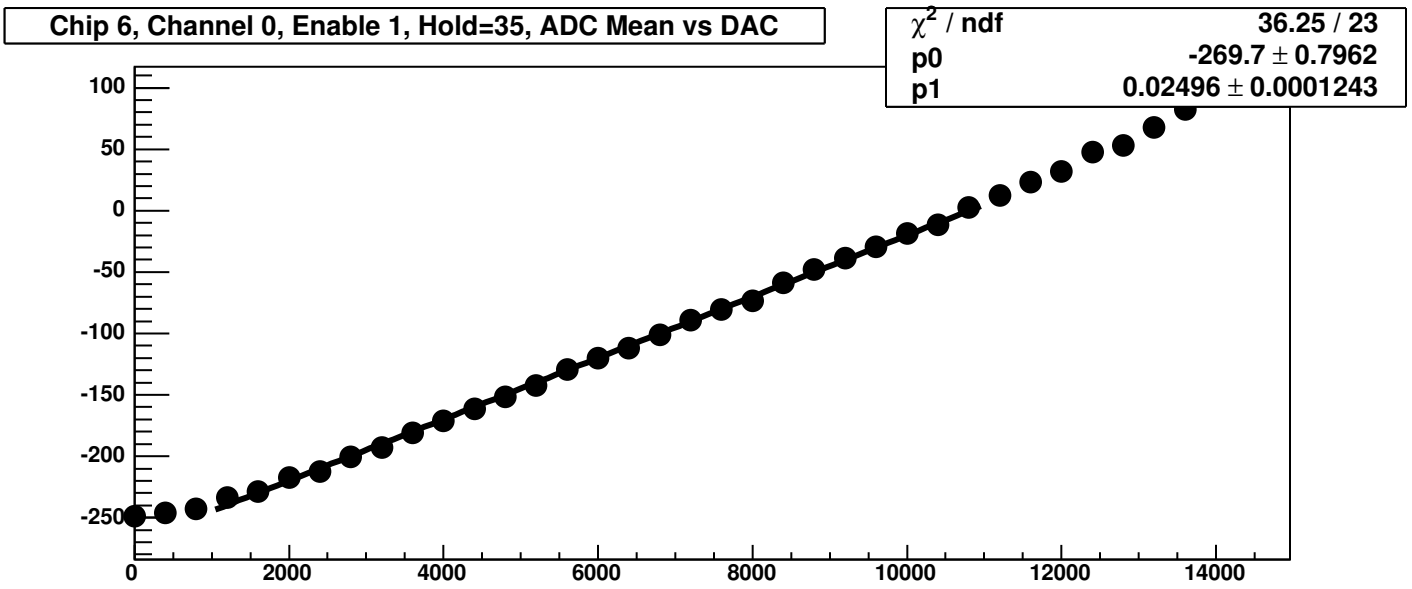

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

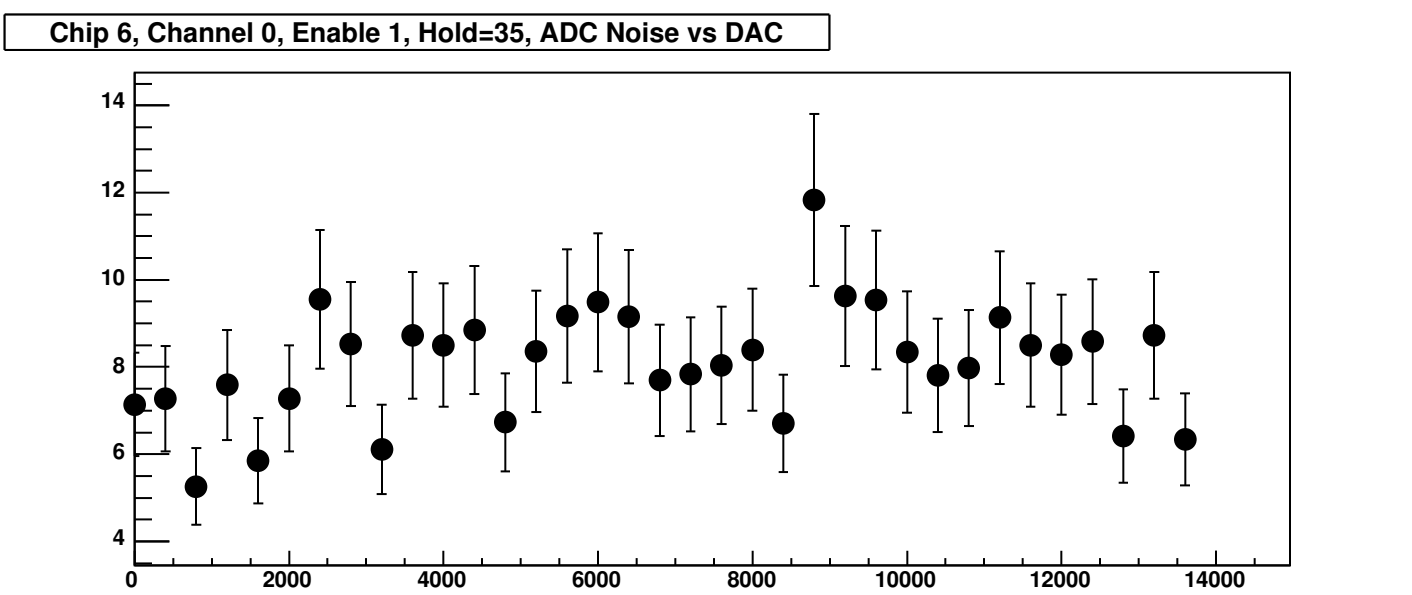

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

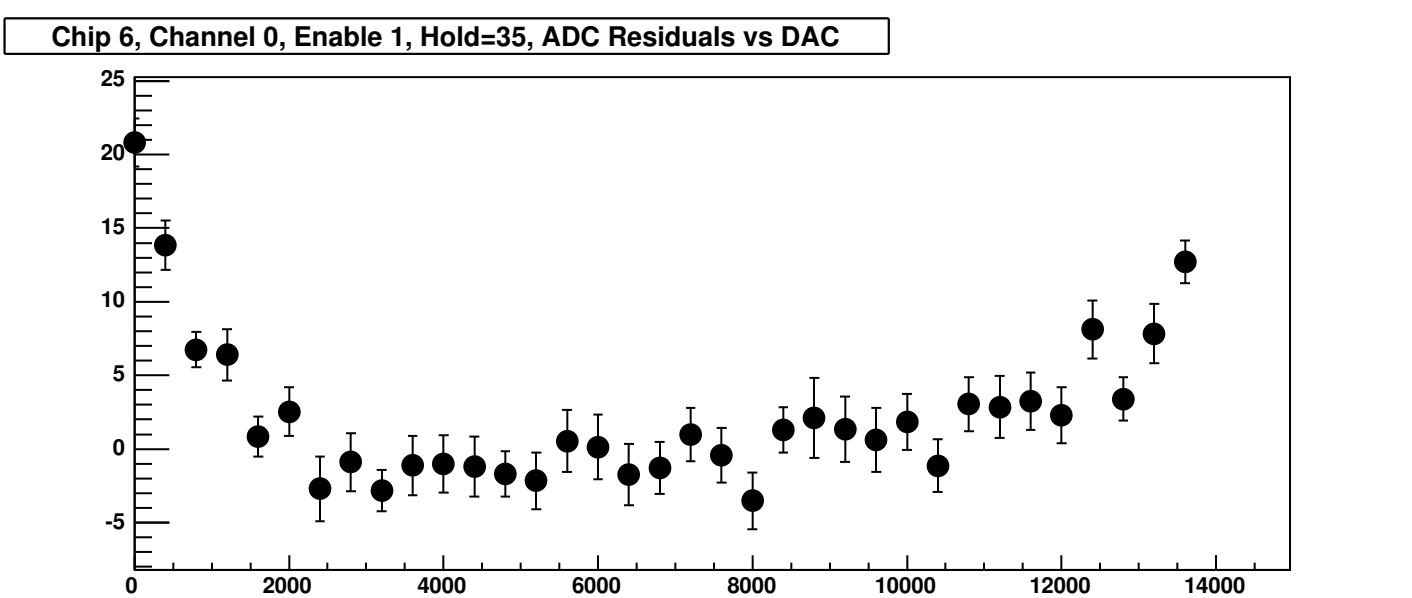

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1394 / 23
p0 2.856e+04 ± 35.53
p1 0.124 ± 0.005192
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

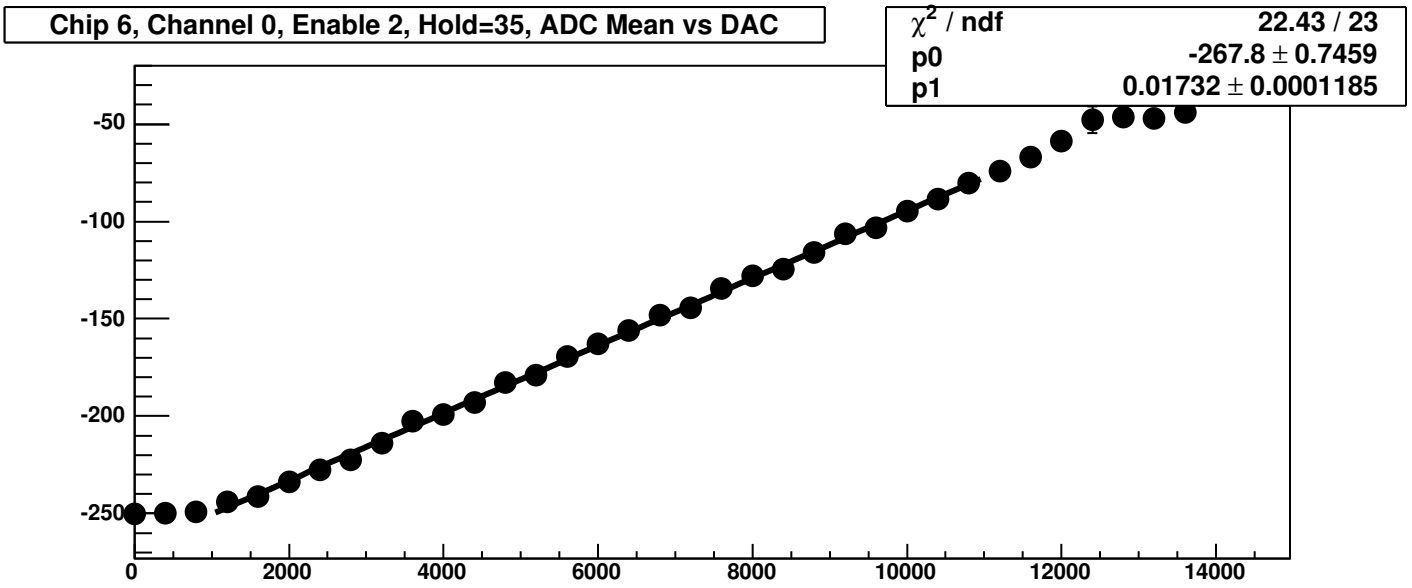

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

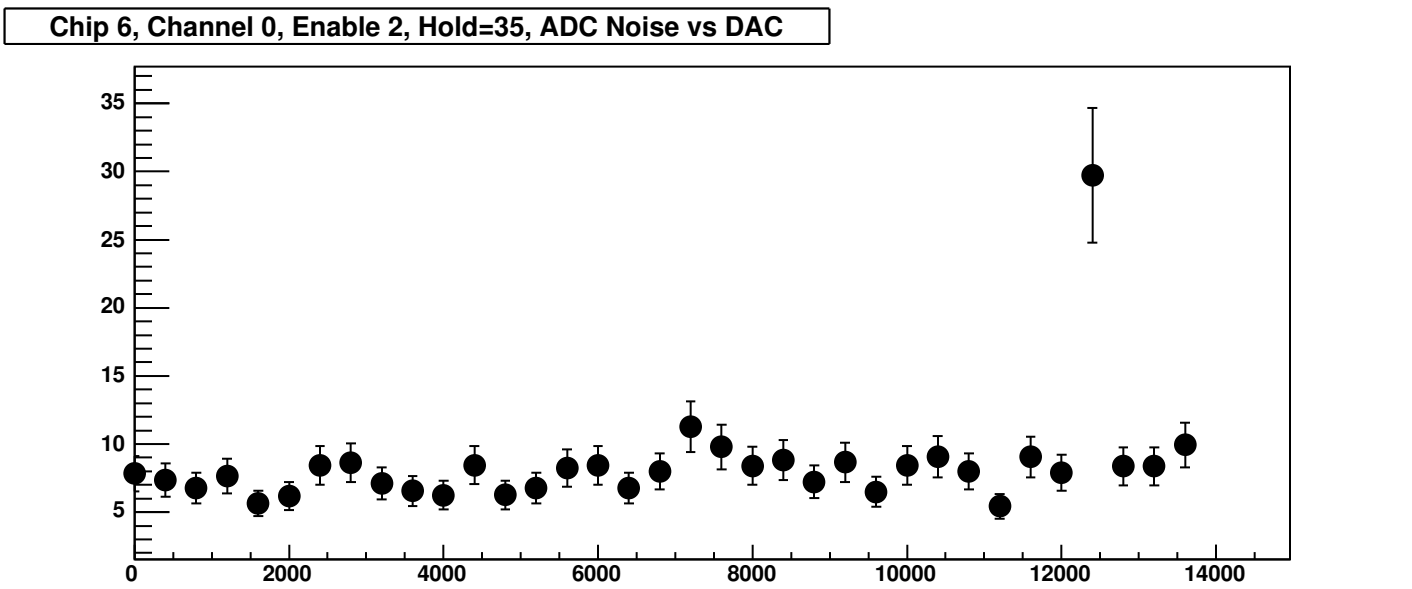

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

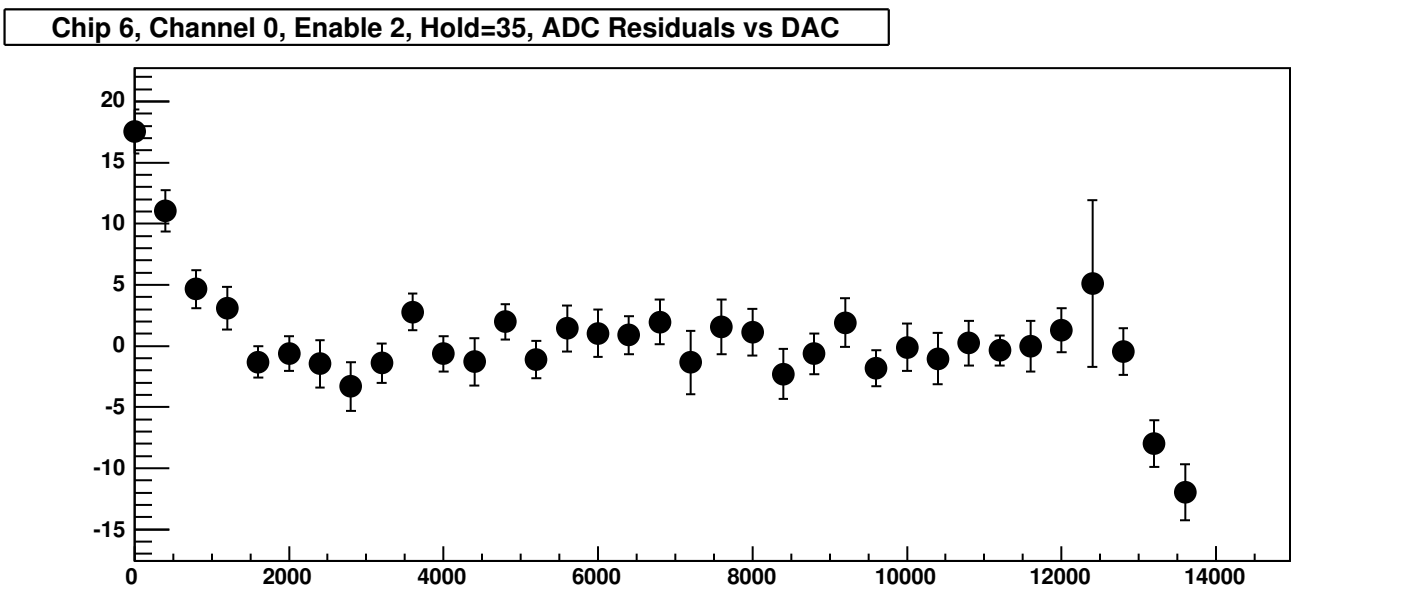

Chip 6, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.25 / 23
p0 -269.7 ± 0.7962
p1 0.02496 ± 0.0001243
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

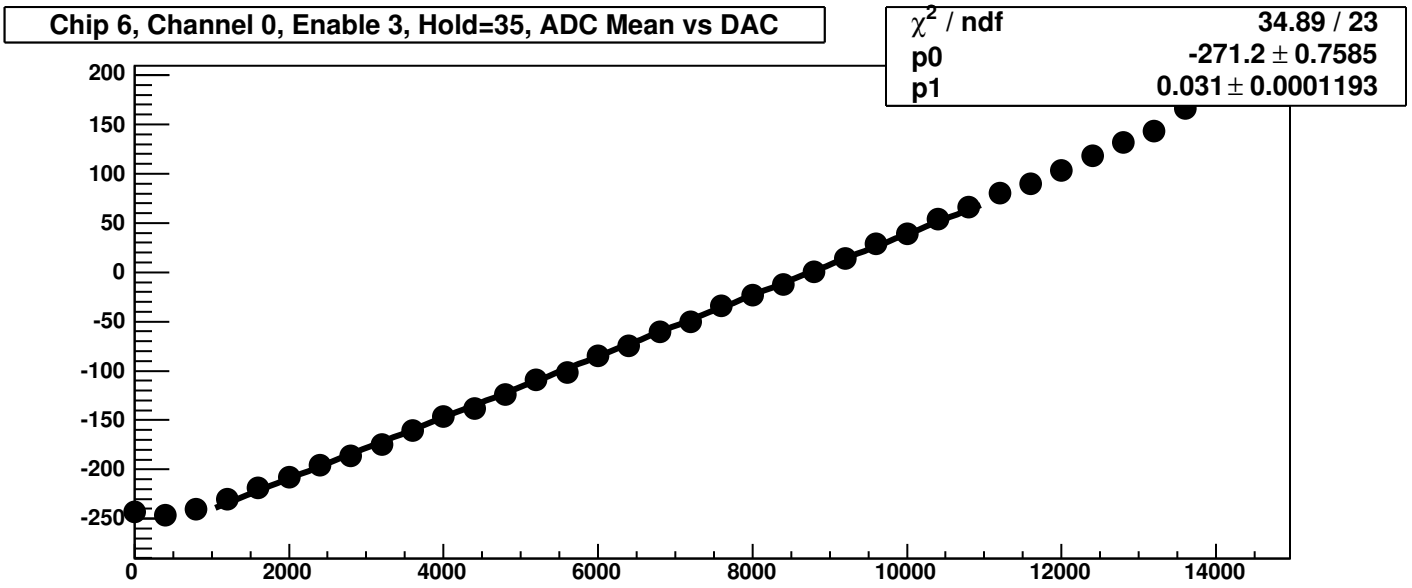

Chip 6, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

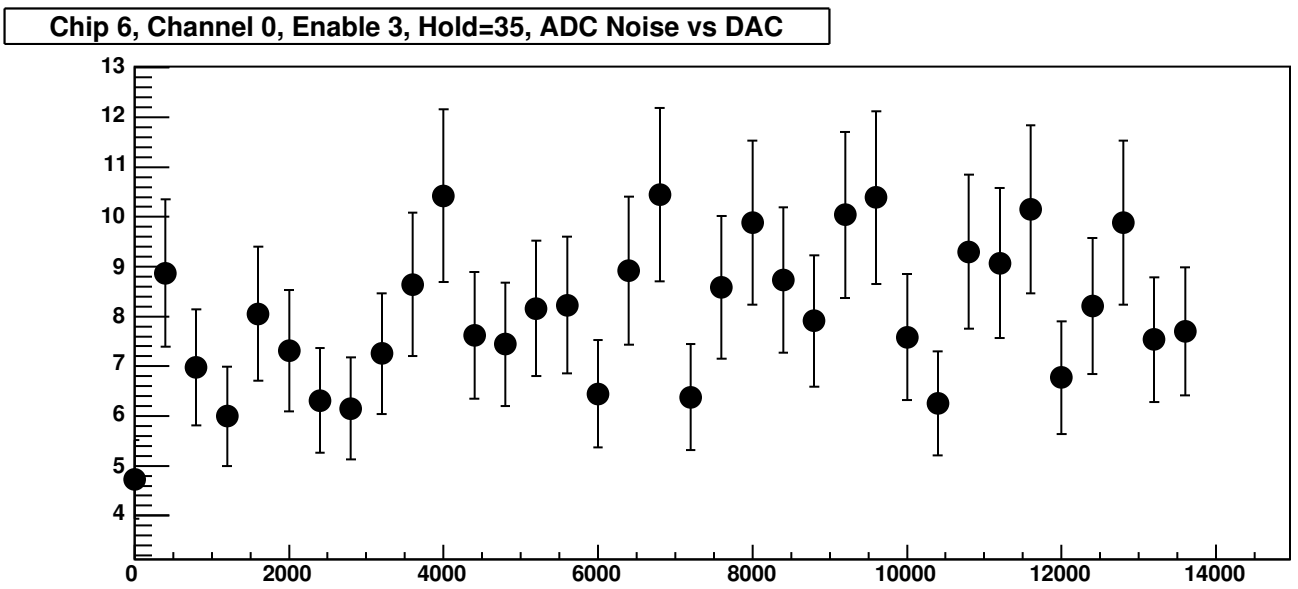

Chip 6, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

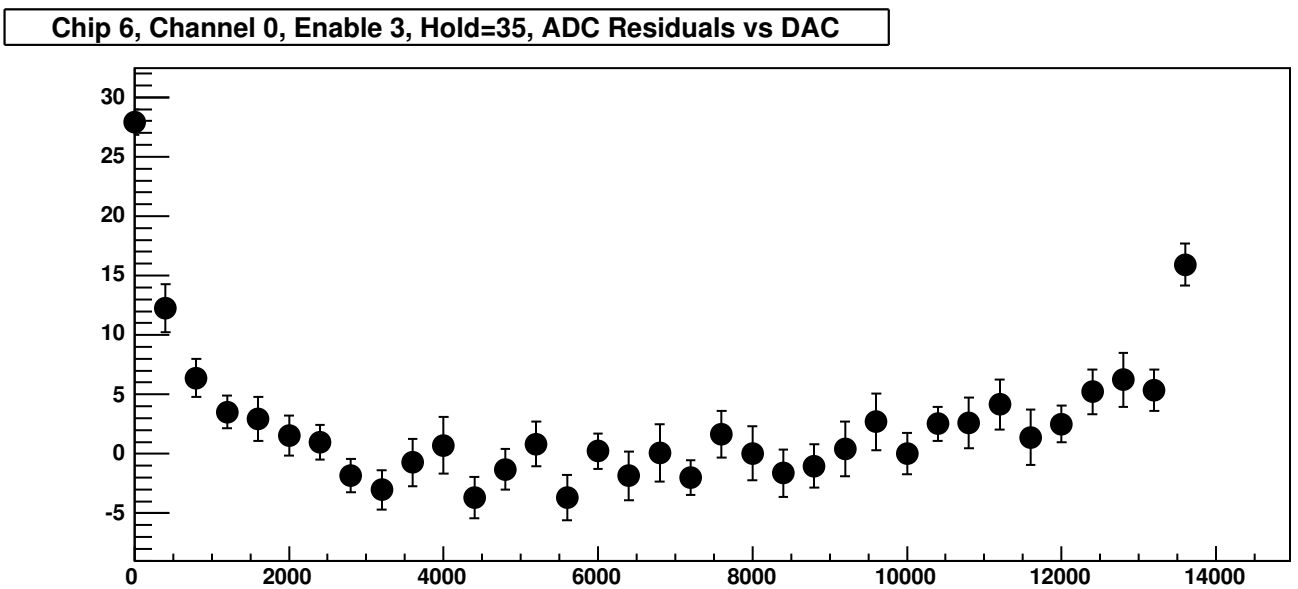

Chip 6, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.43 / 23
p0 -267.8 ± 0.7459
p1 0.01732 ± 0.0001185
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

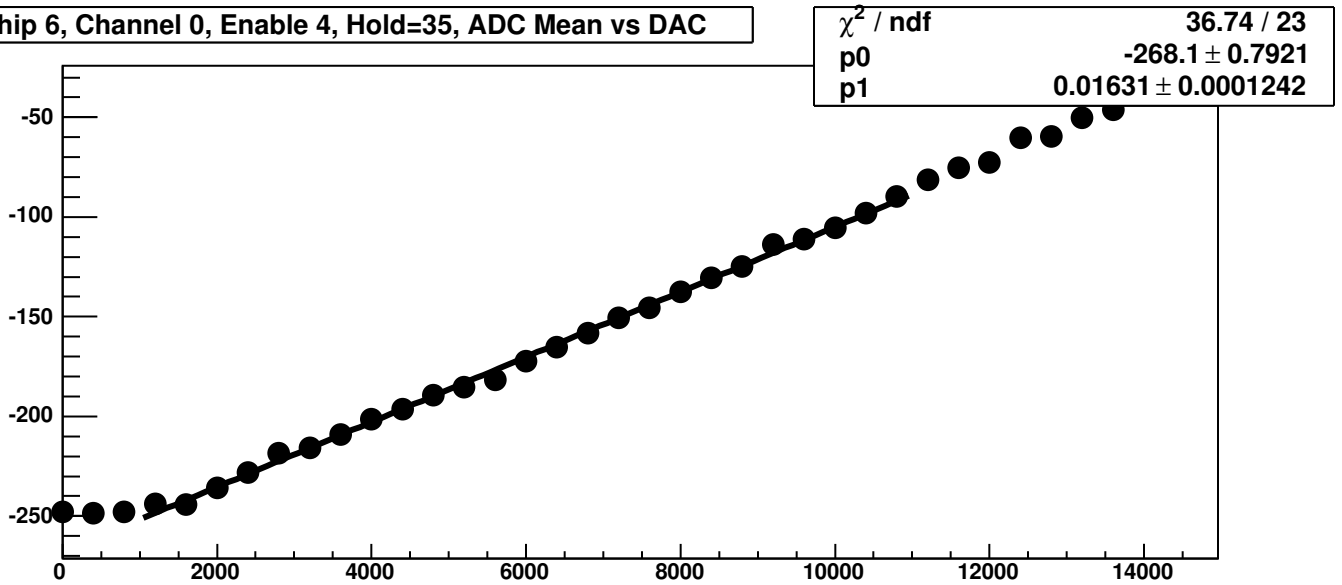

Chip 6, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.89 / 23
p0 -271.2 ± 0.7585
p1 0.031 ± 0.0001193
200
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

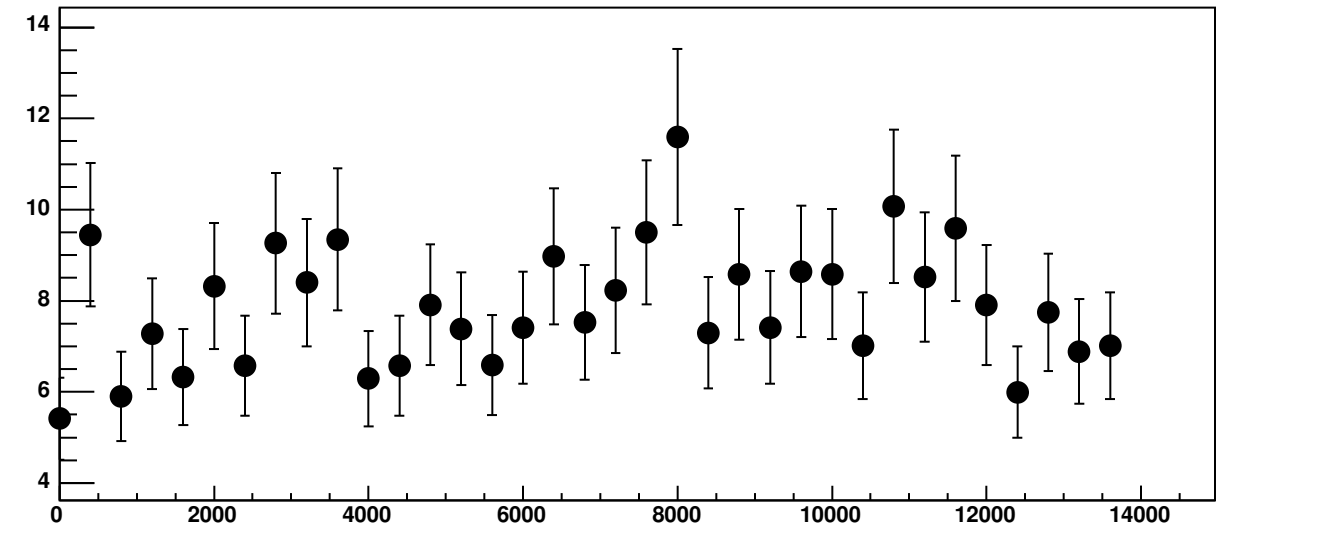

Chip 6, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

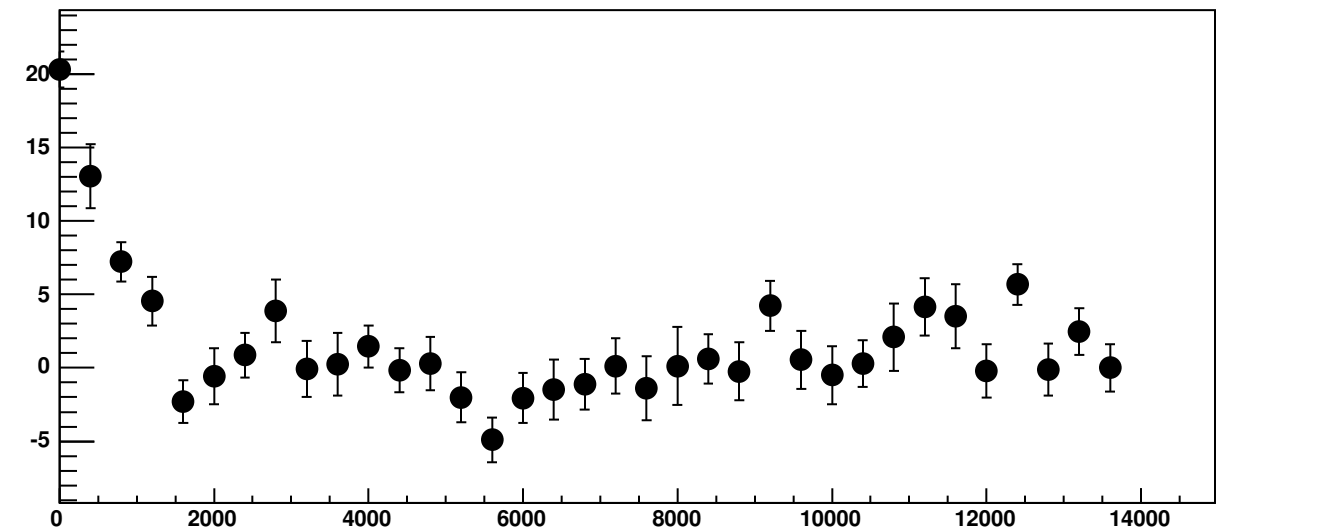
Chip 6, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

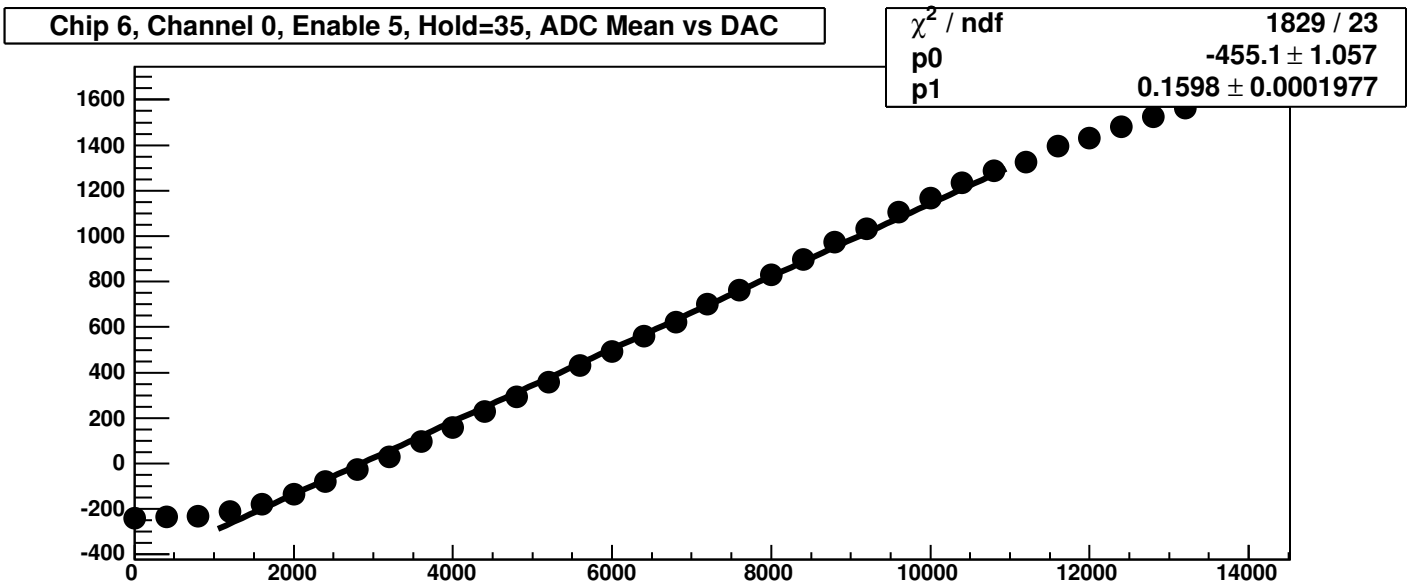


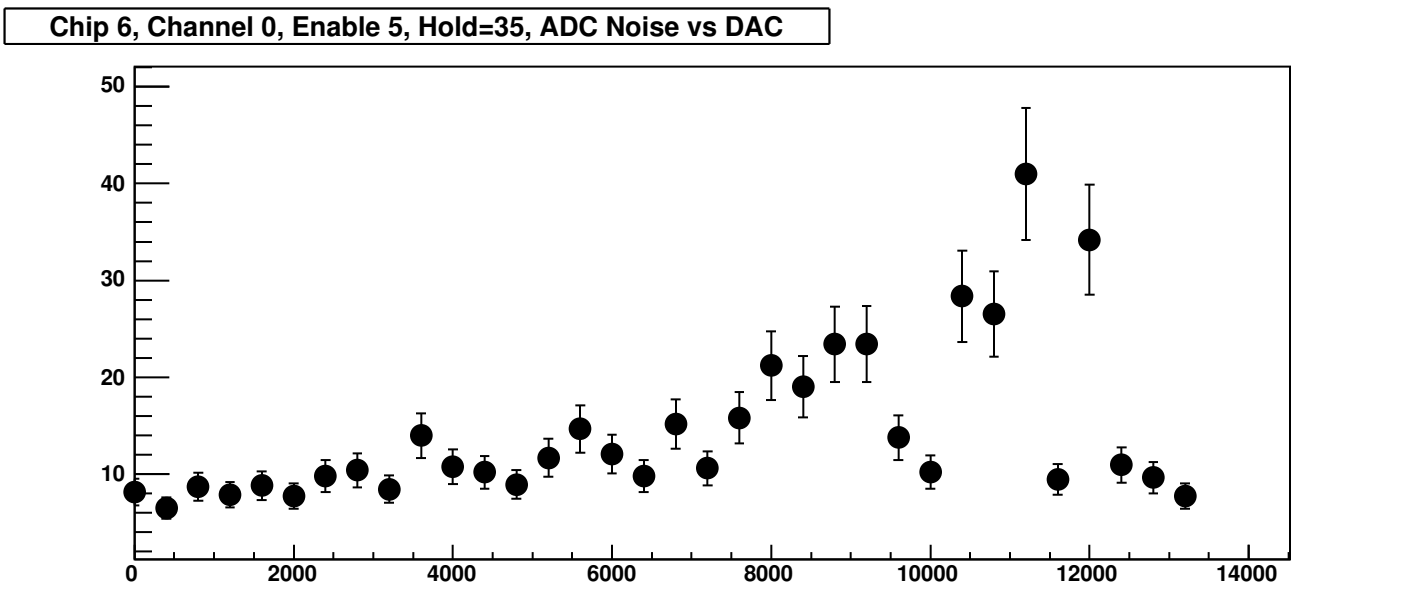
Chip 6, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

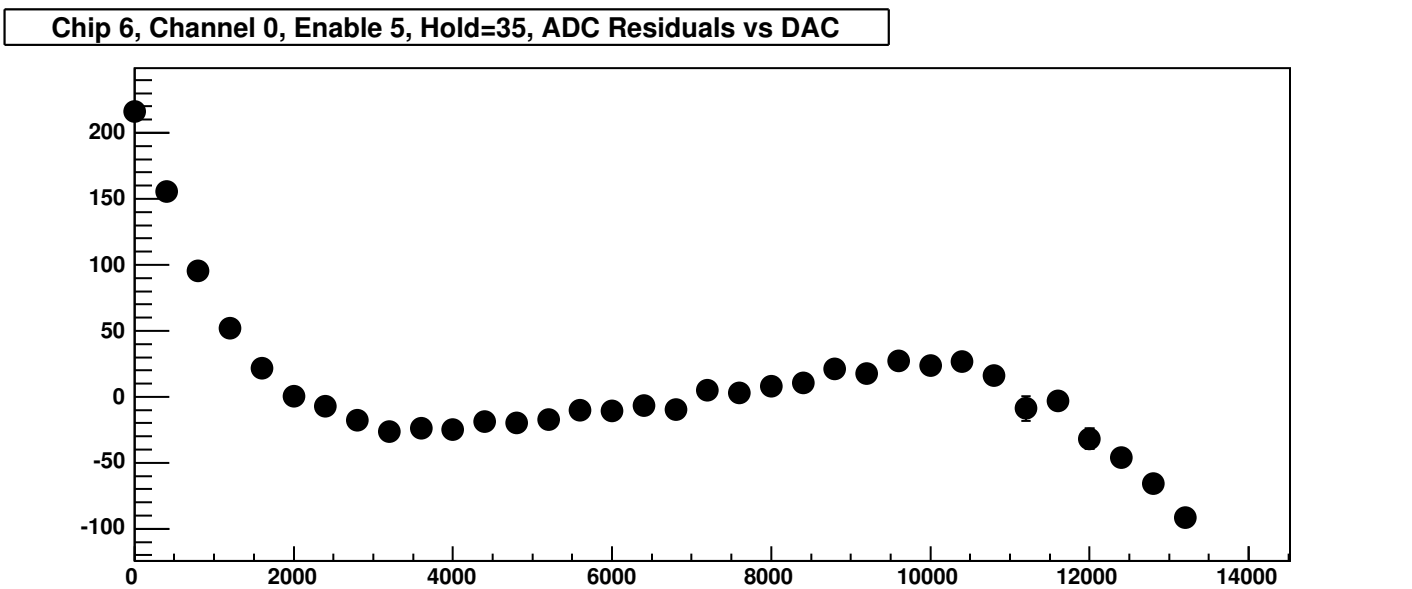


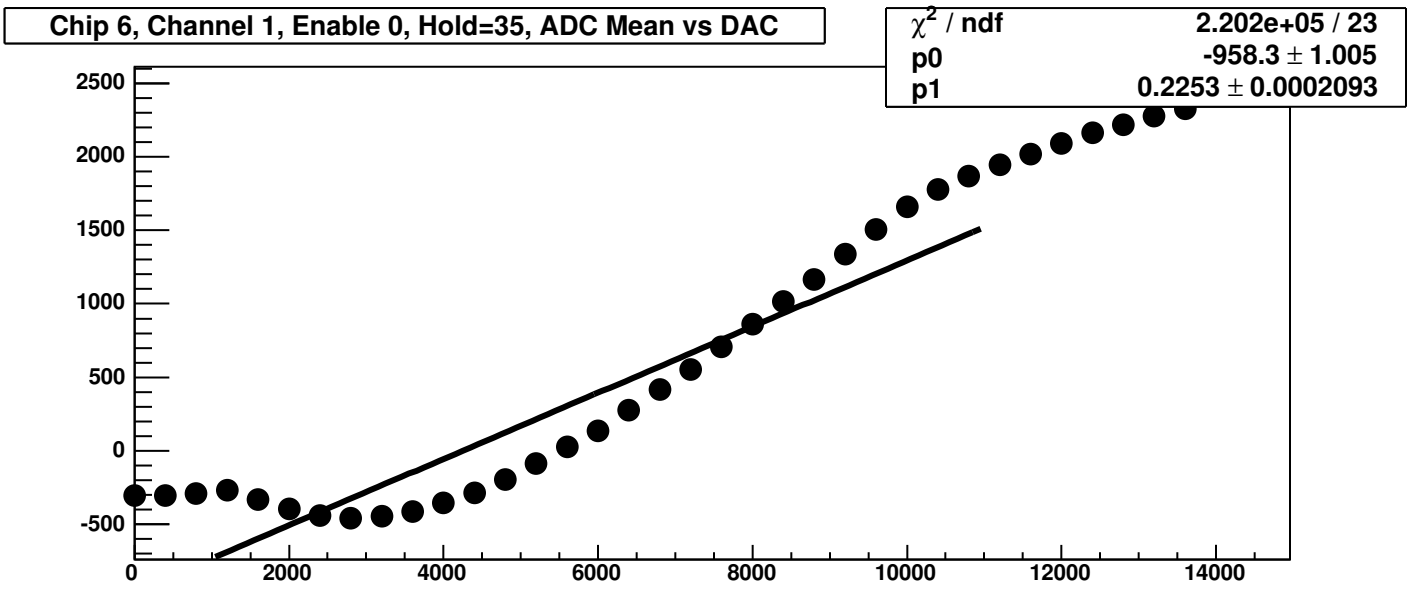
Chip 6, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

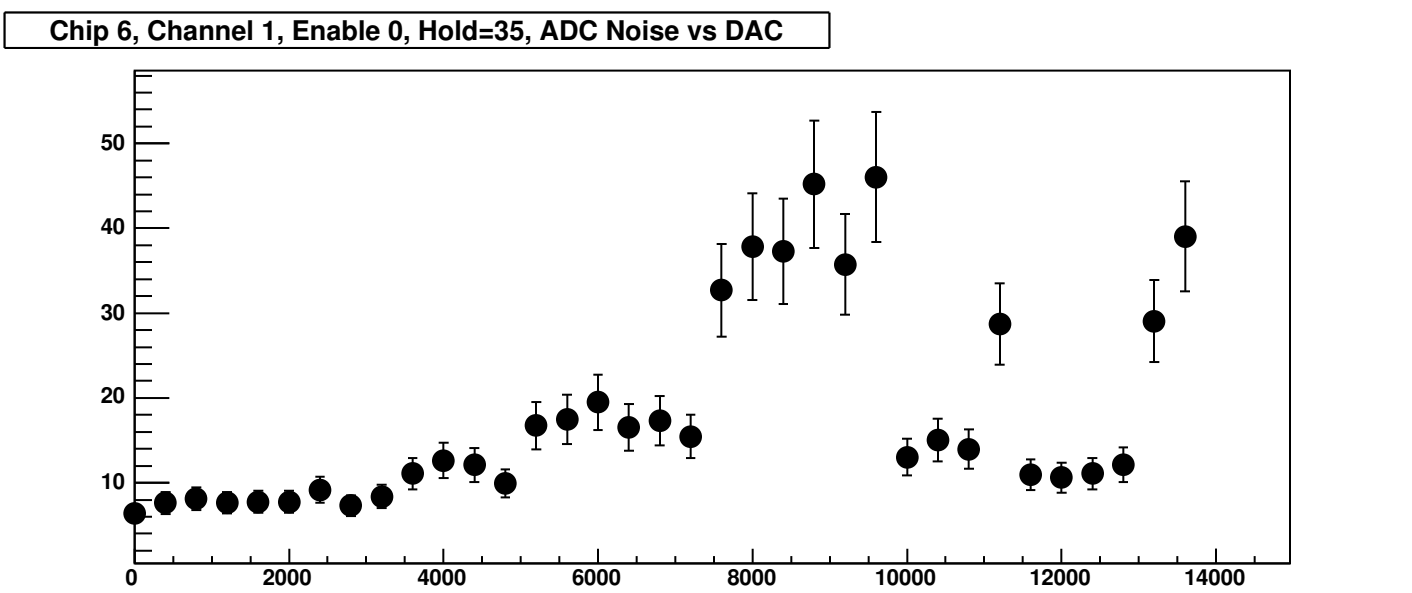


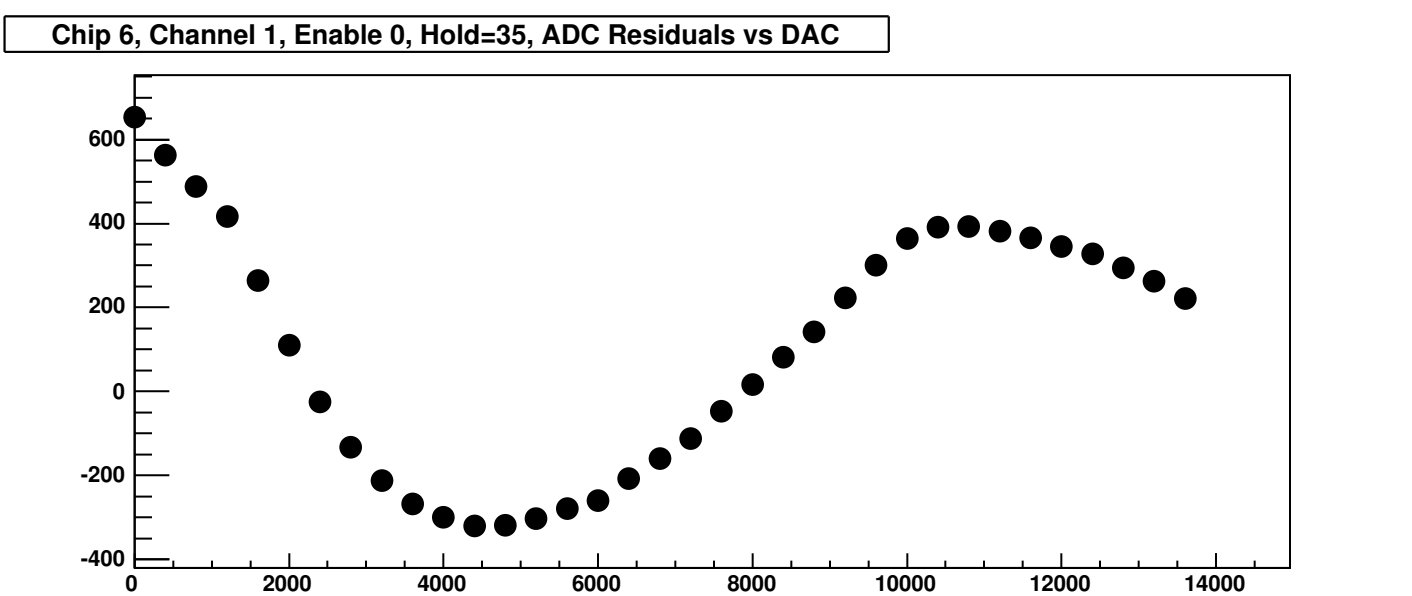

Chip 6, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1829 / 23
p0 -455.1 ± 1.057
p1 0.1598 ± 0.0001977
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

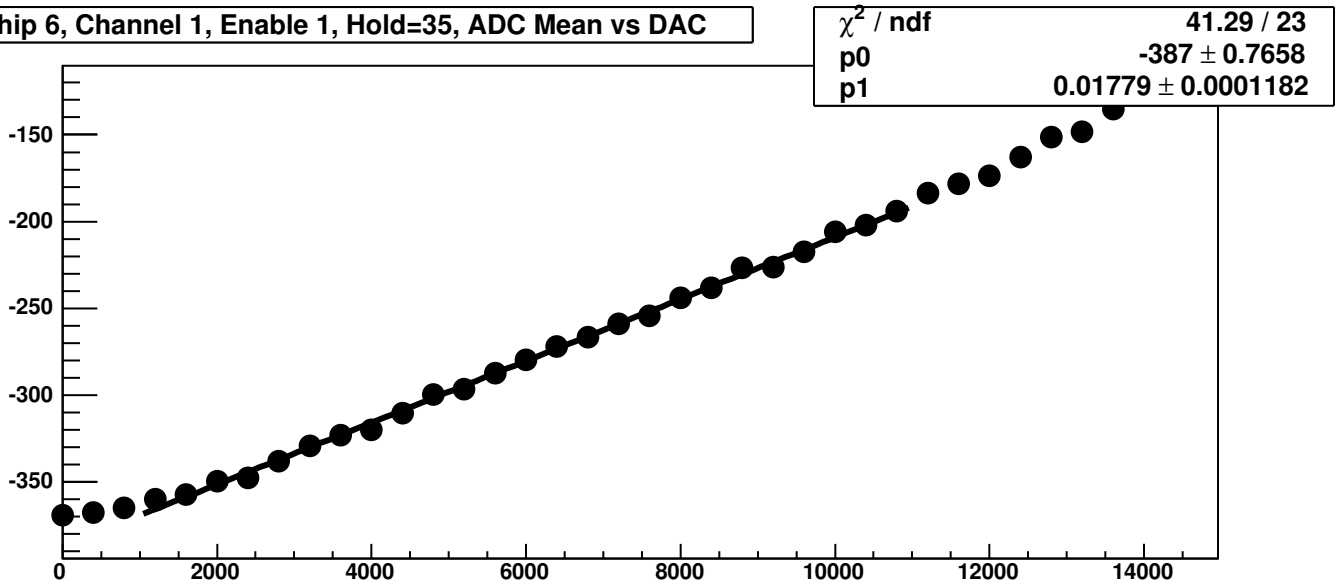

Chip 6, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.202e+05 / 23
p0 -958.3 ± 1.005
p1 0.2253 ± 0.0002093
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

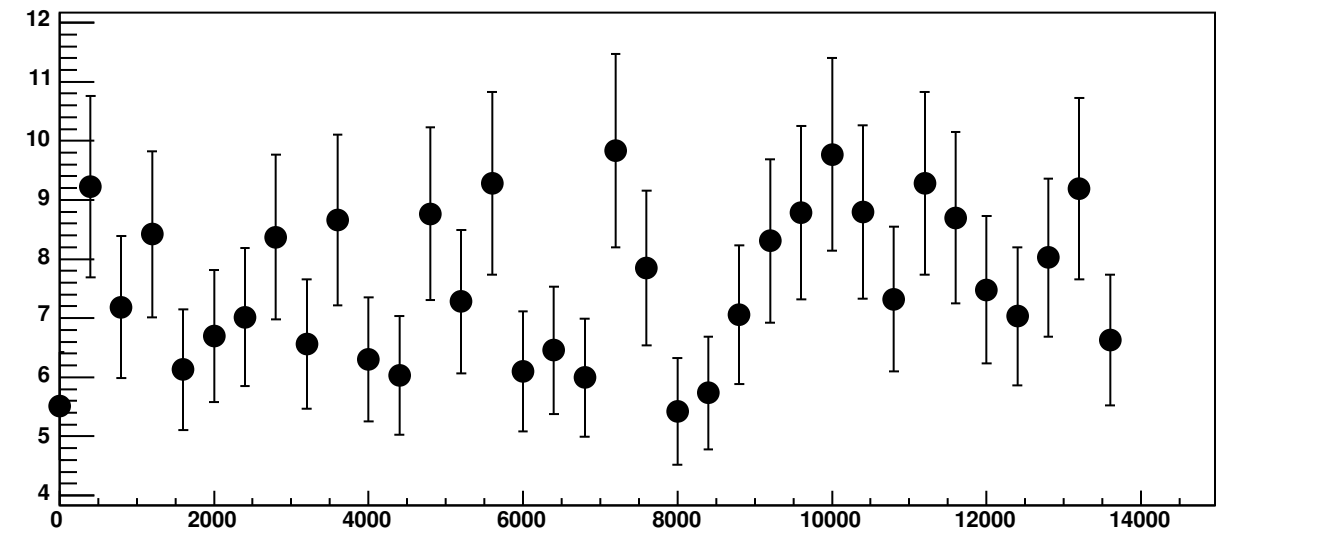

Chip 6, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

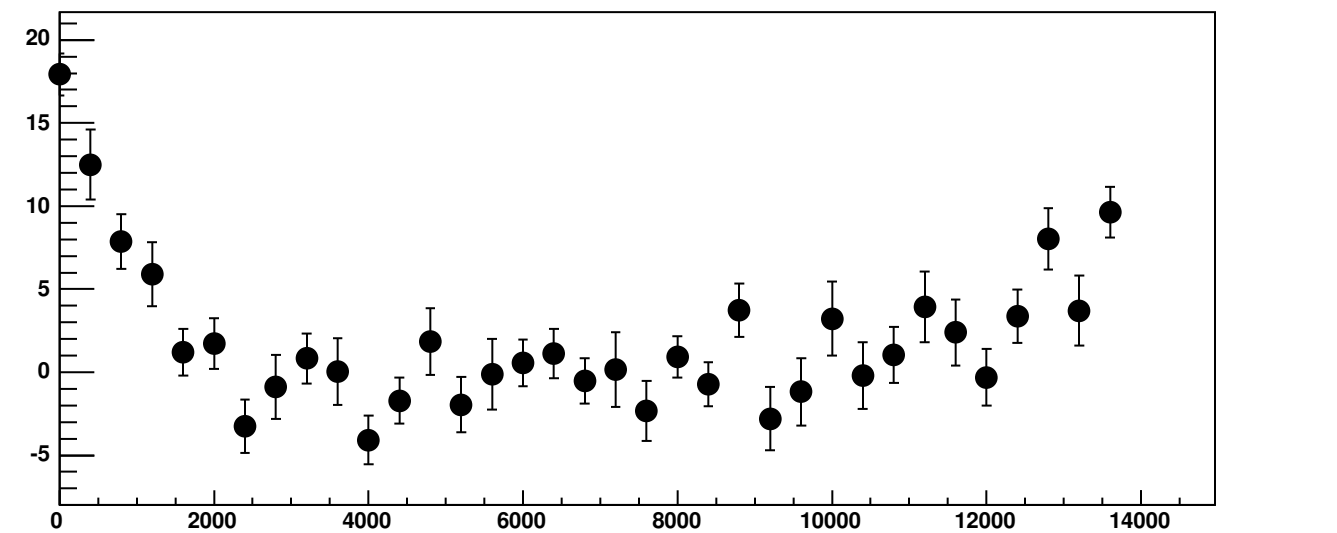
Chip 6, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



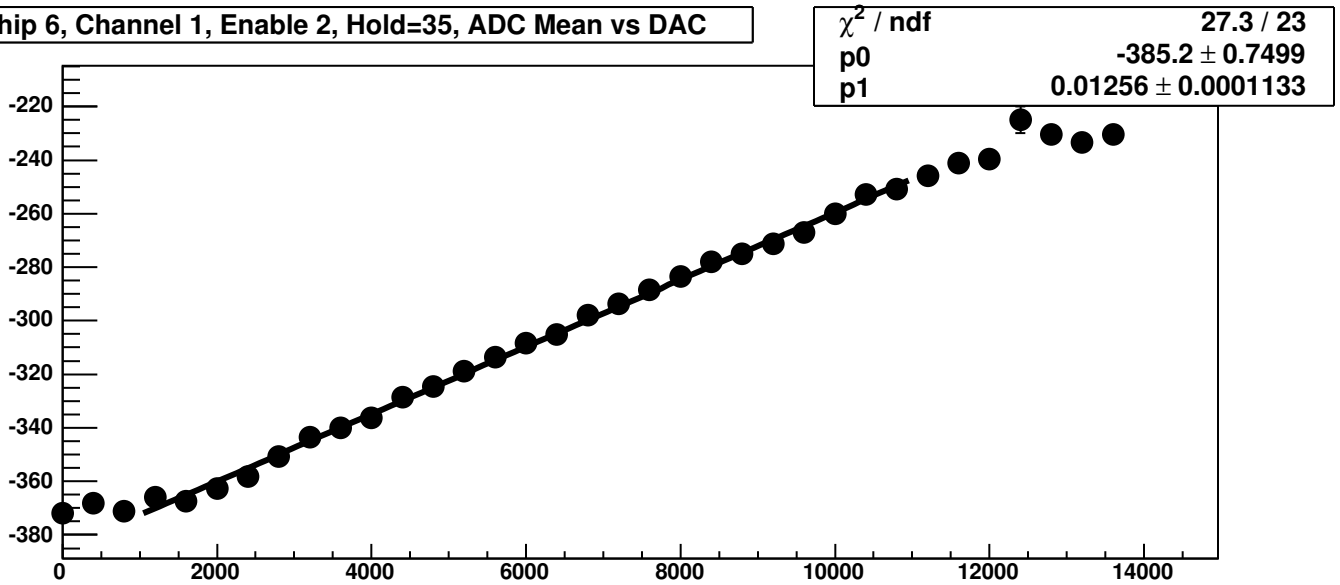
Chip 6, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



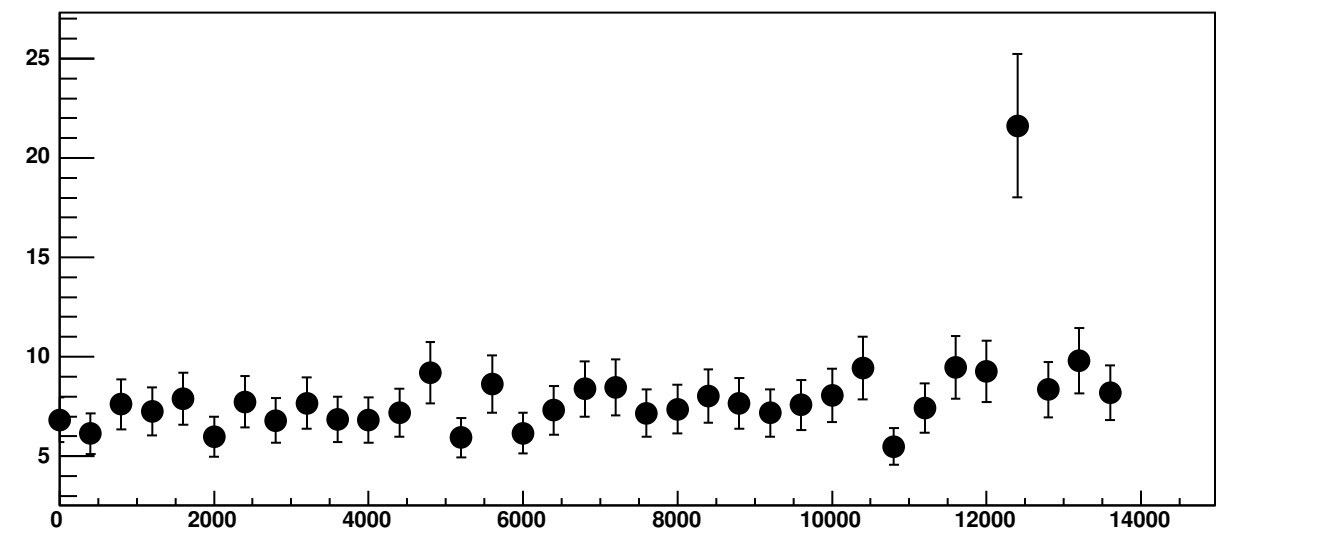
Chip 6, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



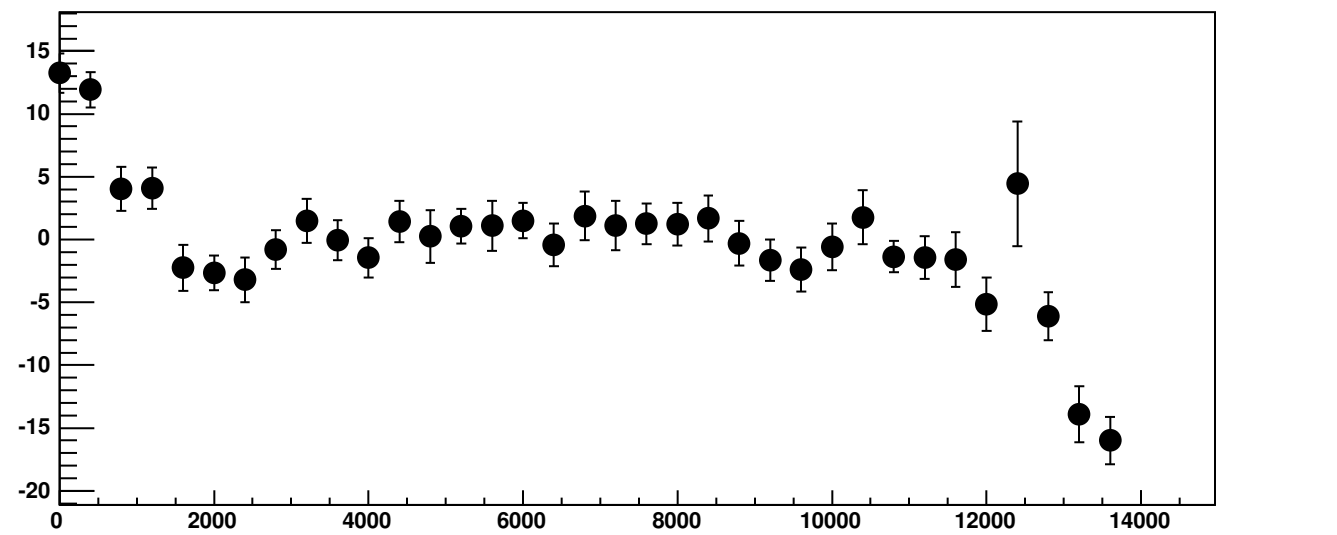
Chip 6, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

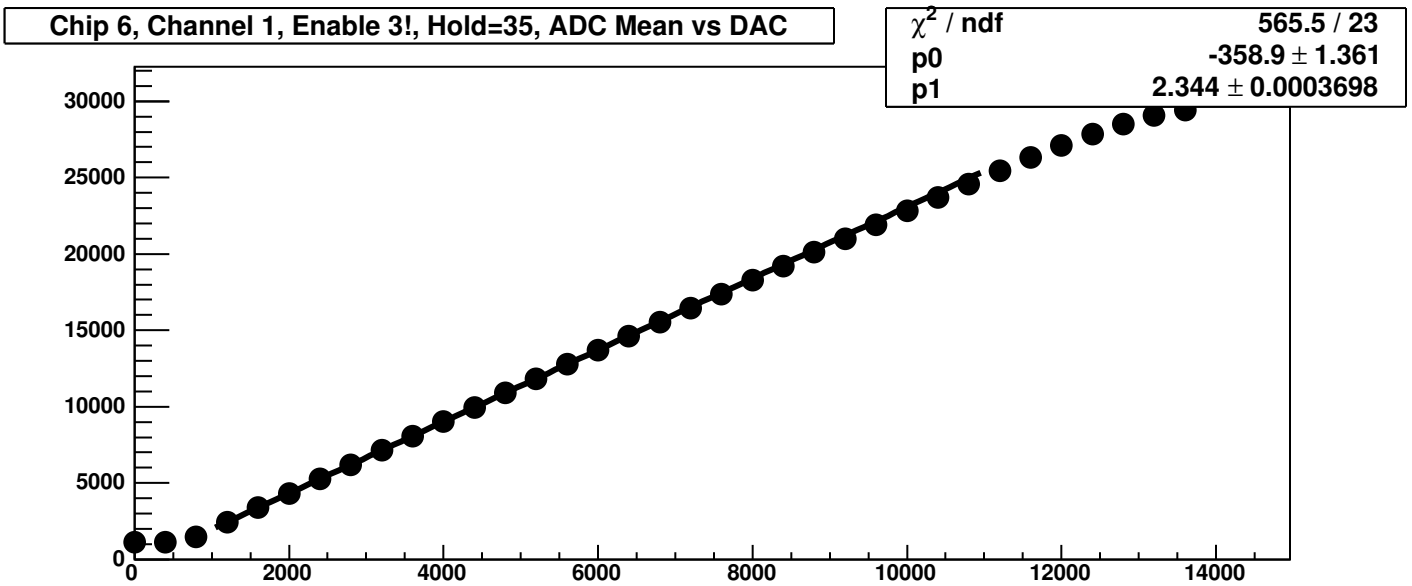


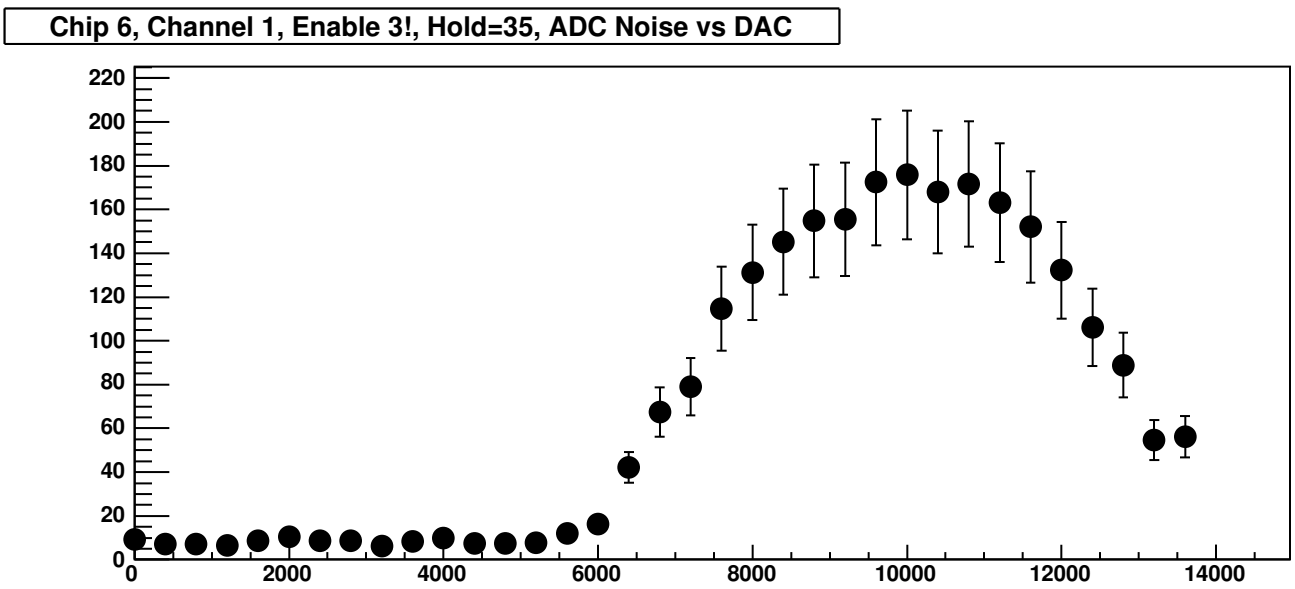
Chip 6, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

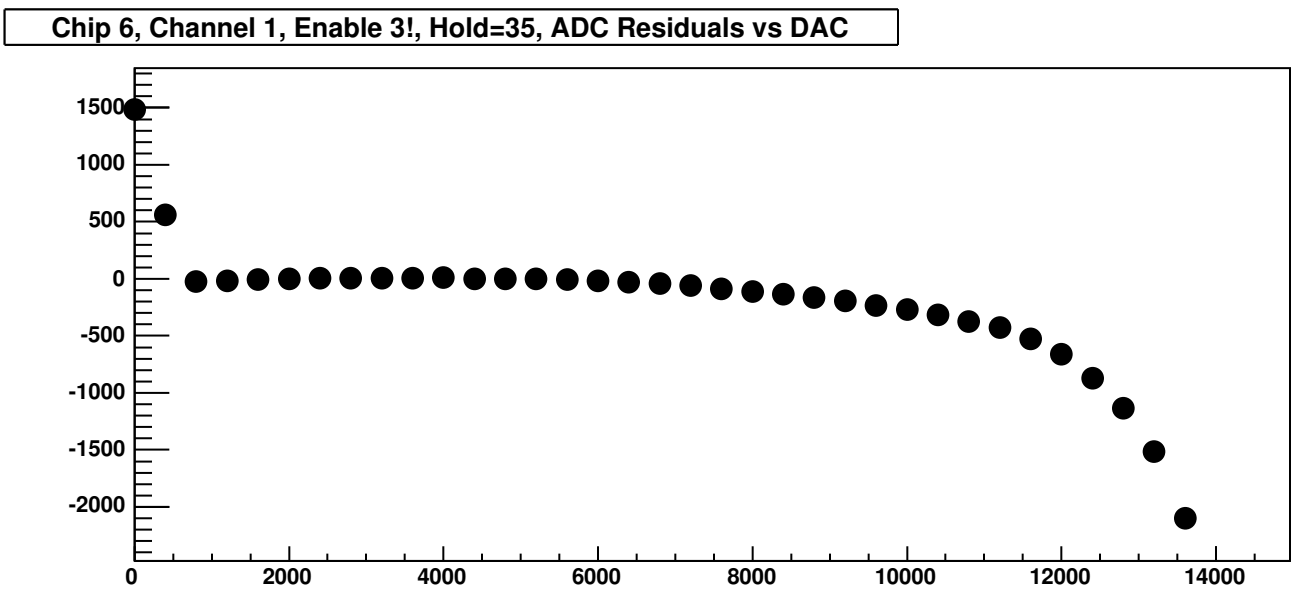


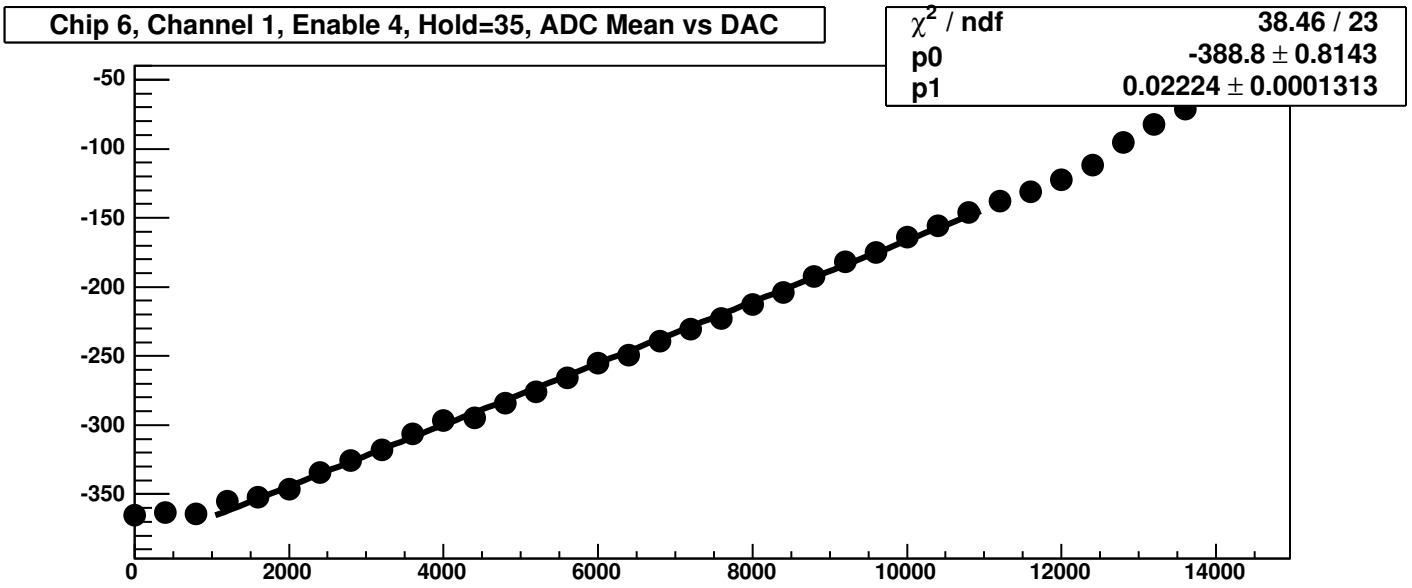
Chip 6, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

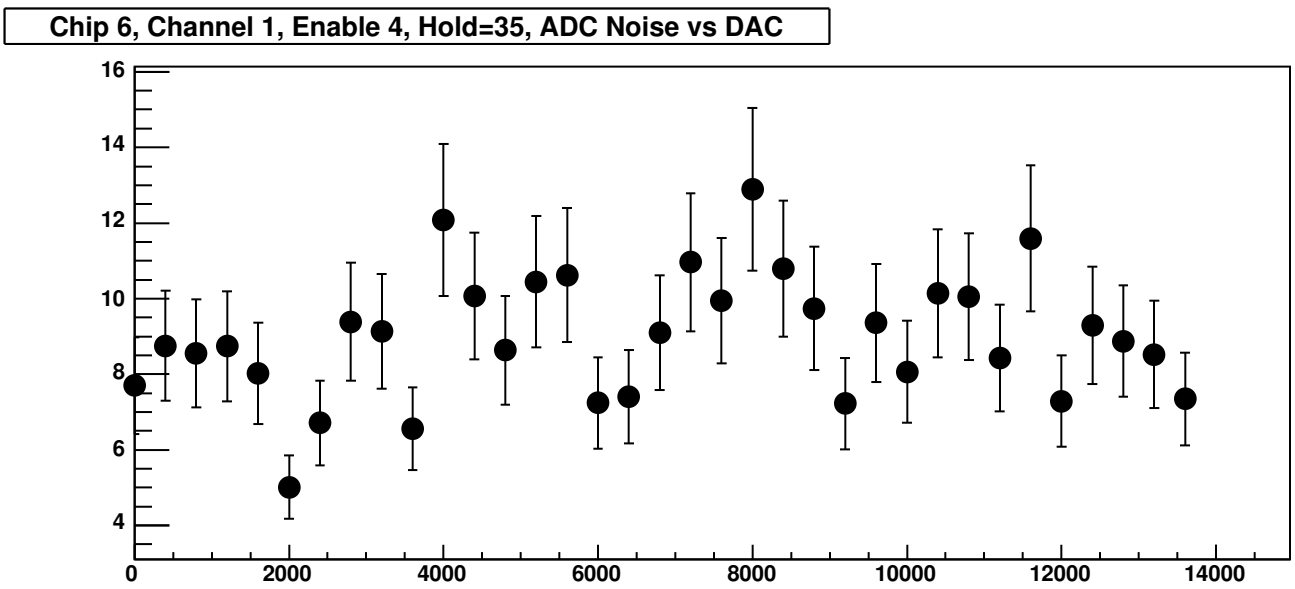


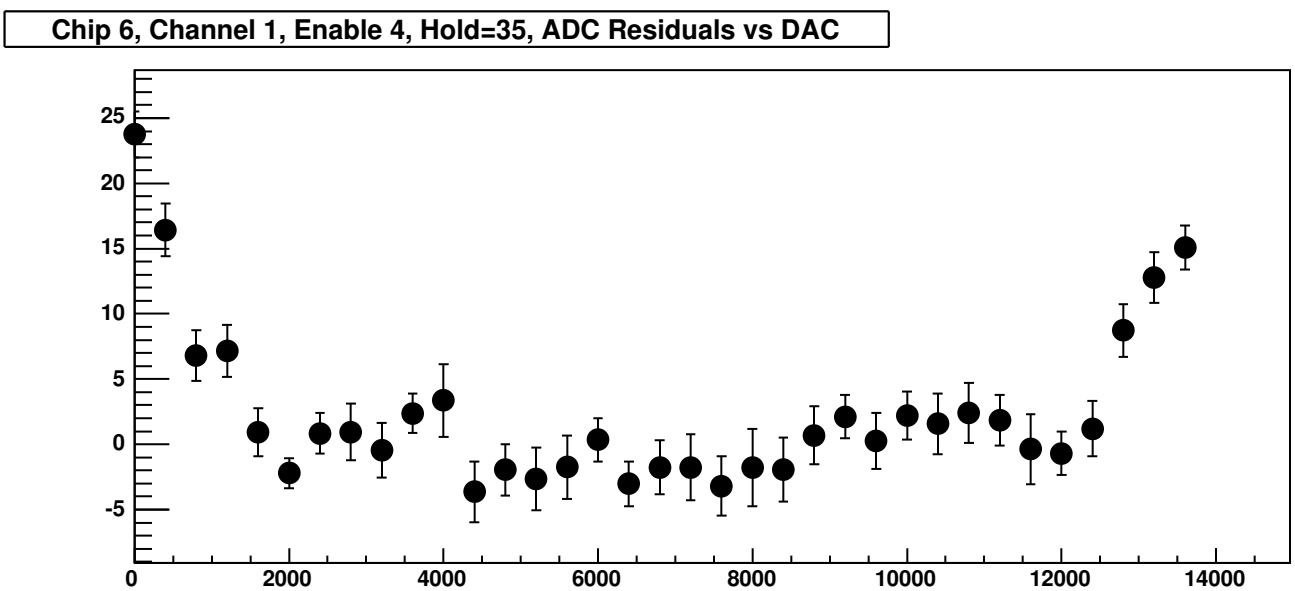

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 565.5 / 23
p0 -358.9 ± 1.361
p1 2.344 ± 0.0003698
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

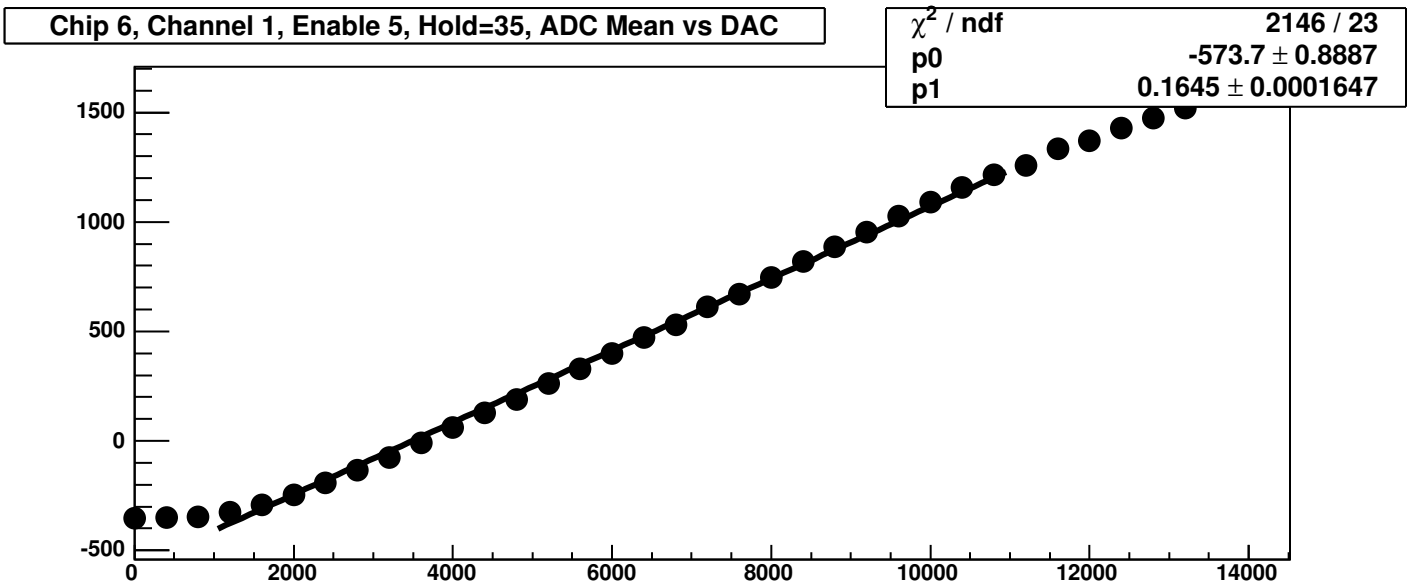

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

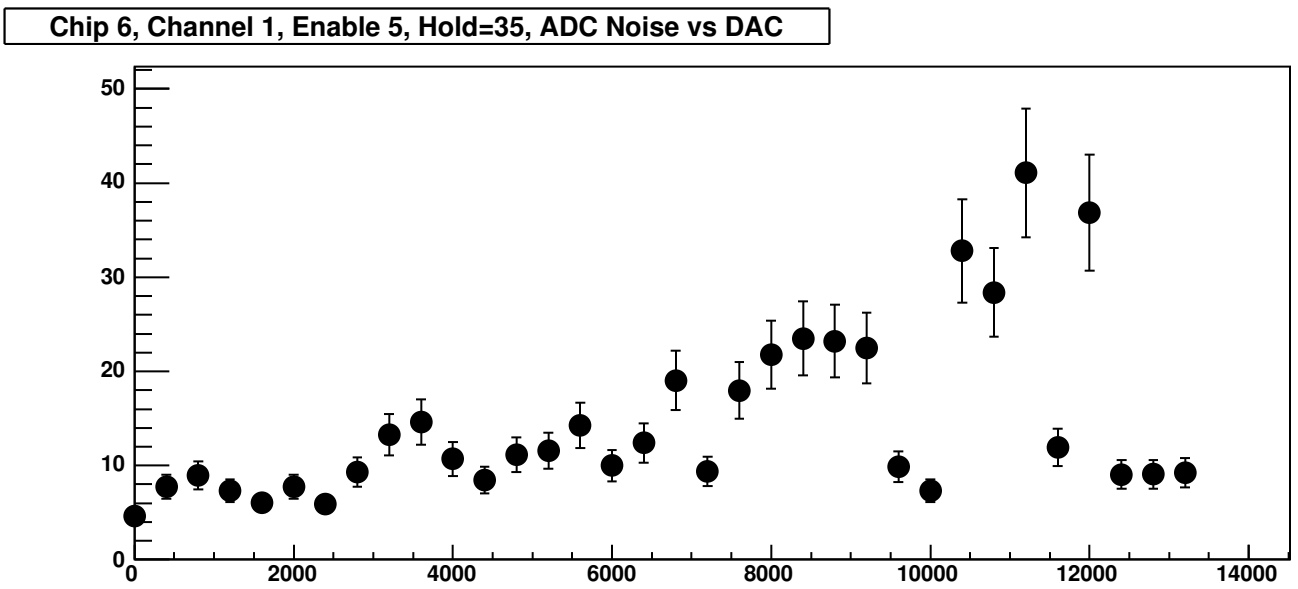

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

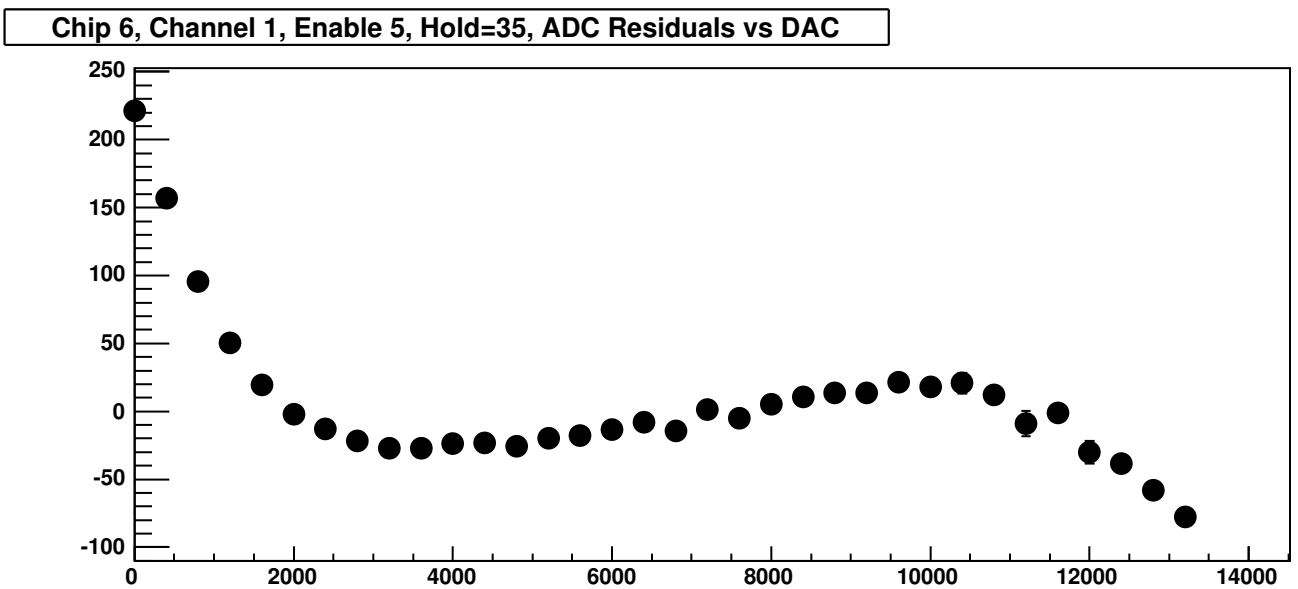

Chip 6, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.46 / 23
p0 -388.8 ± 0.8143
p1 0.02224 ± 0.0001313
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

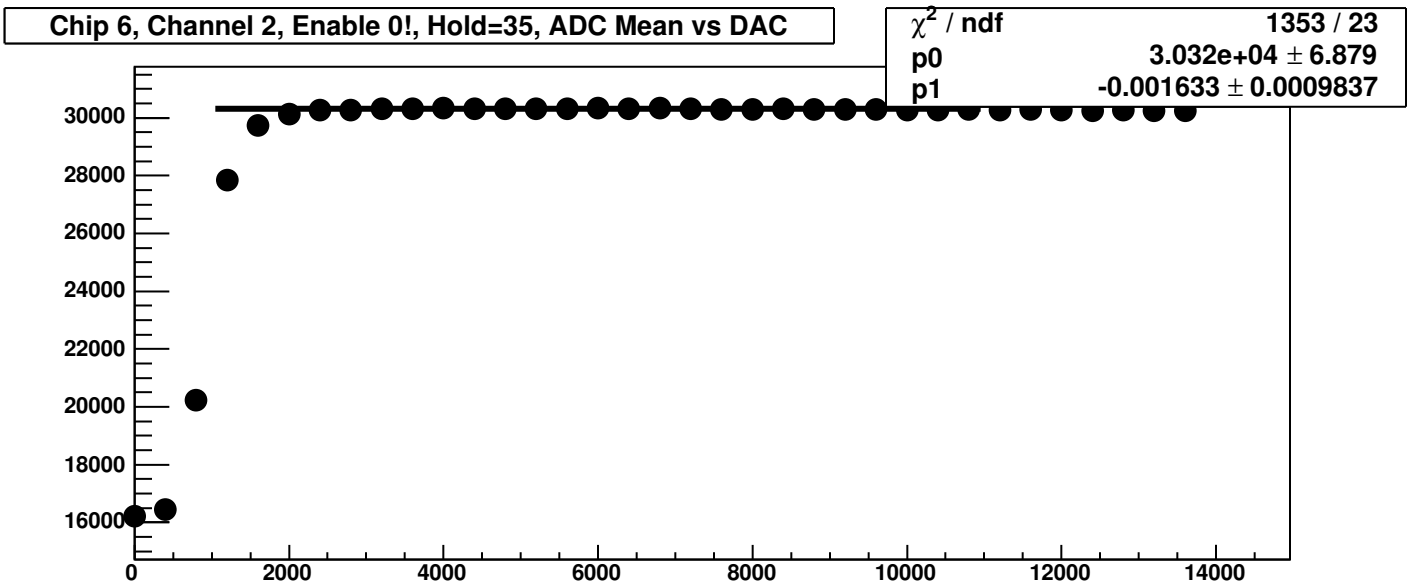

Chip 6, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

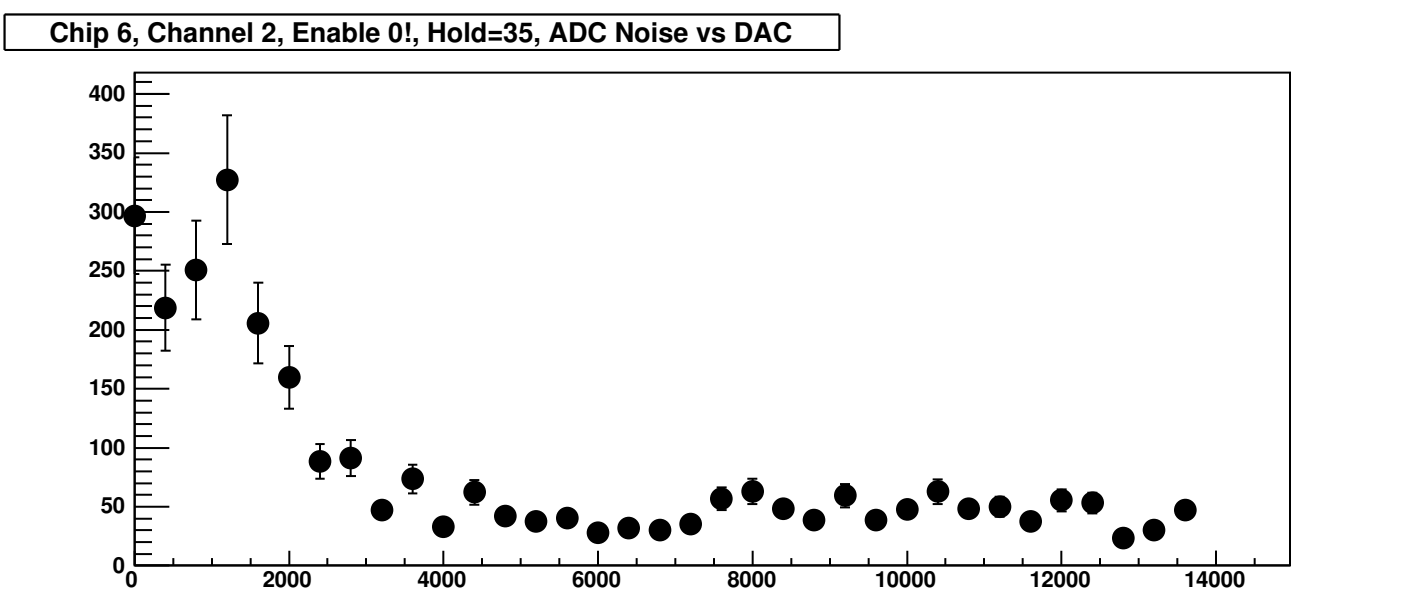

Chip 6, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

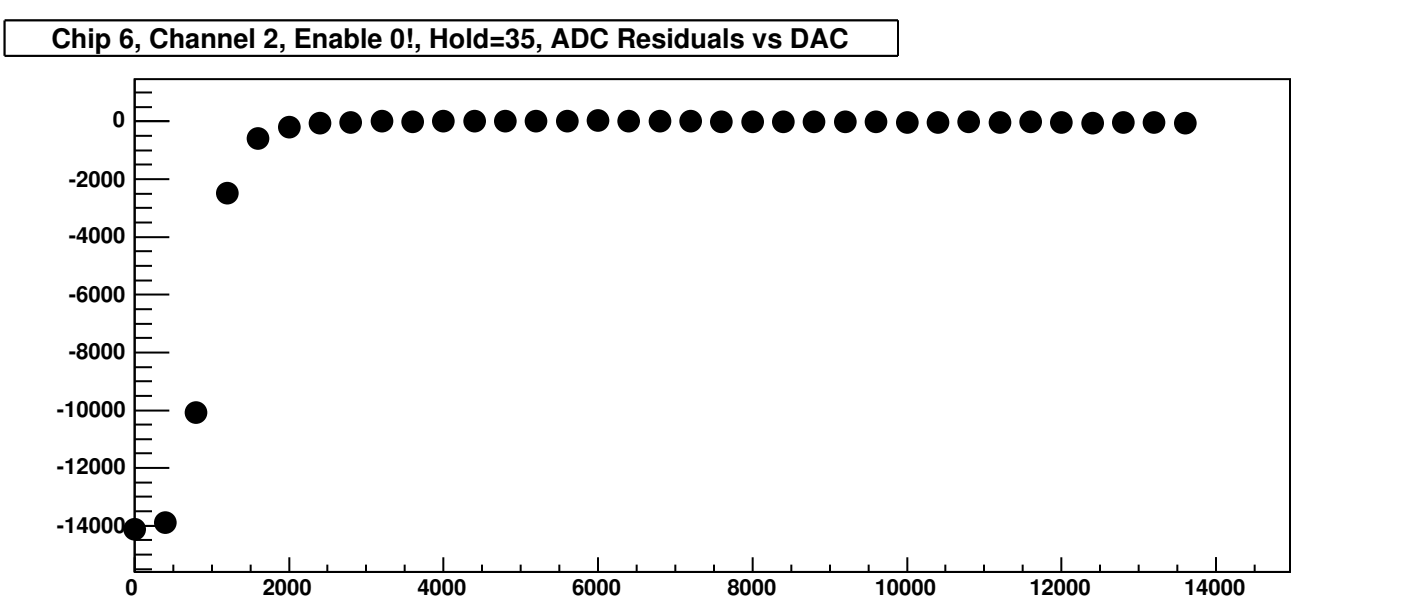

Chip 6, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2146 / 23
p0 -573.7 ± 0.8887
p1 0.1645 ± 0.0001647
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

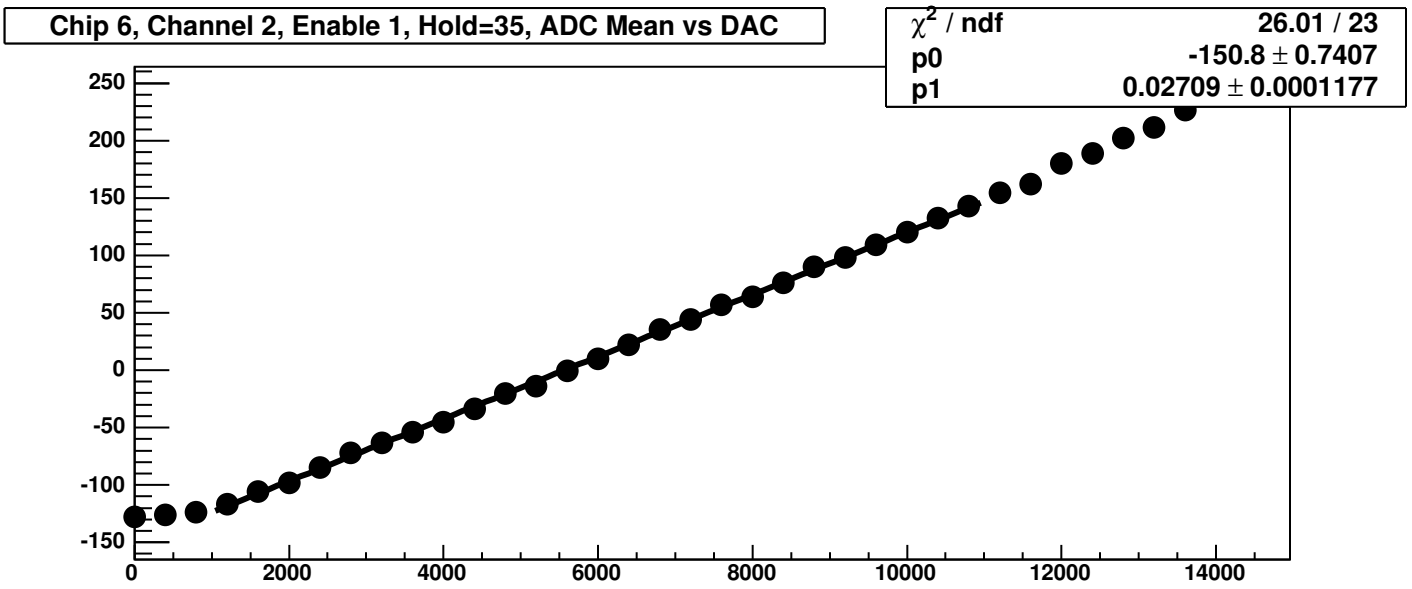

Chip 6, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

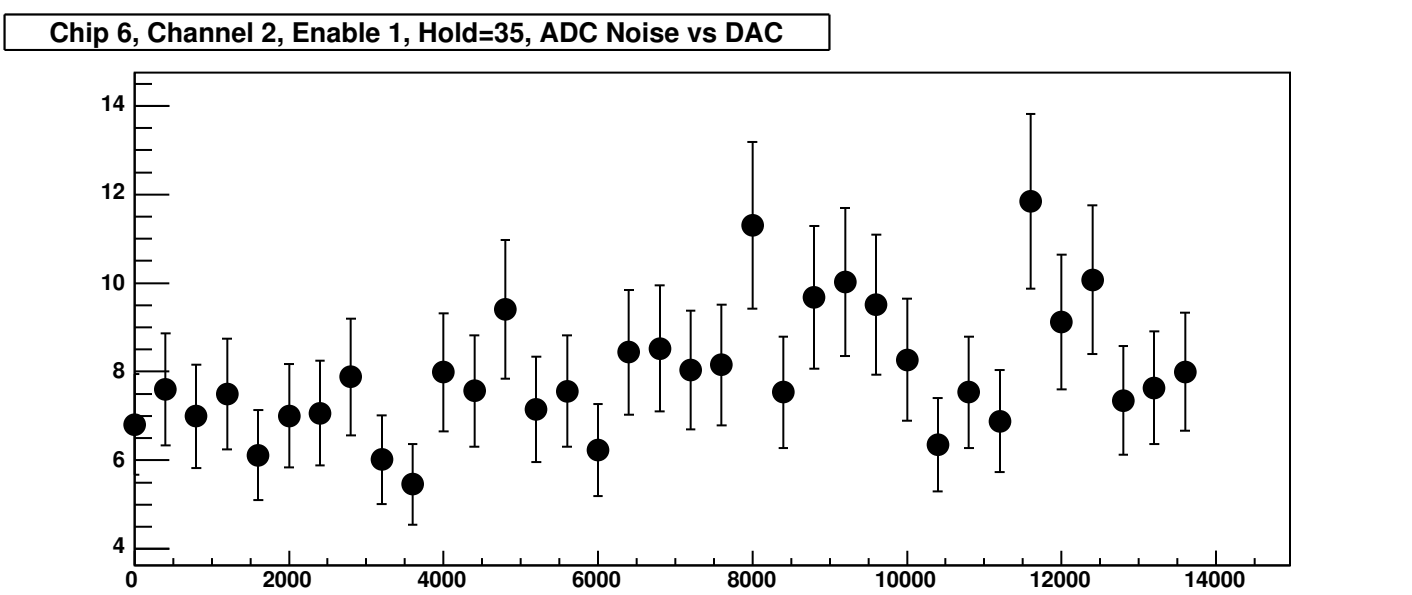

Chip 6, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

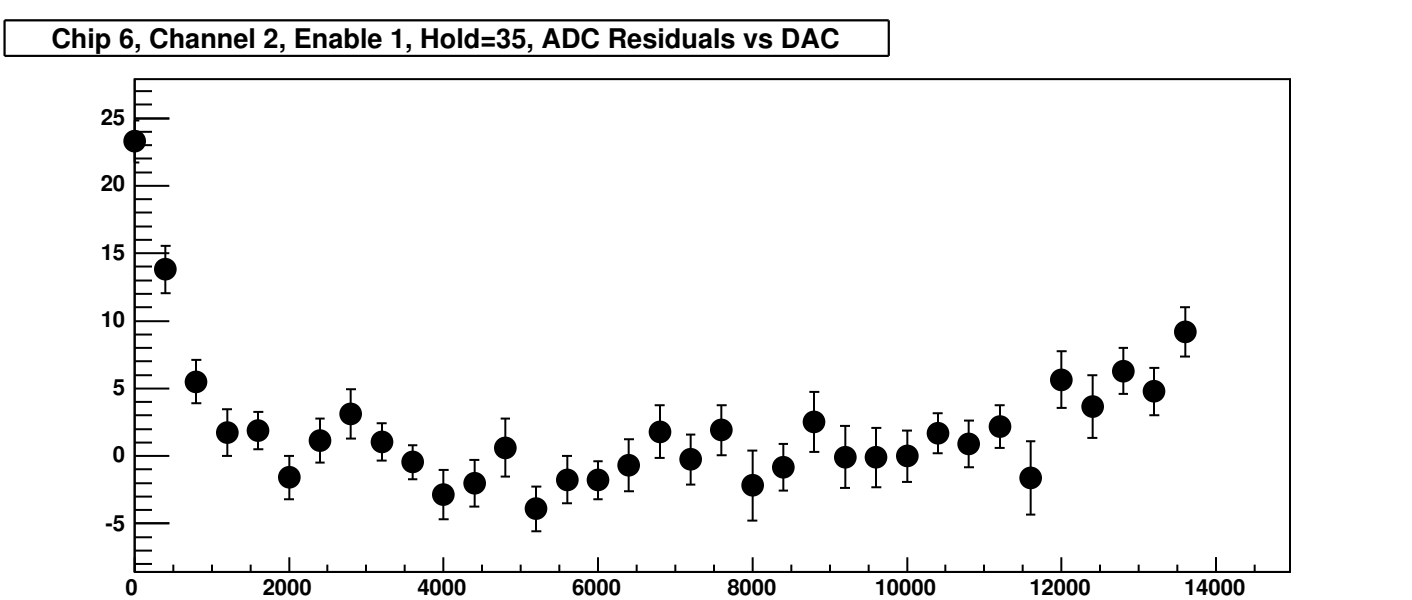

Chip 6, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1353 / 23
p0 3.032e+04 ± 6.879
p1 -0.001633 ± 0.0009837
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

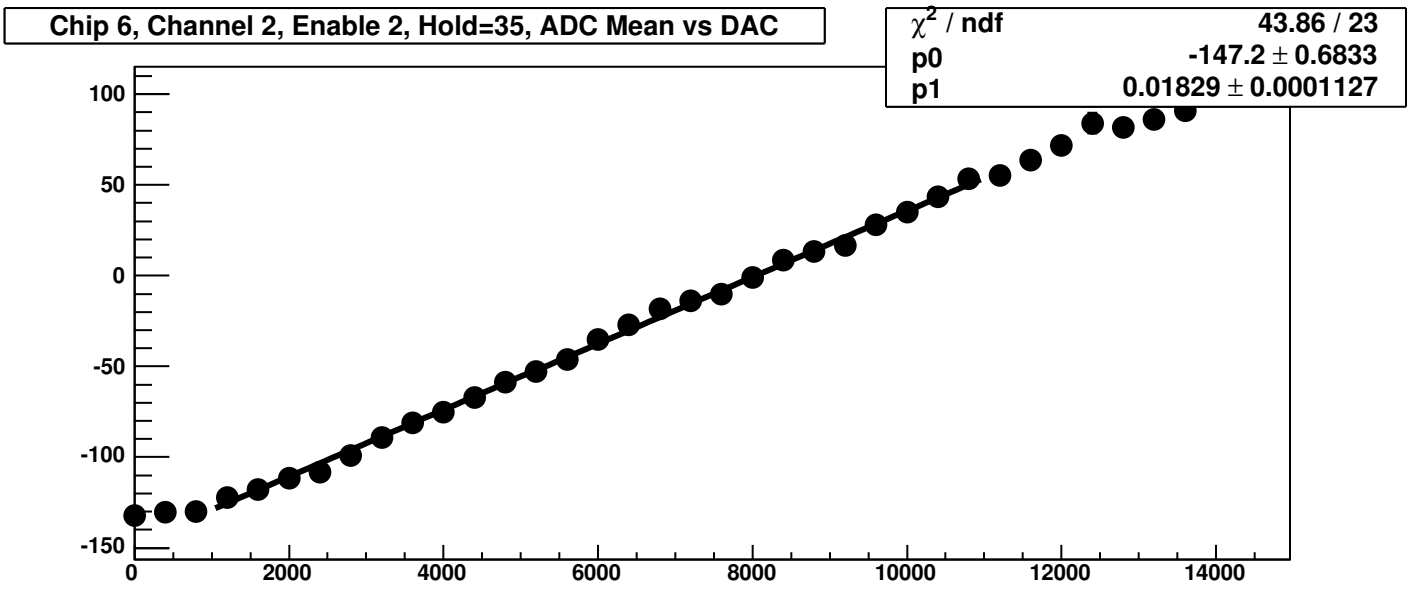

Chip 6, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

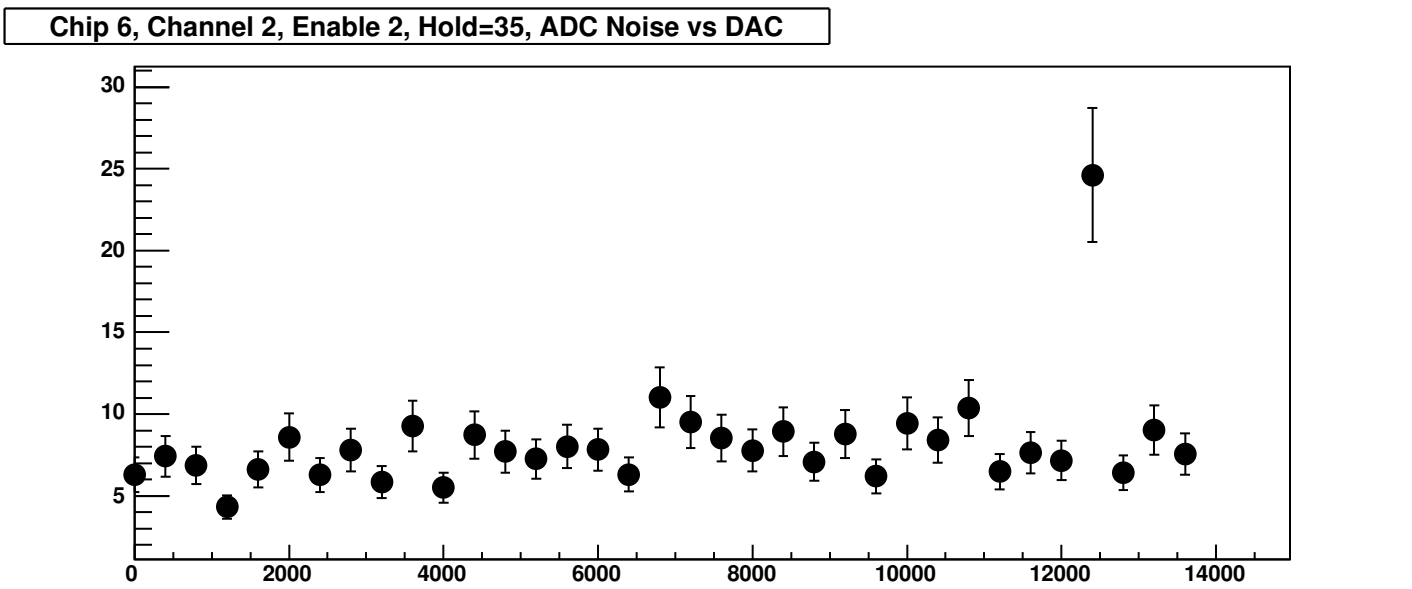

Chip 6, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

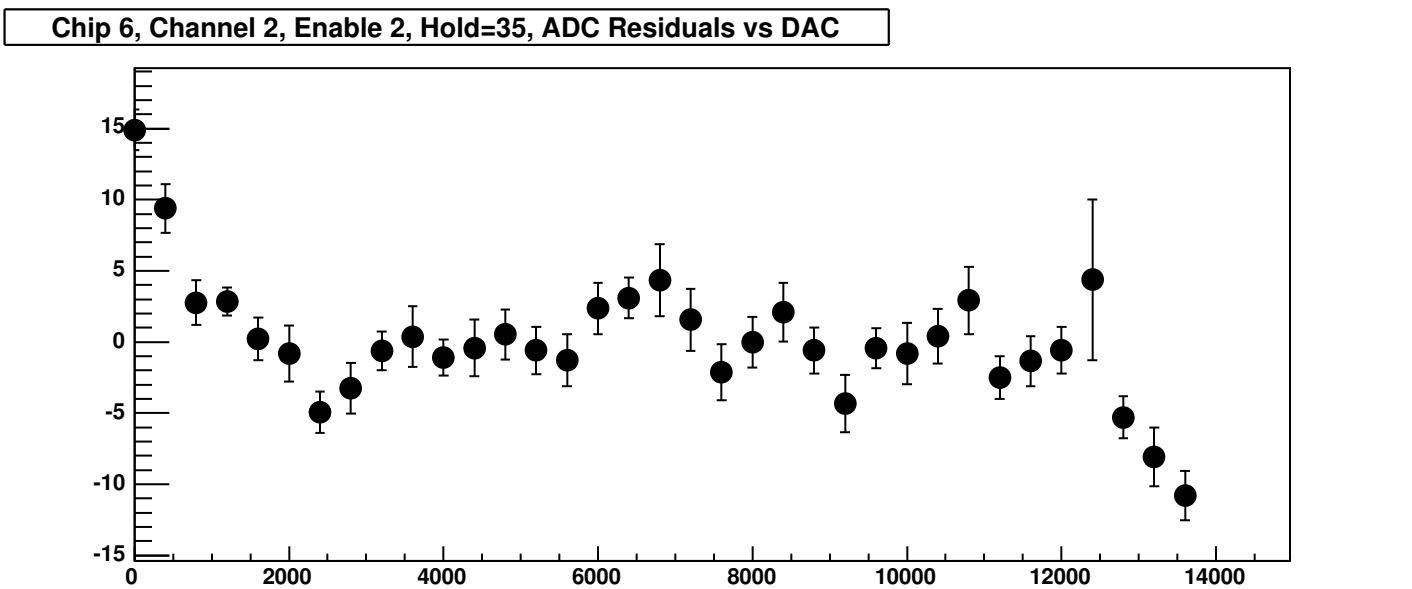

Chip 6, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.01 / 23
p0 -150.8 ± 0.7407
p1 0.02709 ± 0.0001177
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

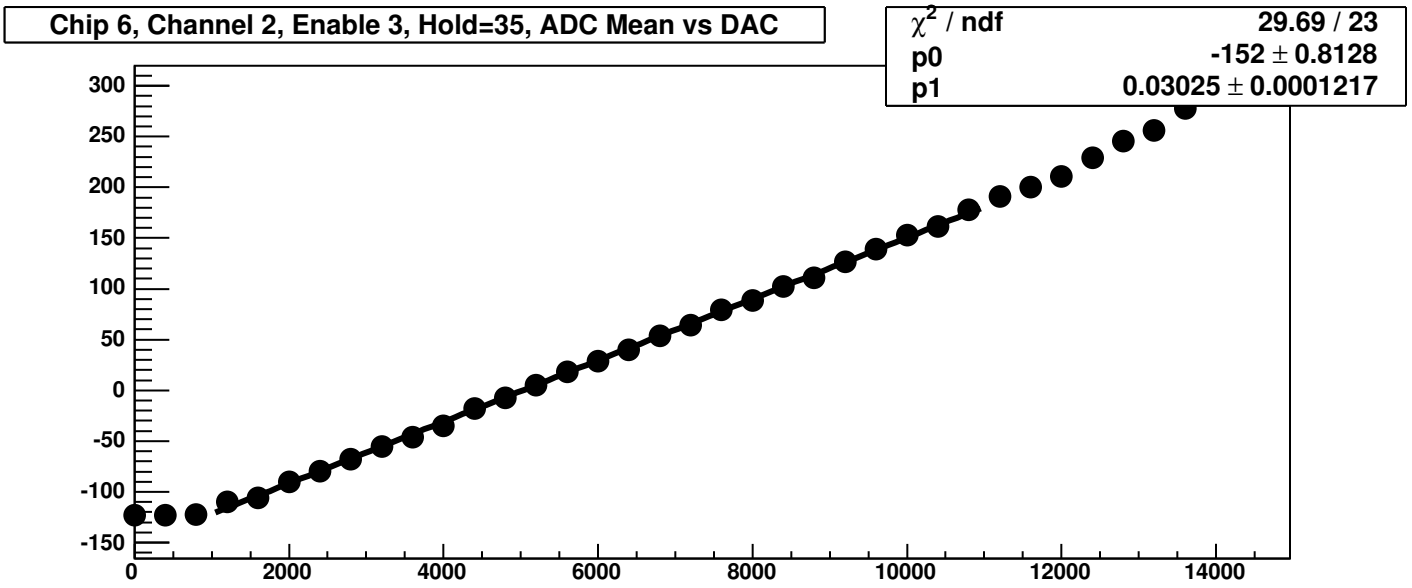

Chip 6, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

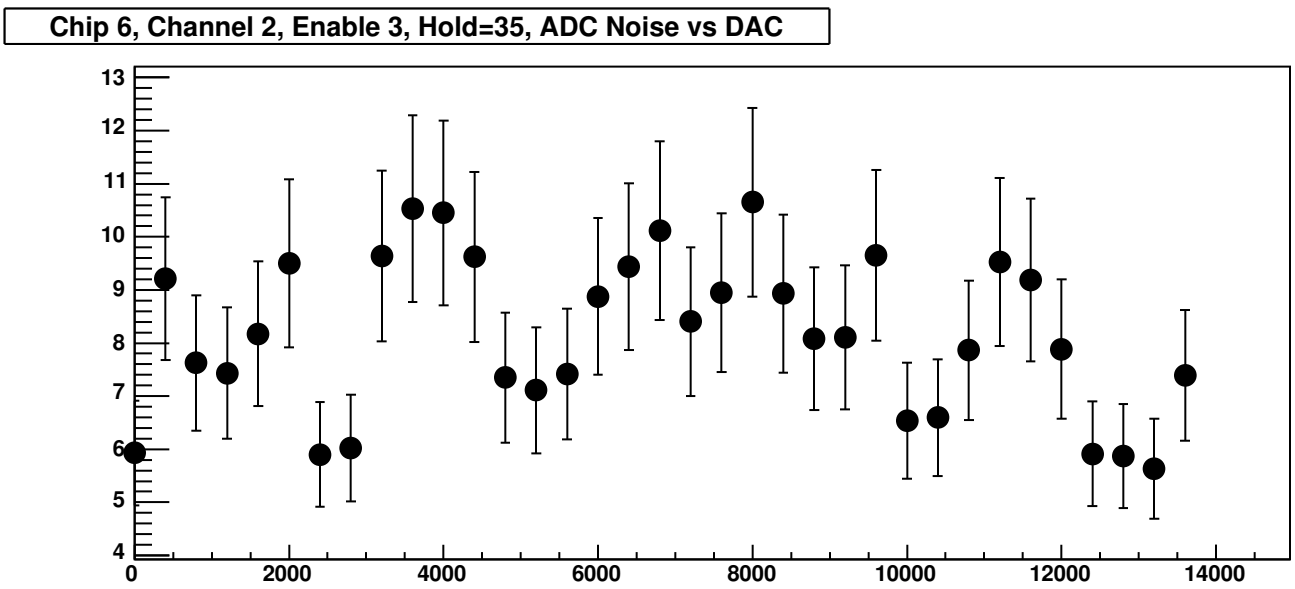

Chip 6, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

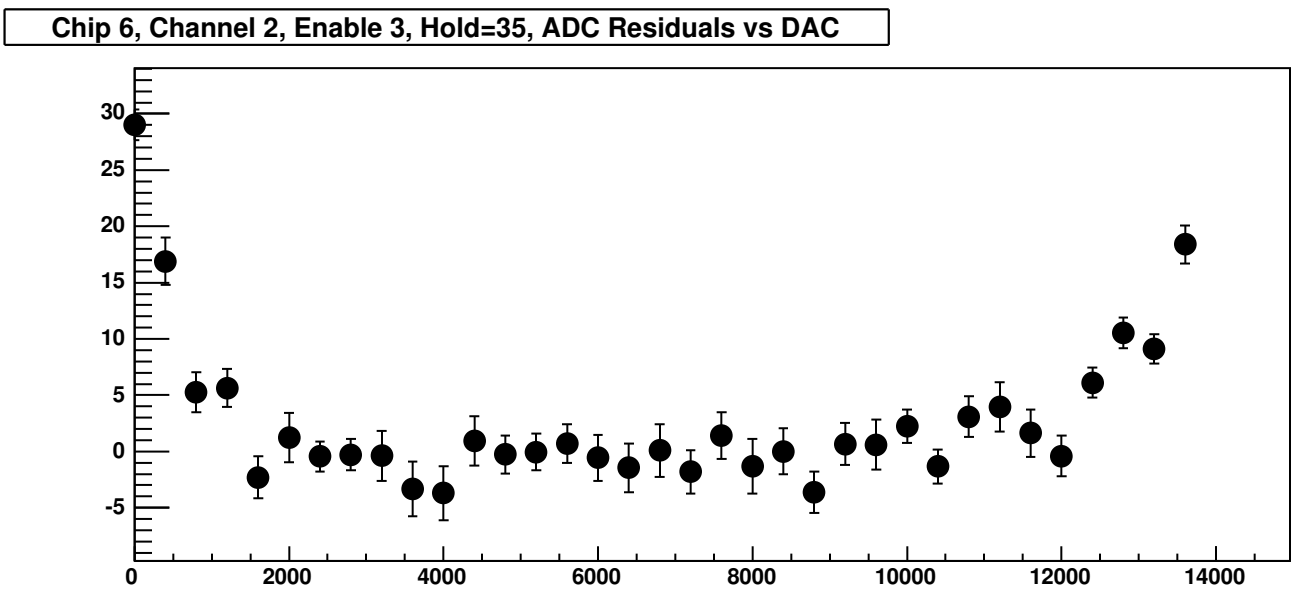

Chip 6, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.86 / 23
p0 -147.2 ± 0.6833
p1 0.01829 ± 0.0001127
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

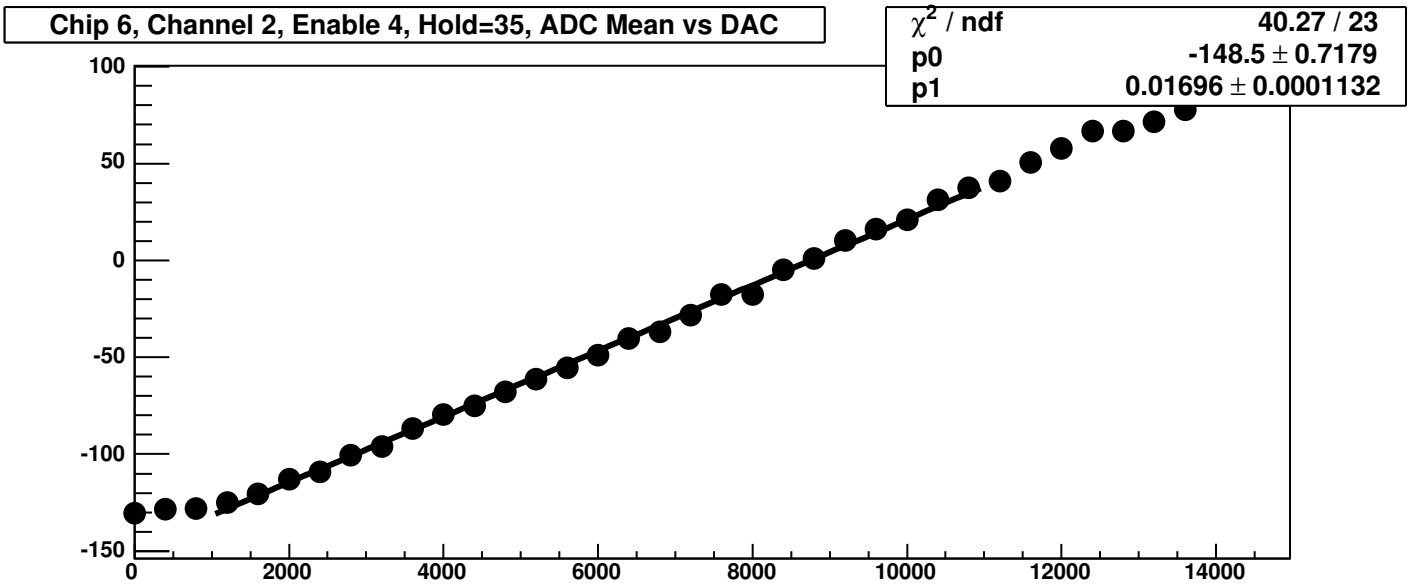

Chip 6, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

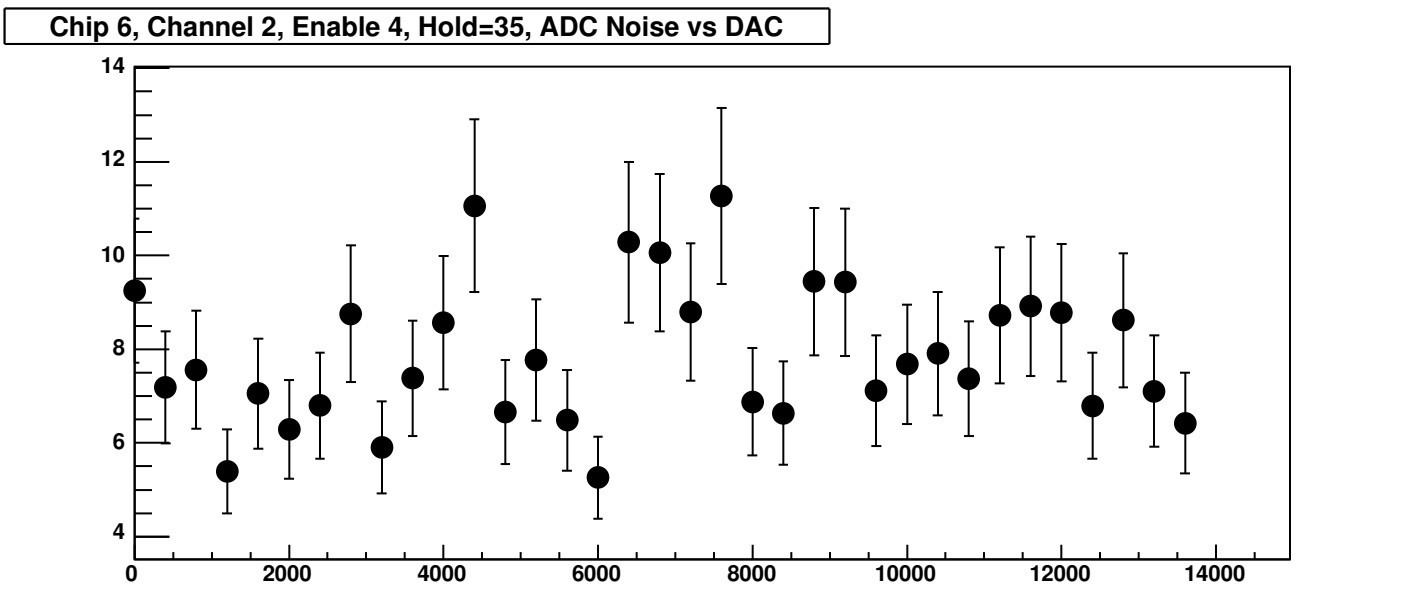

Chip 6, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

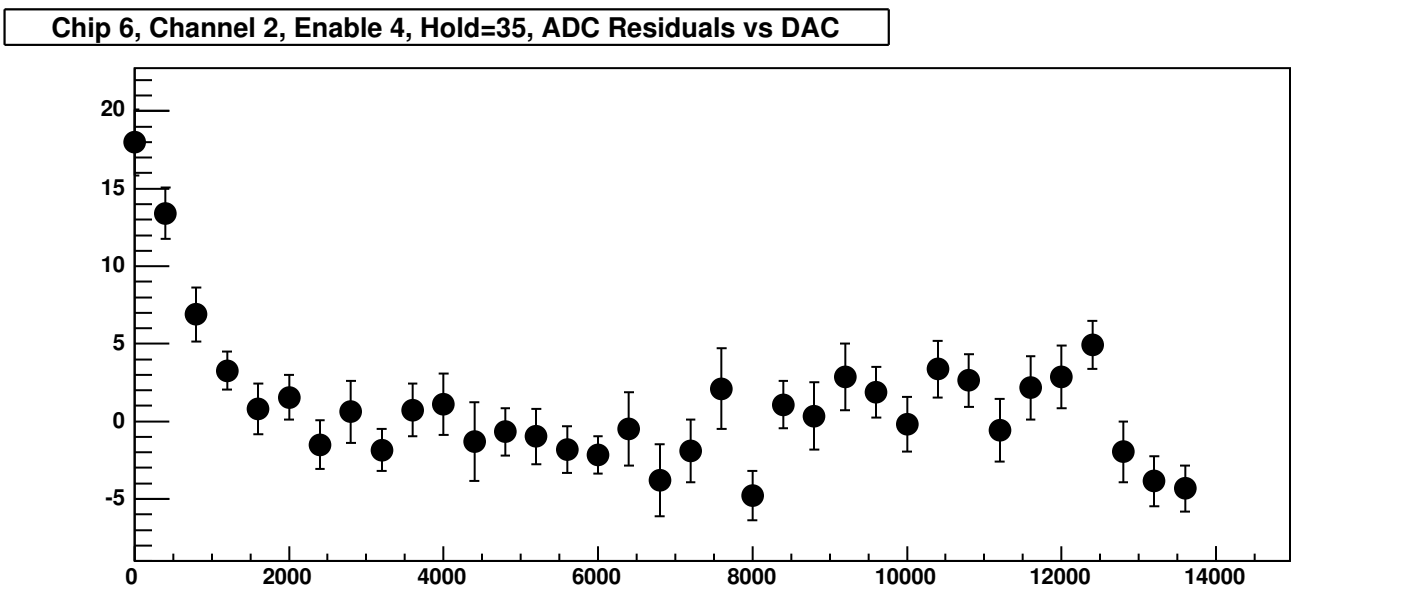

Chip 6, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.69 / 23
p0 -152 ± 0.8128
p1 0.03025 ± 0.0001217
300
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

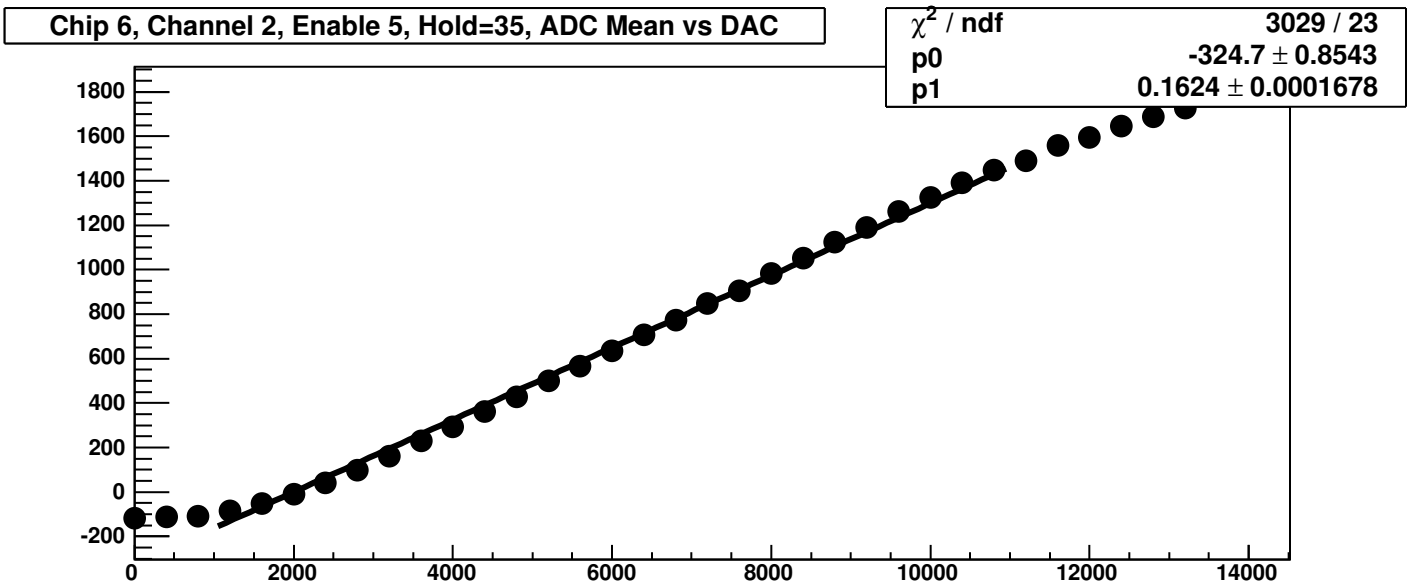

Chip 6, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

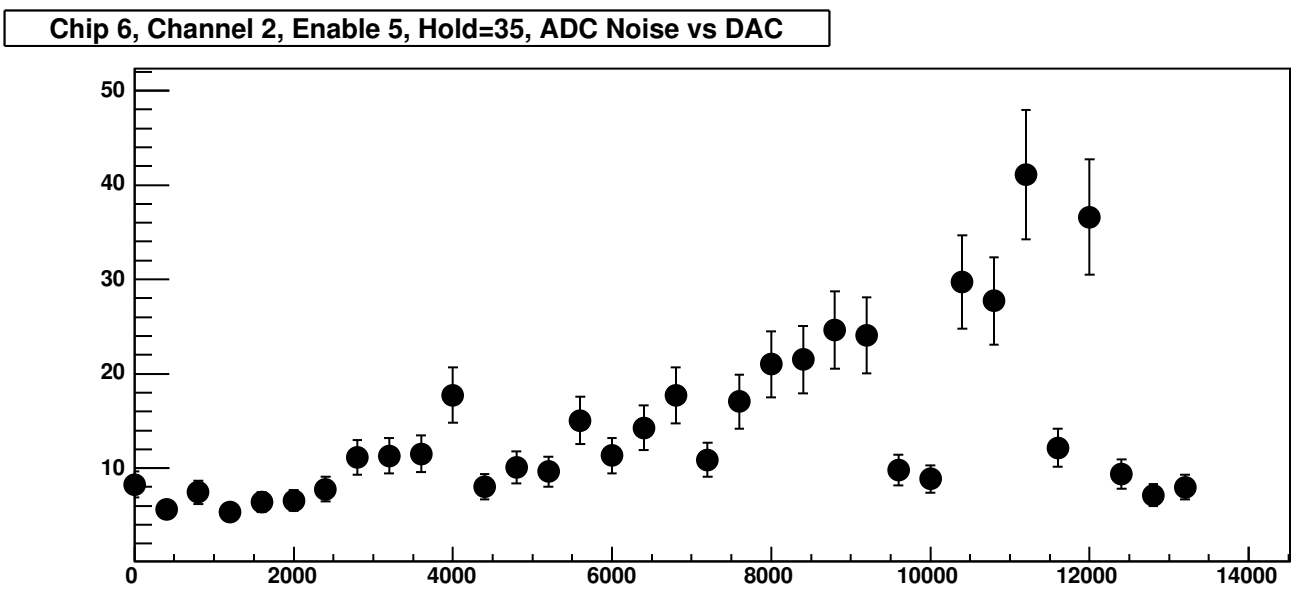

Chip 6, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

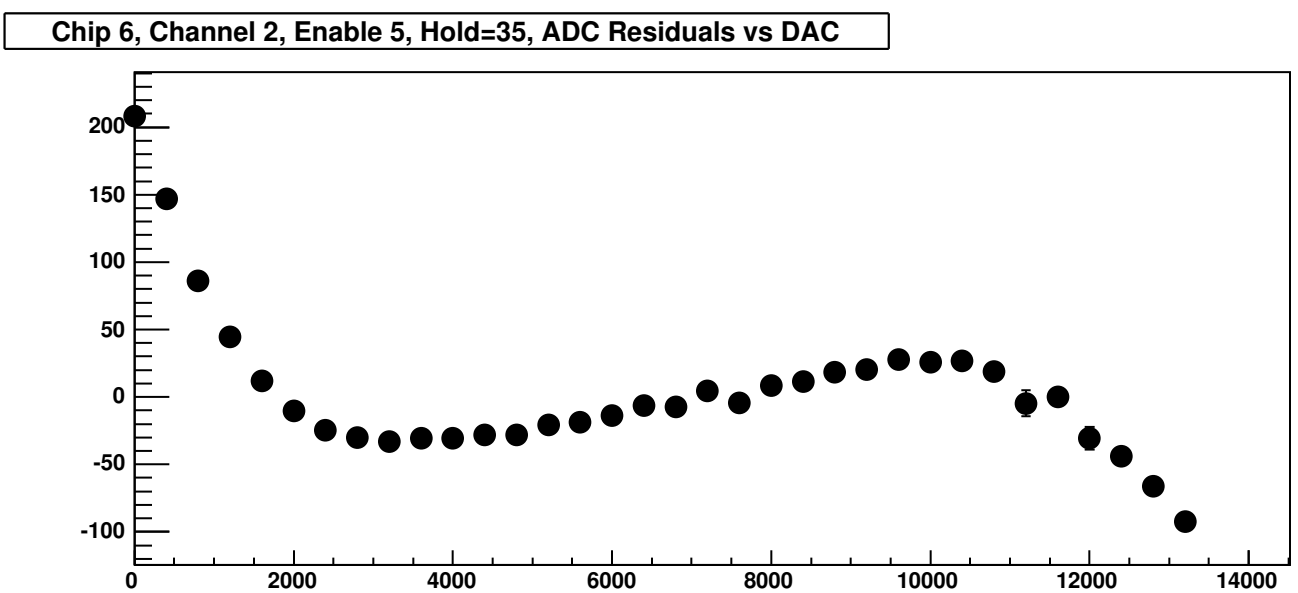

Chip 6, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.27 / 23
p0 -148.5 ± 0.7179
p1 0.01696 ± 0.0001132
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

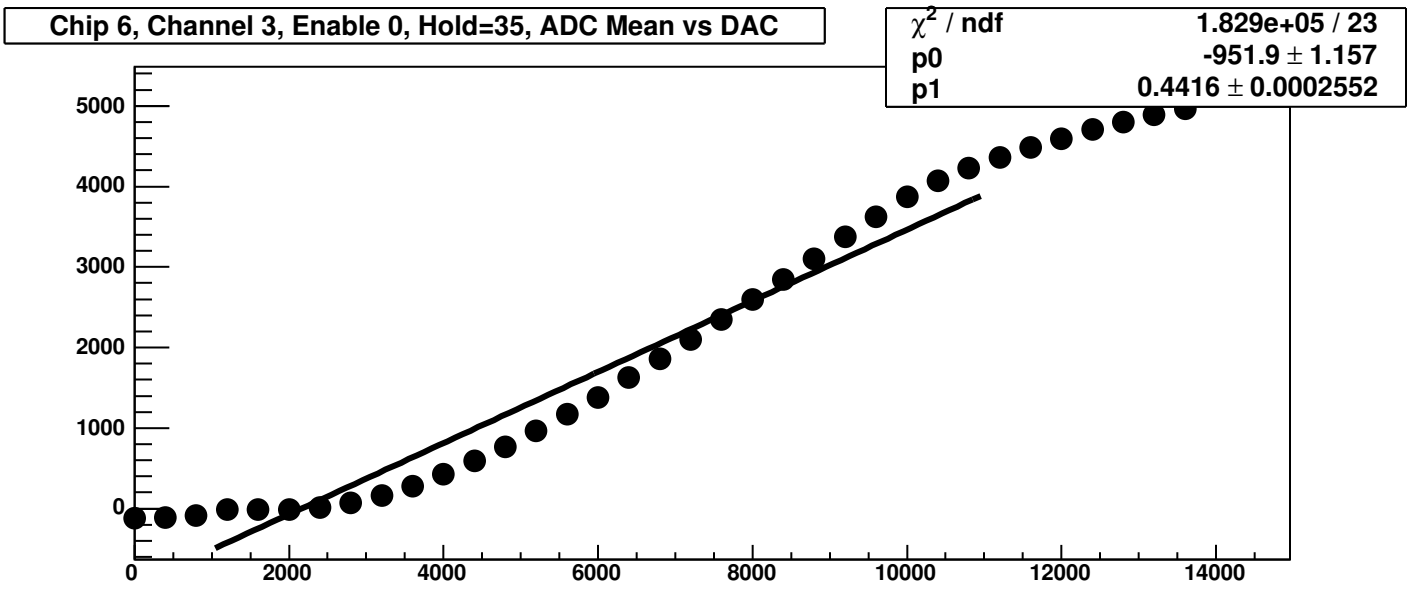

Chip 6, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

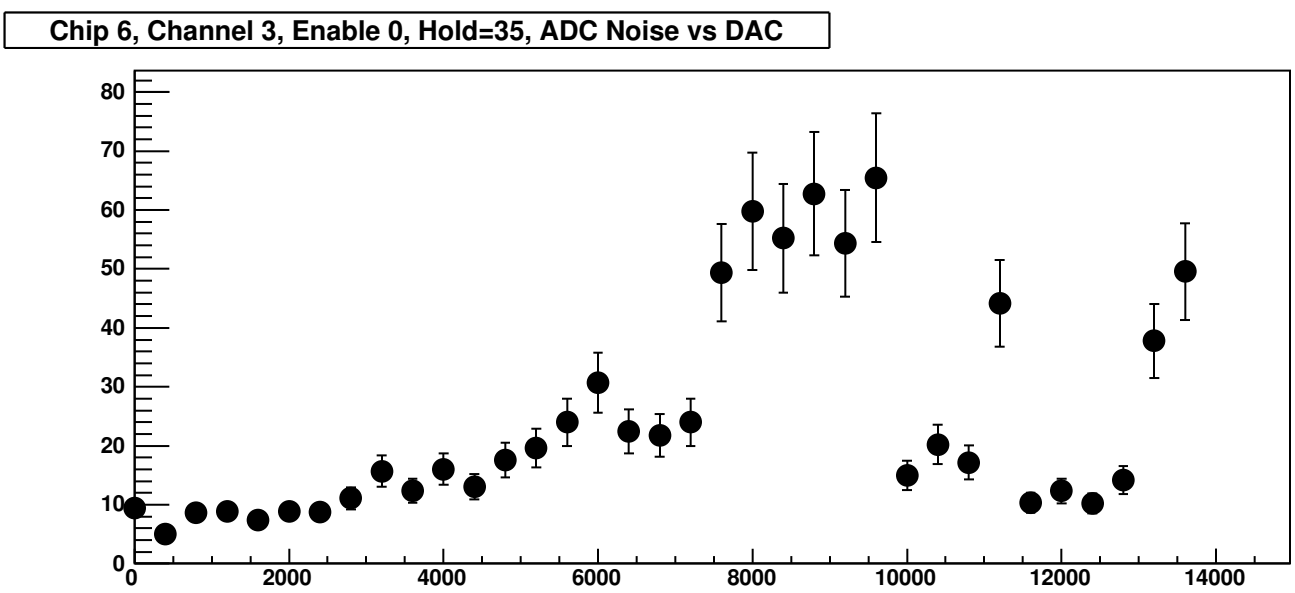

Chip 6, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

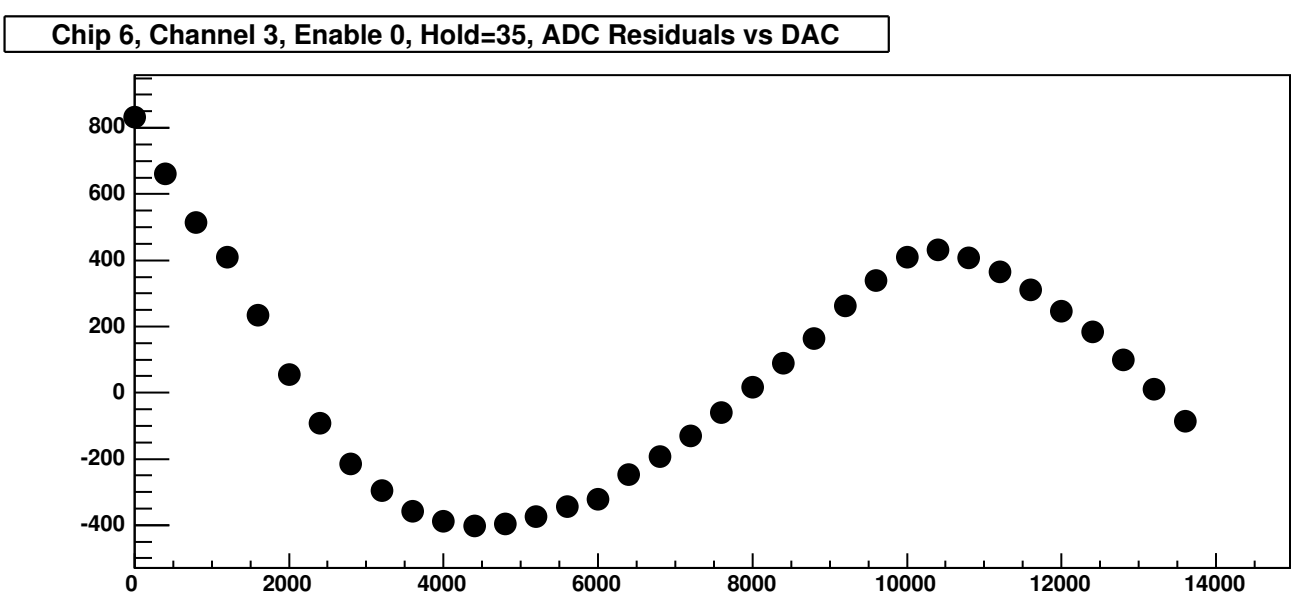

Chip 6, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3029 / 23
p0 -324.7 ± 0.8543
p1 0.1624 ± 0.0001678
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

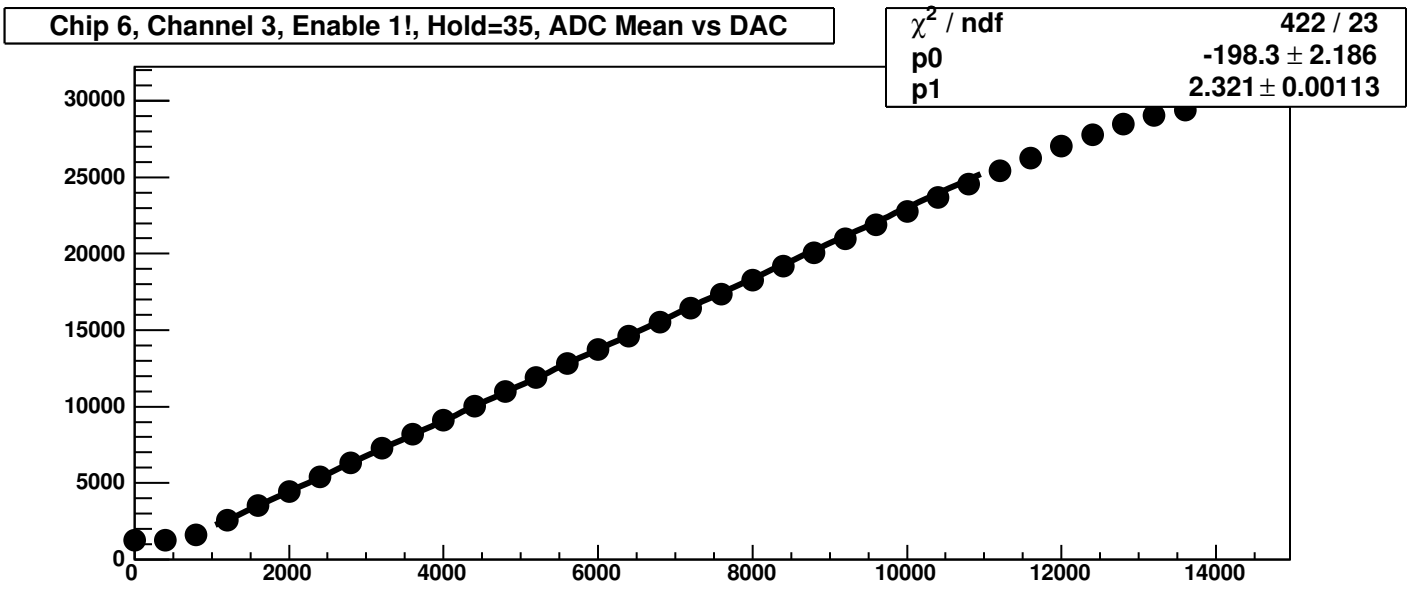

Chip 6, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

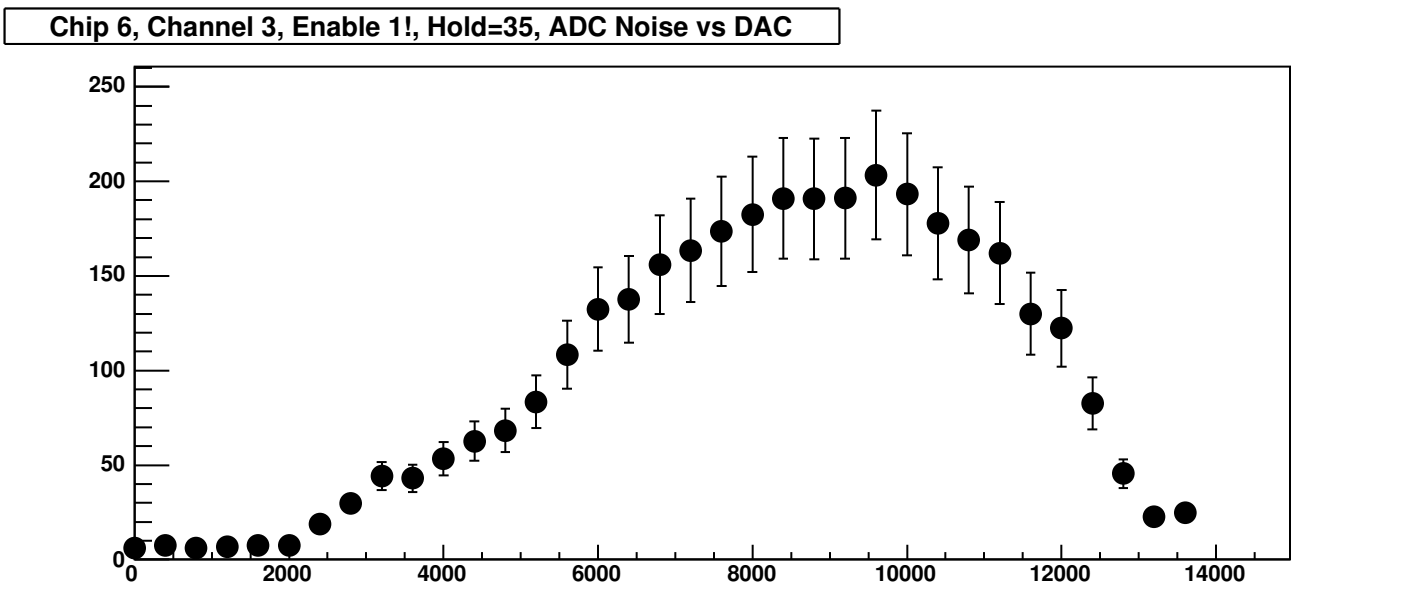

Chip 6, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

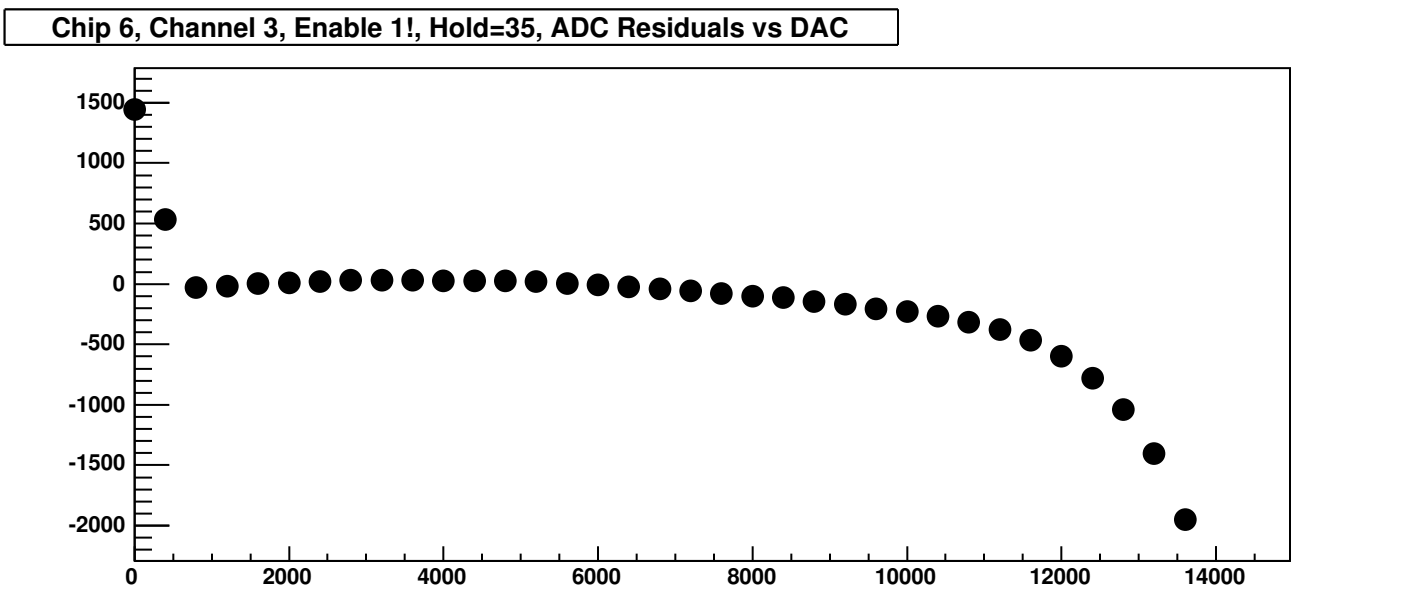

Chip 6, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.829e+05 / 23
p0 -951.9 ± 1.157
p1 0.4416 ± 0.0002552
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

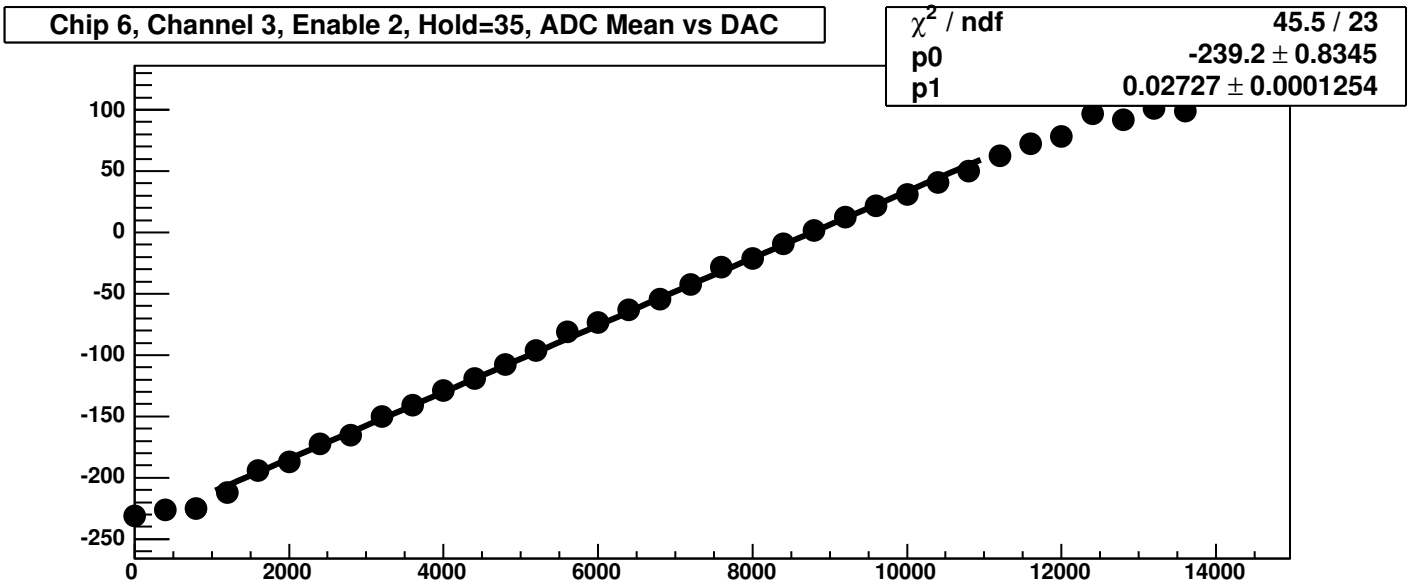

Chip 6, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

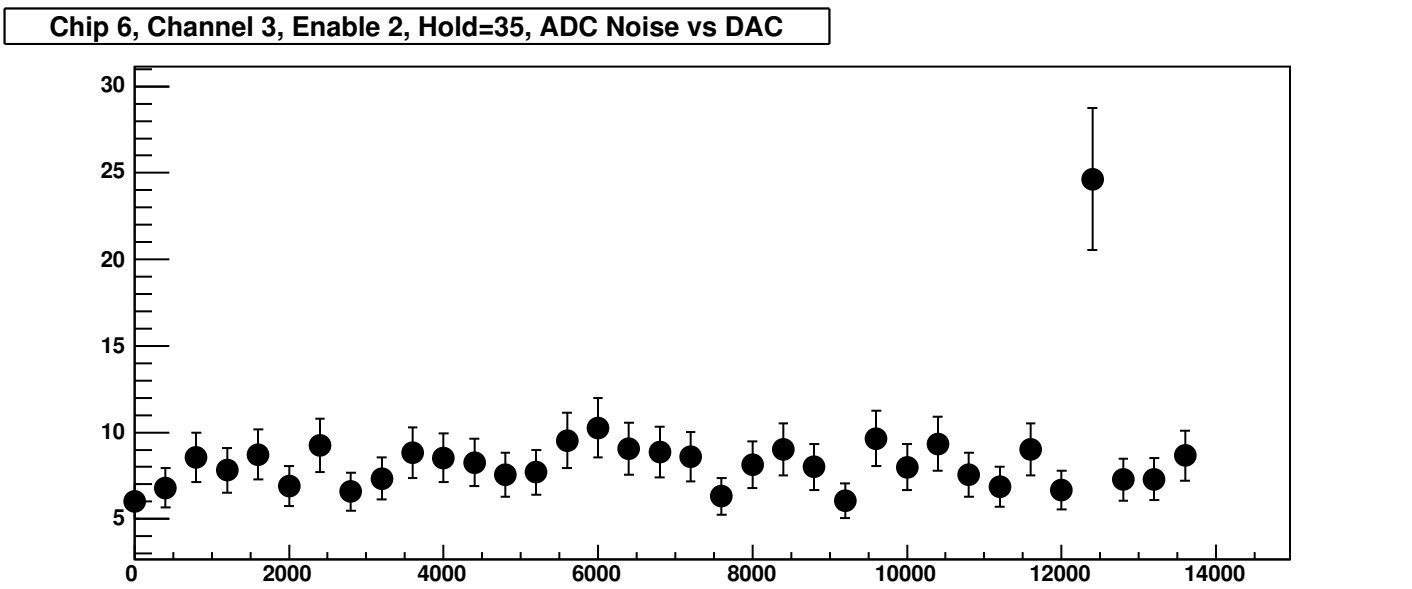

Chip 6, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

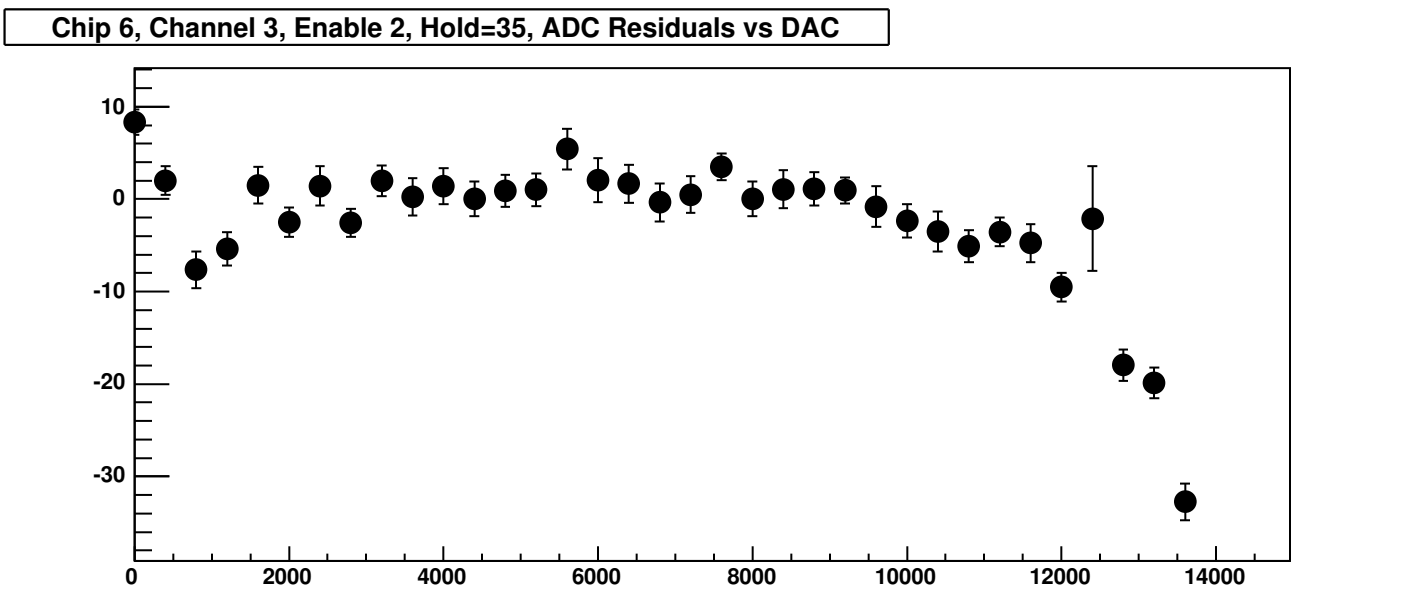

Chip 6, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 422 / 23
p0 -198.3 ± 2.186
p1 2.321 ± 0.00113
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

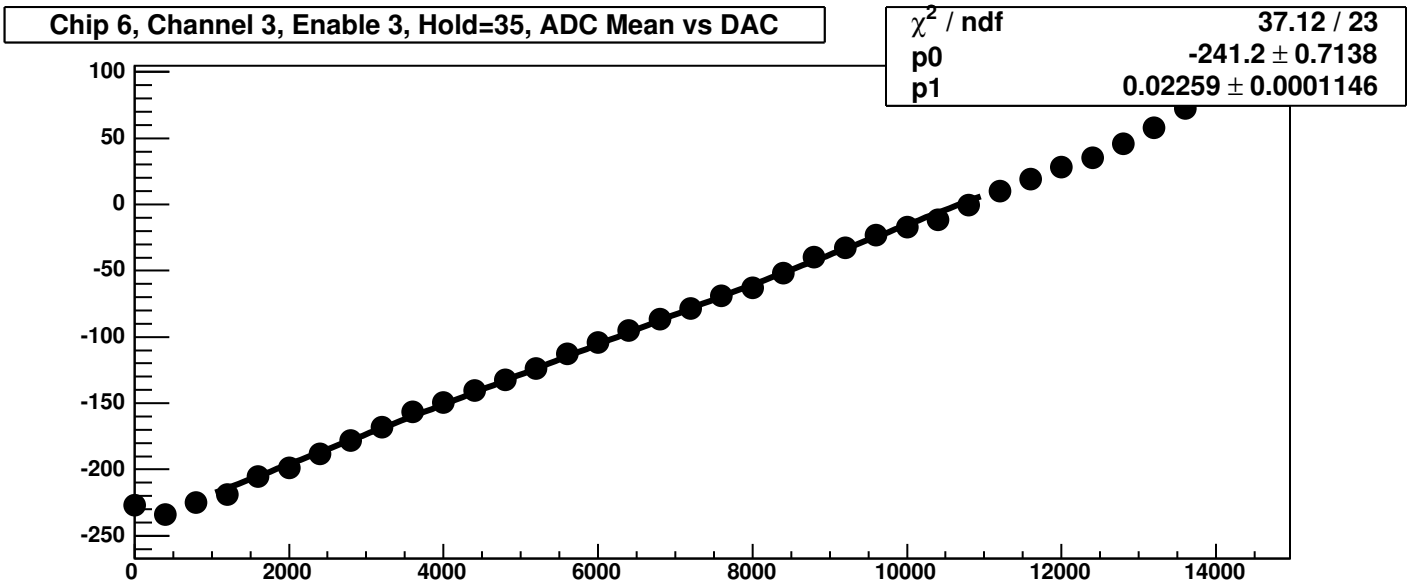

Chip 6, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

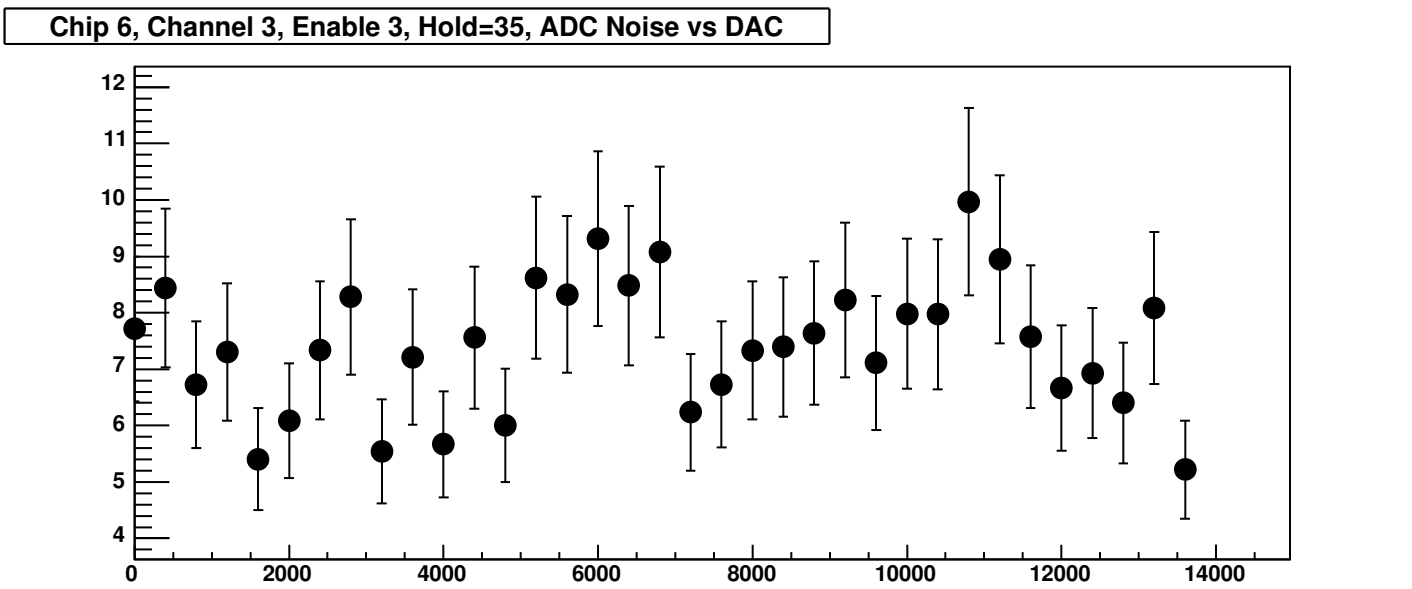

Chip 6, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

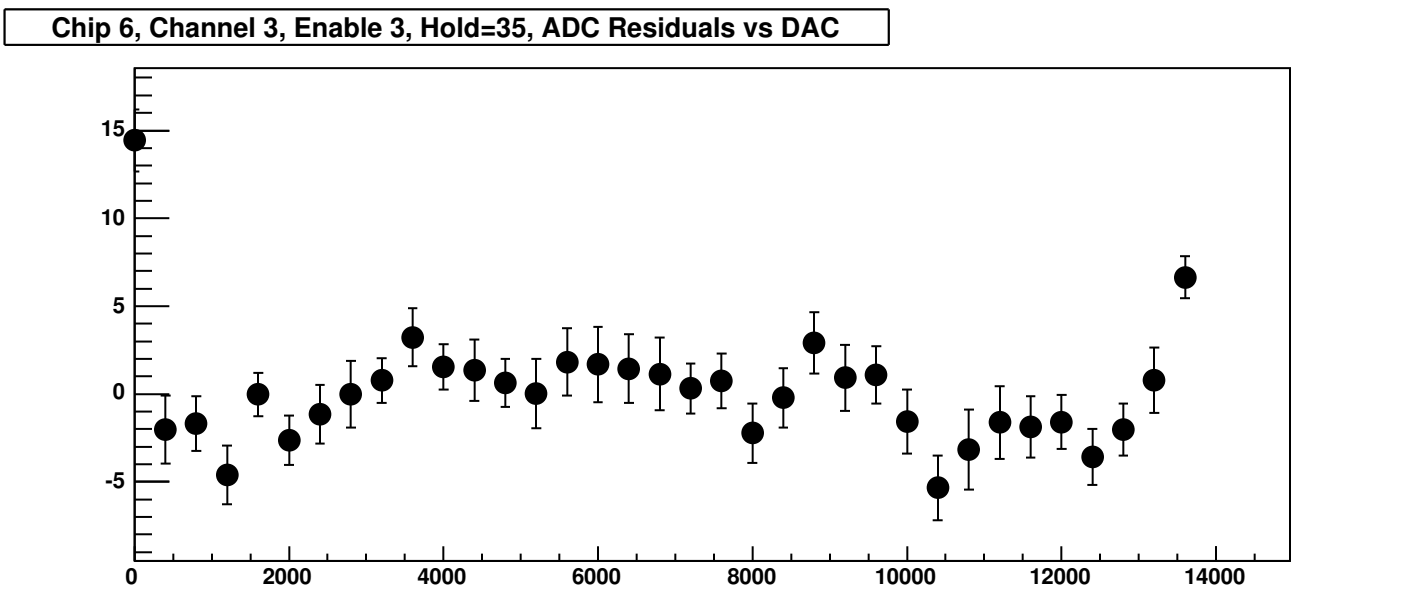

Chip 6, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.5 / 23
p0 -239.2 ± 0.8345
p1 0.02727 ± 0.0001254
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

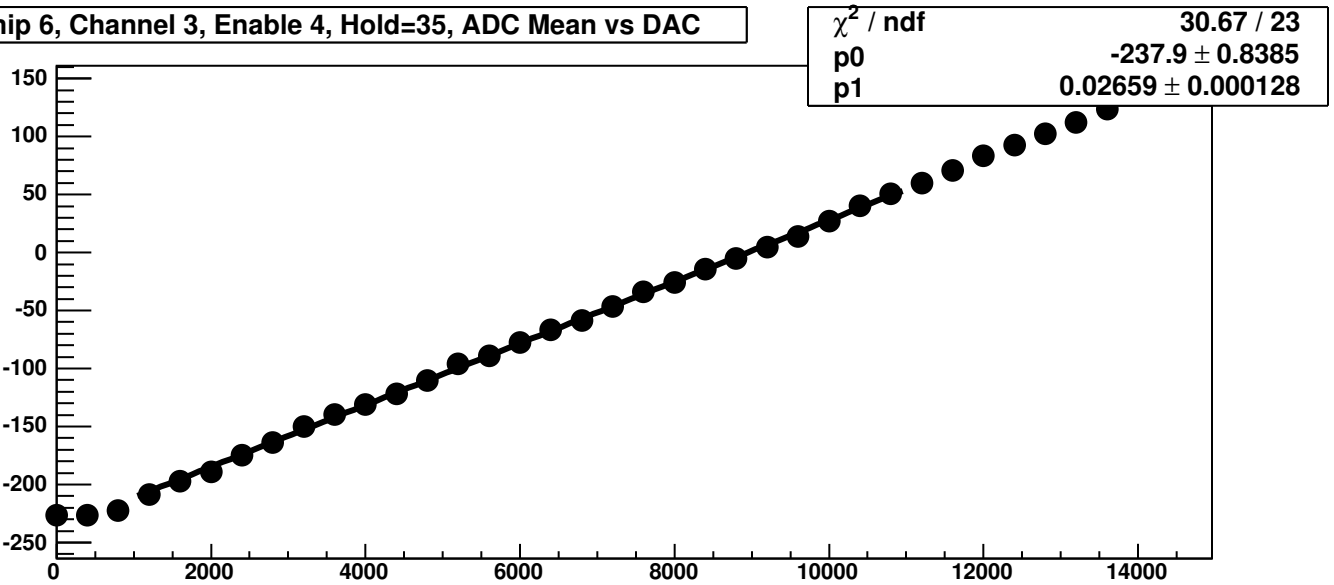

Chip 6, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.12 / 23
p0 -241.2 ± 0.7138
p1 0.02259 ± 0.0001146
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

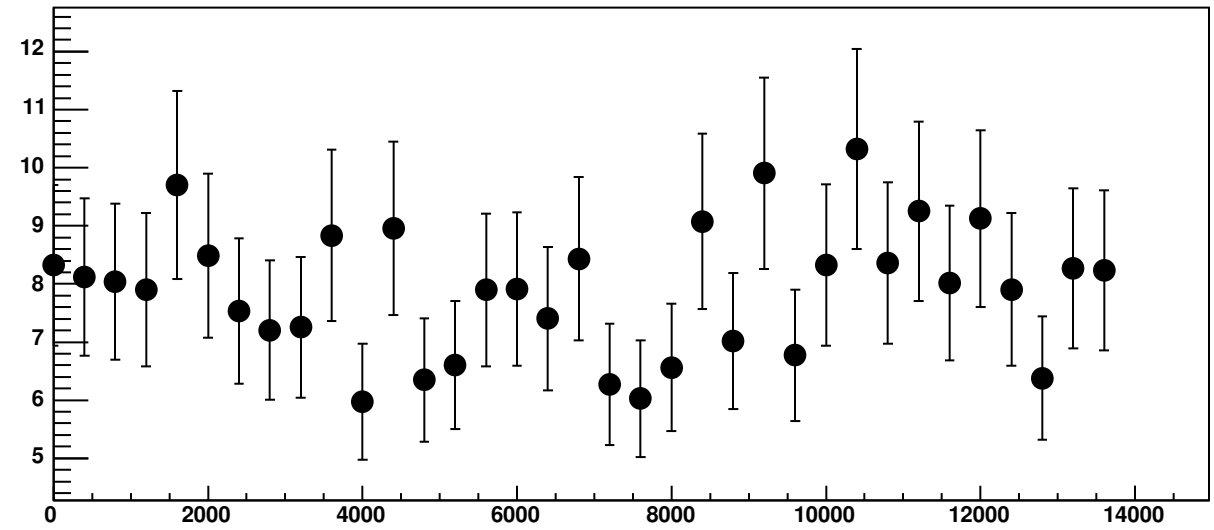

Chip 6, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

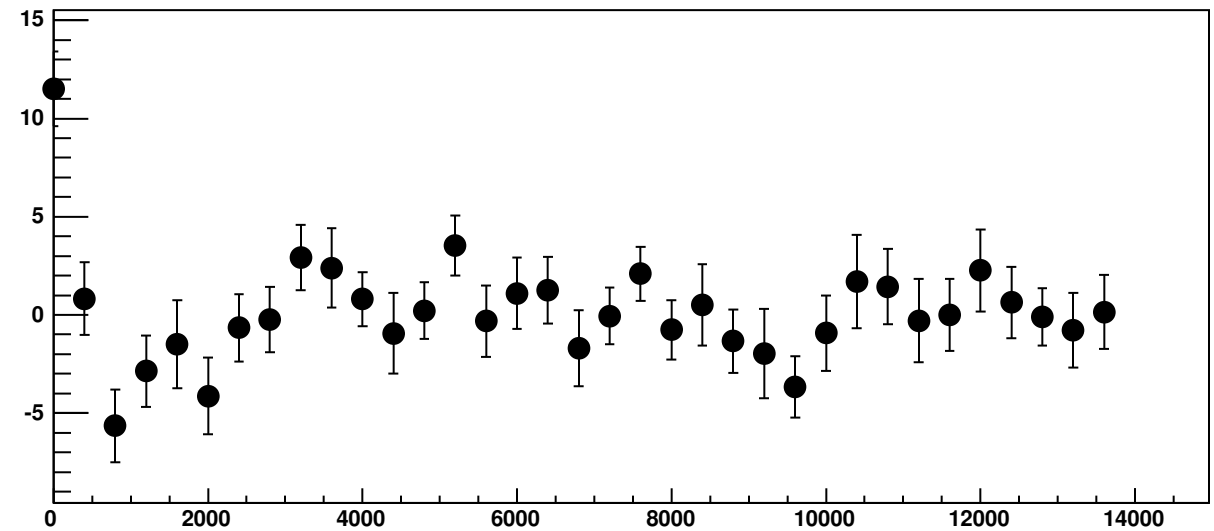
Chip 6, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

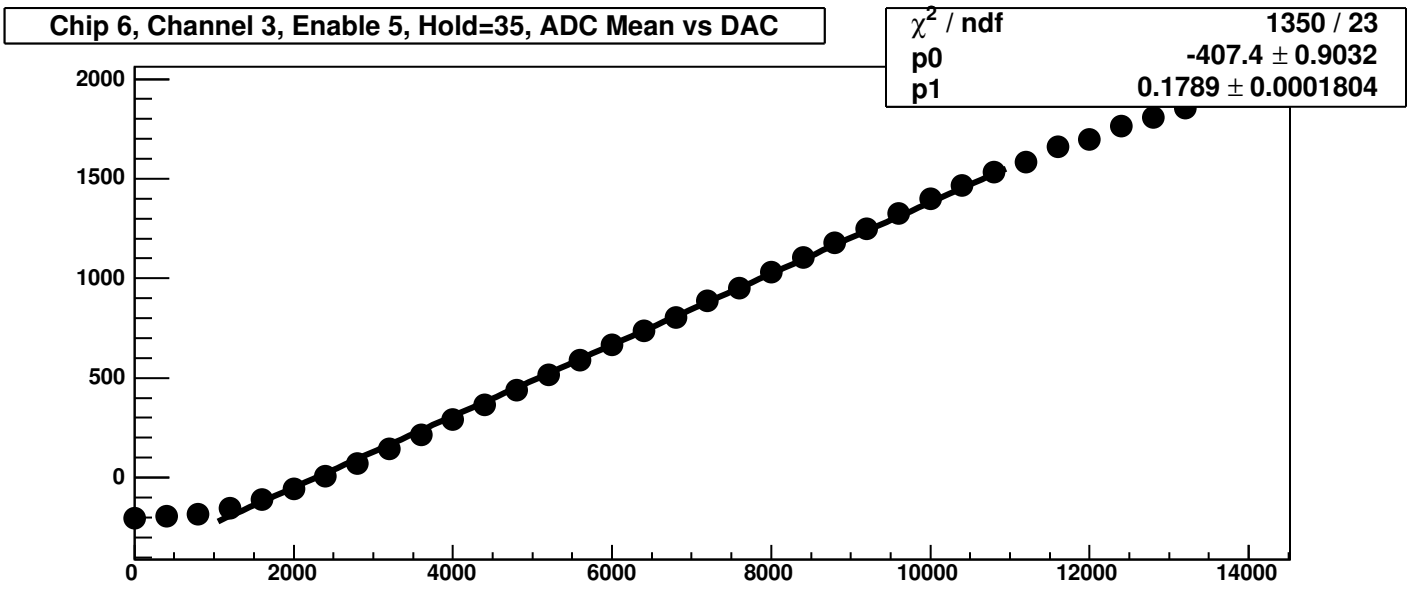


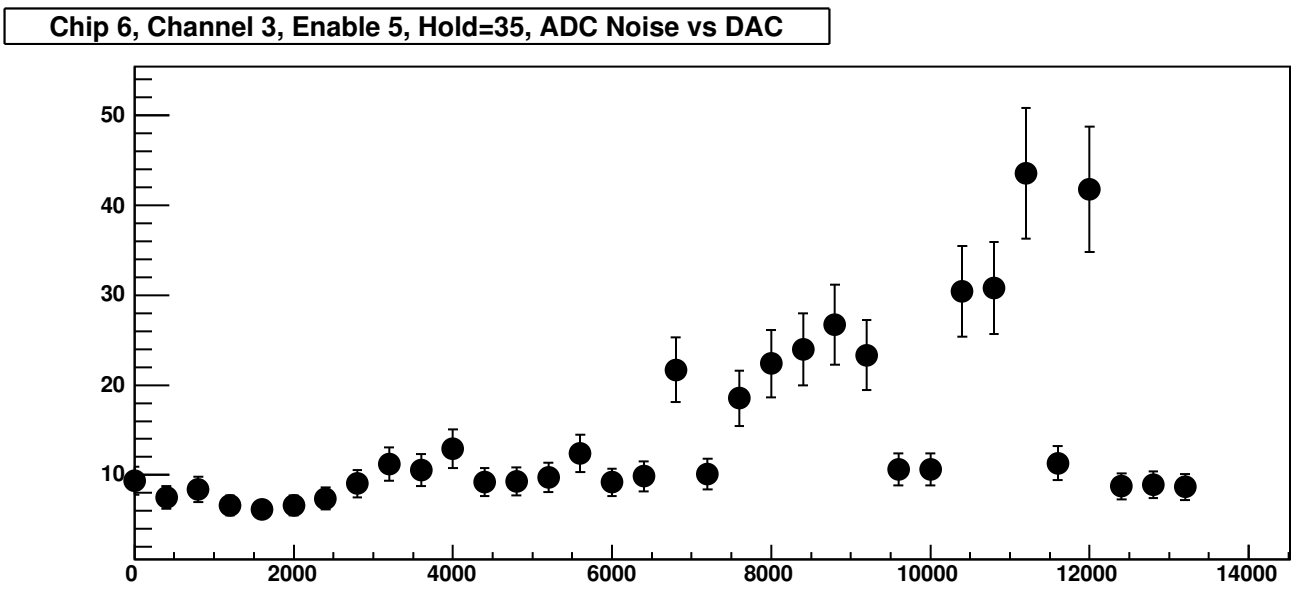
Chip 6, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

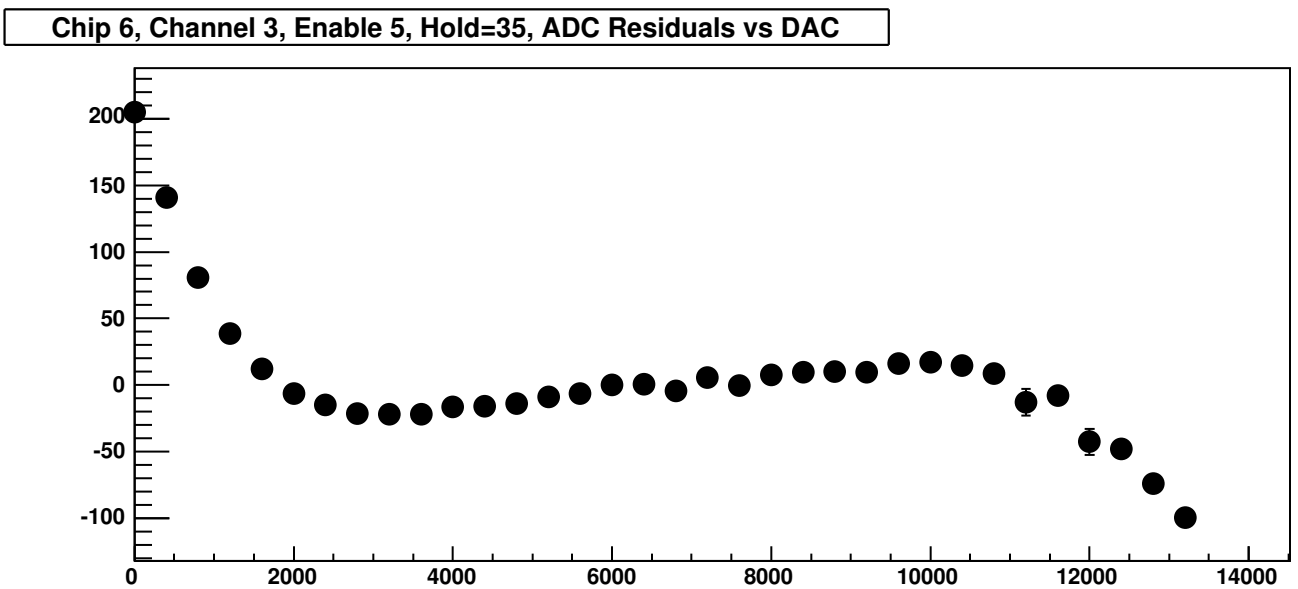


Chip 6, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

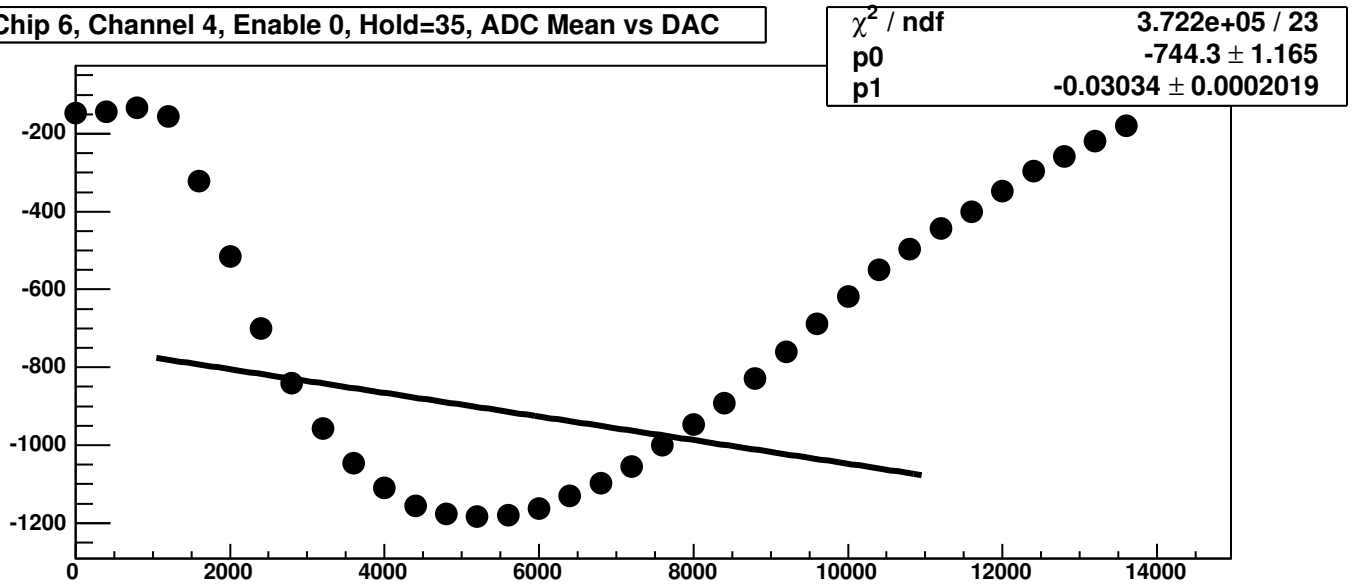



Chip 6, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1350 / 23
p0 -407.4 ± 0.9032
p1 0.1789 ± 0.0001804
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

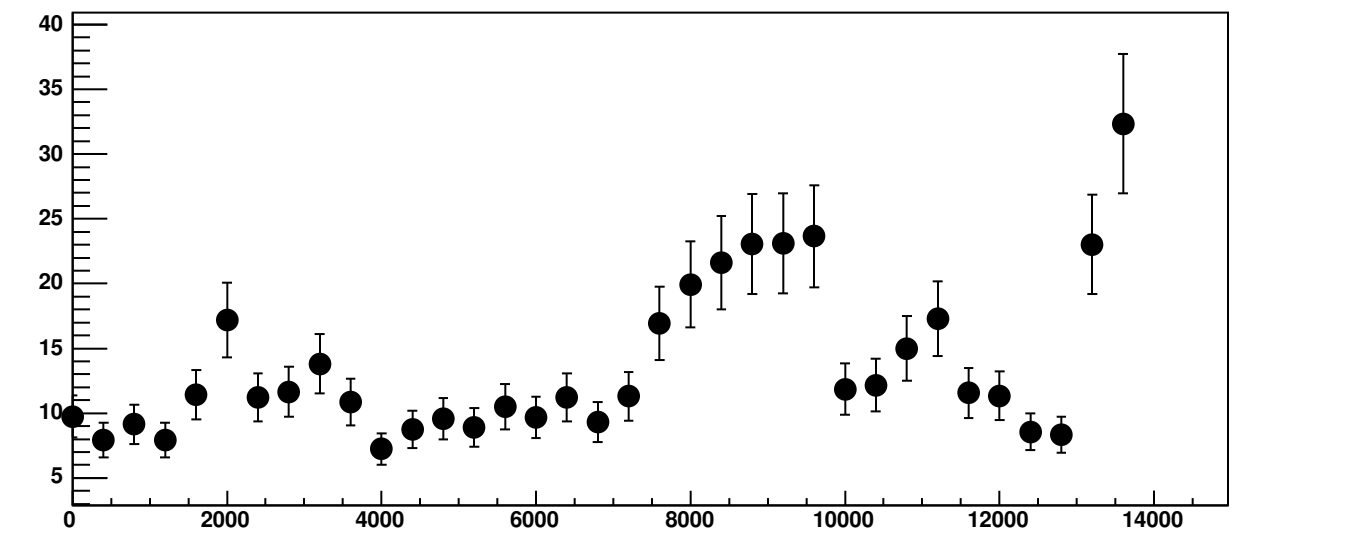

Chip 6, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

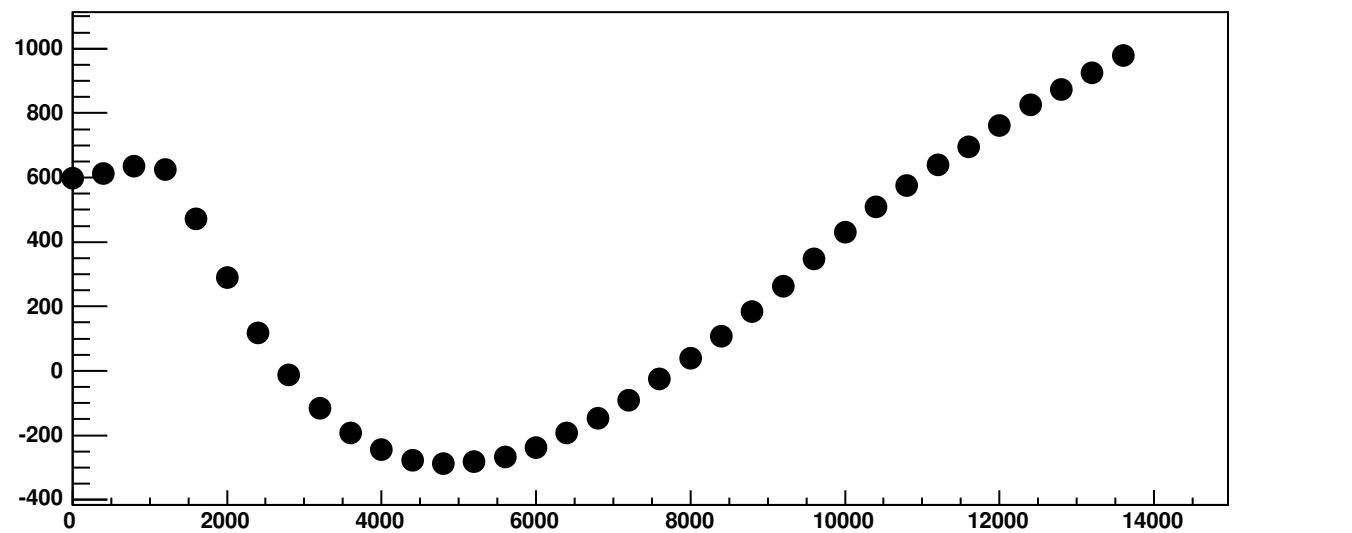
Chip 6, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



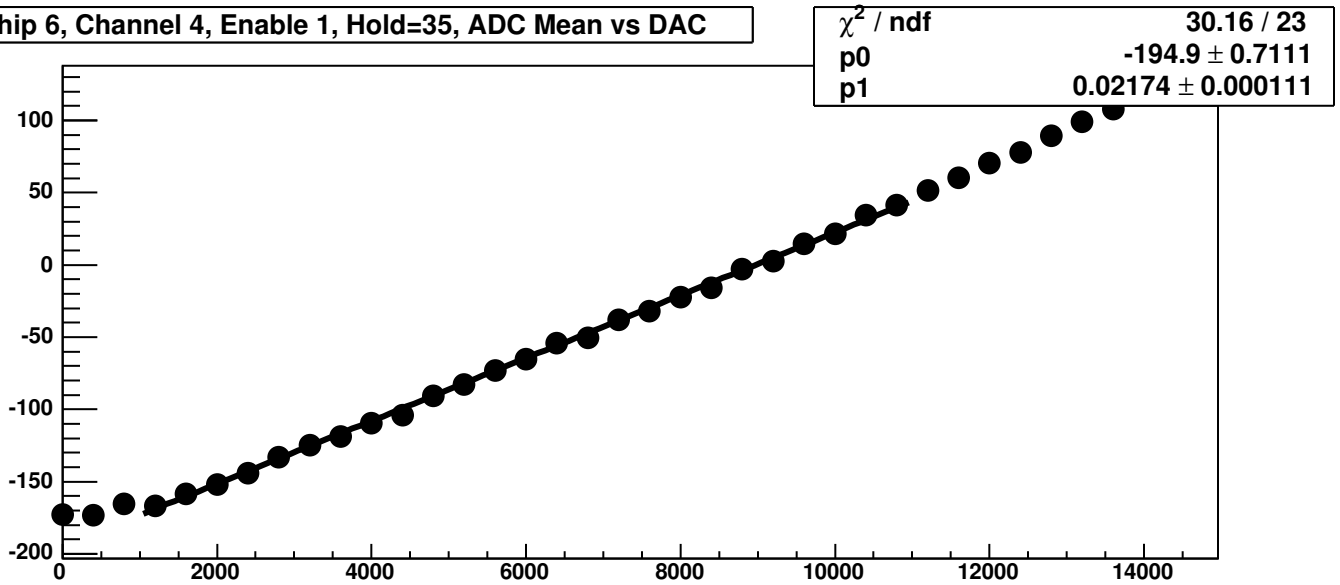
Chip 6, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



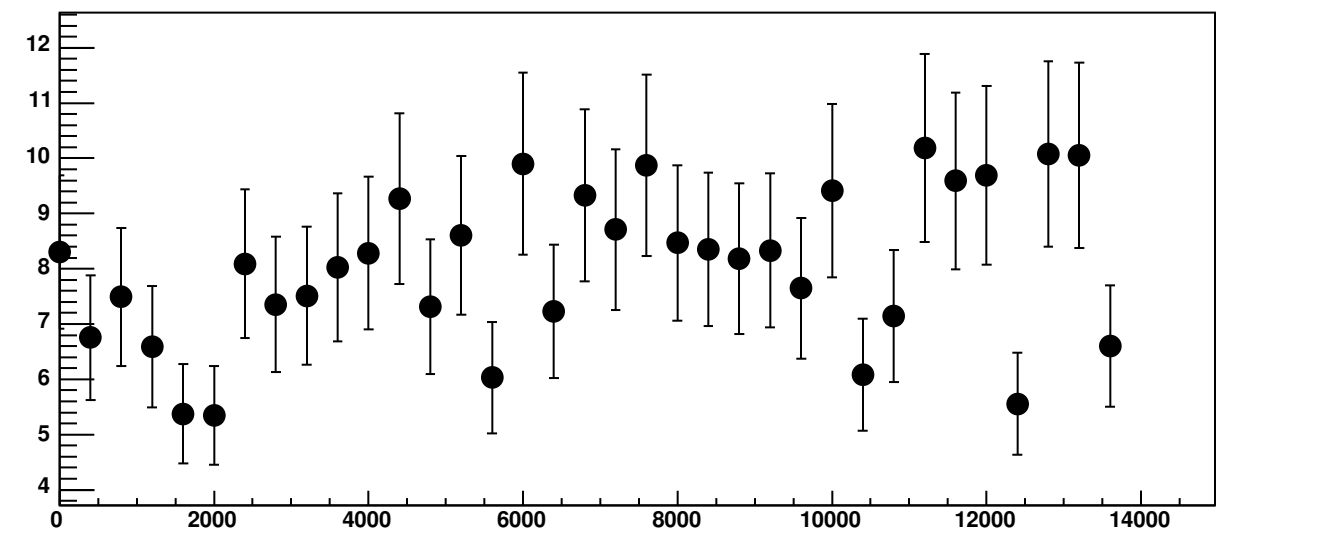
Chip 6, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



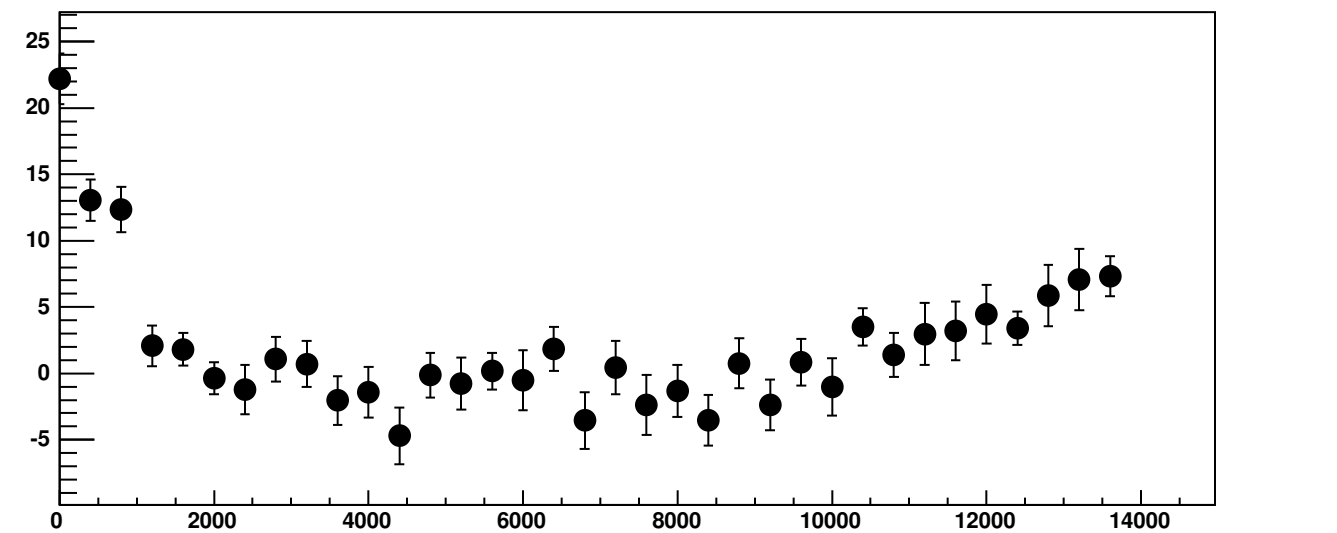
Chip 6, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



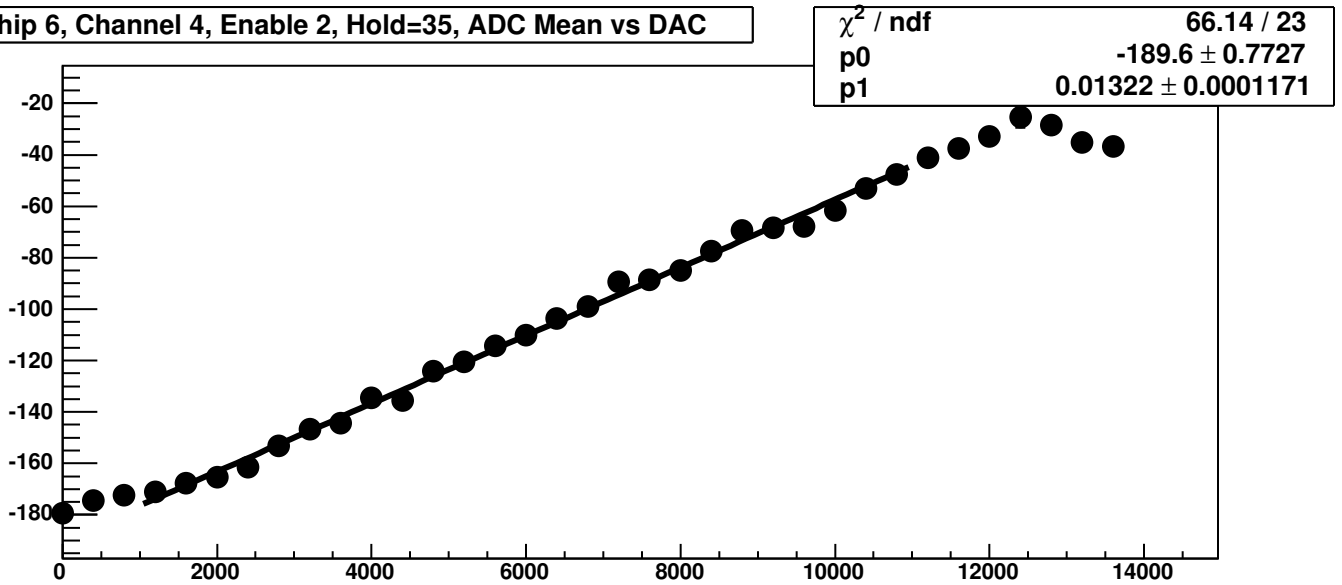
Chip 6, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



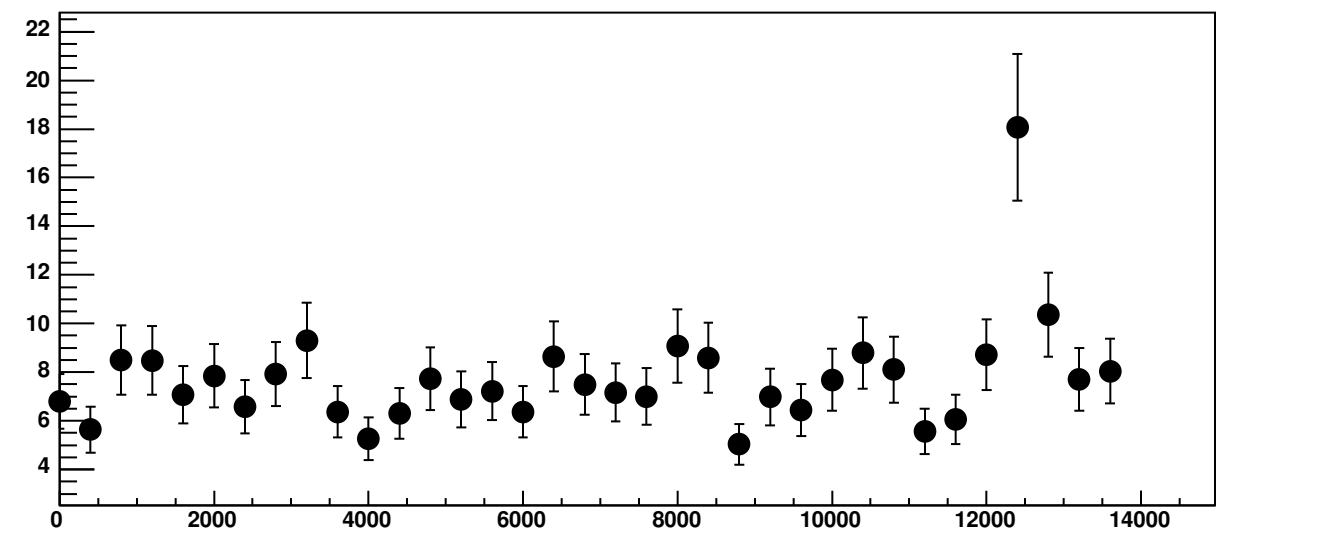
Chip 6, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



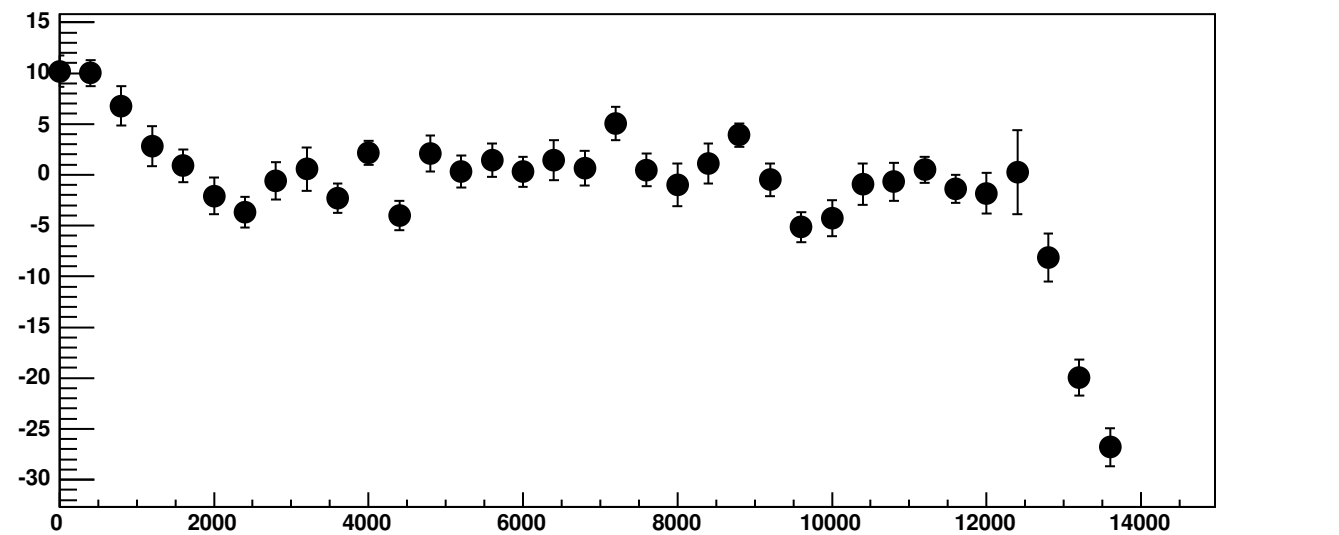
Chip 6, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

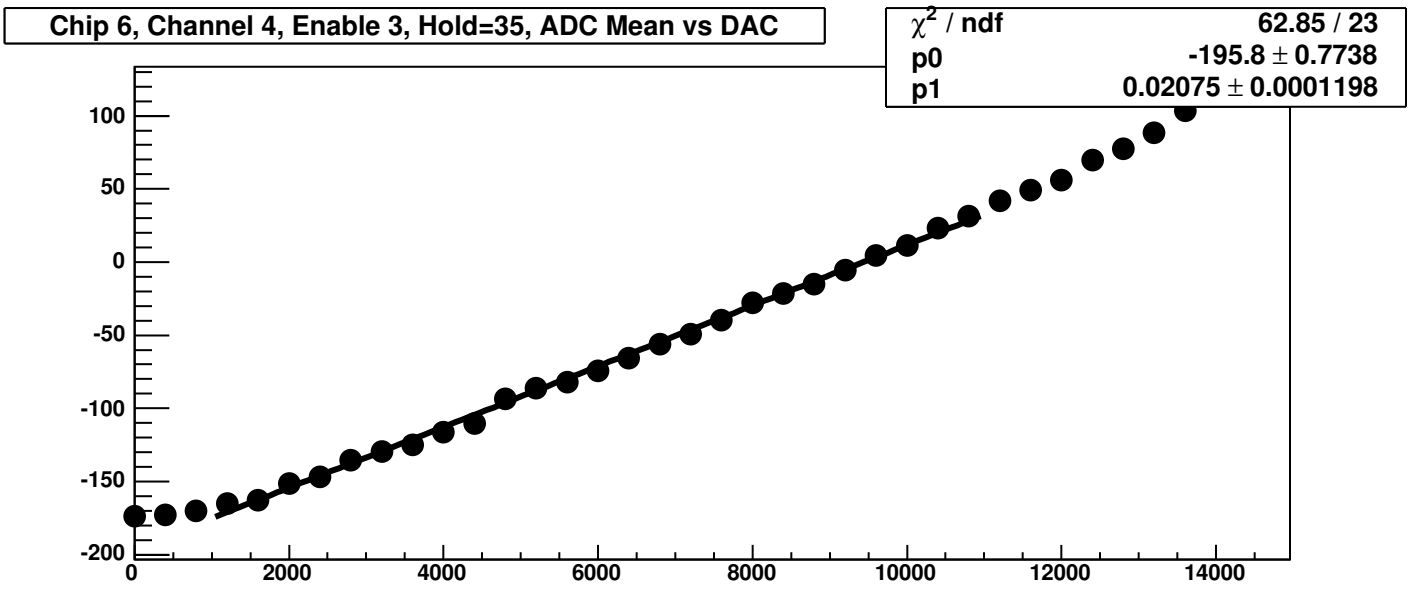


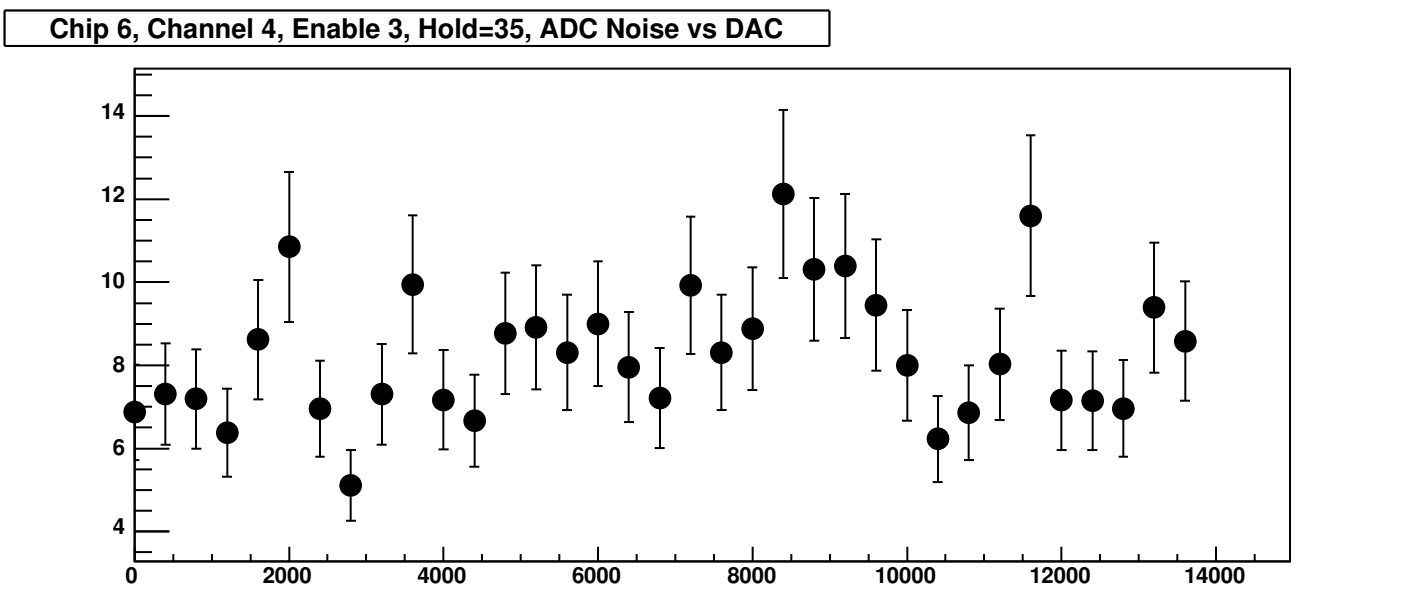
Chip 6, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

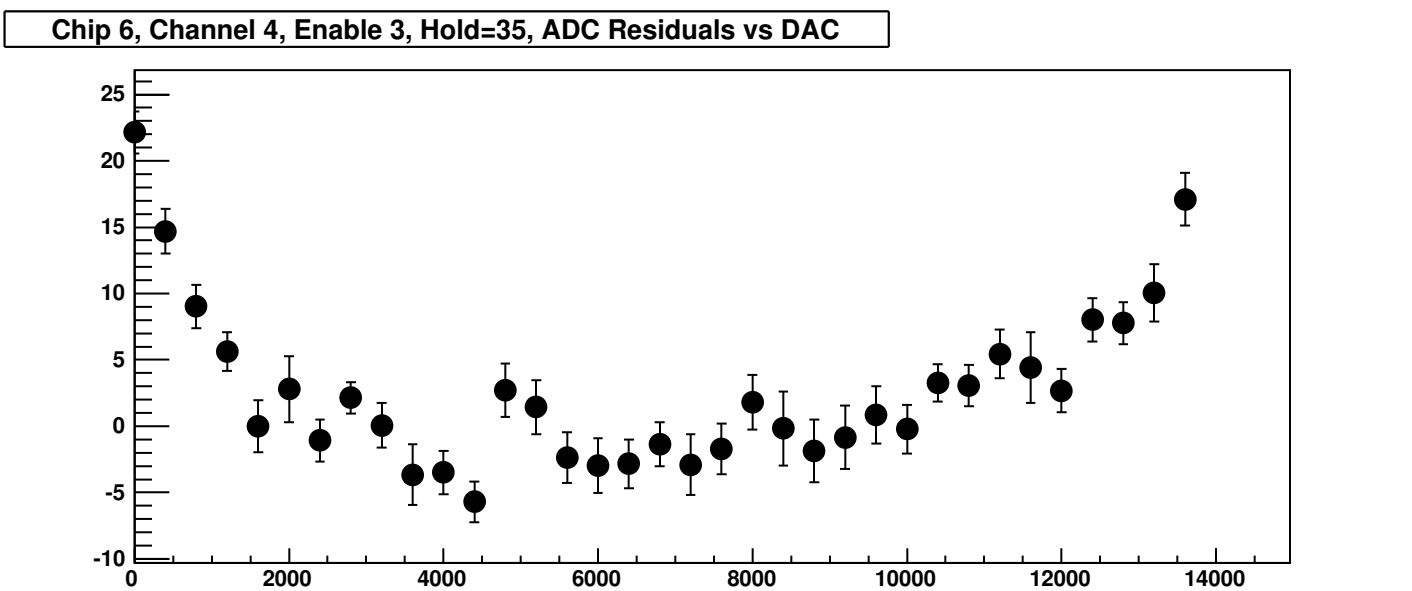


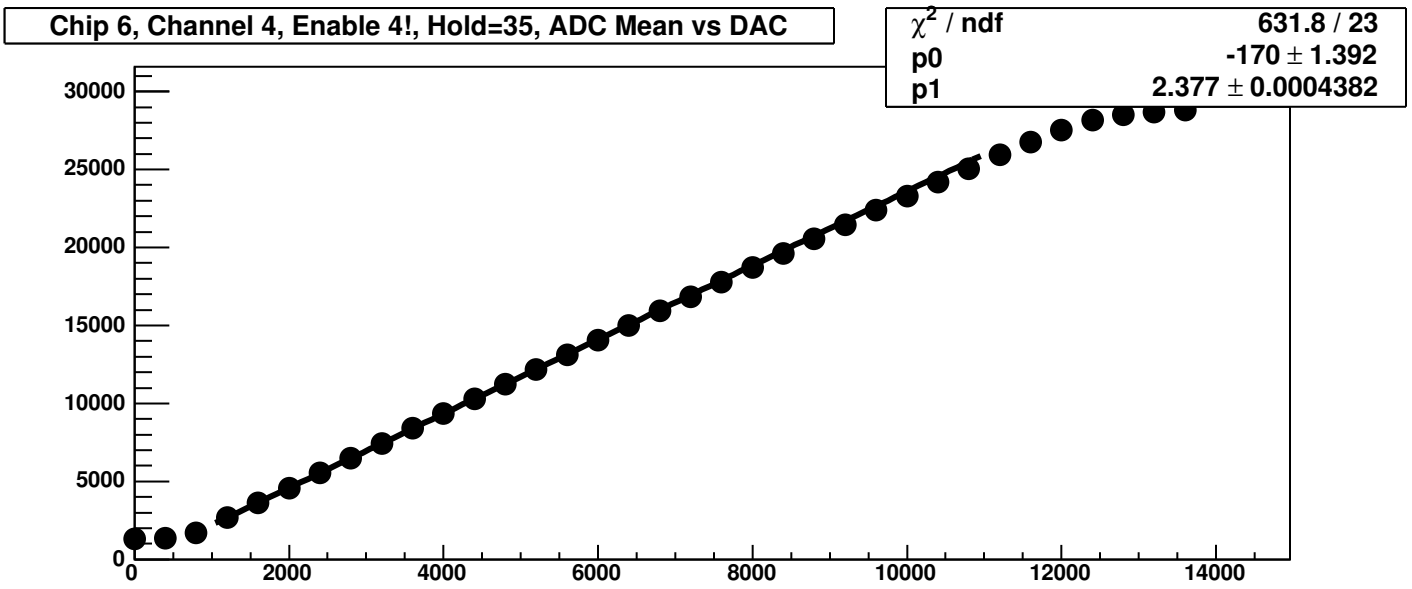
Chip 6, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

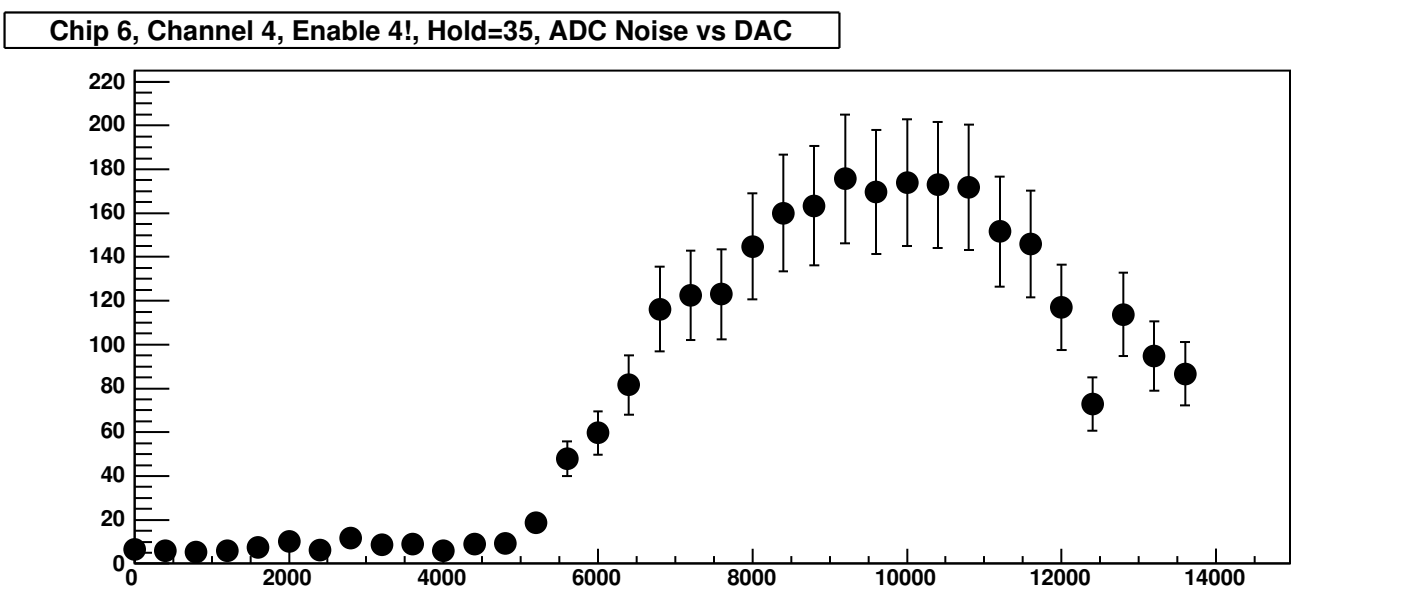


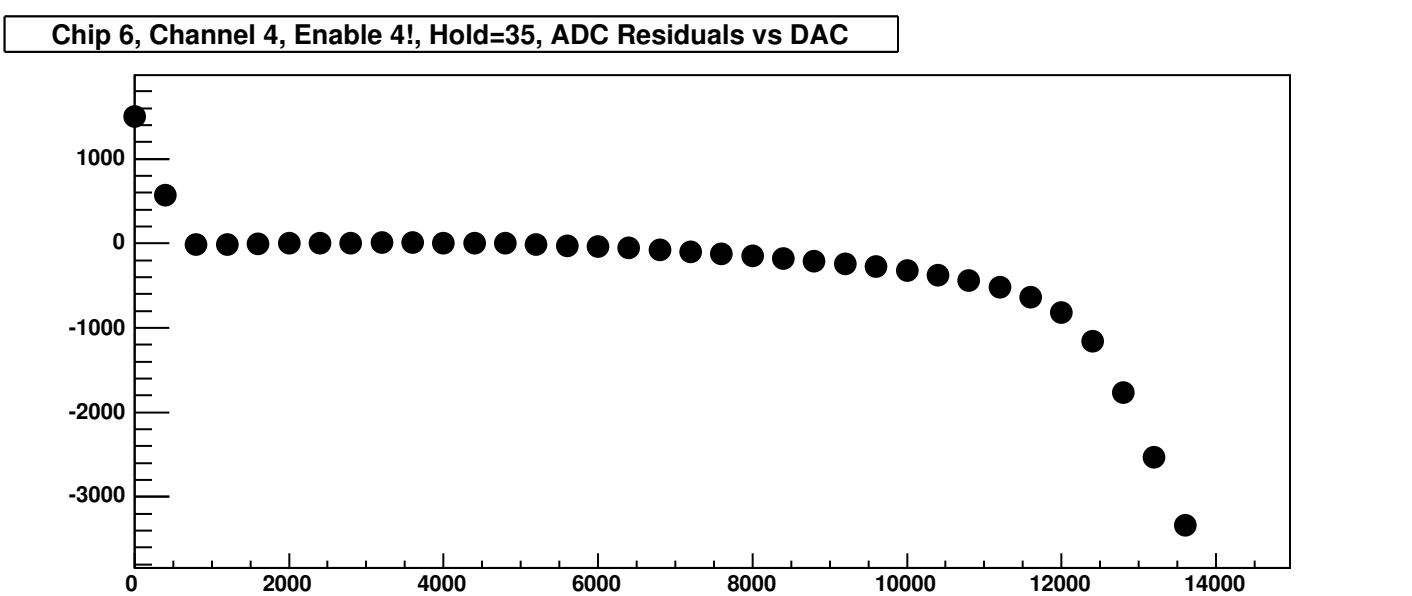

Chip 6, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 62.85 / 23
p0 -195.8 ± 0.7738
p1 0.02075 ± 0.0001198
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

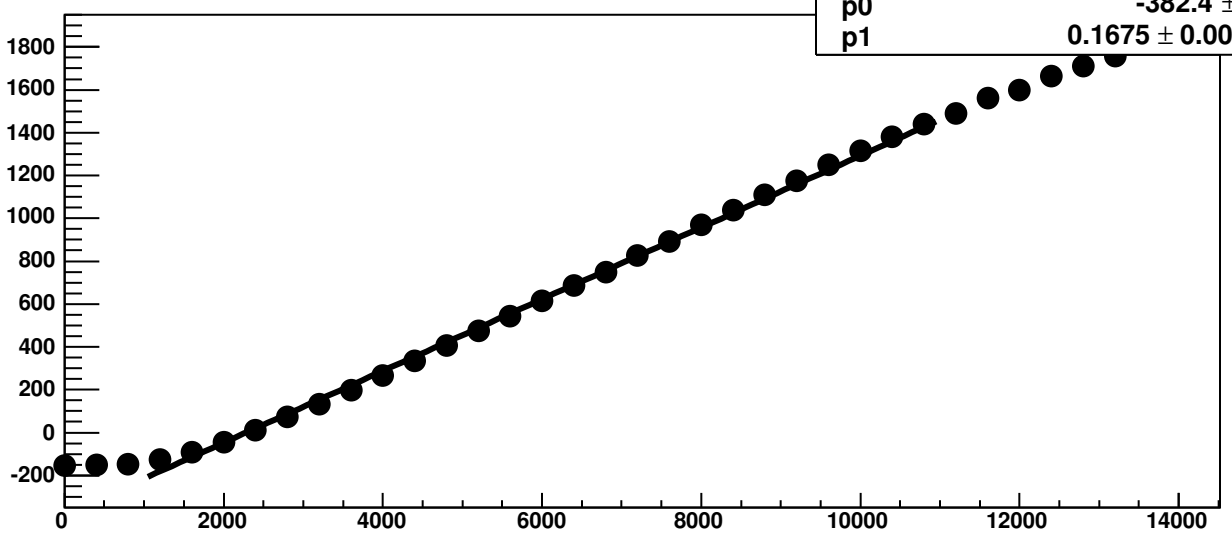

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 631.8 / 23
p0 -170 ± 1.392
p1 2.377 ± 0.0004382
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

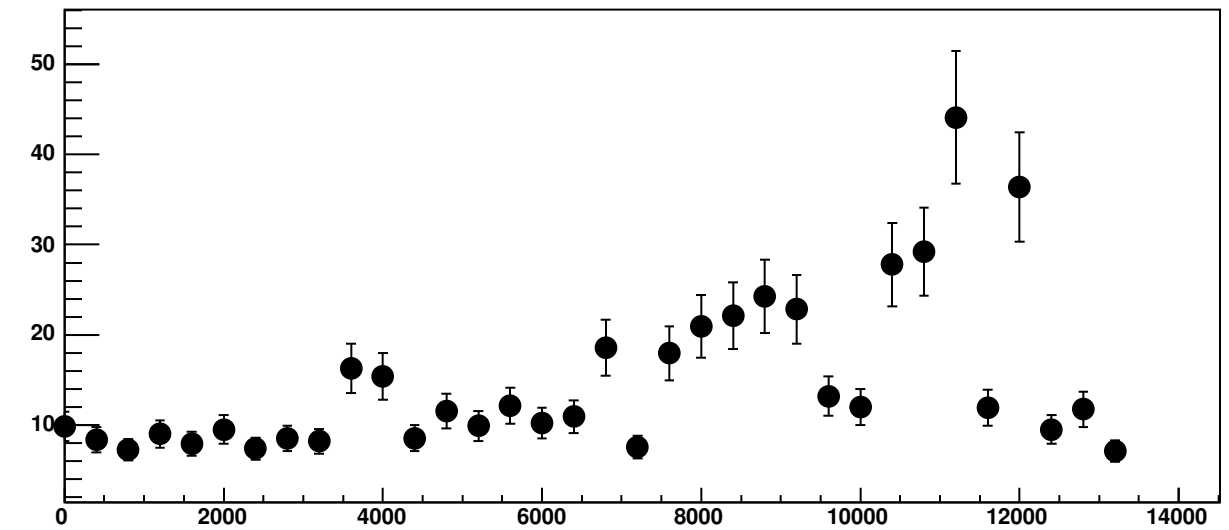

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

Chip 6, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

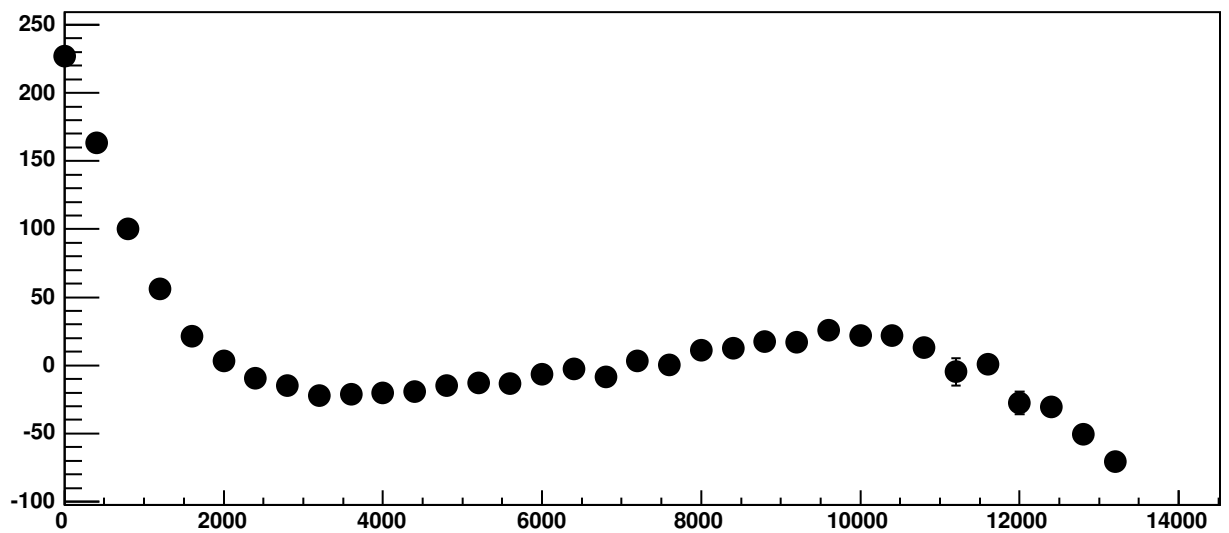
χ^2 / ndf	1559 / 23
p0	-382.4 ± 1.061
p1	0.1675 ± 0.0002006

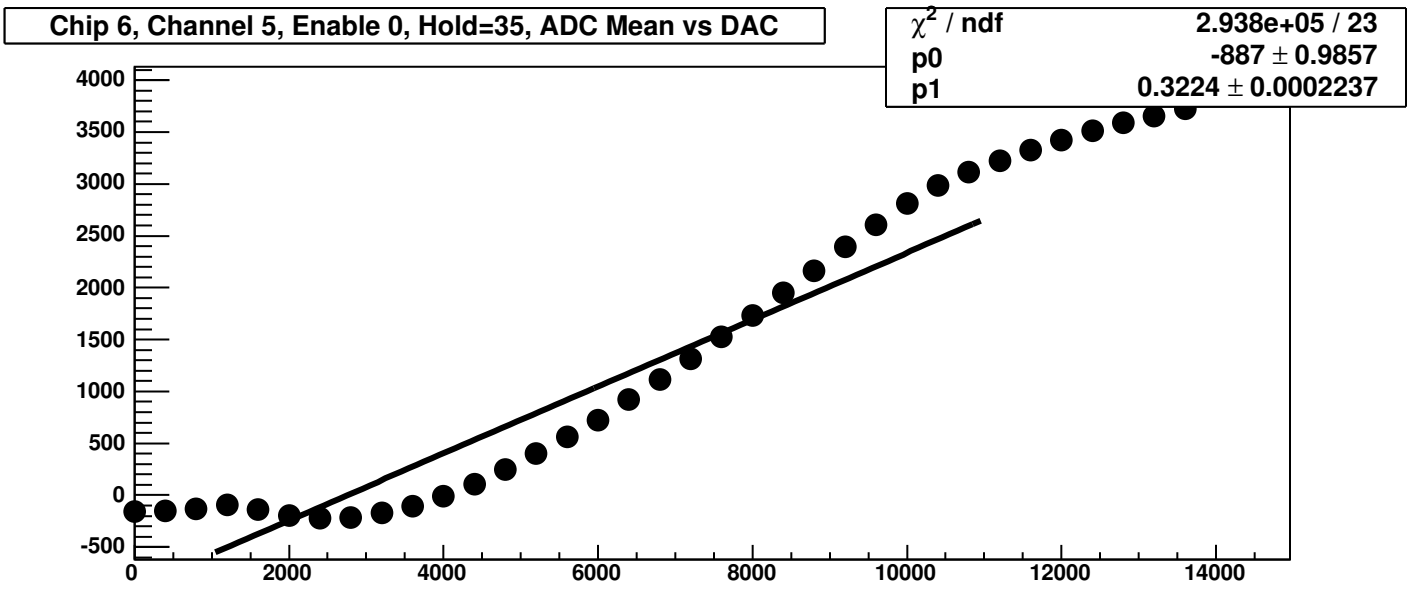


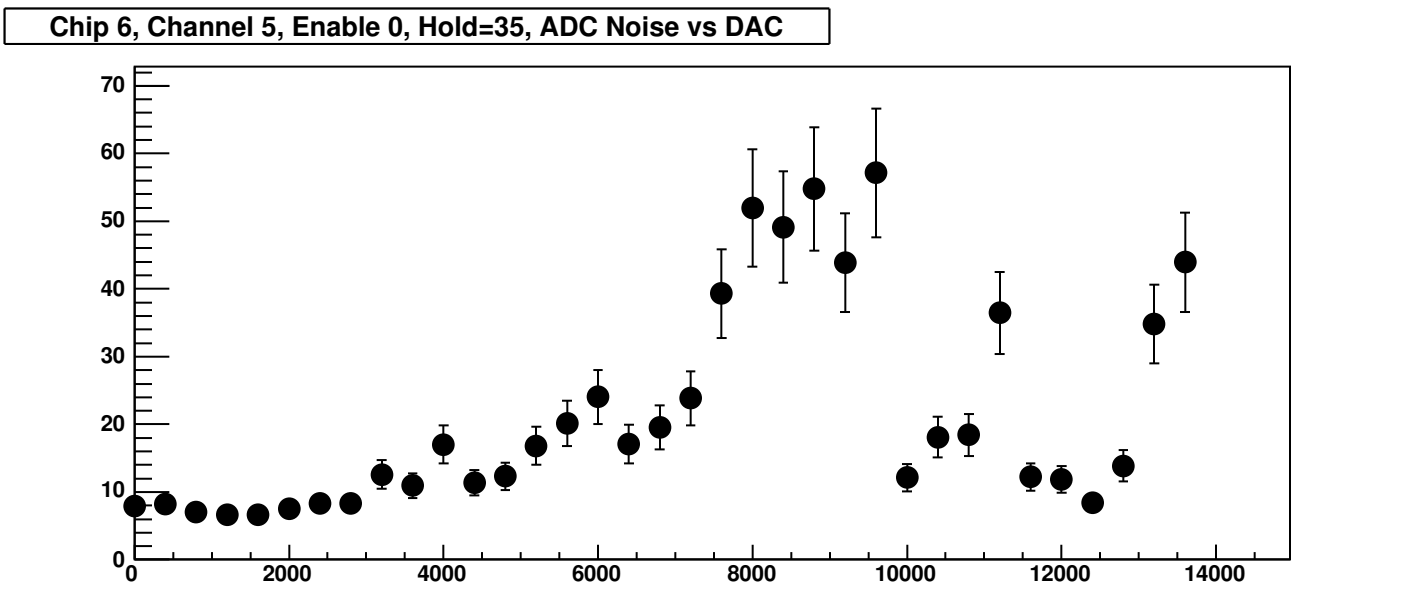
Chip 6, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

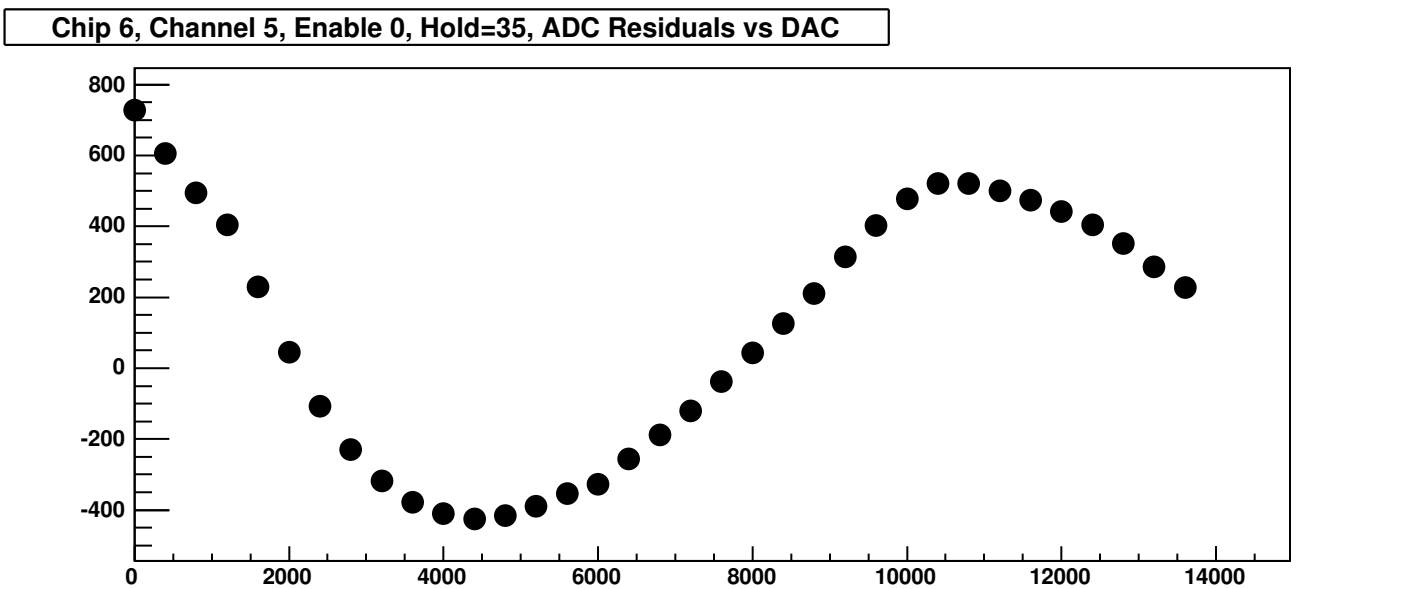


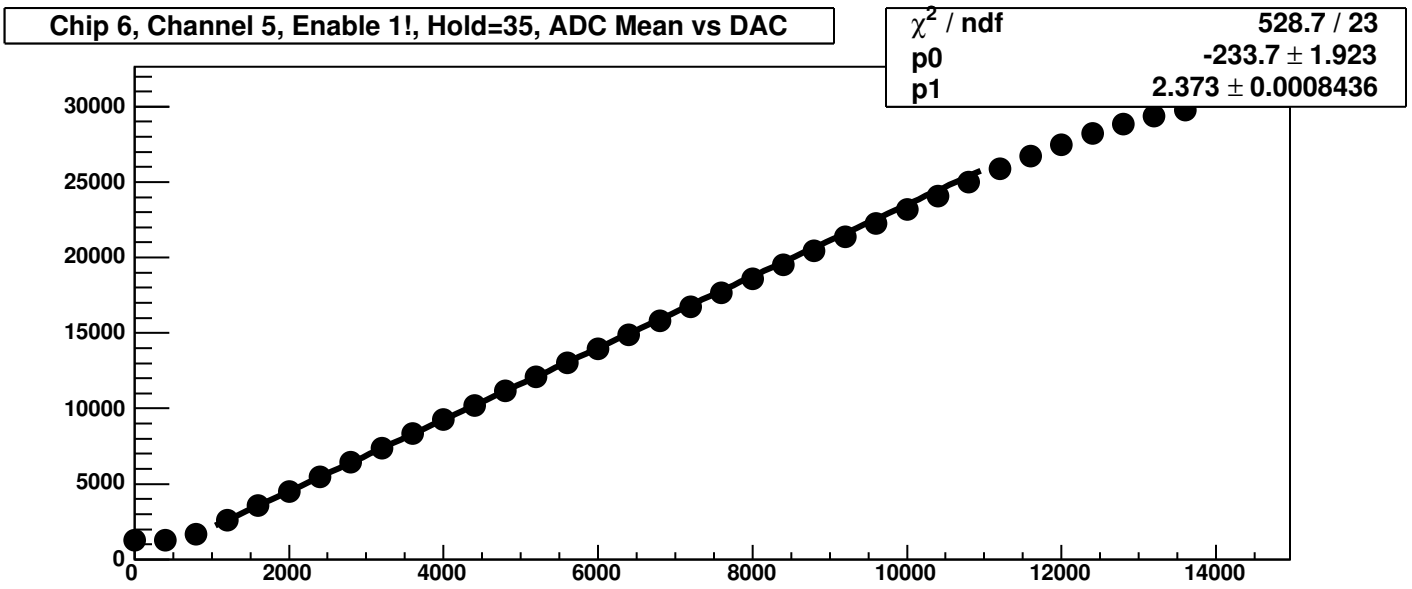
Chip 6, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

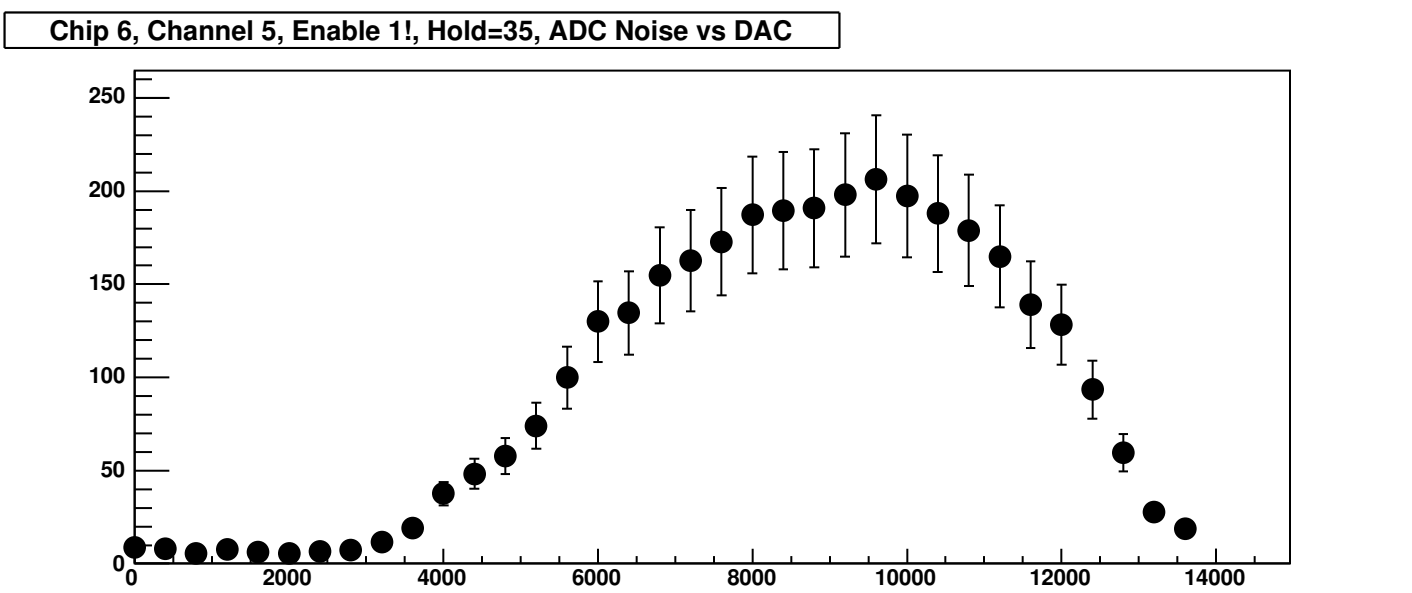


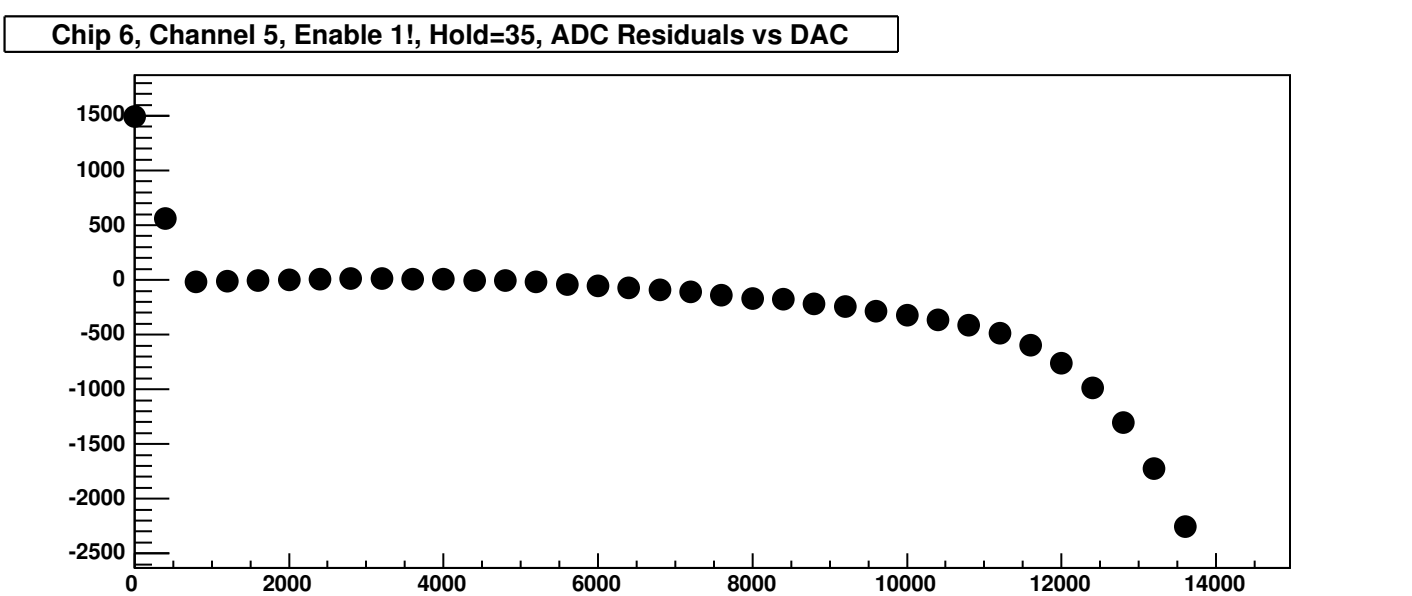

Chip 6, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.938e+05 / 23
p0 -887 ± 0.9857
p1 0.3224 ± 0.0002237
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

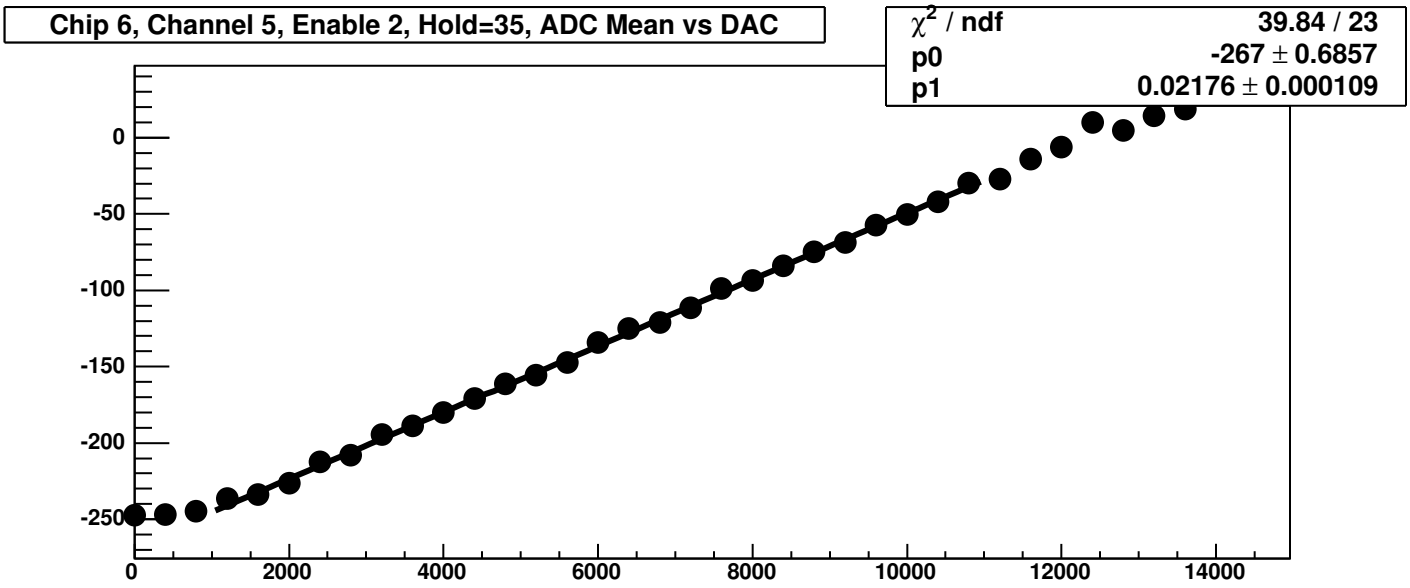

Chip 6, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

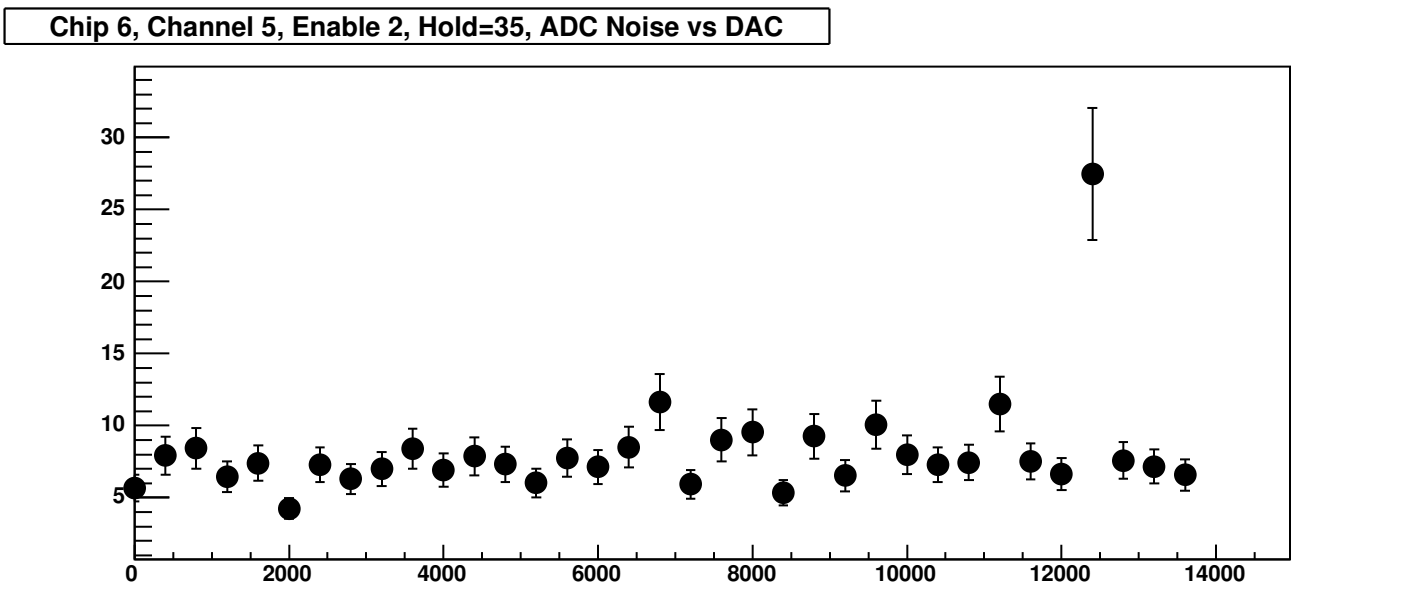

Chip 6, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

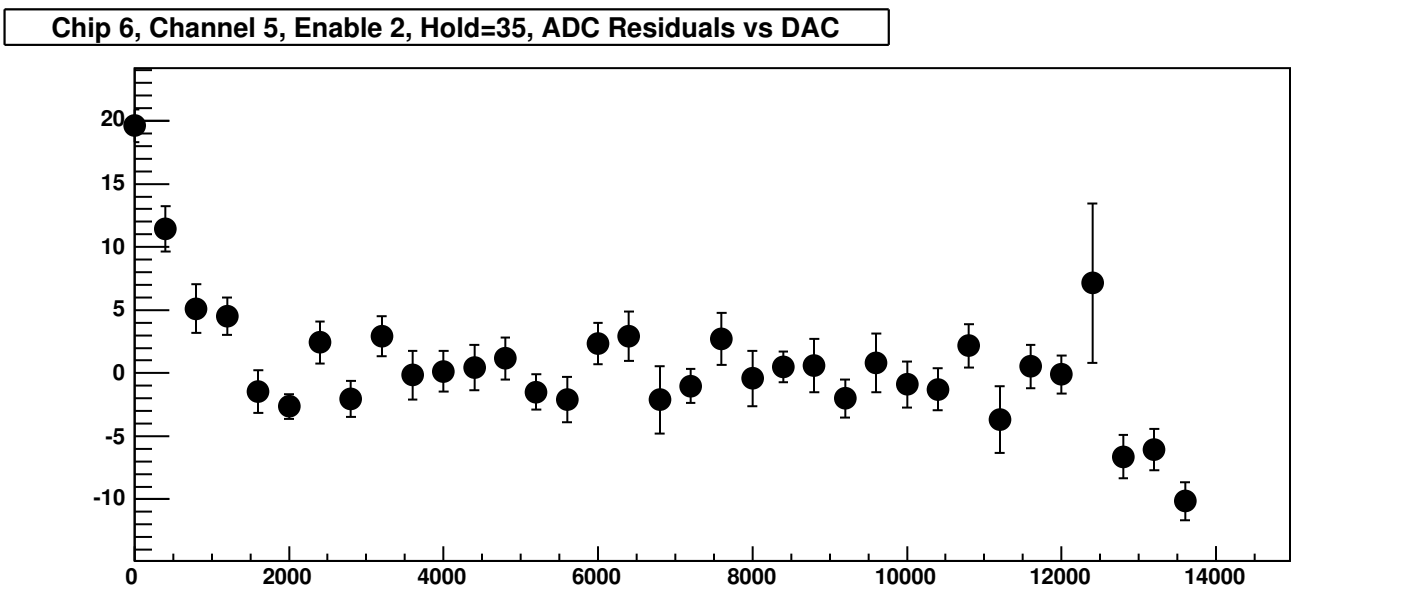

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 528.7 / 23
p0 -233.7 ± 1.923
p1 2.373 ± 0.0008436
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

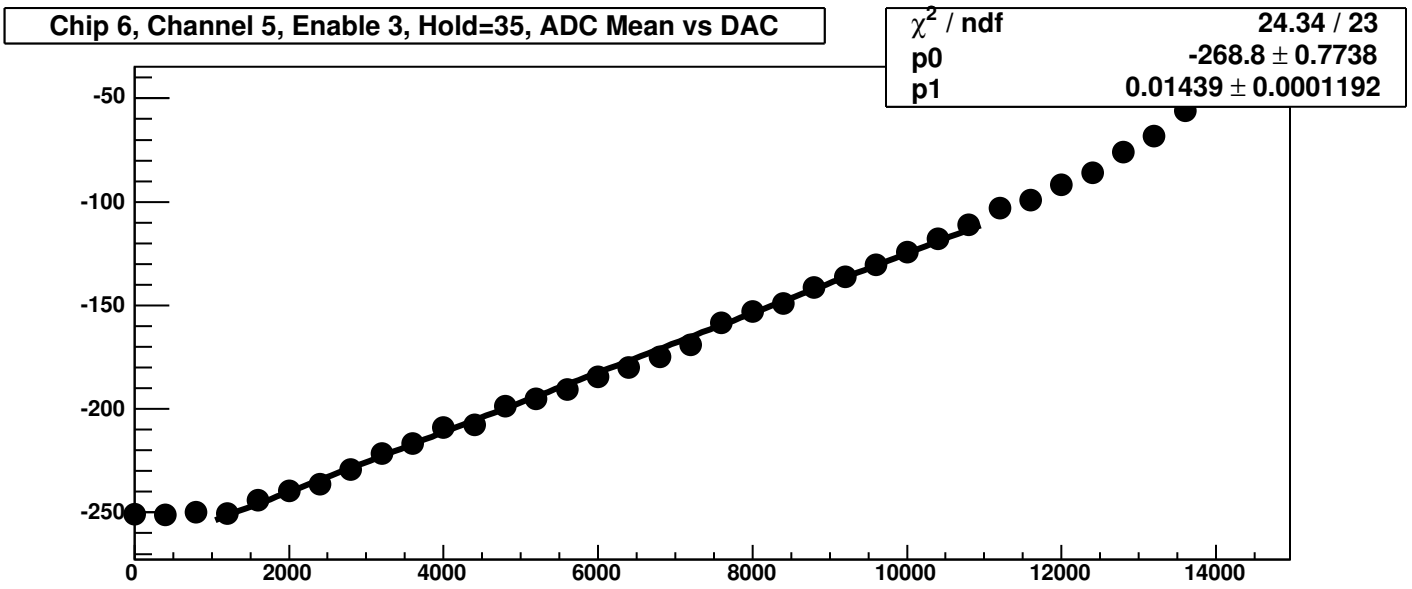

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

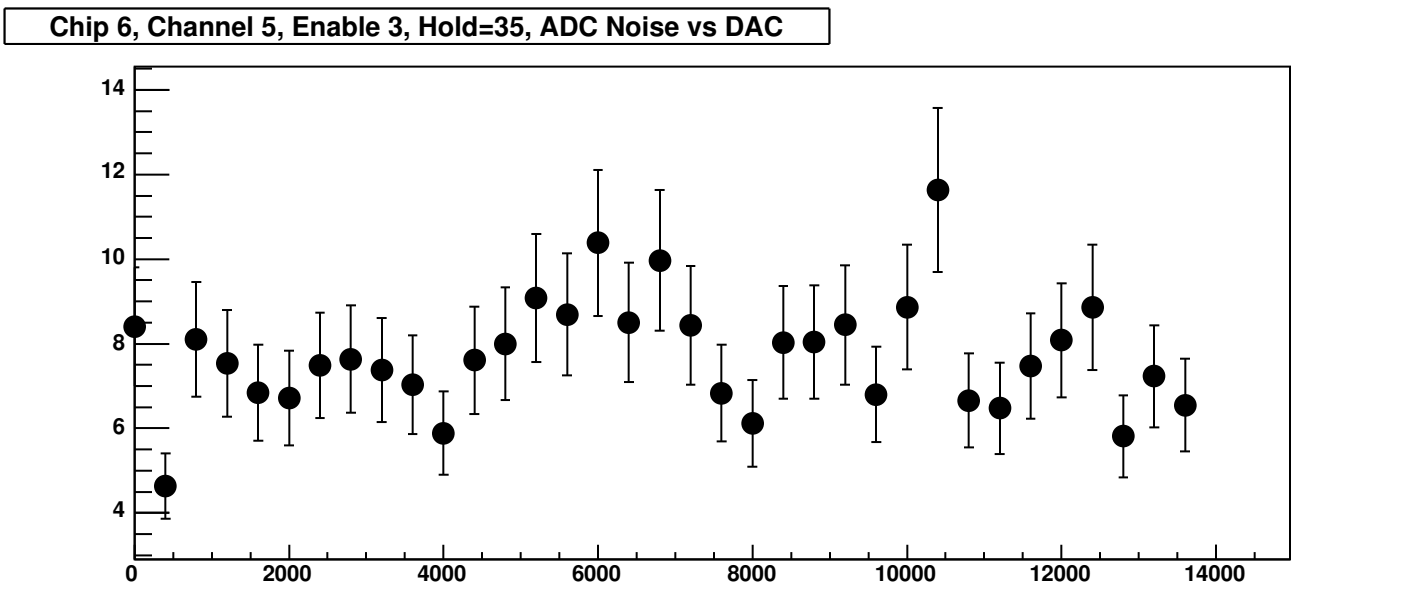

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

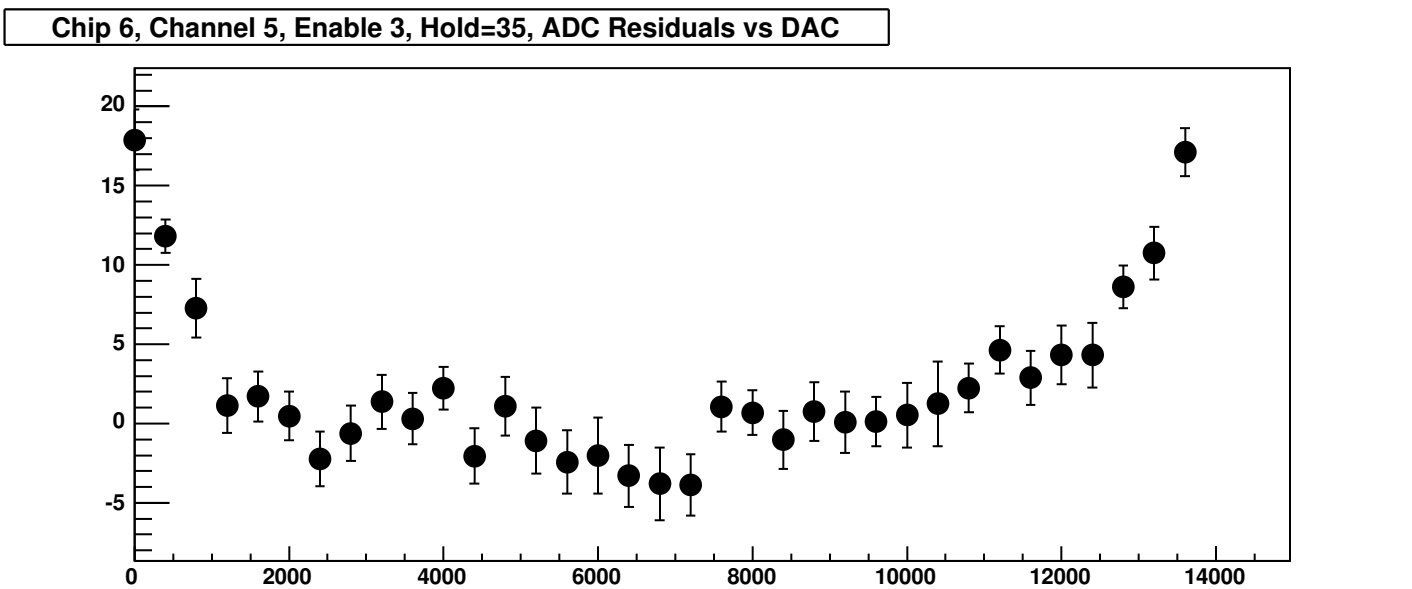

Chip 6, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.84 / 23
p0 -267 ± 0.6857
p1 0.02176 ± 0.000109
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

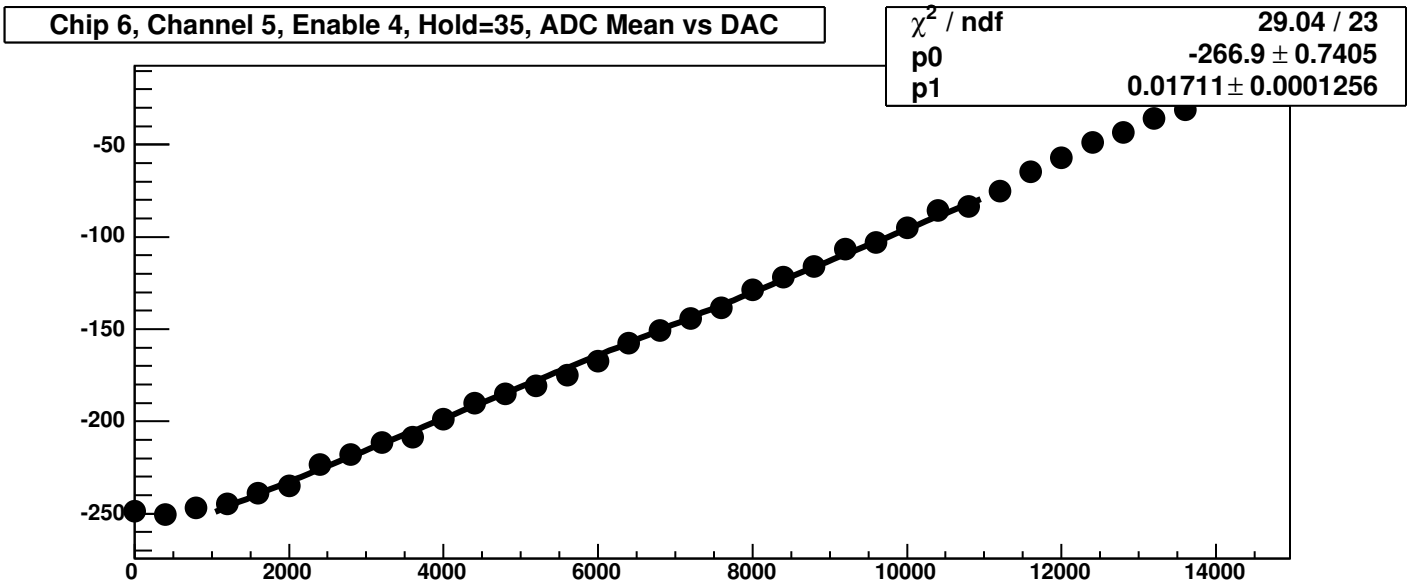

Chip 6, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

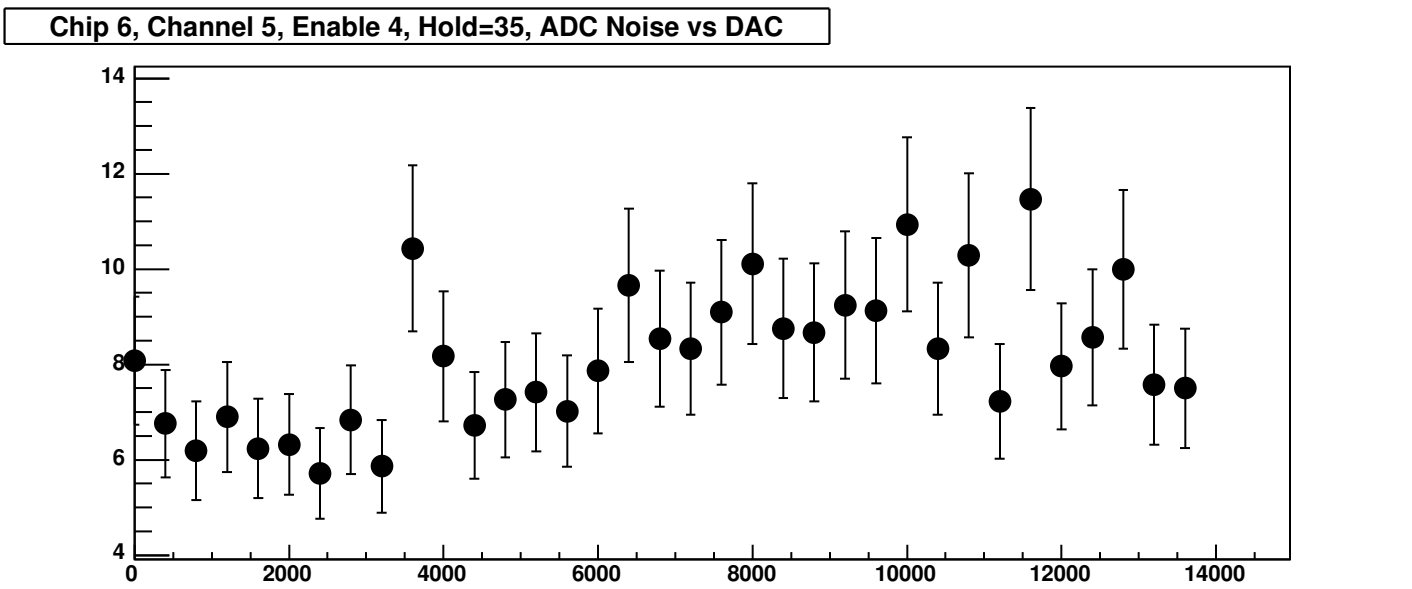

Chip 6, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

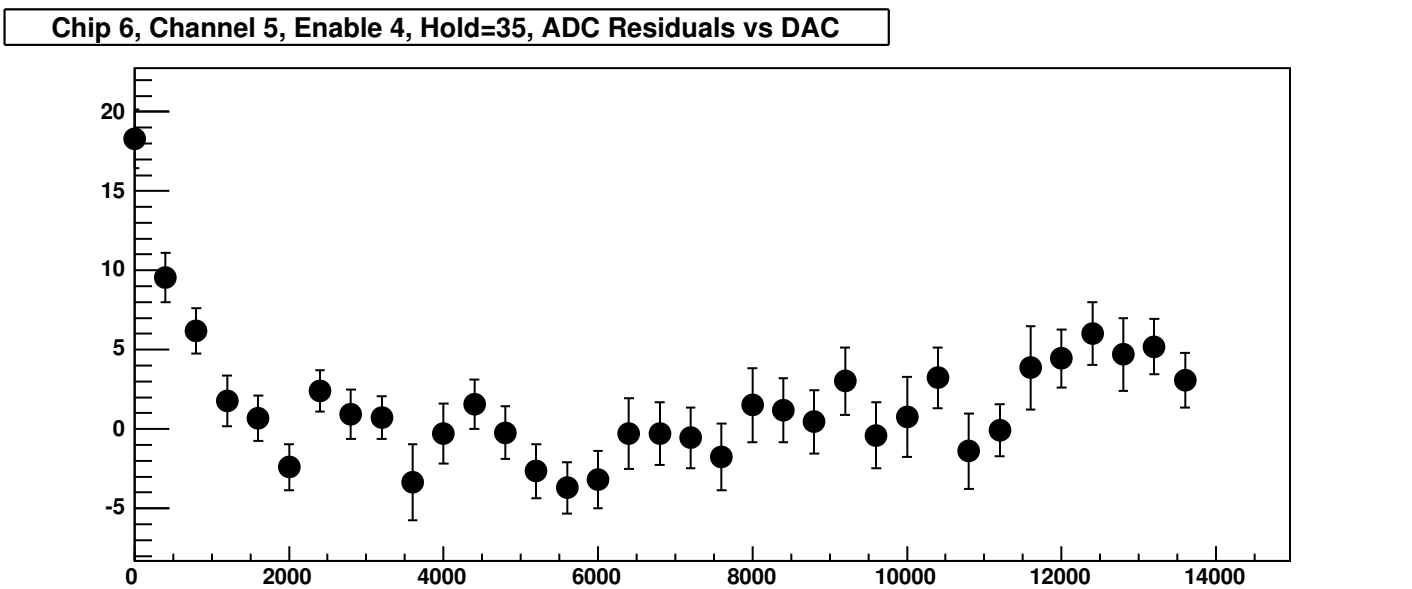

Chip 6, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.34 / 23
p0 -268.8 ± 0.7738
p1 0.01439 ± 0.0001192
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

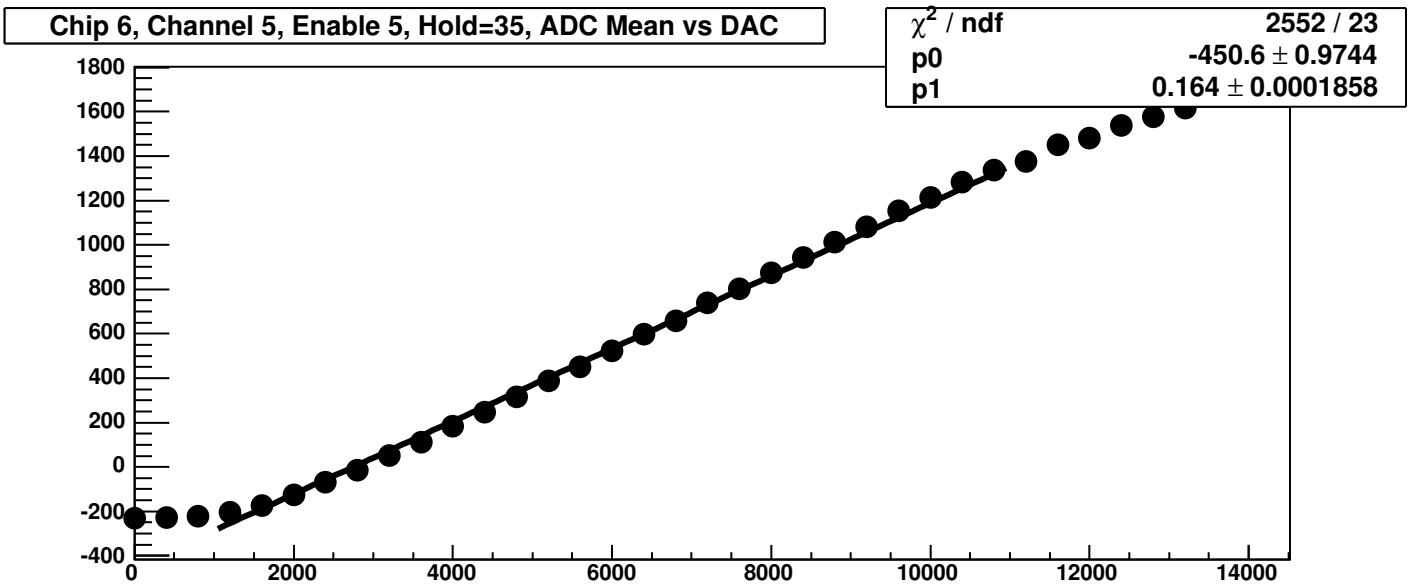

Chip 6, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

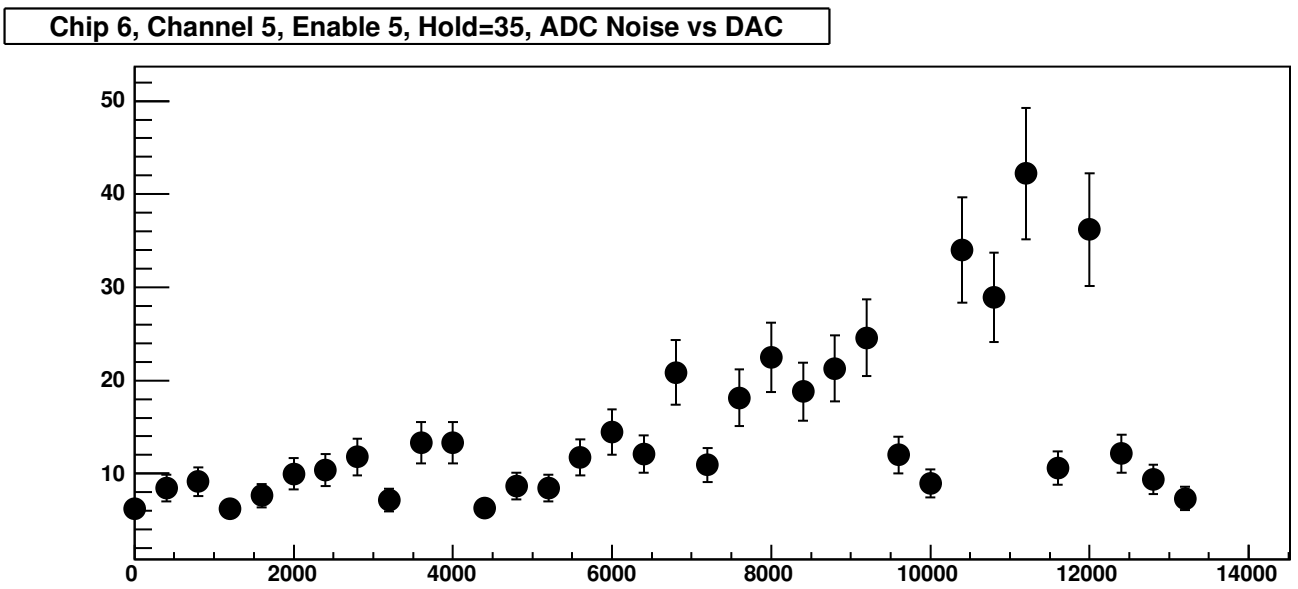

Chip 6, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

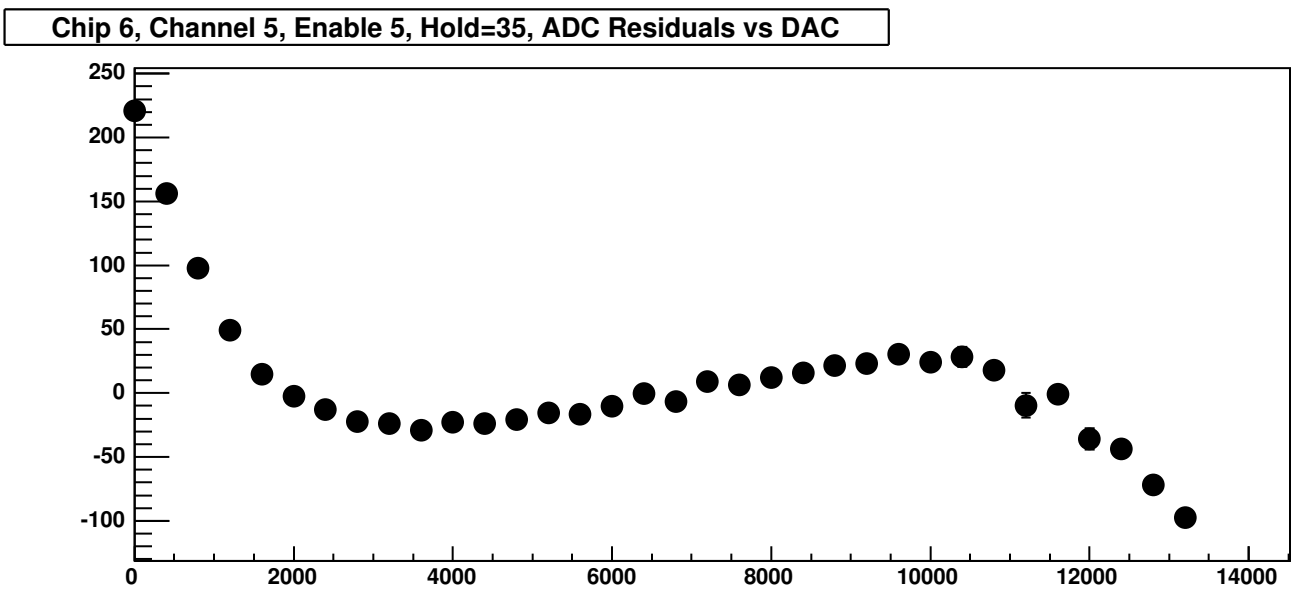

Chip 6, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.04 / 23
p0 -266.9 ± 0.7405
p1 0.01711 ± 0.0001256
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

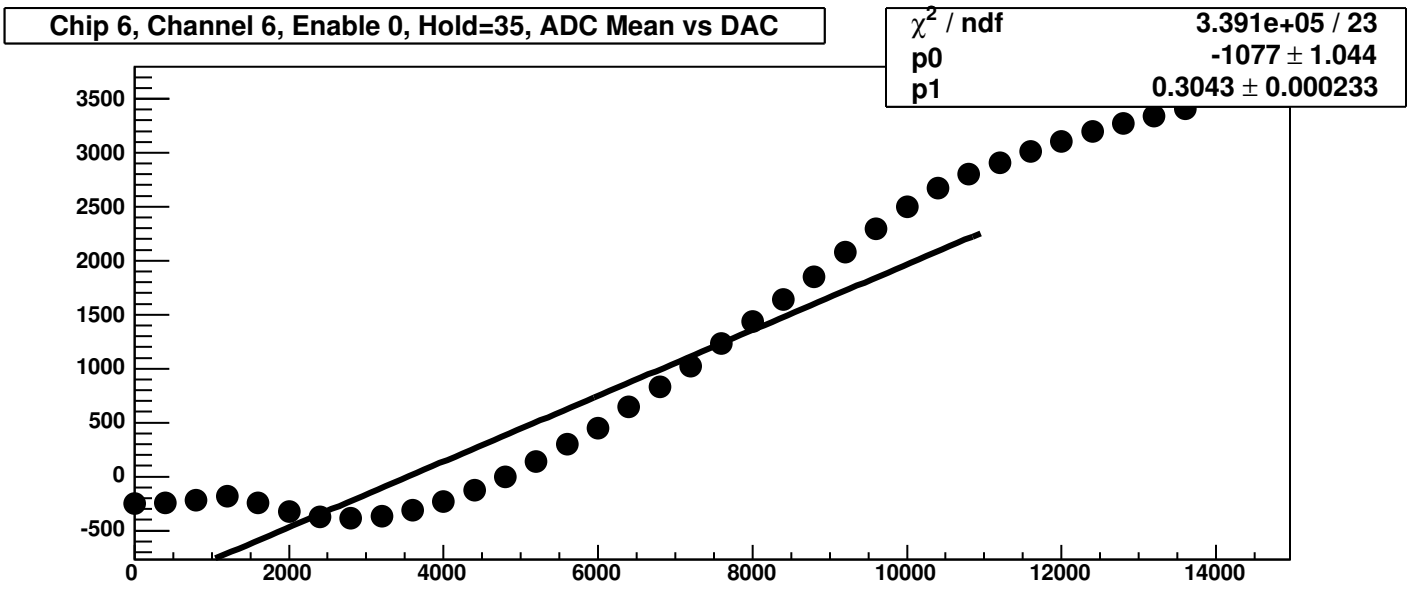

Chip 6, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

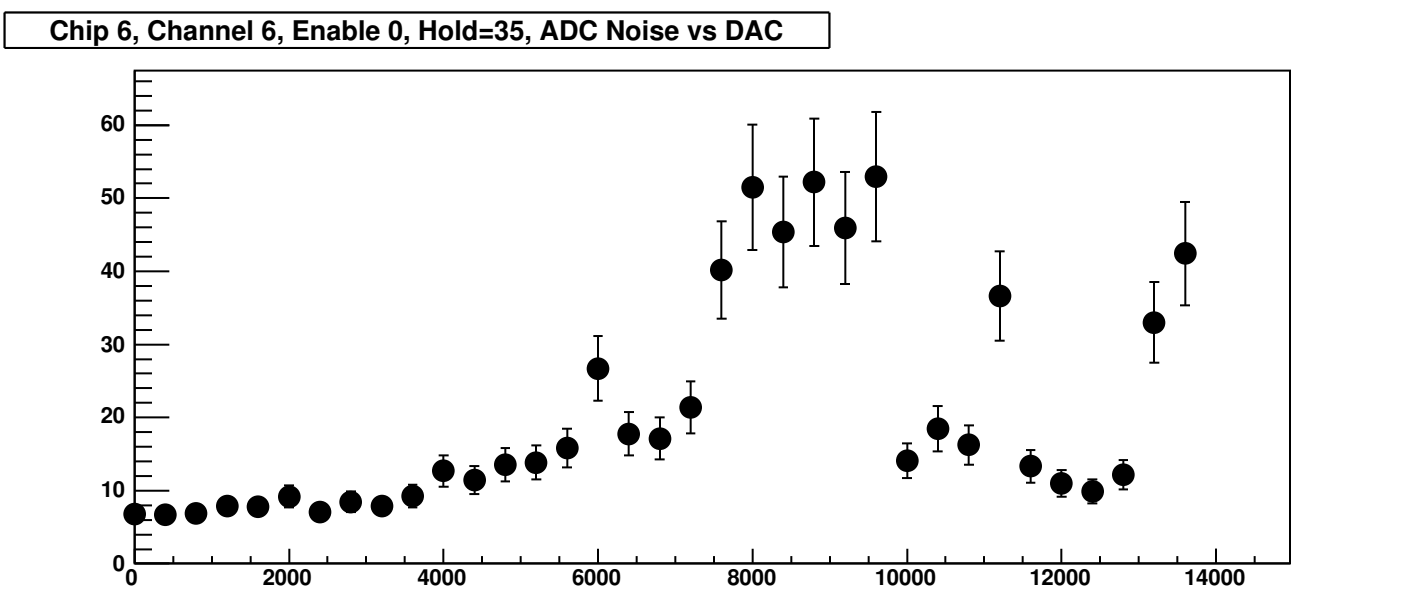

Chip 6, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

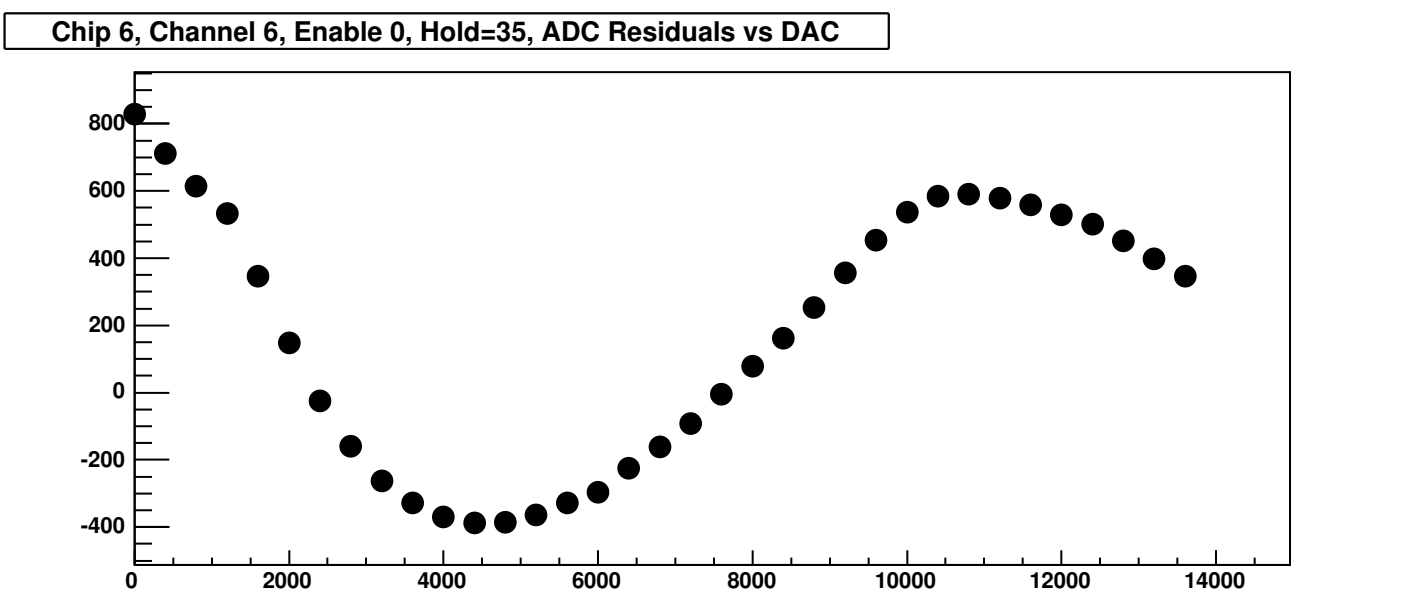

Chip 6, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2552 / 23
p0 -450.6 ± 0.9744
p1 0.164 ± 0.0001858
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

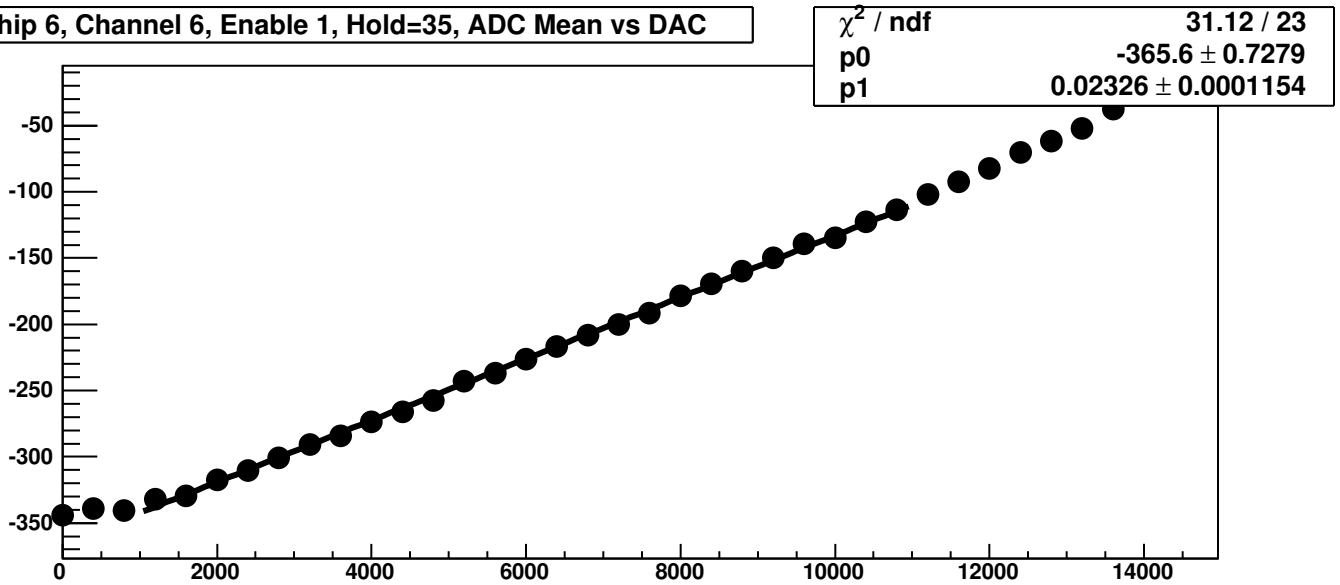

Chip 6, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.391e+05 / 23
p0 -1077 ± 1.044
p1 0.3043 ± 0.000233
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

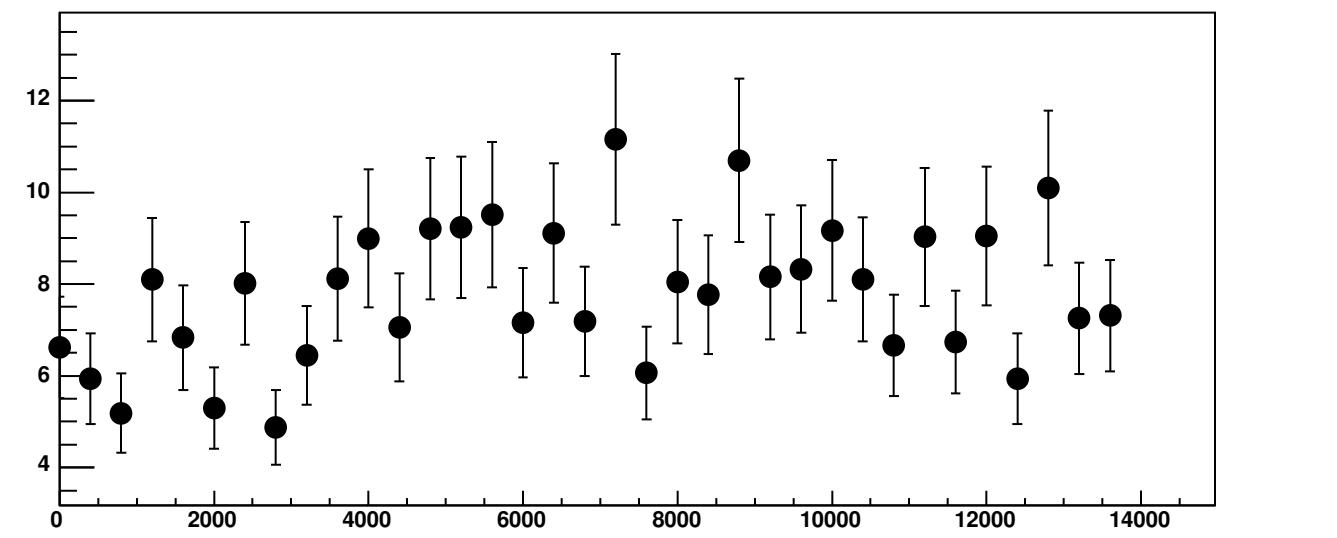

Chip 6, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

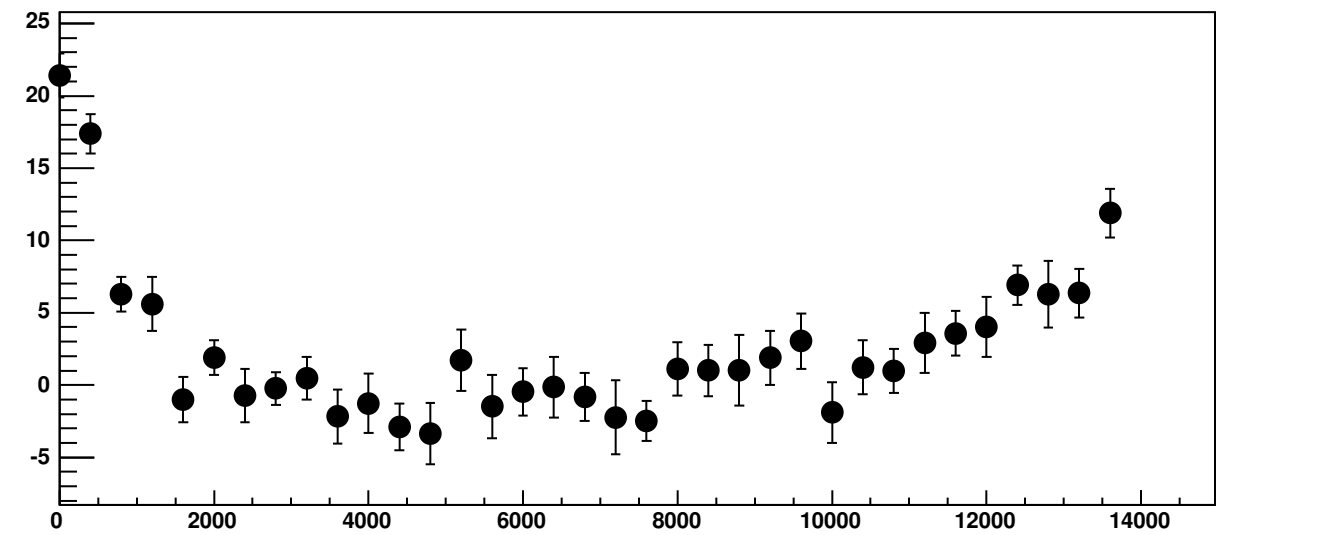
Chip 6, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

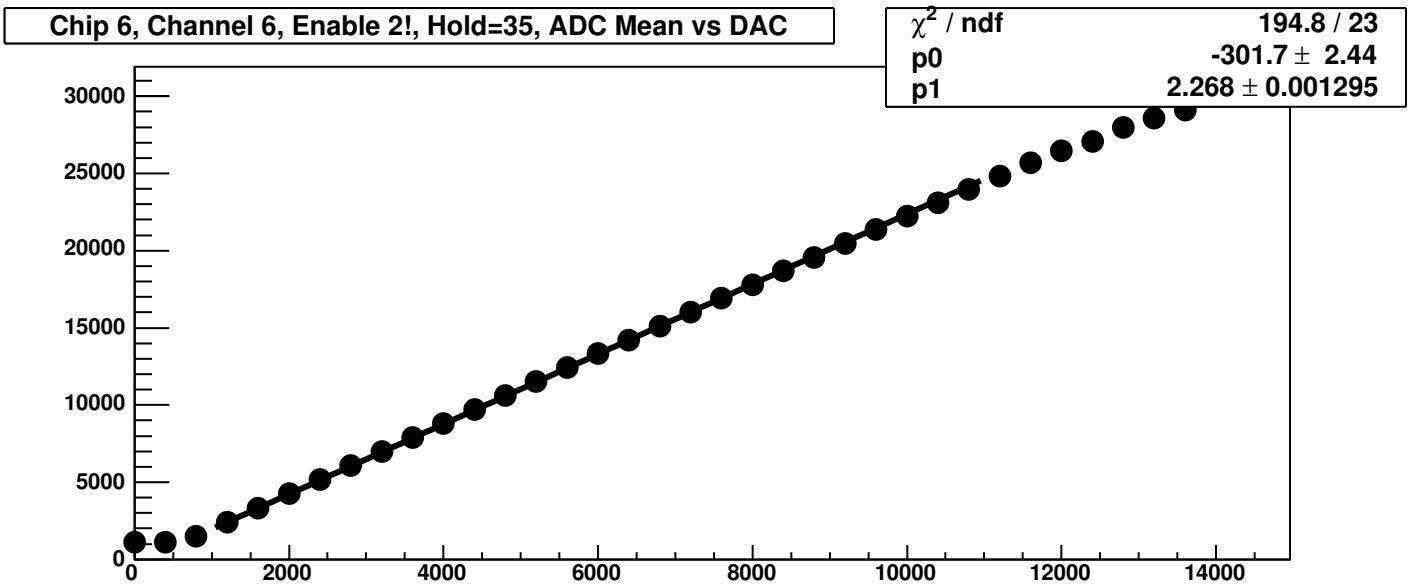


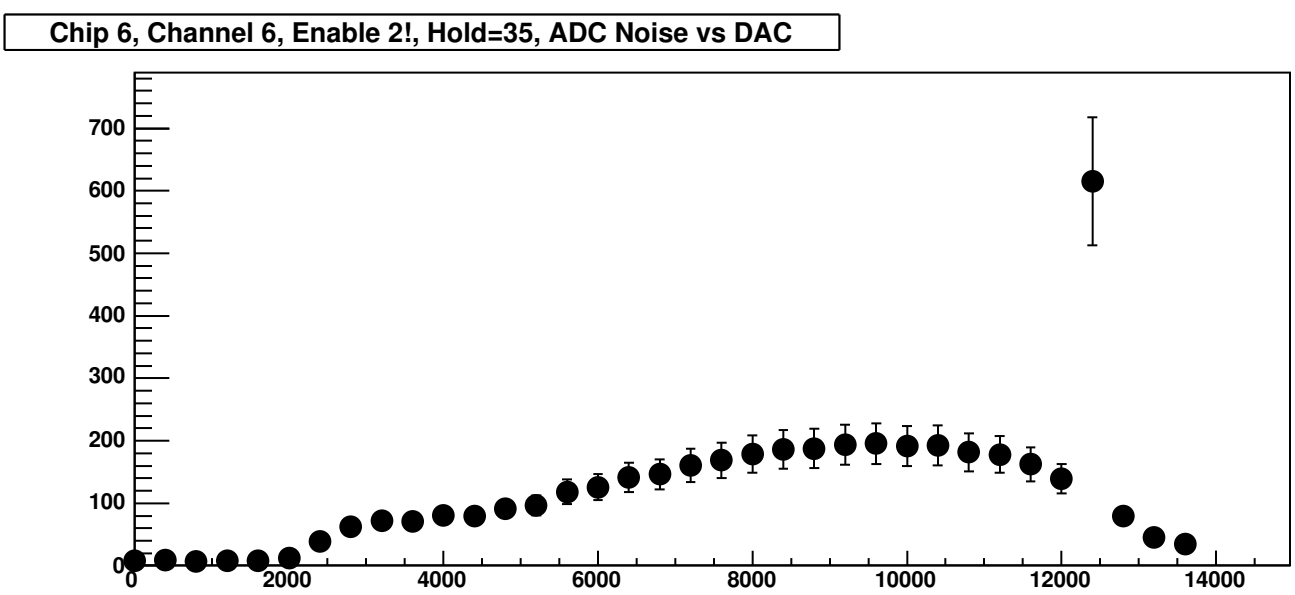
Chip 6, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

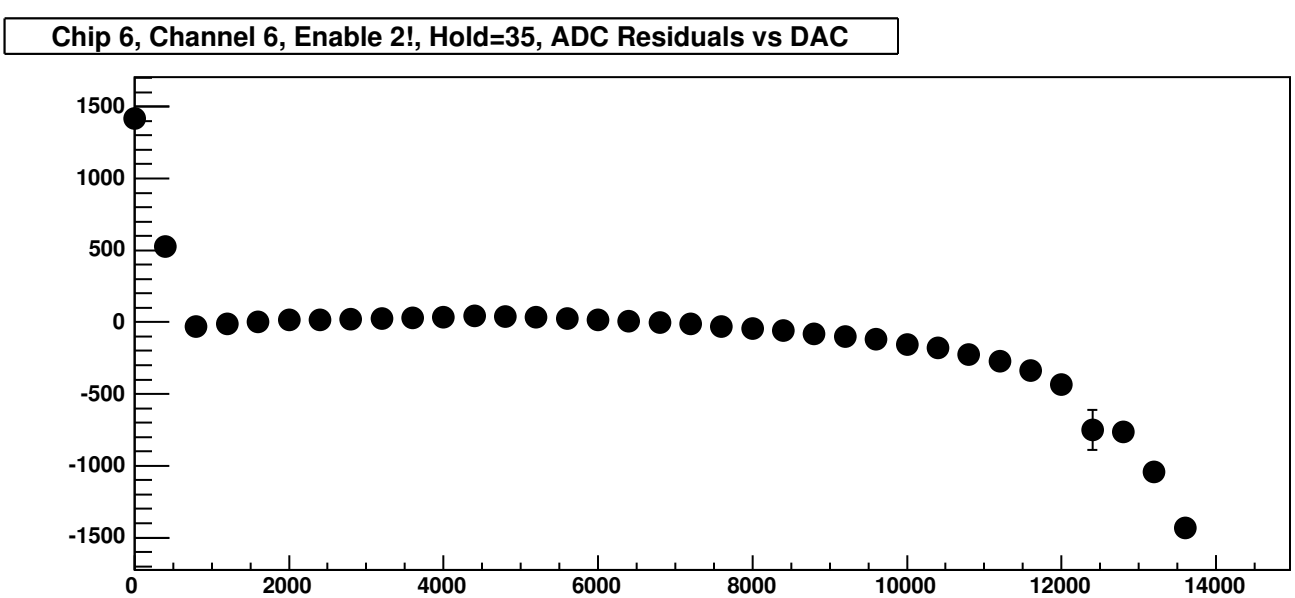


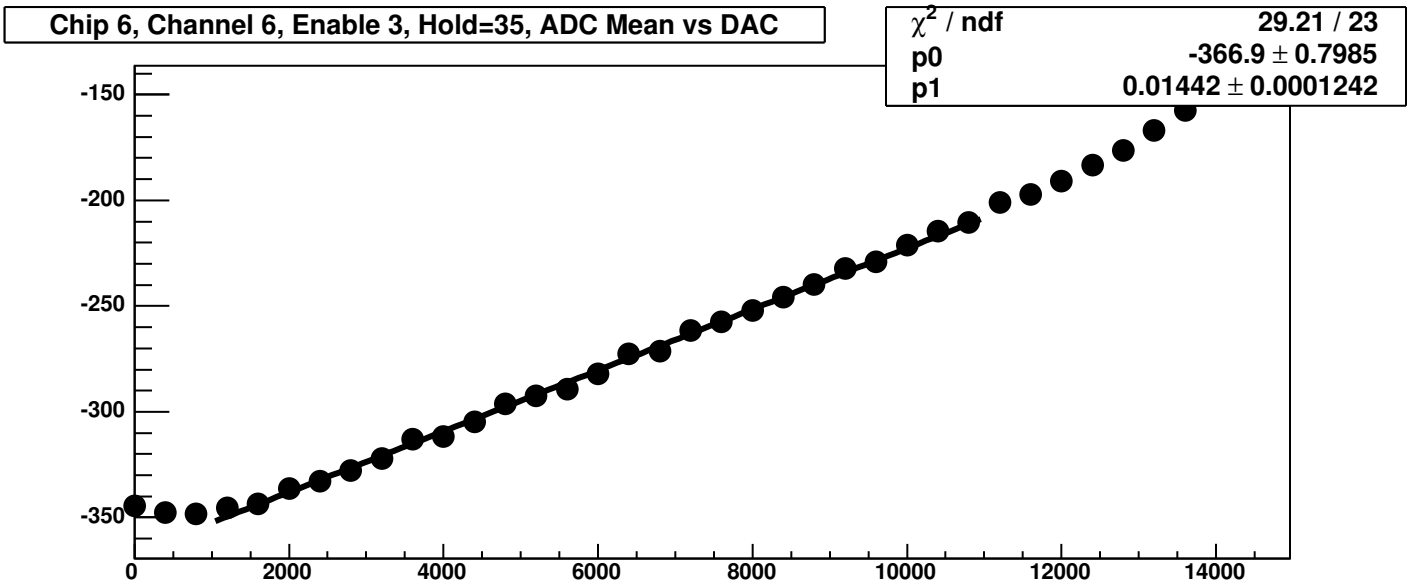
Chip 6, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

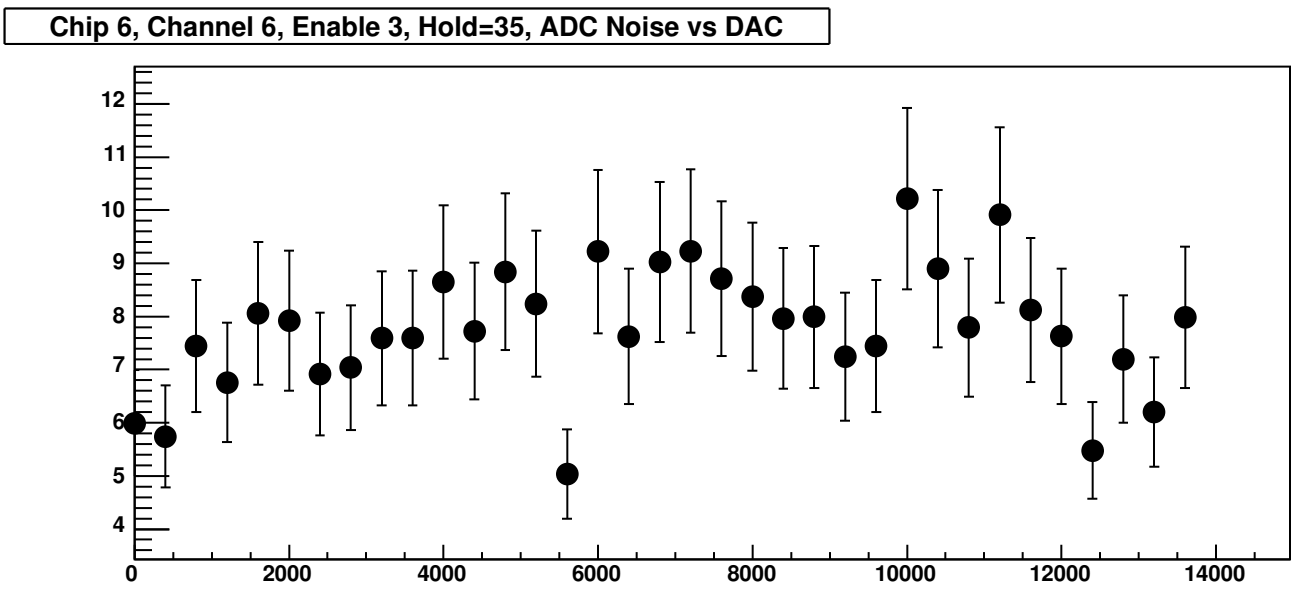


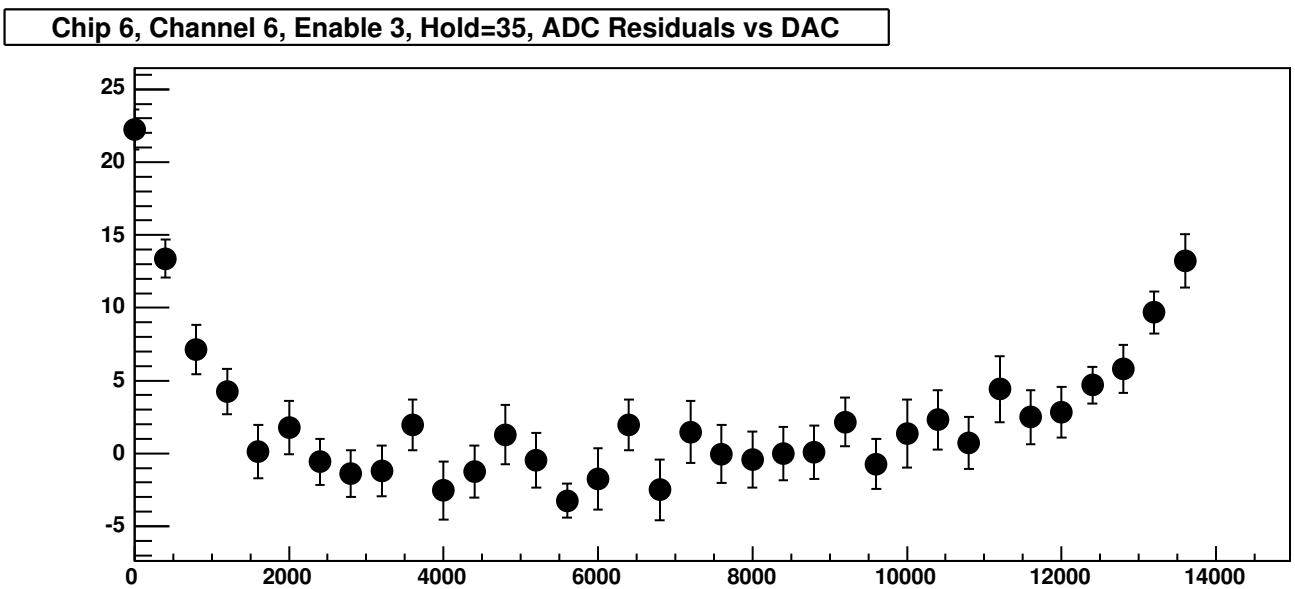

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 194.8 / 23
p0 -301.7 ± 2.44
p1 2.268 ± 0.001295
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

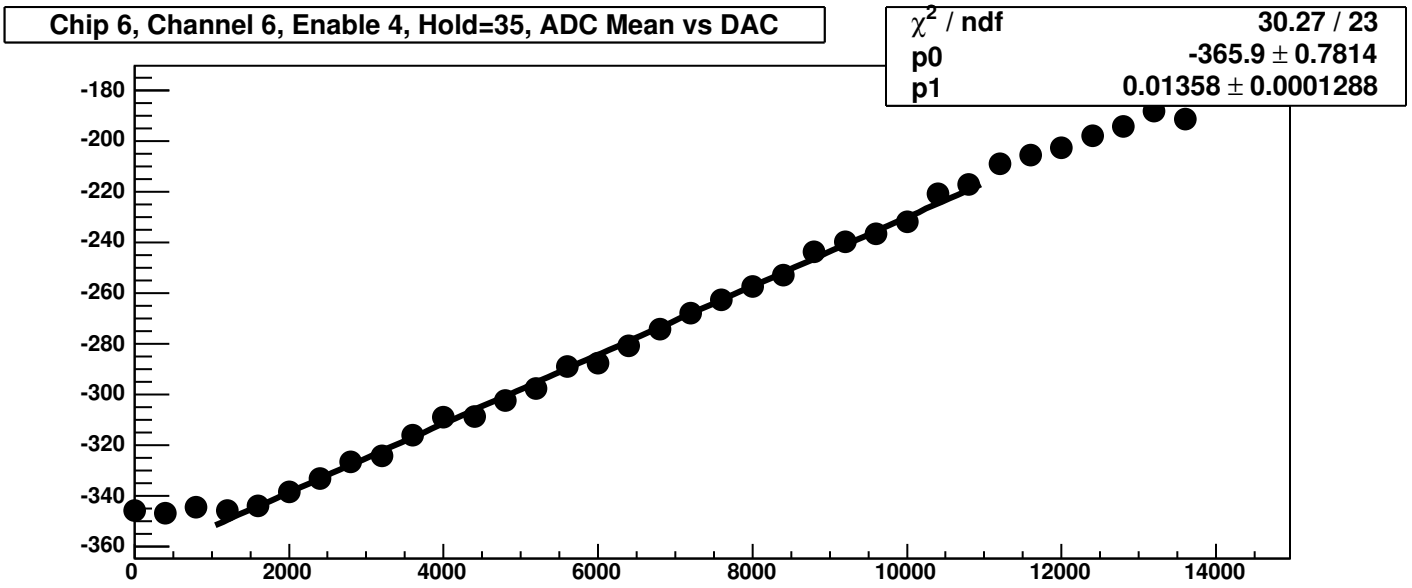

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

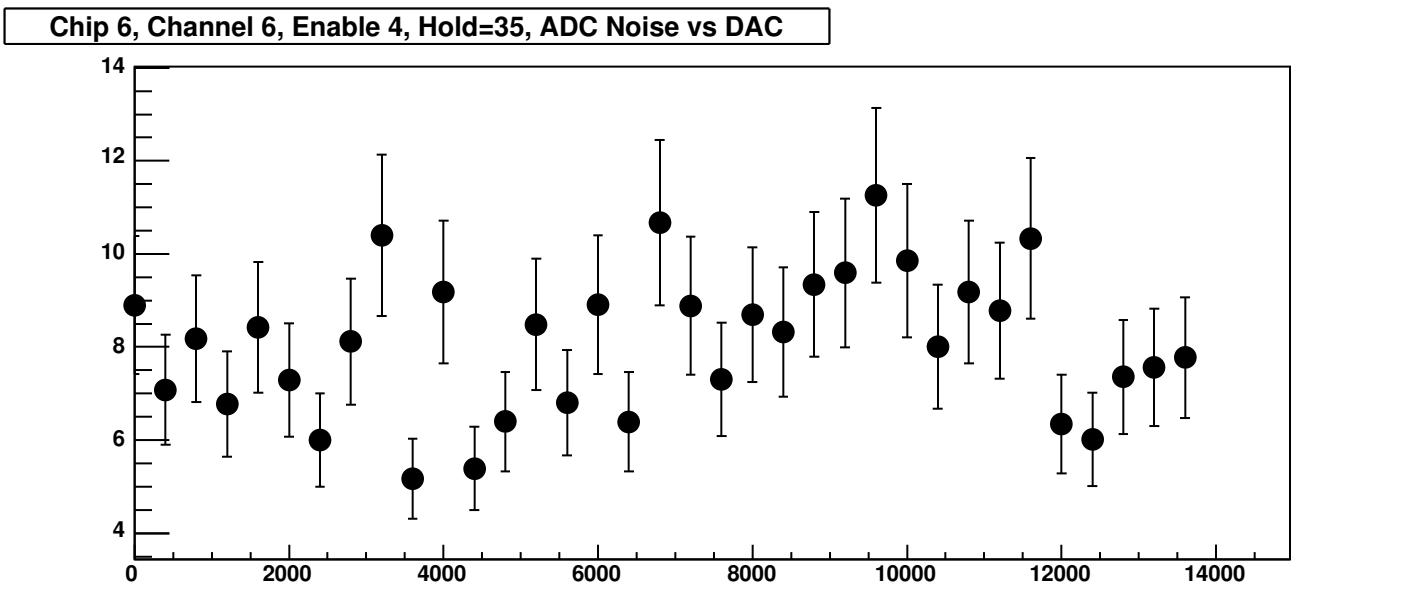

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

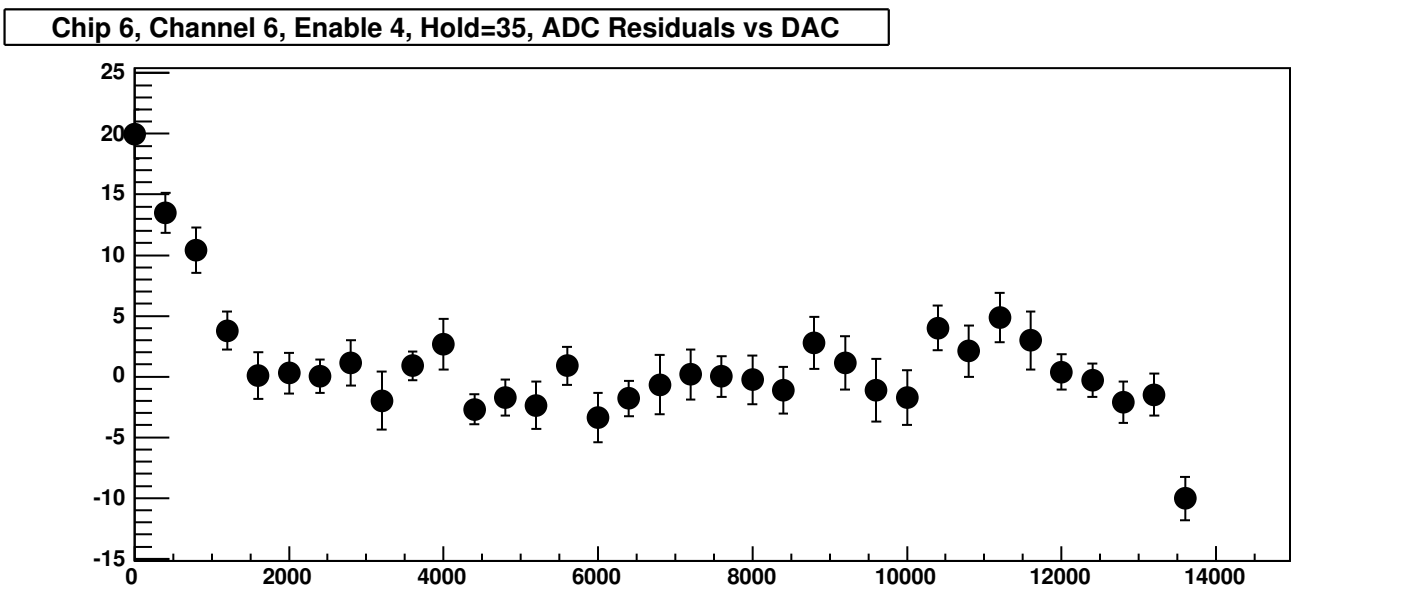

Chip 6, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.21 / 23
p0 -366.9 ± 0.7985
p1 0.01442 ± 0.0001242
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

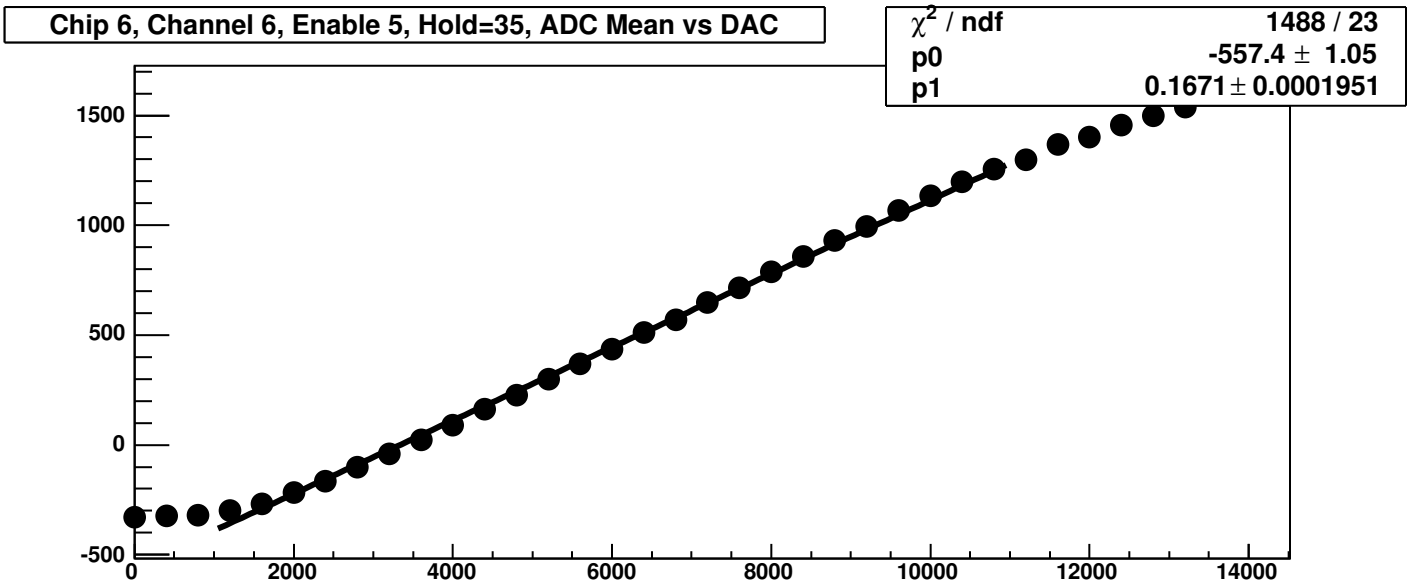

Chip 6, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

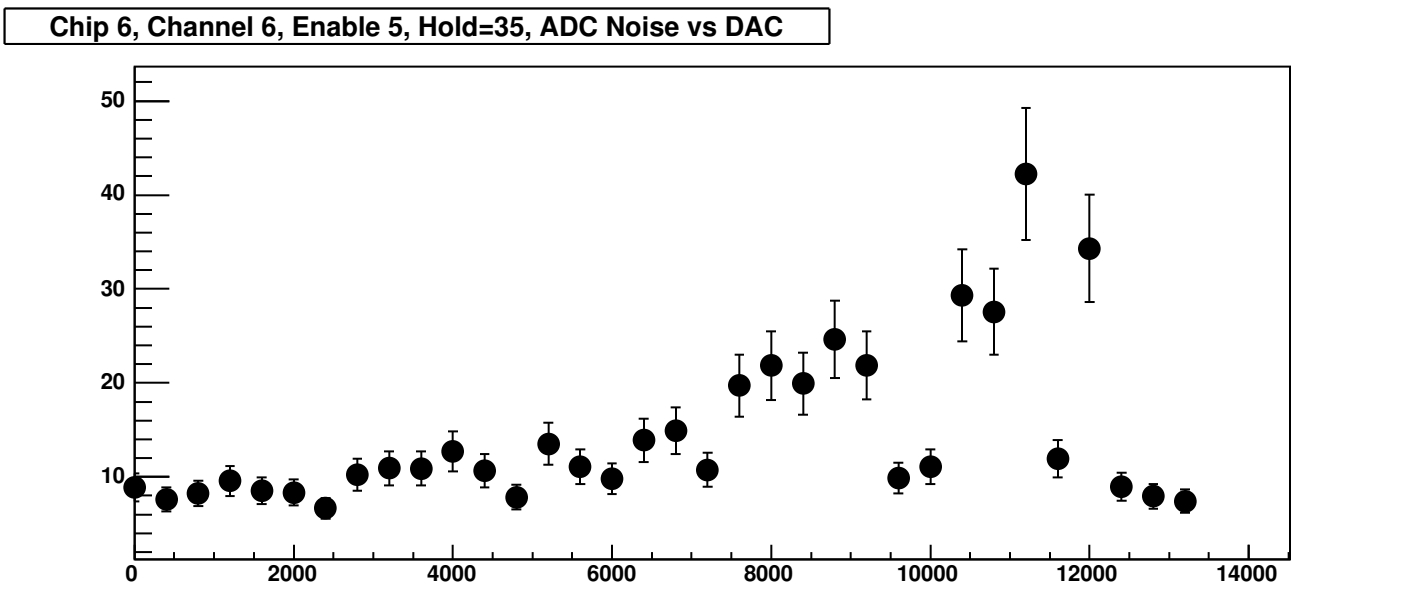

Chip 6, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

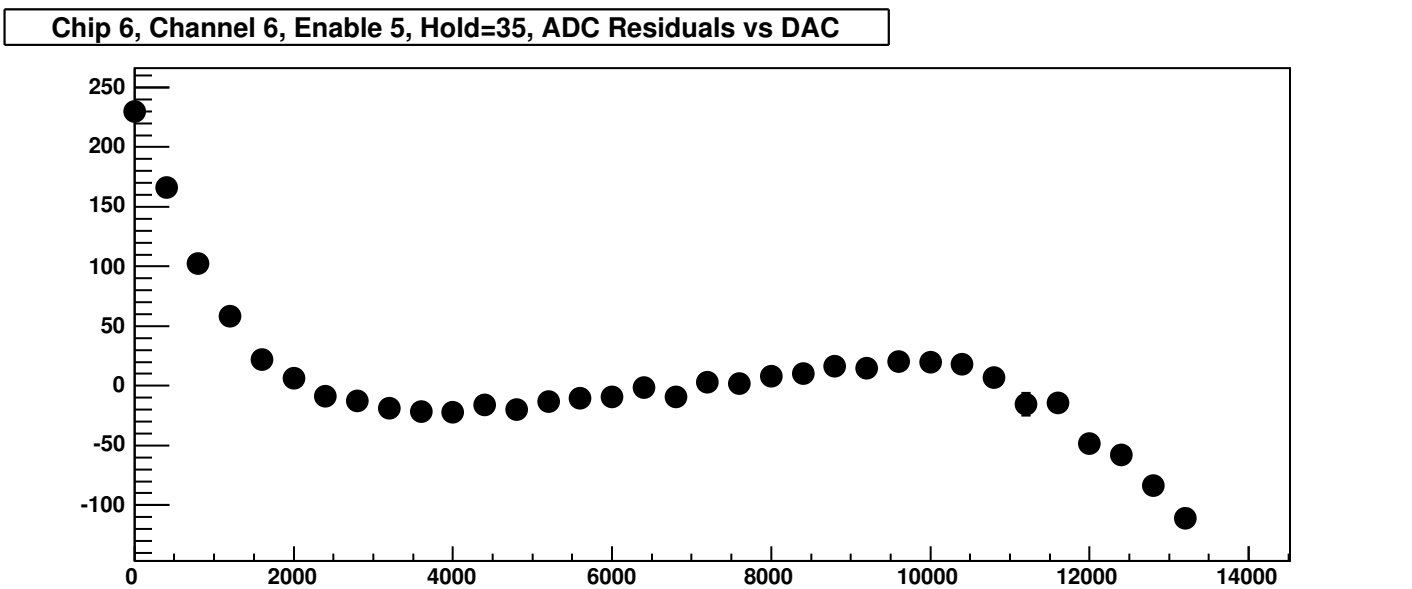

Chip 6, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.27 / 23
p0 -365.9 ± 0.7814
p1 0.01358 ± 0.0001288
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

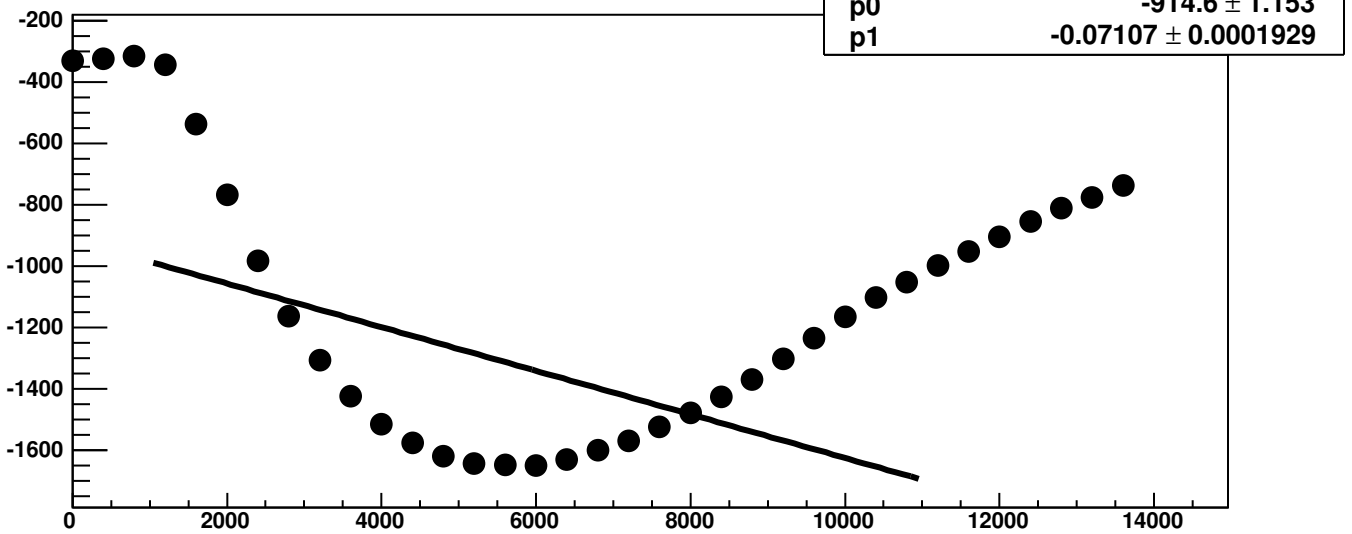

Chip 6, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1488 / 23
p0 -557.4 ± 1.05
p1 0.1671 ± 0.0001951
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

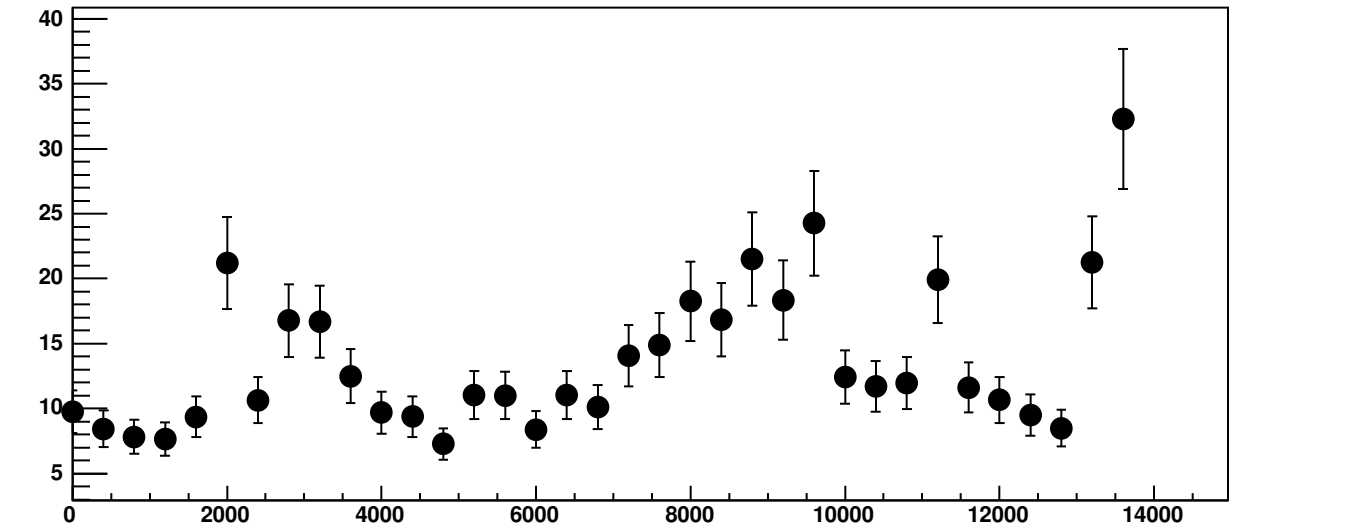

Chip 6, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

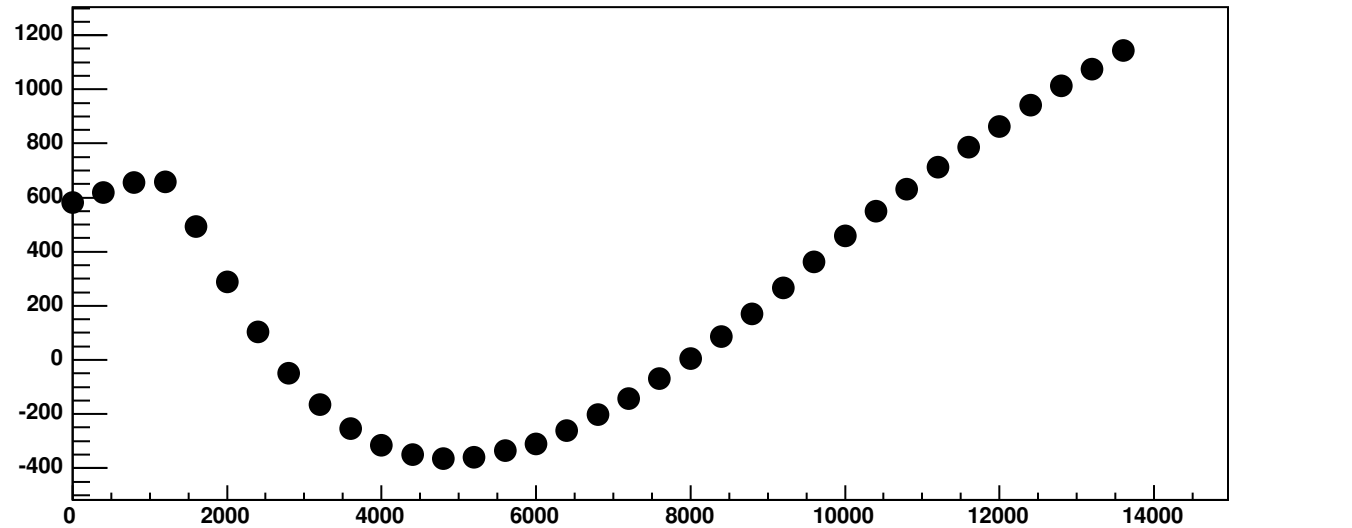
Chip 6, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

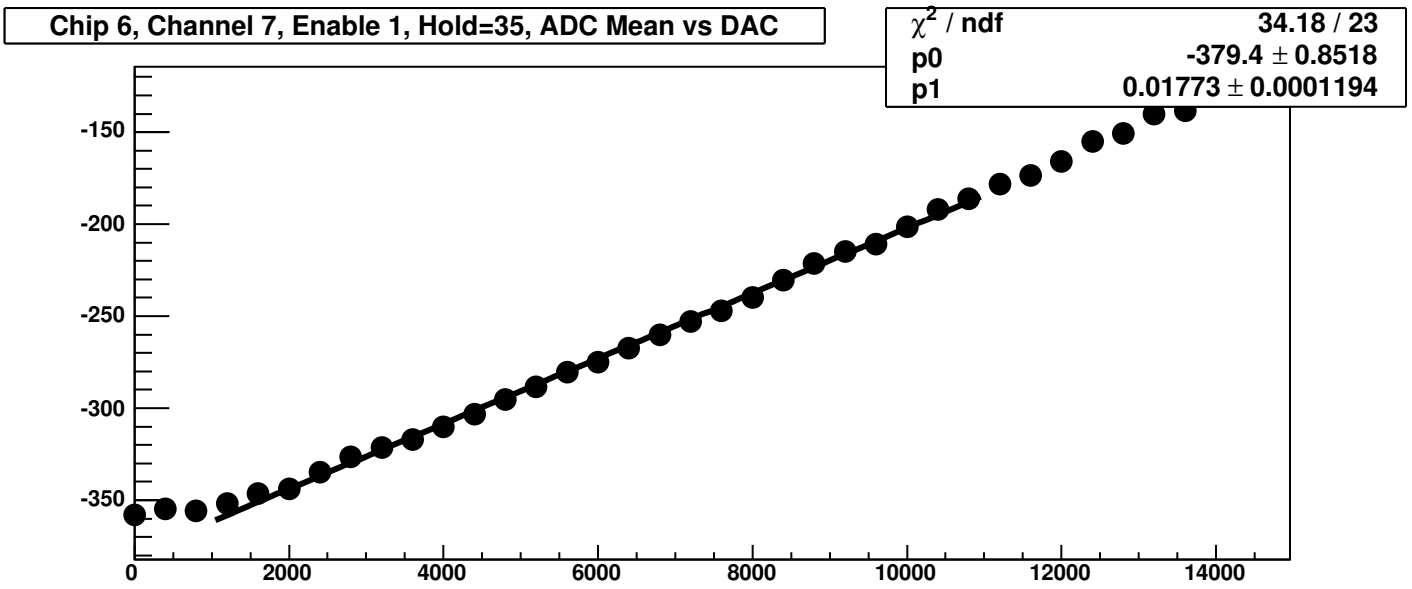


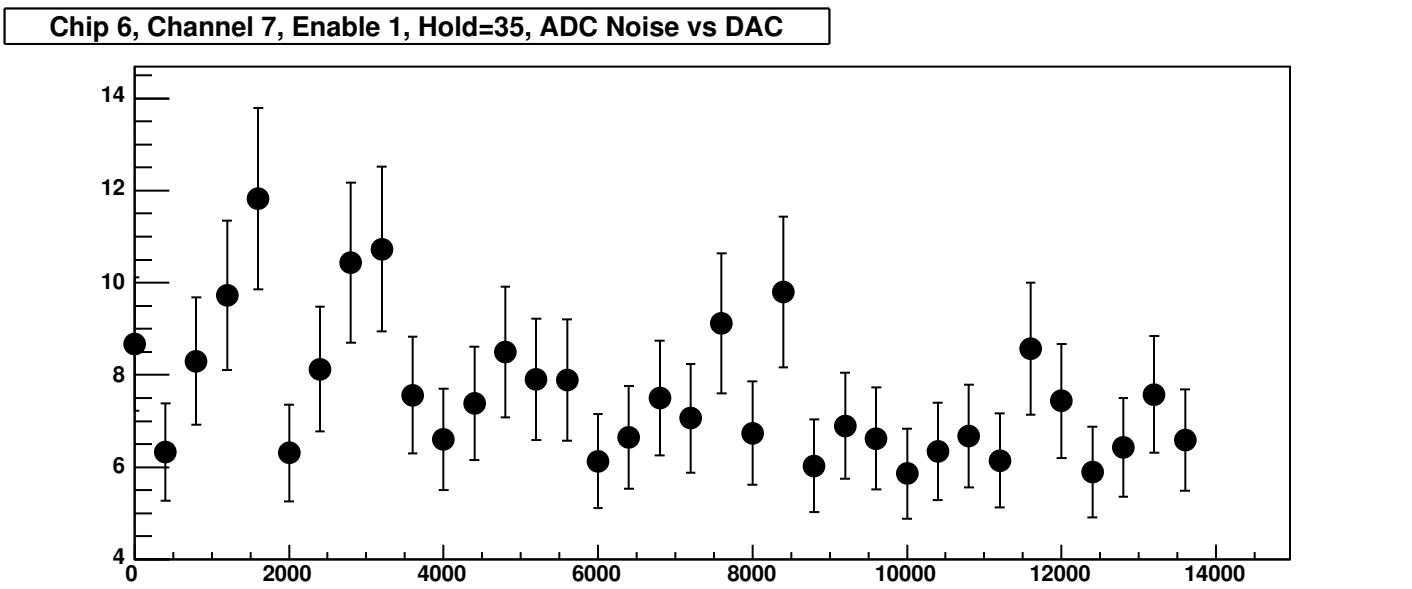
Chip 6, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

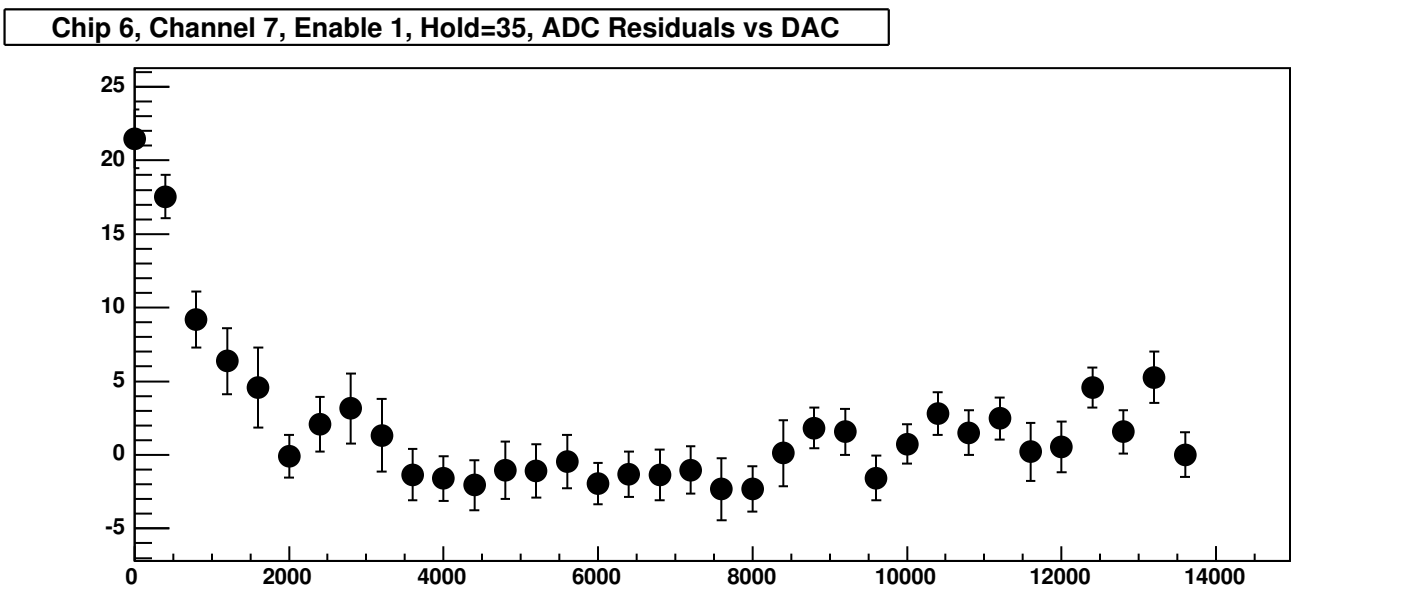


Chip 6, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

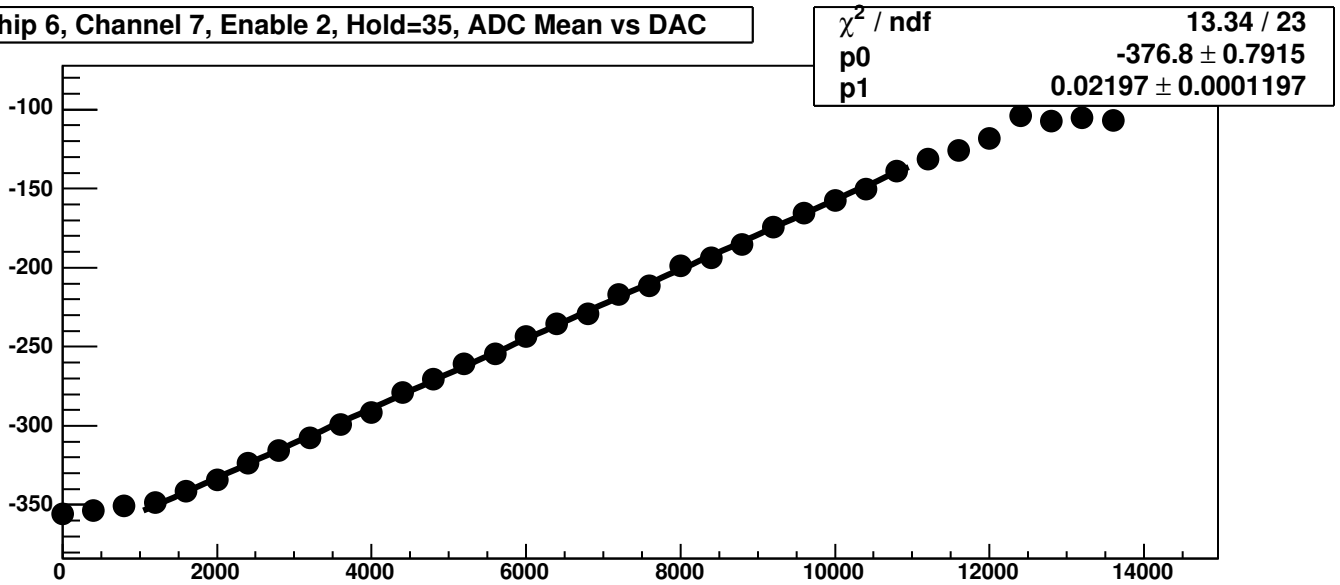



Chip 6, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.18 / 23
p0 -379.4 ± 0.8518
p1 0.01773 ± 0.0001194
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

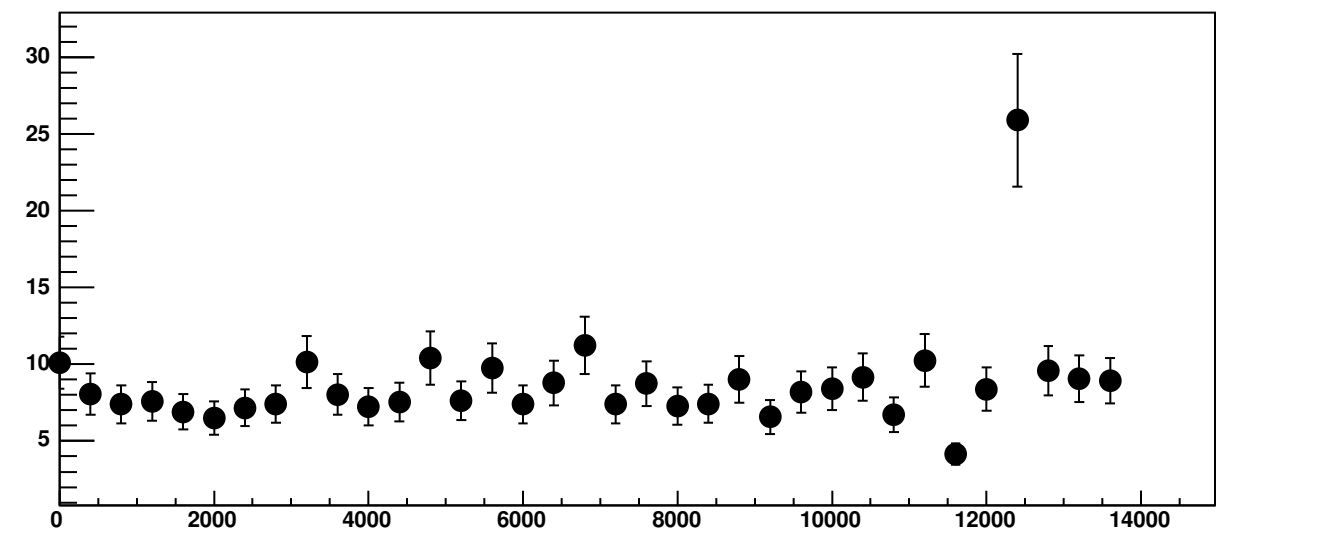

Chip 6, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

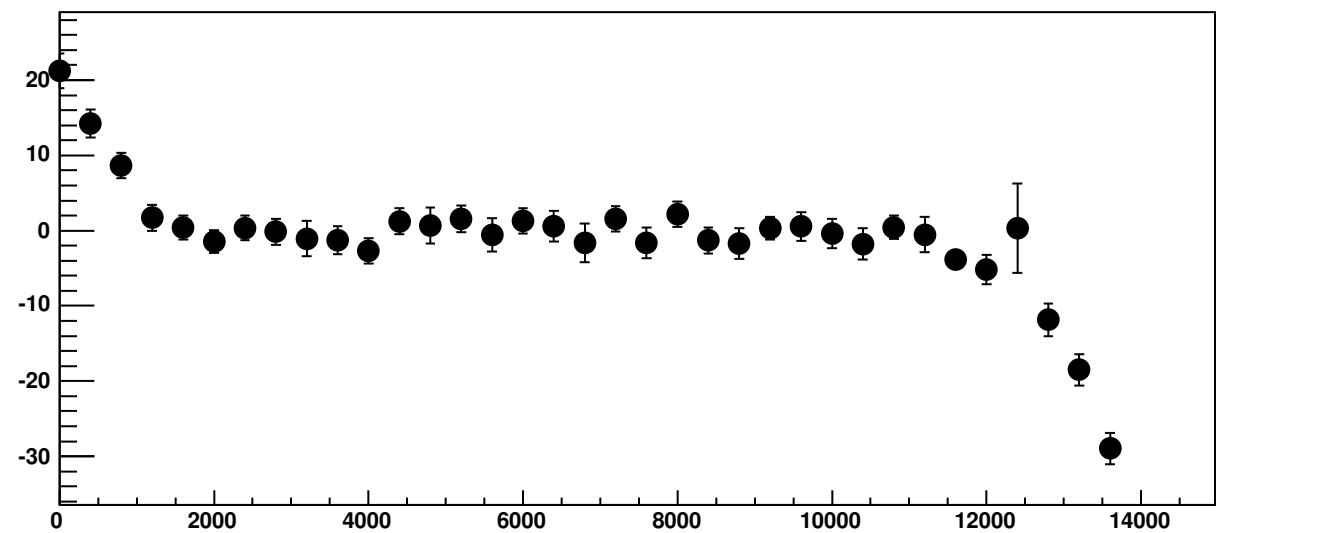
Chip 6, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

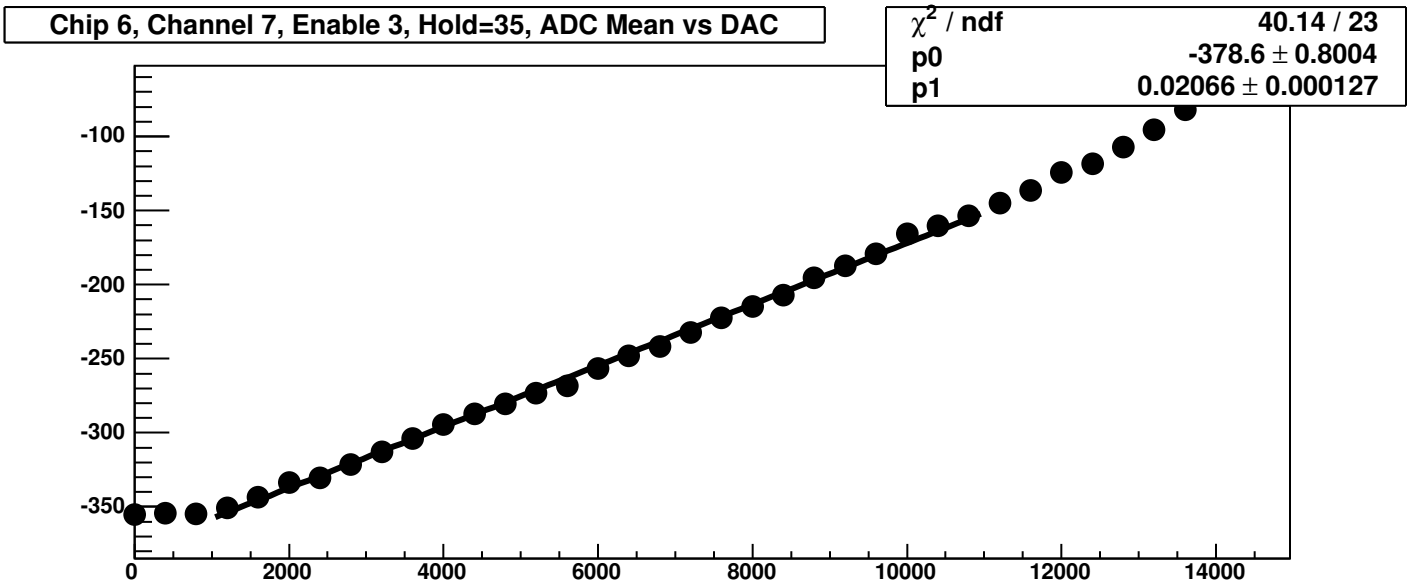


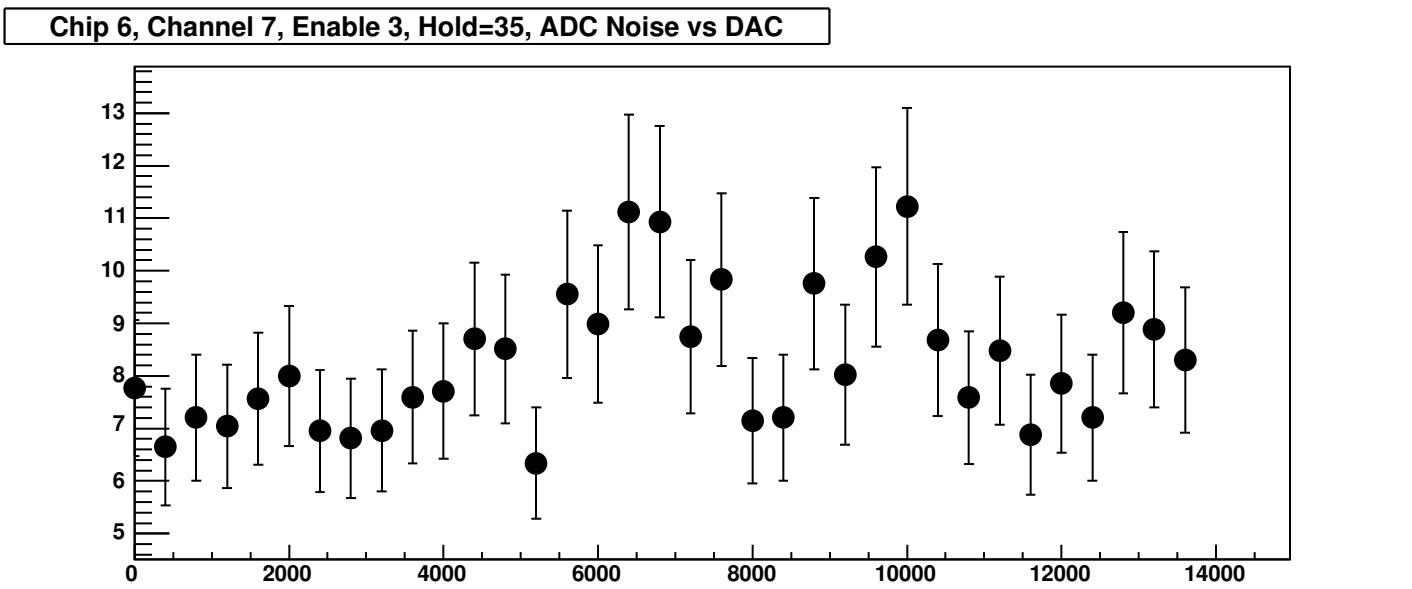
Chip 6, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC

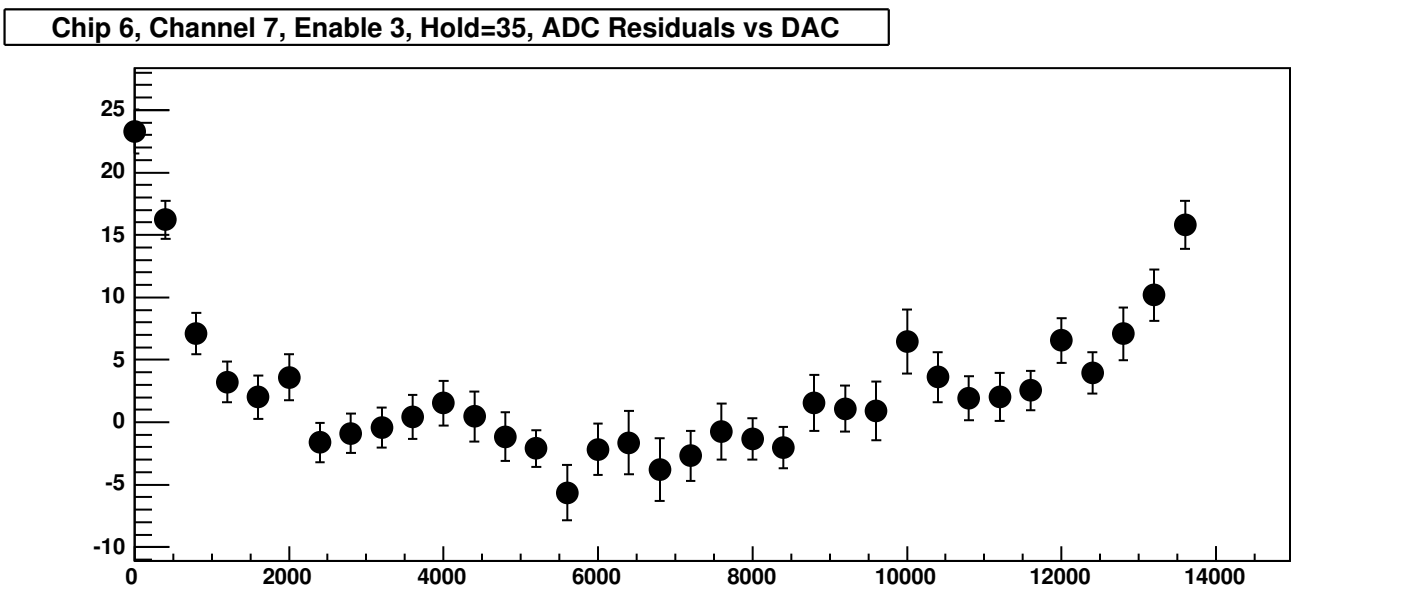


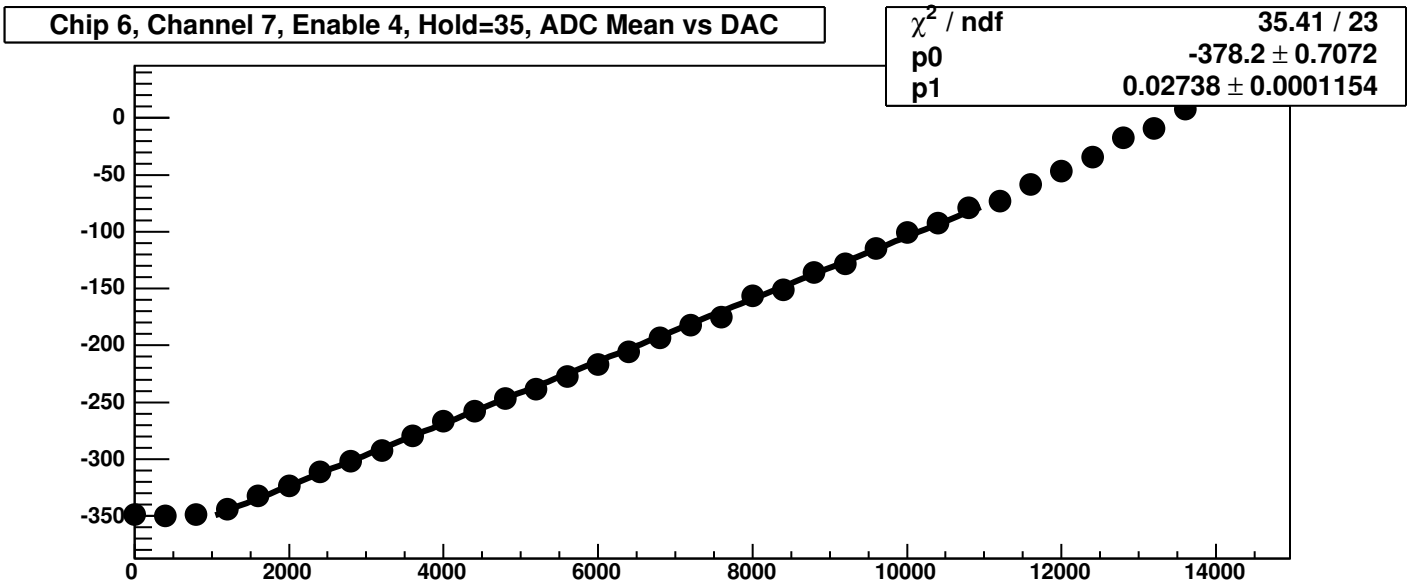
Chip 6, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

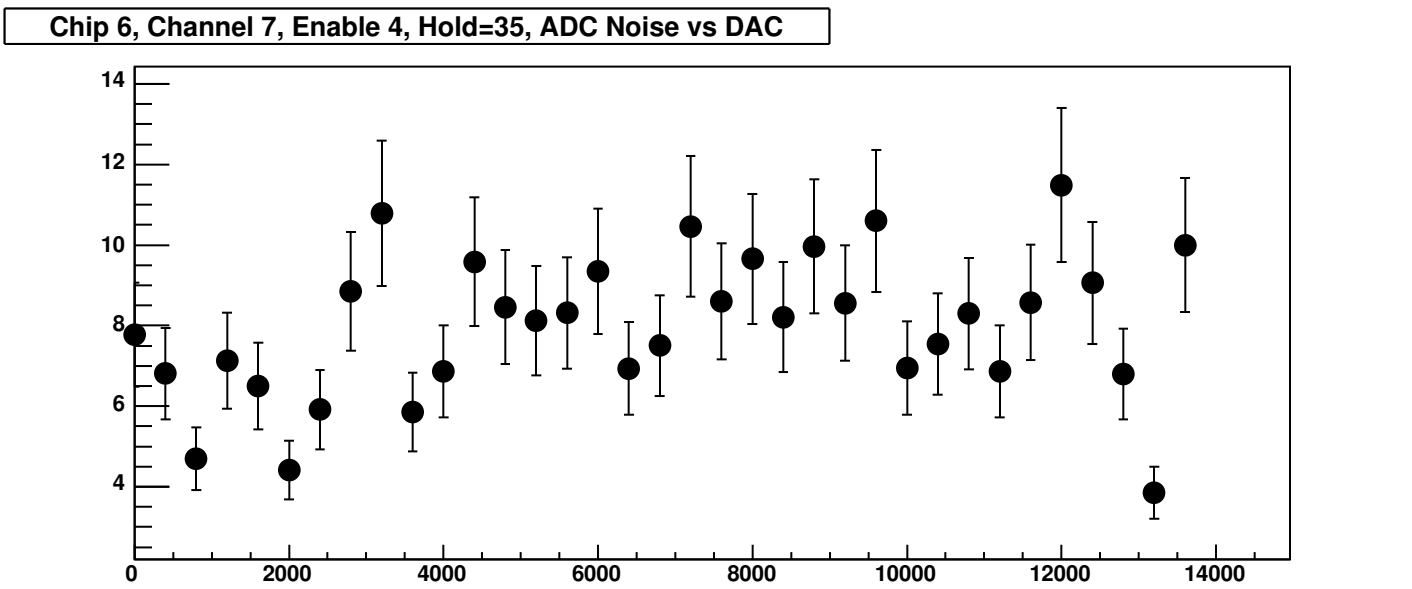


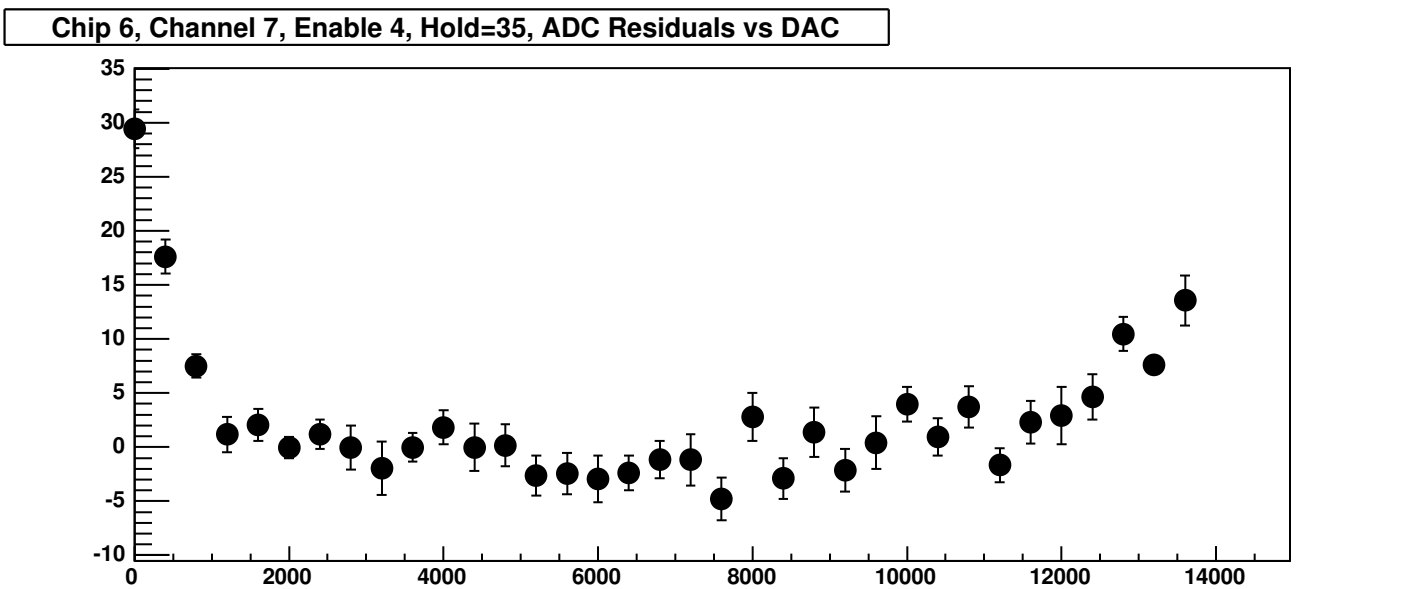

Chip 6, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.14 / 23
p0 -378.6 ± 0.8004
p1 0.02066 ± 0.000127
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

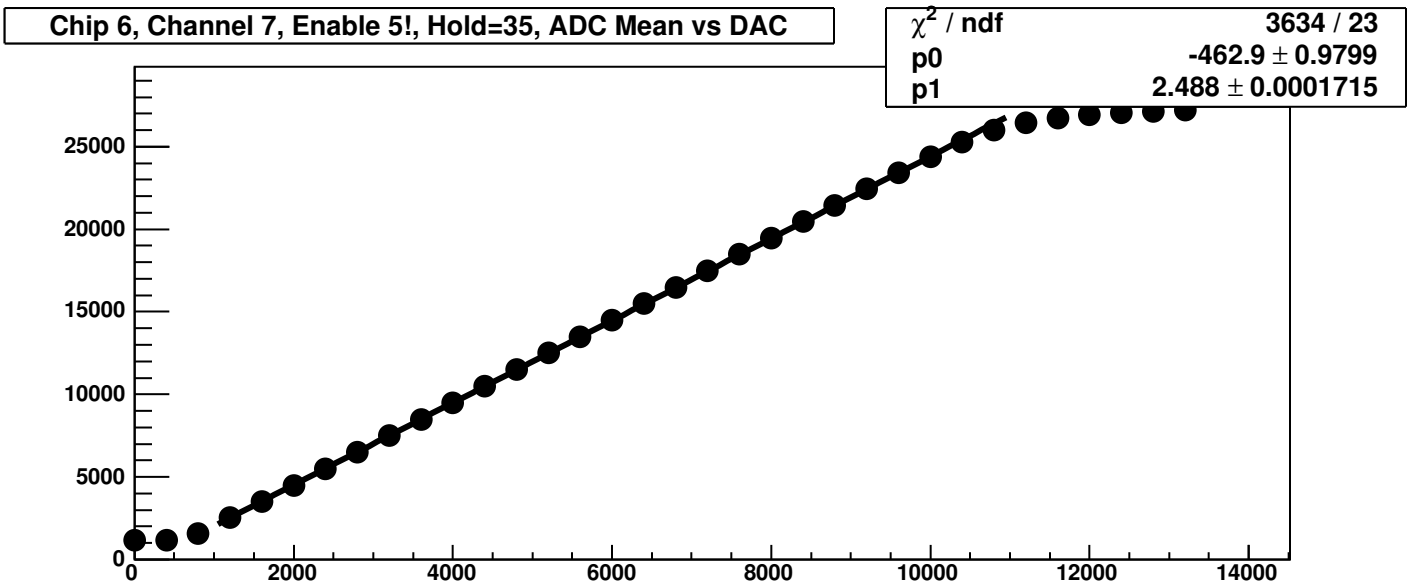

Chip 6, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

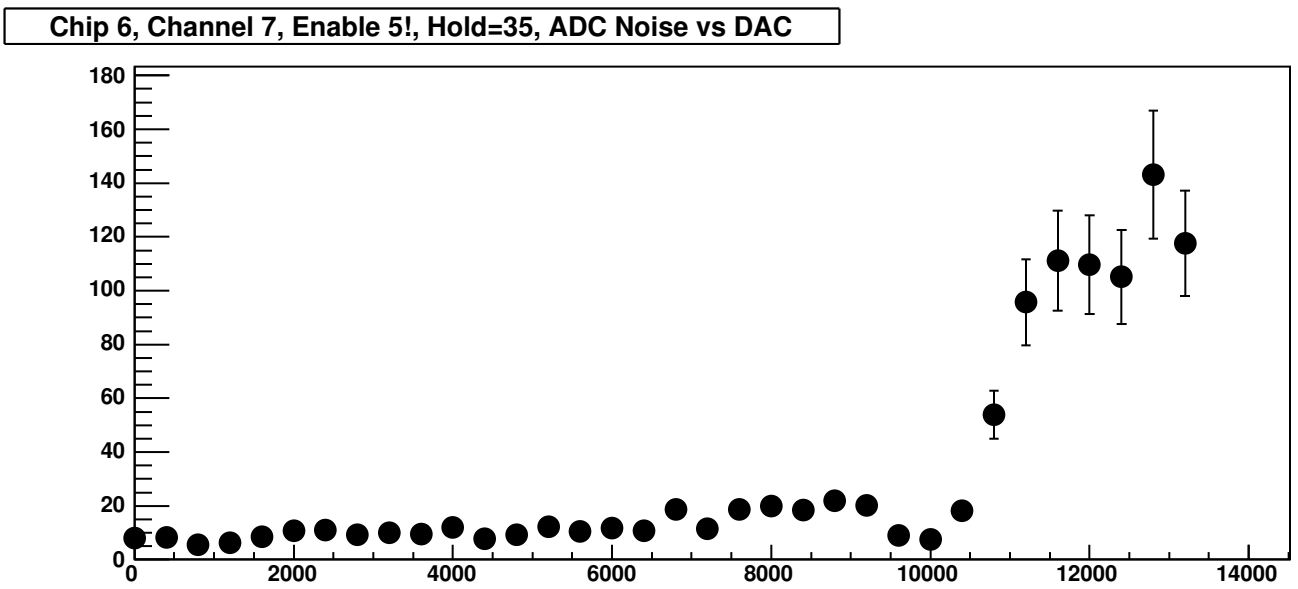

Chip 6, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

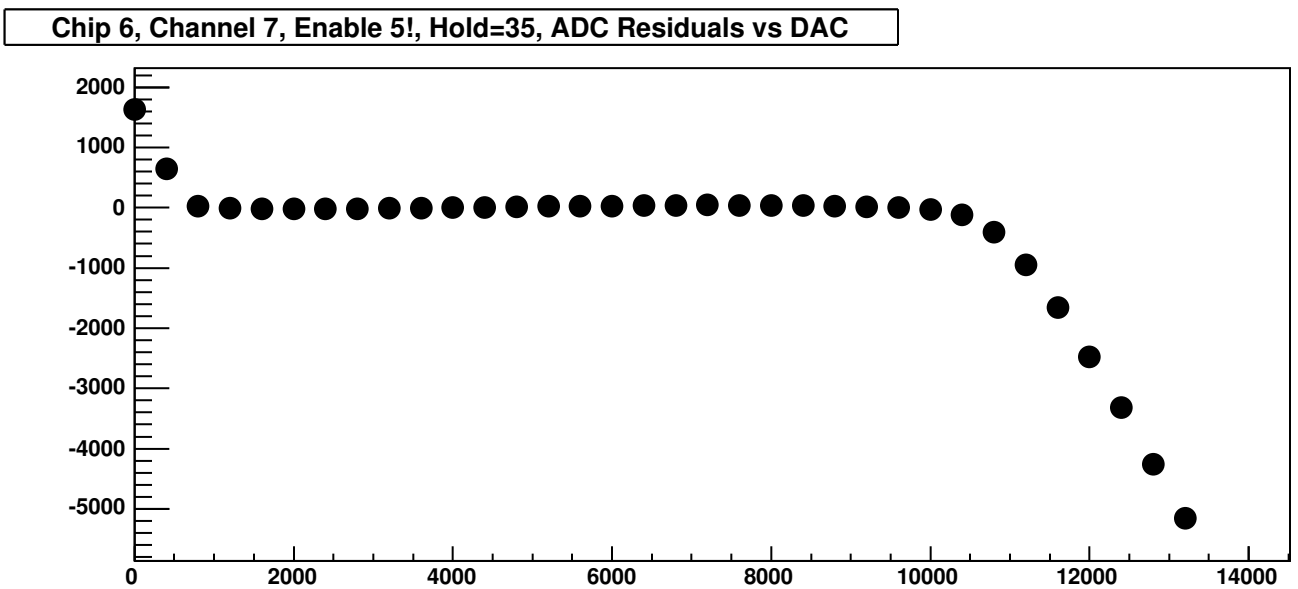

Chip 6, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.41 / 23
p0 -378.2 ± 0.7072
p1 0.02738 ± 0.0001154
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

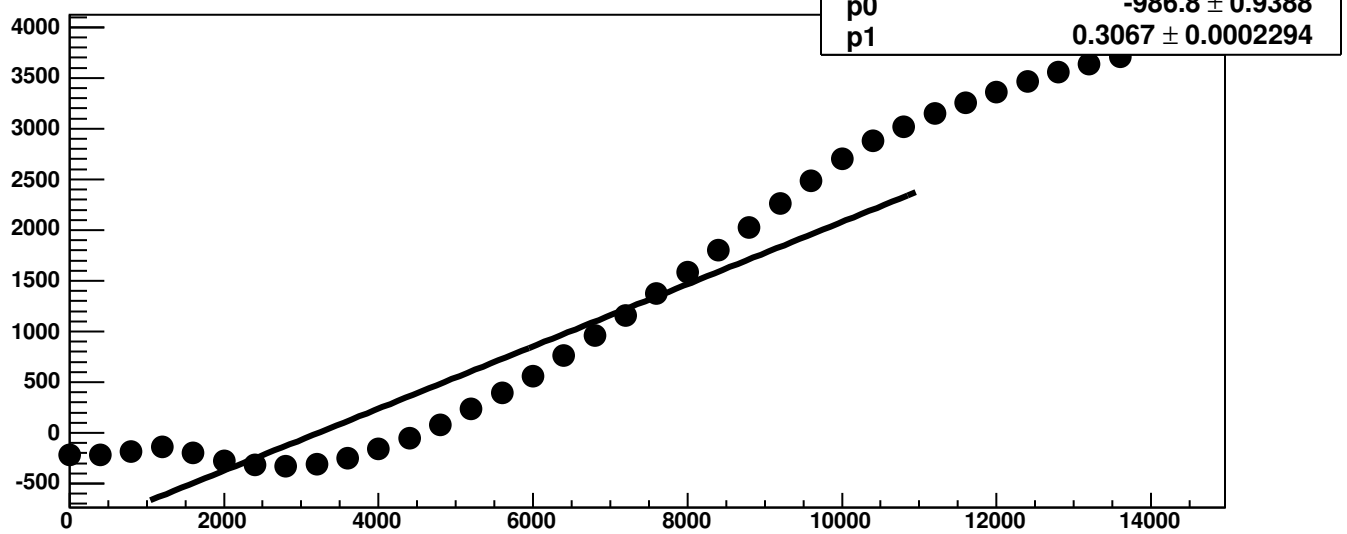

Chip 6, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3634 / 23
p0 -462.9 ± 0.9799
p1 2.488 ± 0.0001715
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

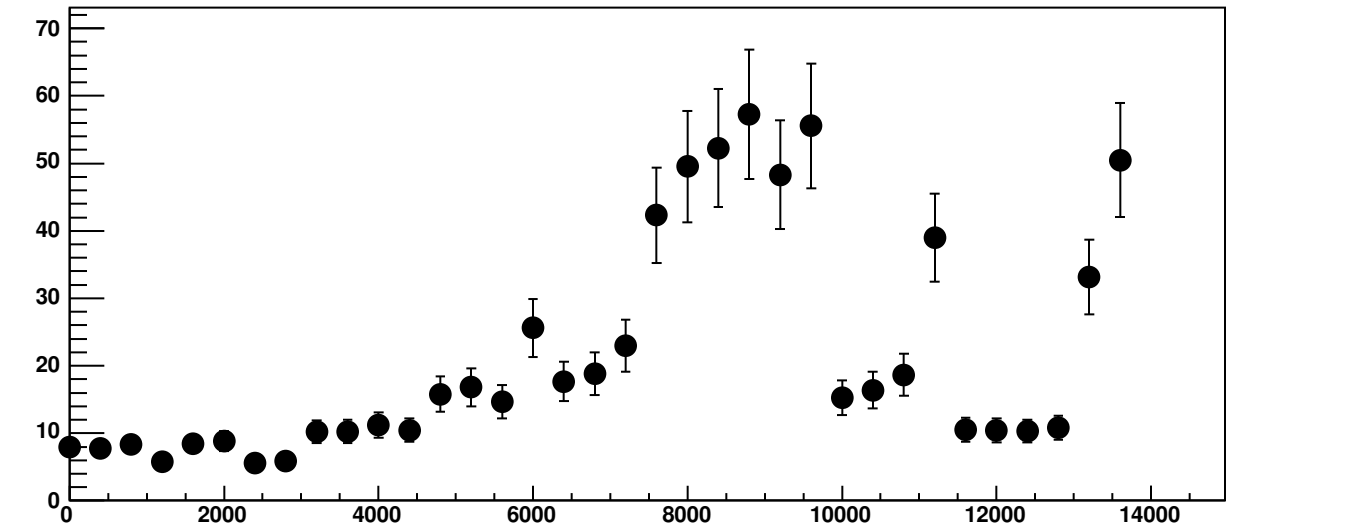

Chip 6, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

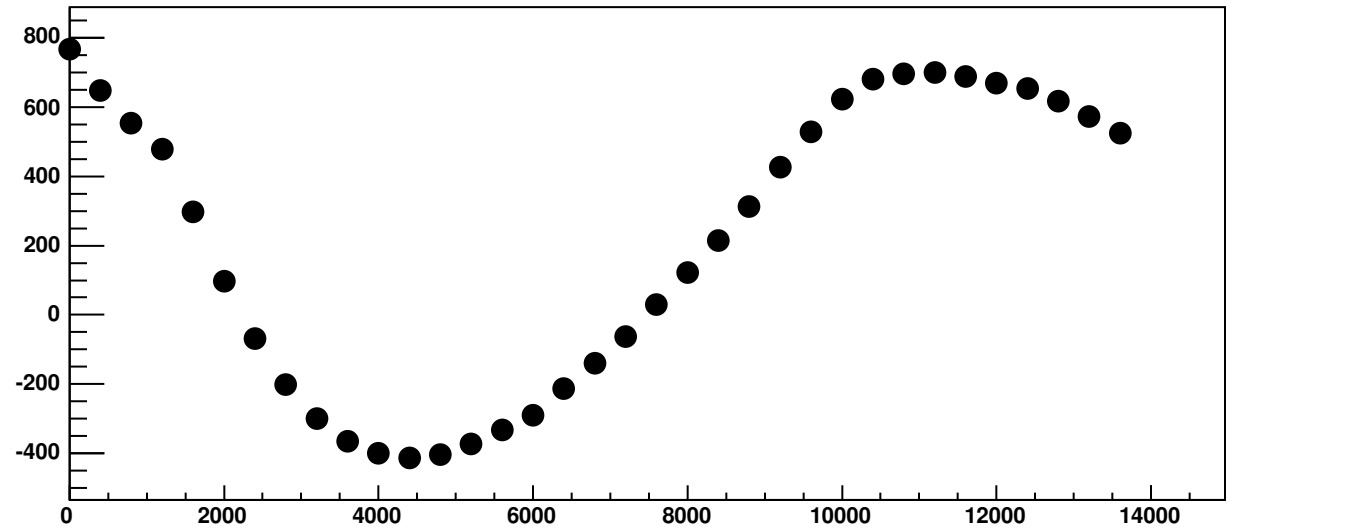
Chip 6, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

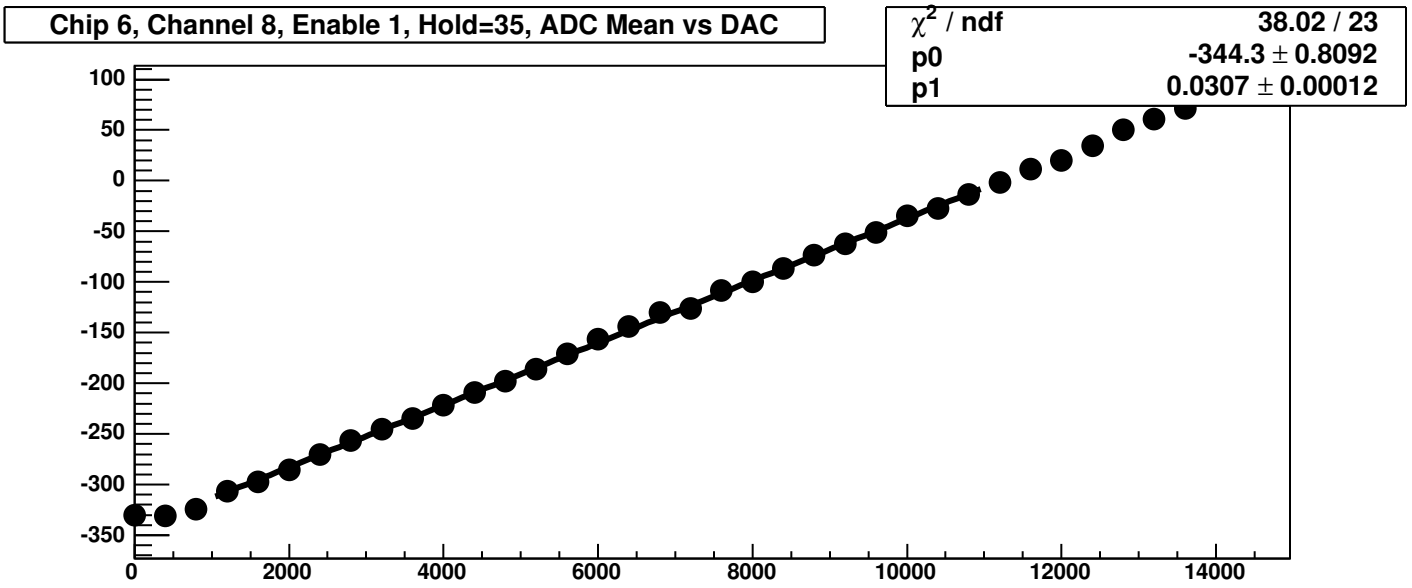


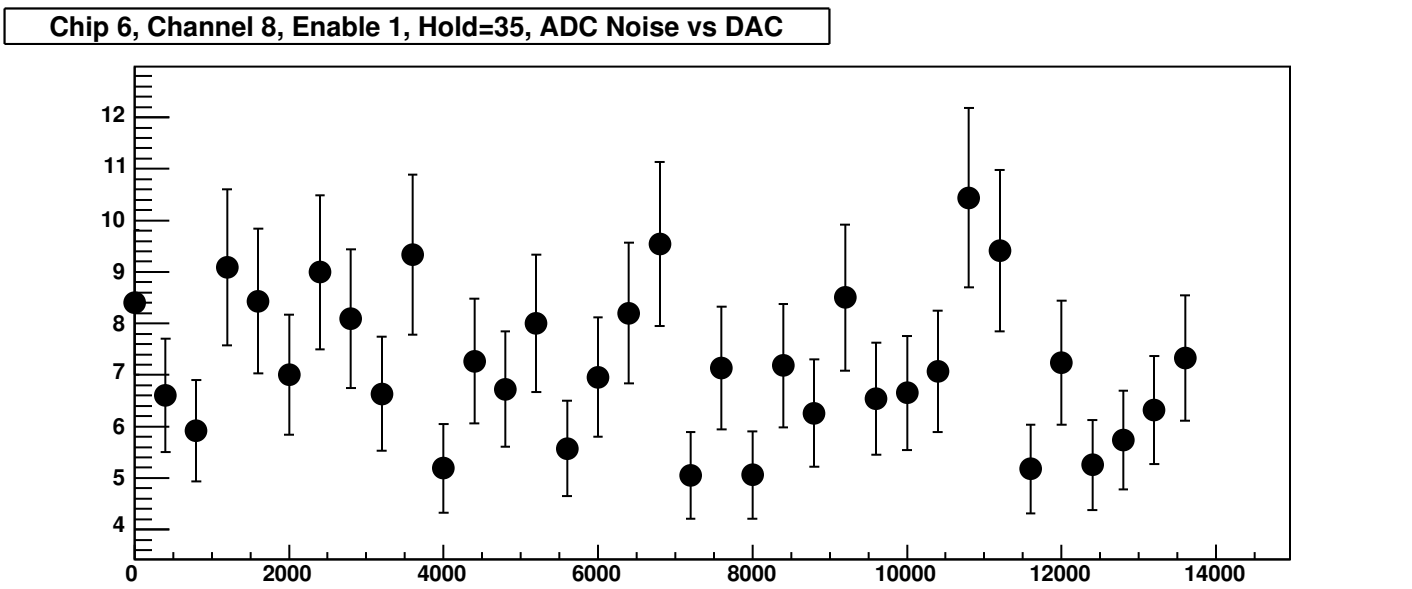
Chip 6, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC

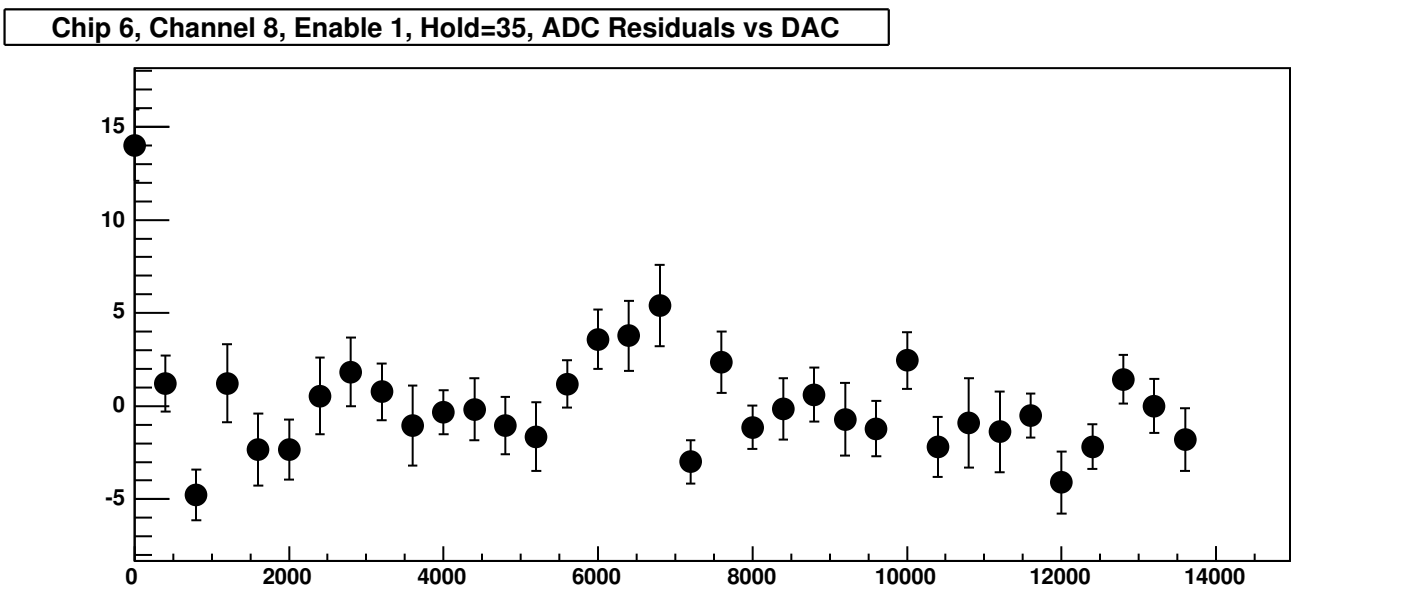


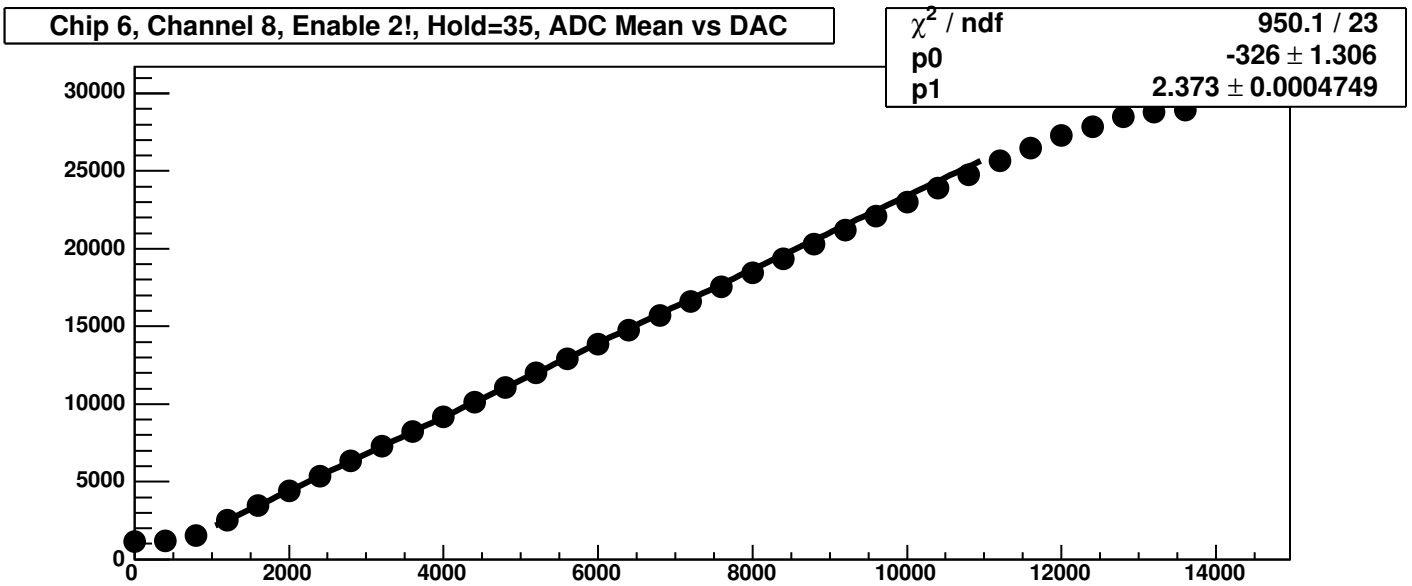
Chip 6, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

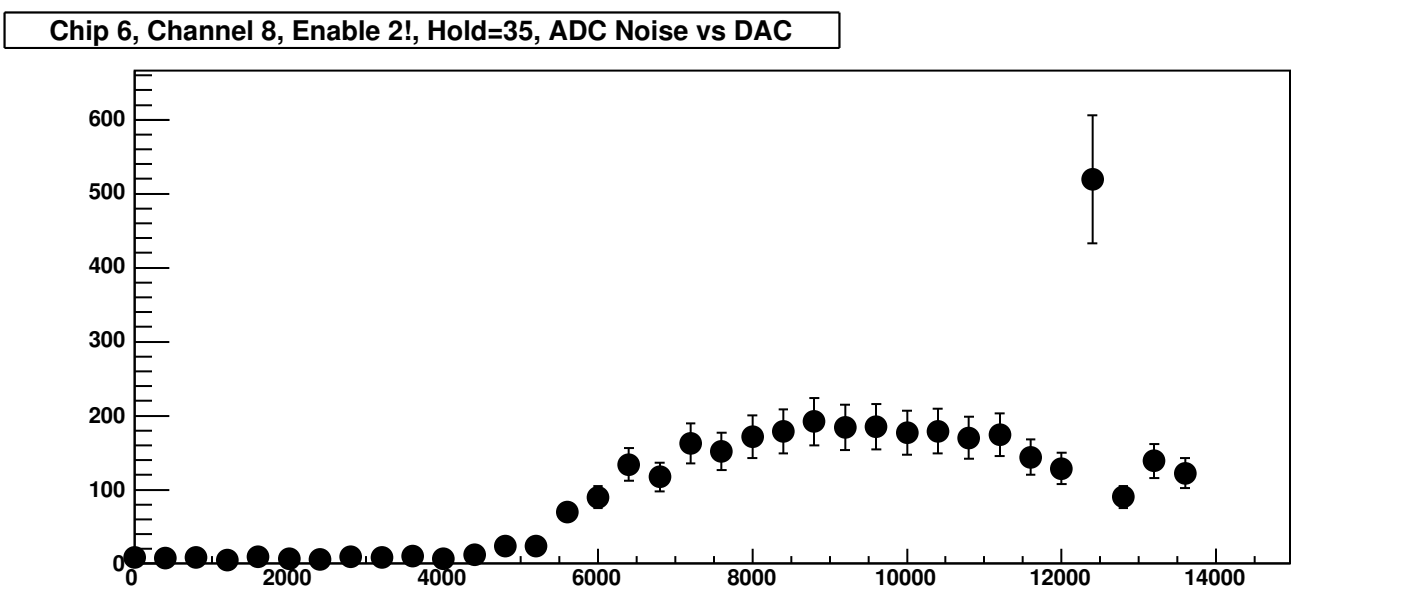


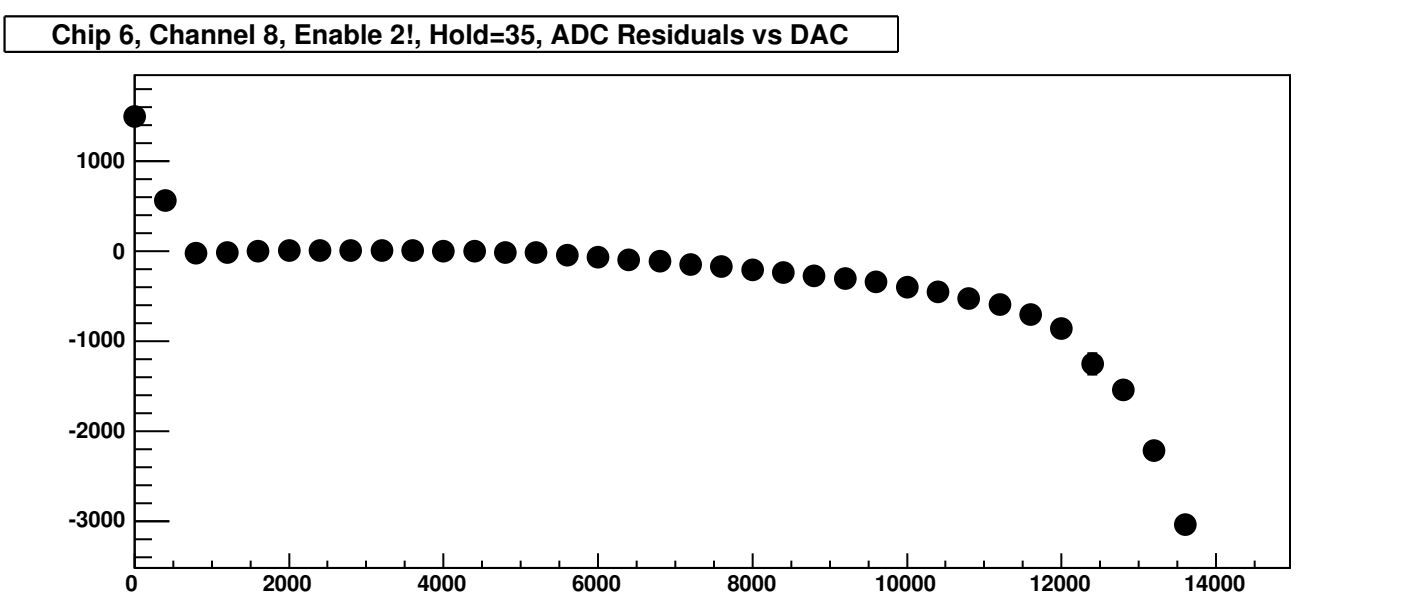

Chip 6, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.02 / 23
p0 -344.3 ± 0.8092
p1 0.0307 ± 0.00012
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

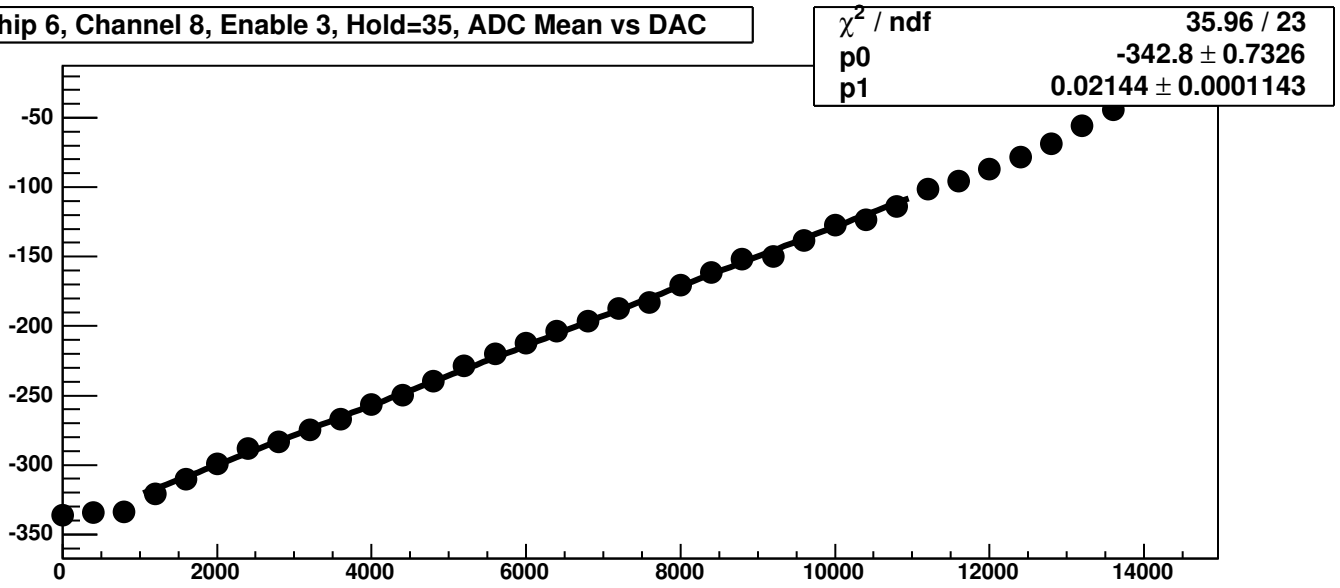

Chip 6, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 950.1 / 23
p0 -326 ± 1.306
p1 2.373 ± 0.0004749
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

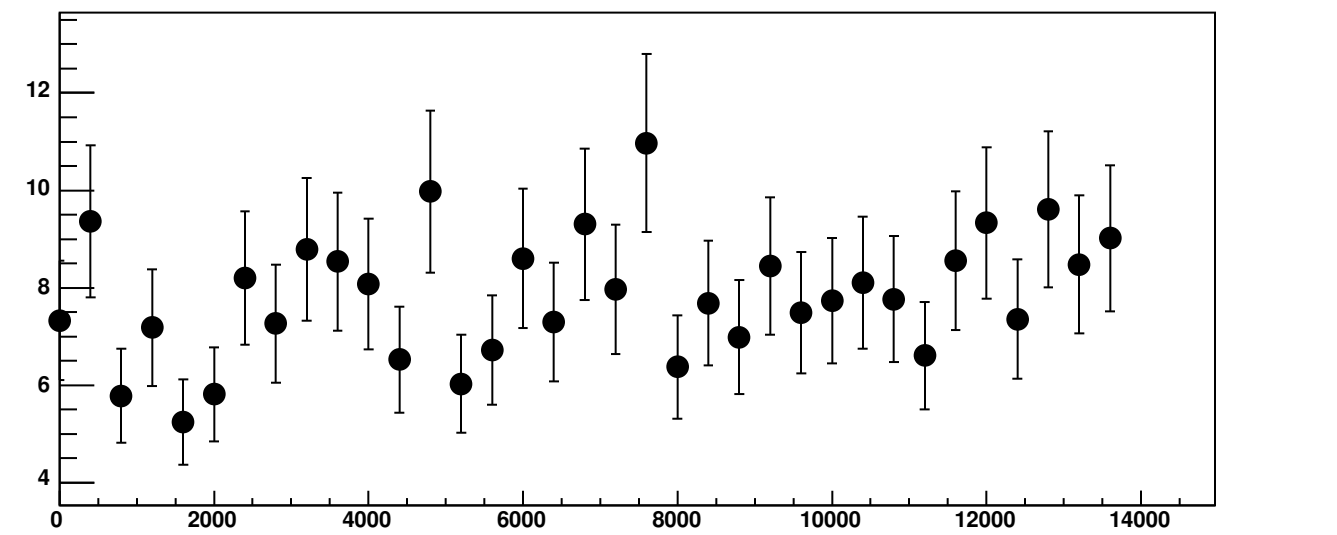

Chip 6, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

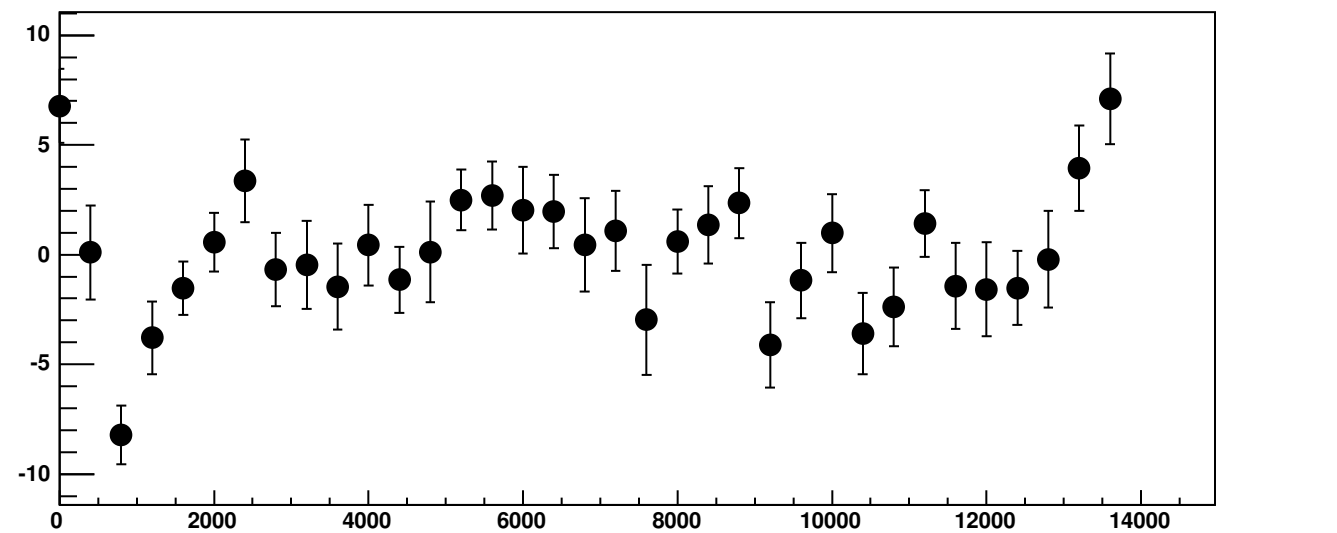
Chip 6, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



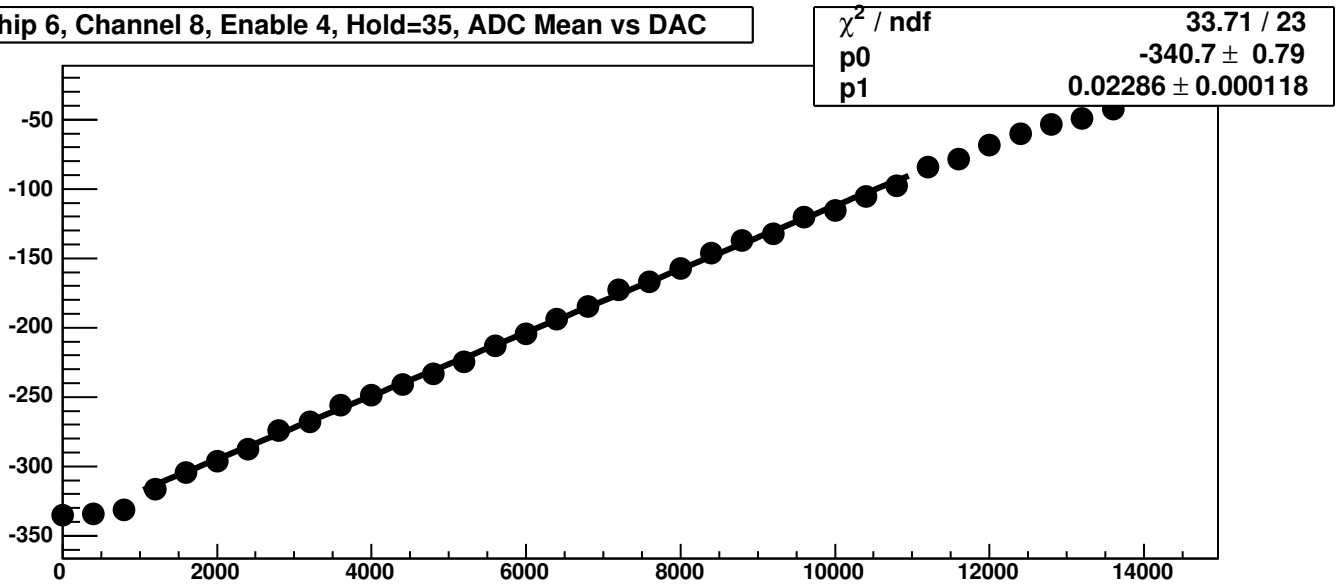
Chip 6, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



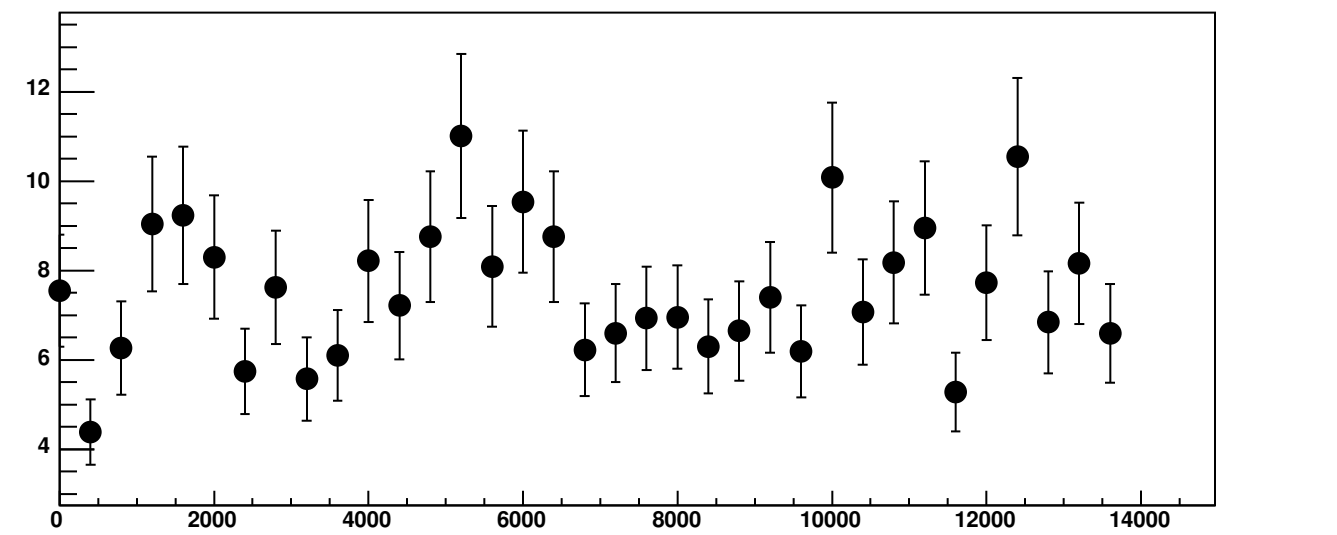
Chip 6, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



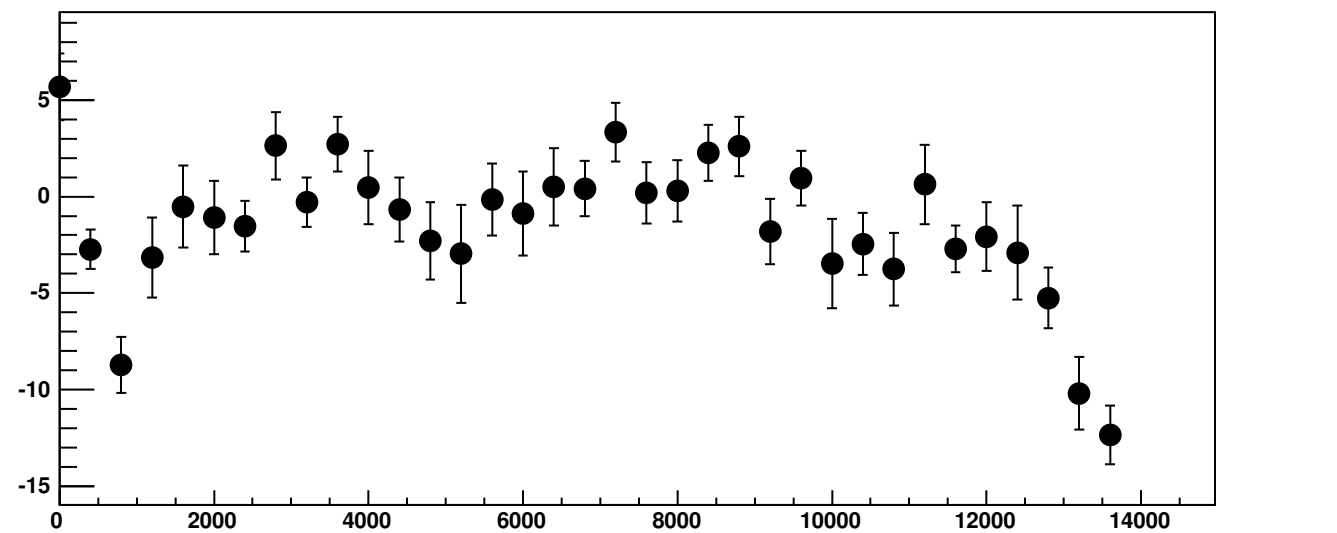
Chip 6, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

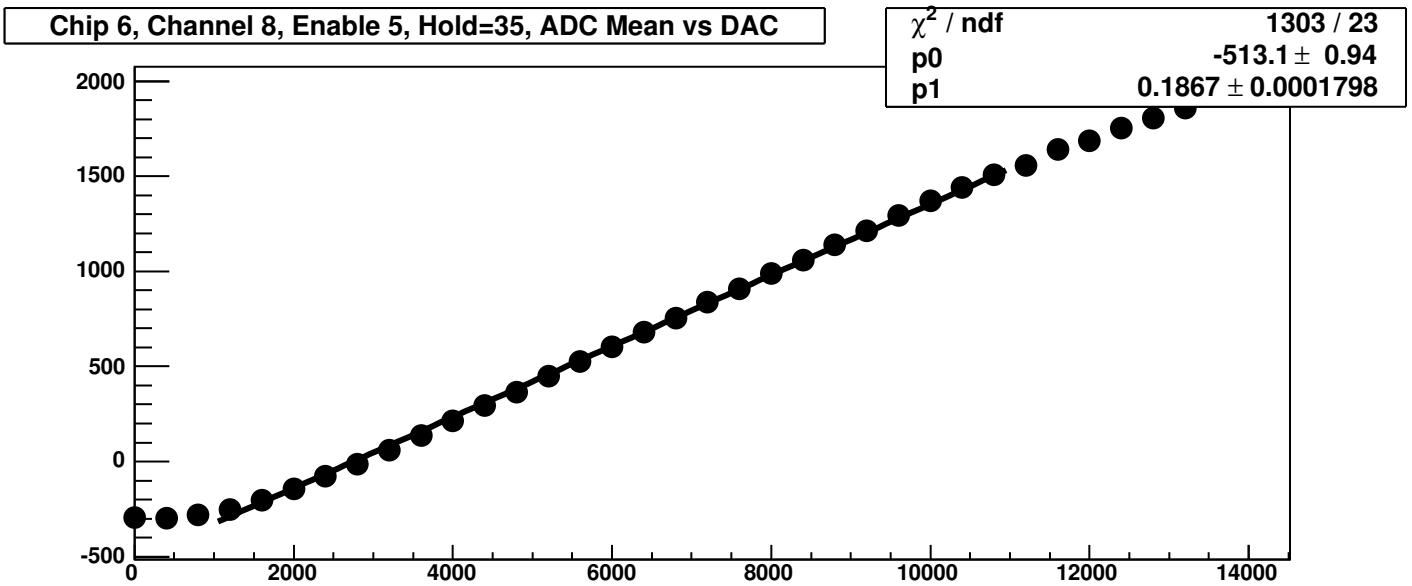


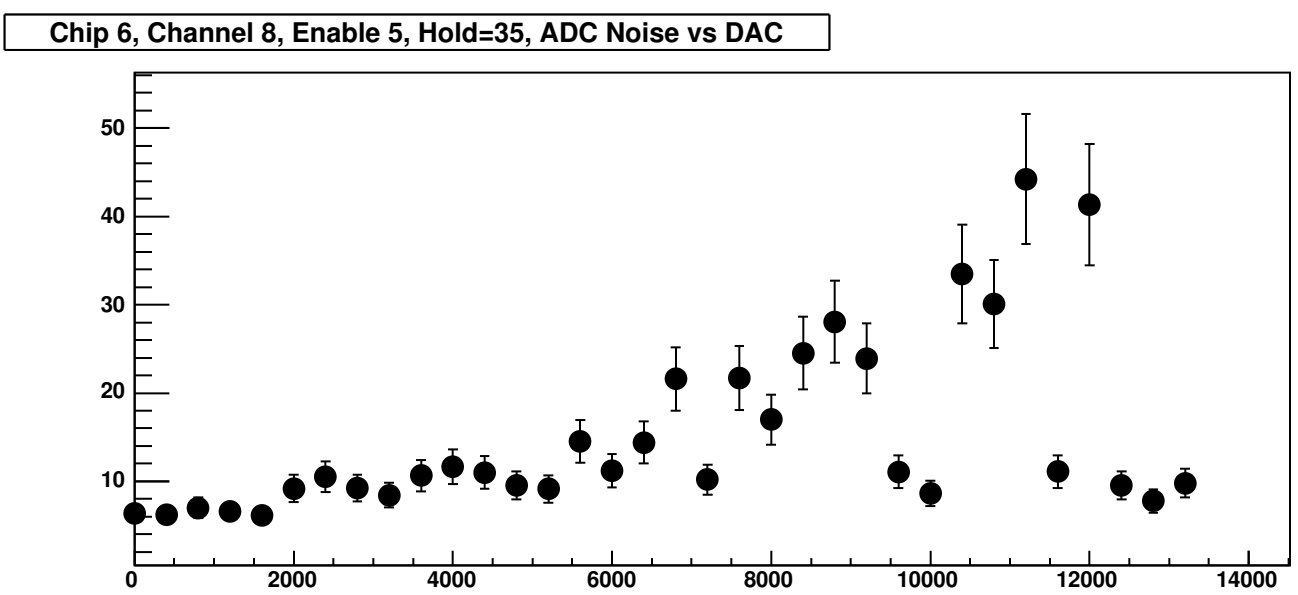
Chip 6, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

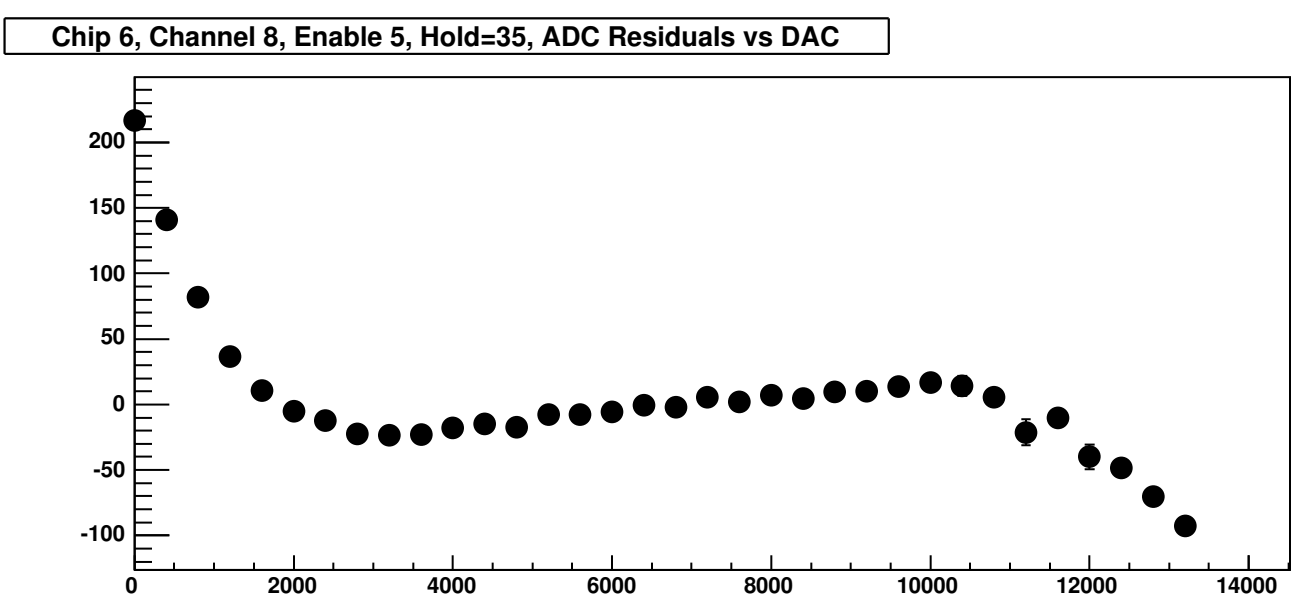


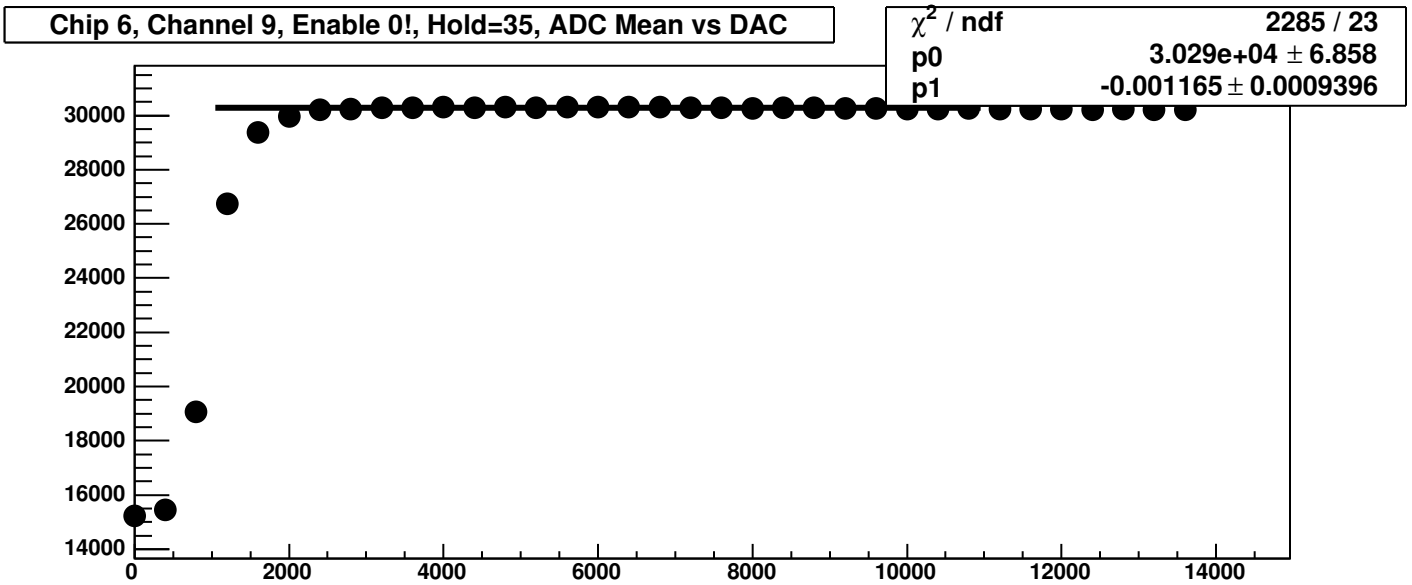
Chip 6, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

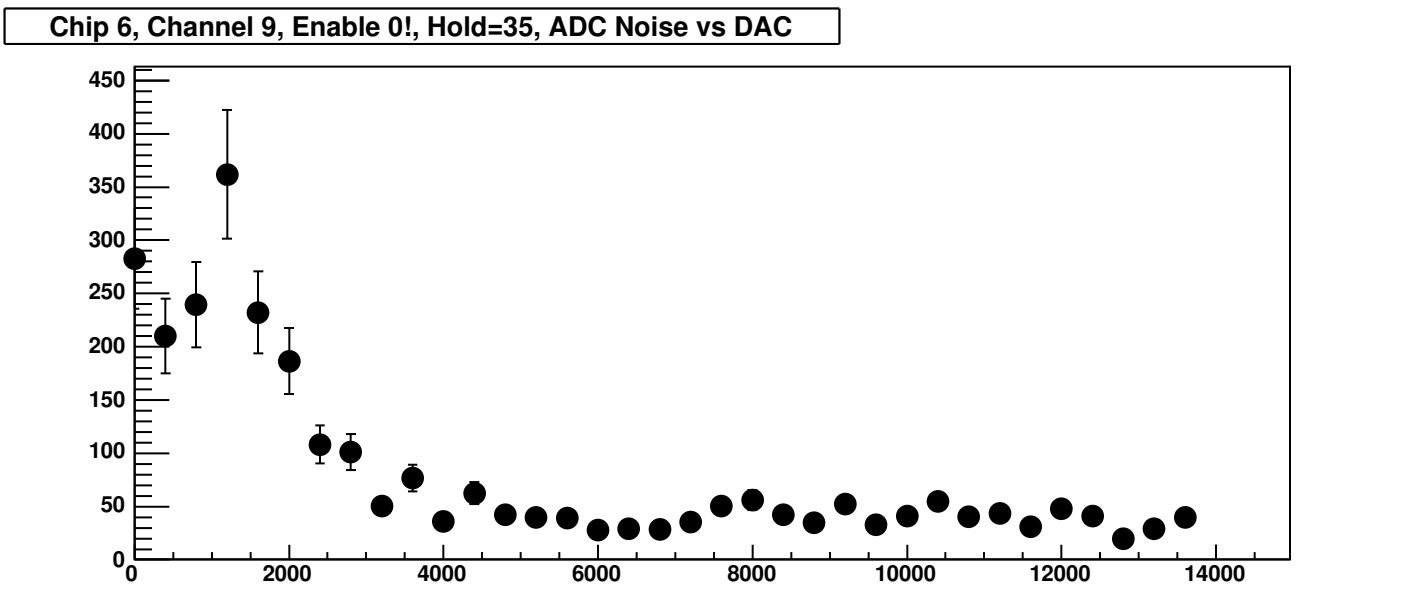


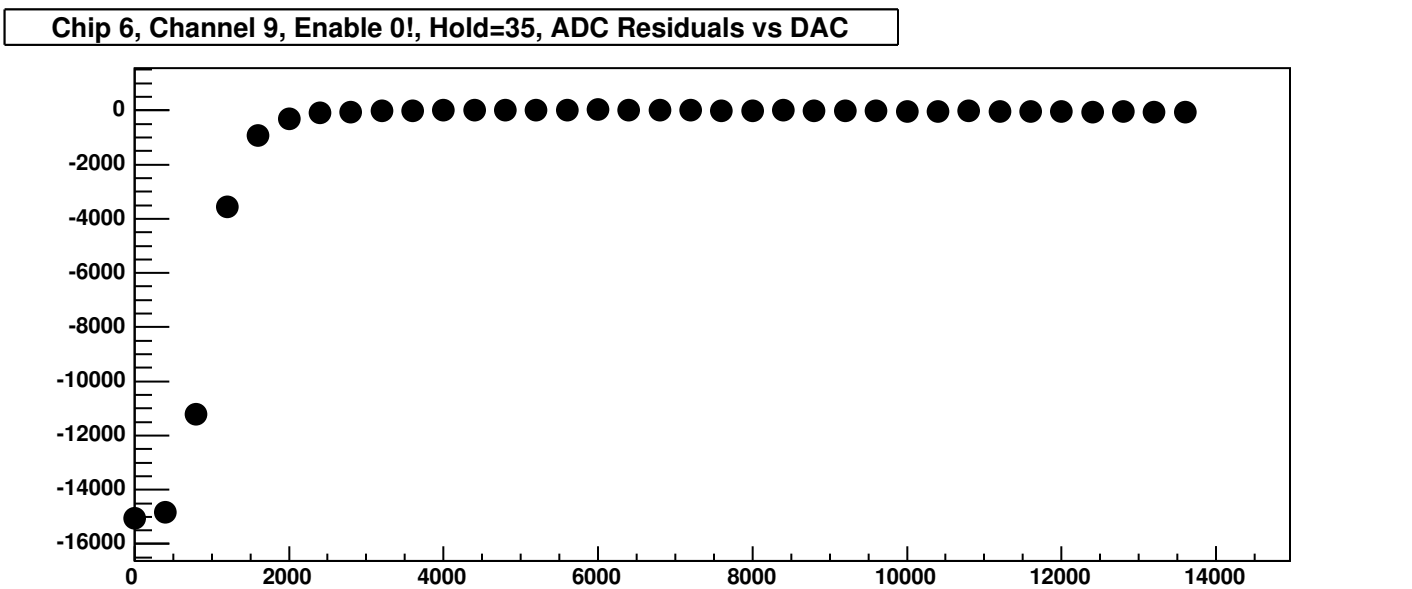

Chip 6, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1303 / 23
p0 -513.1 ± 0.94
p1 0.1867 ± 0.0001798
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

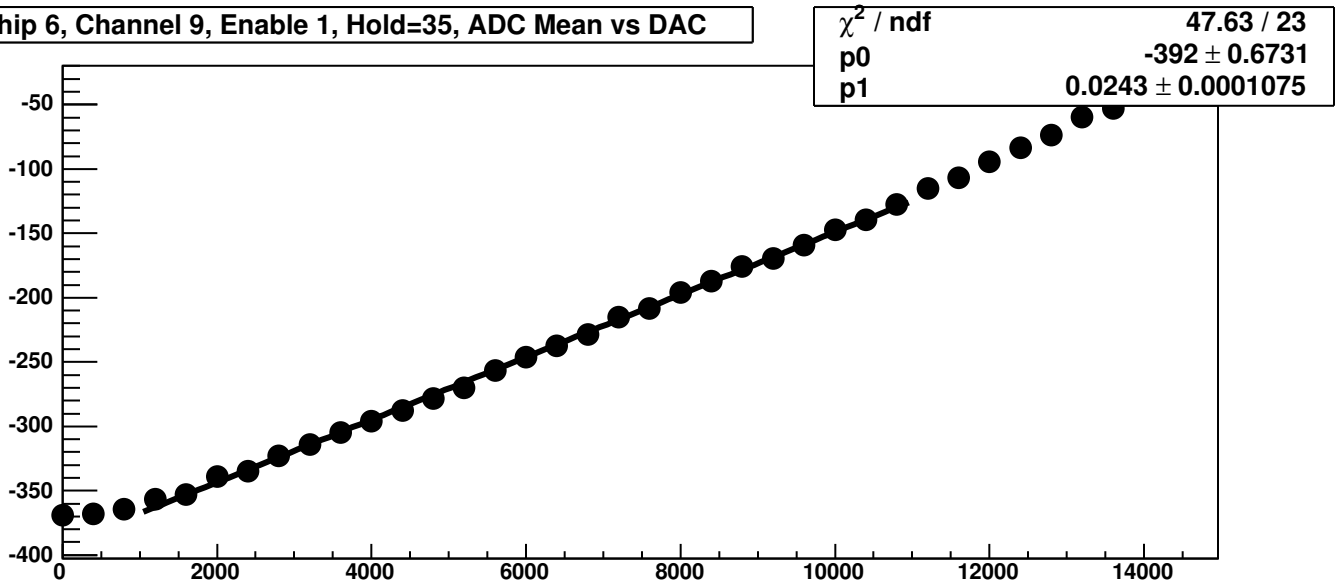

Chip 6, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2285 / 23
p0 3.029e+04 ± 6.858
p1 -0.001165 ± 0.0009396
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

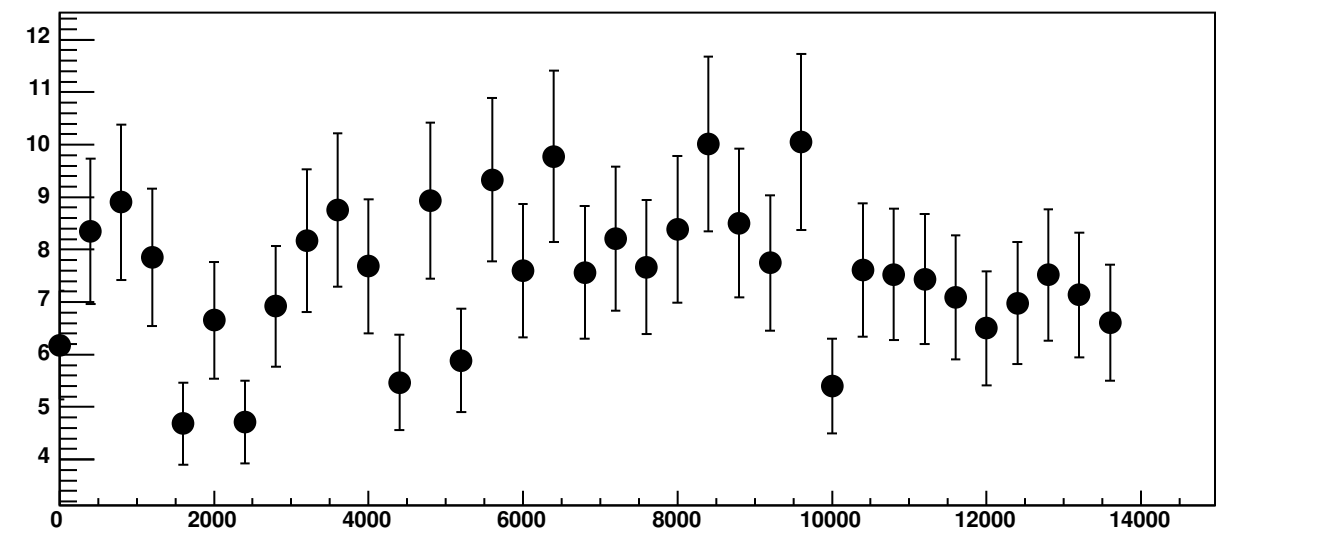

Chip 6, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

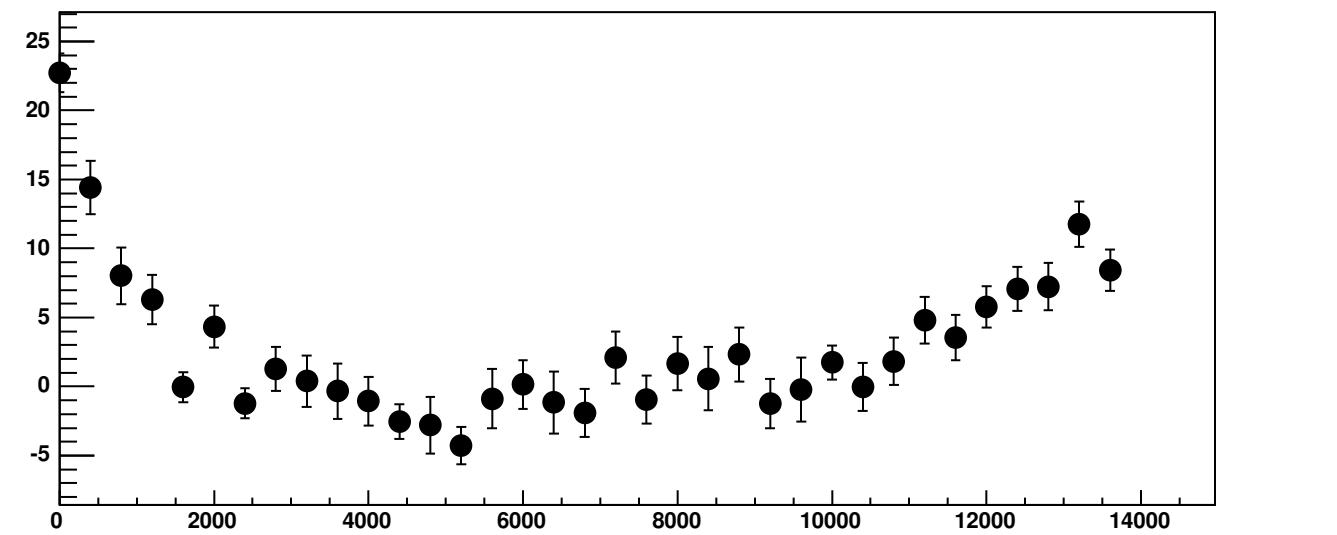
Chip 6, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

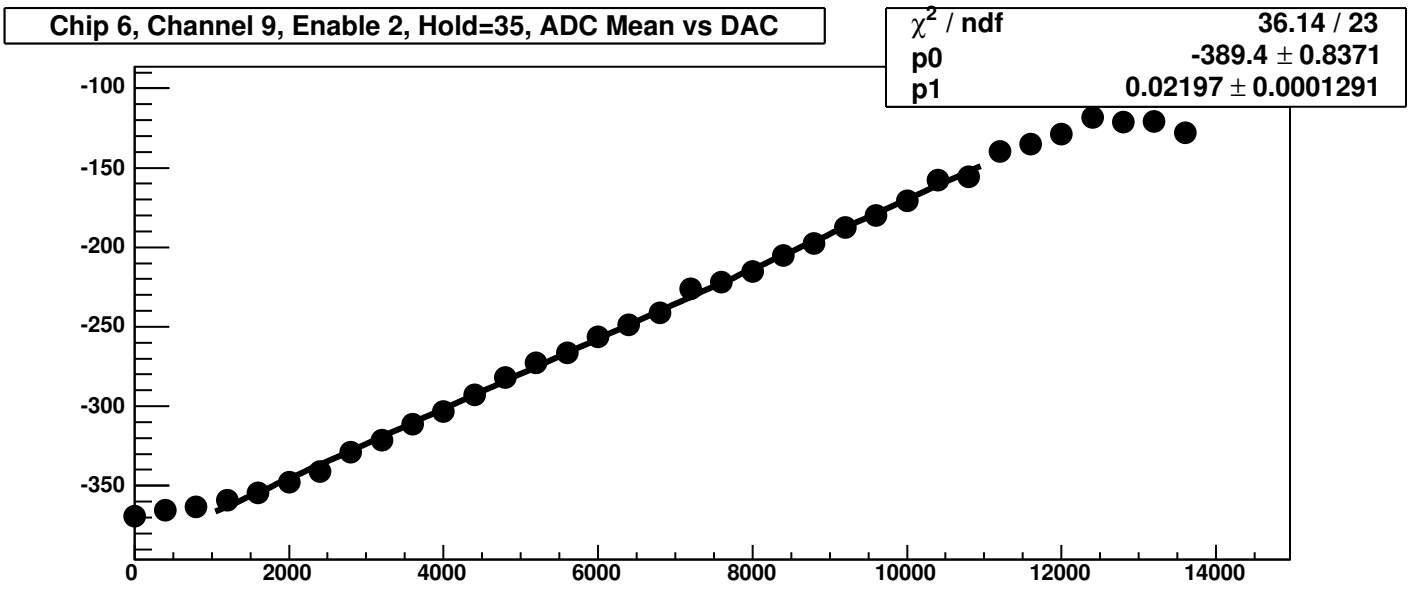


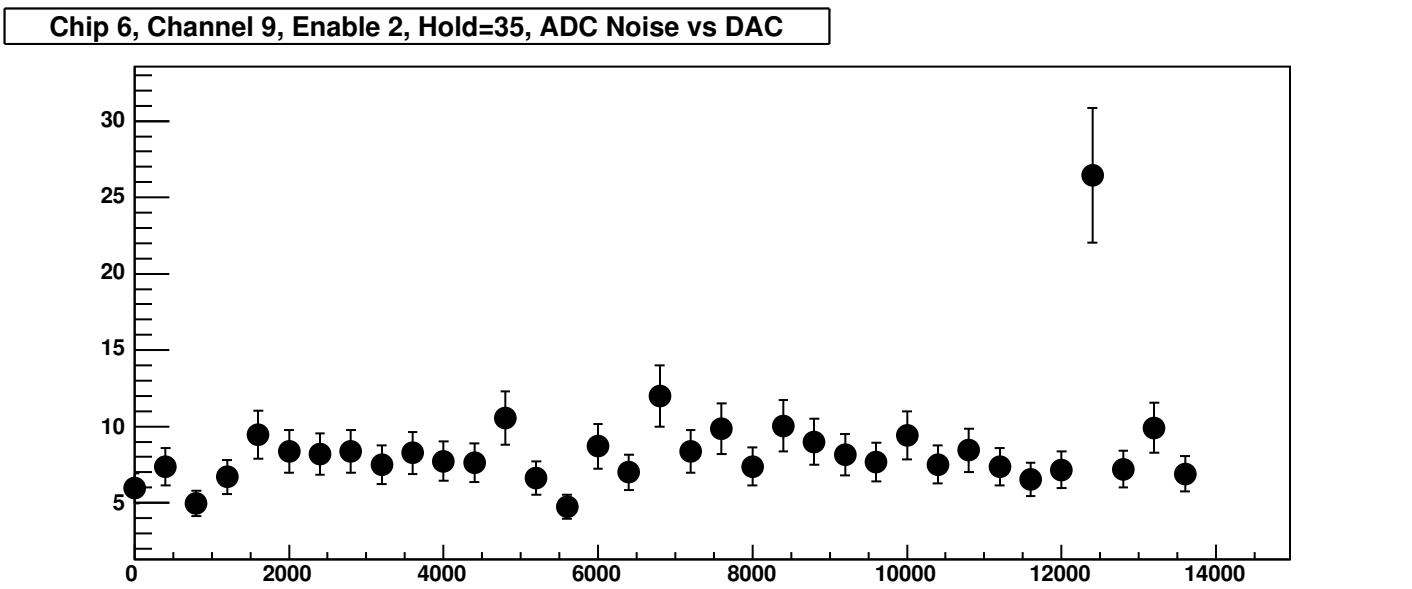
Chip 6, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

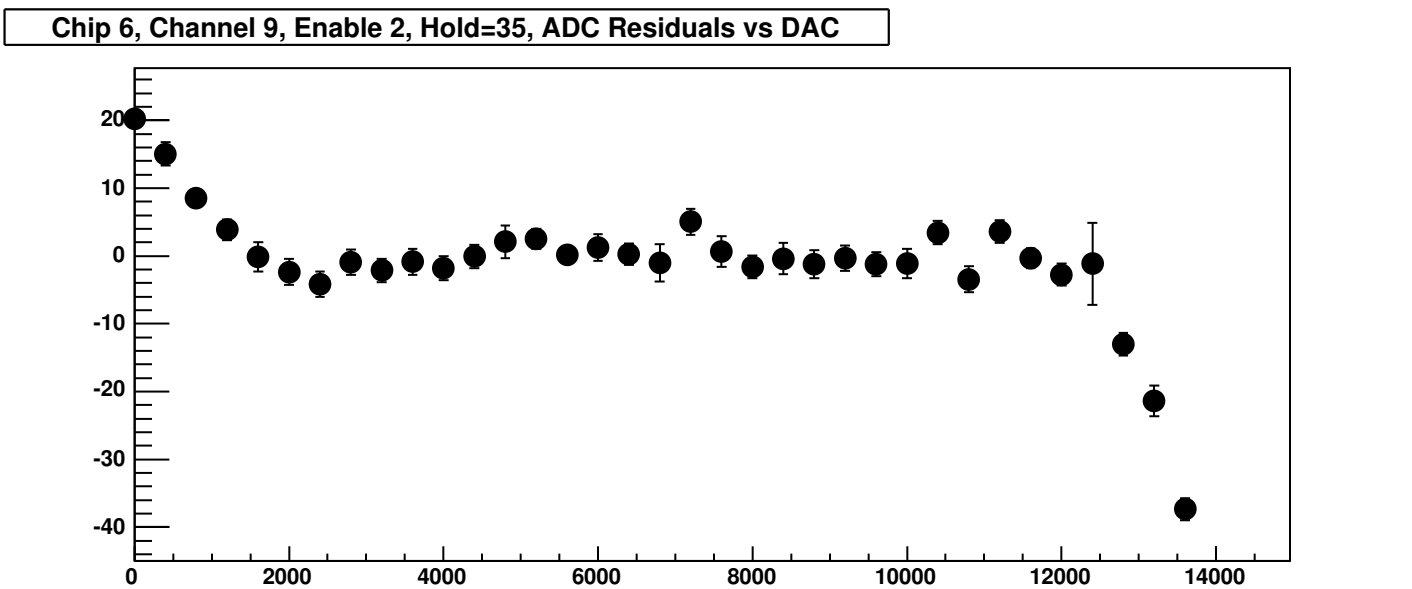


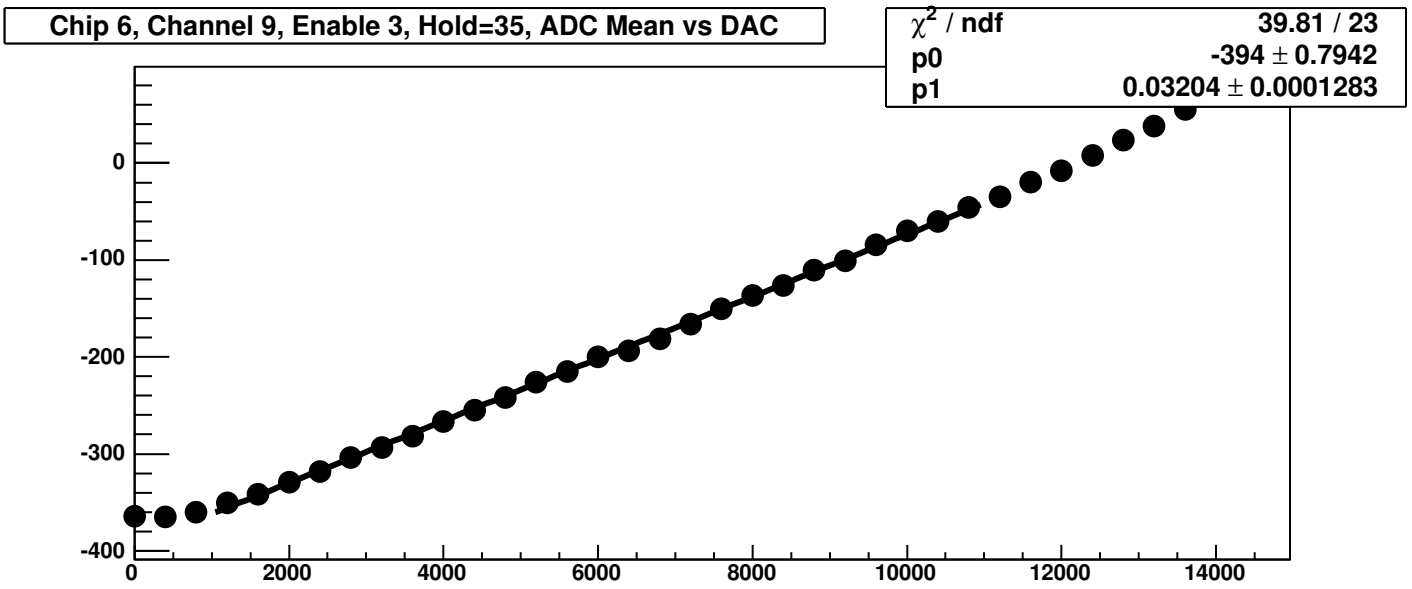
Chip 6, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

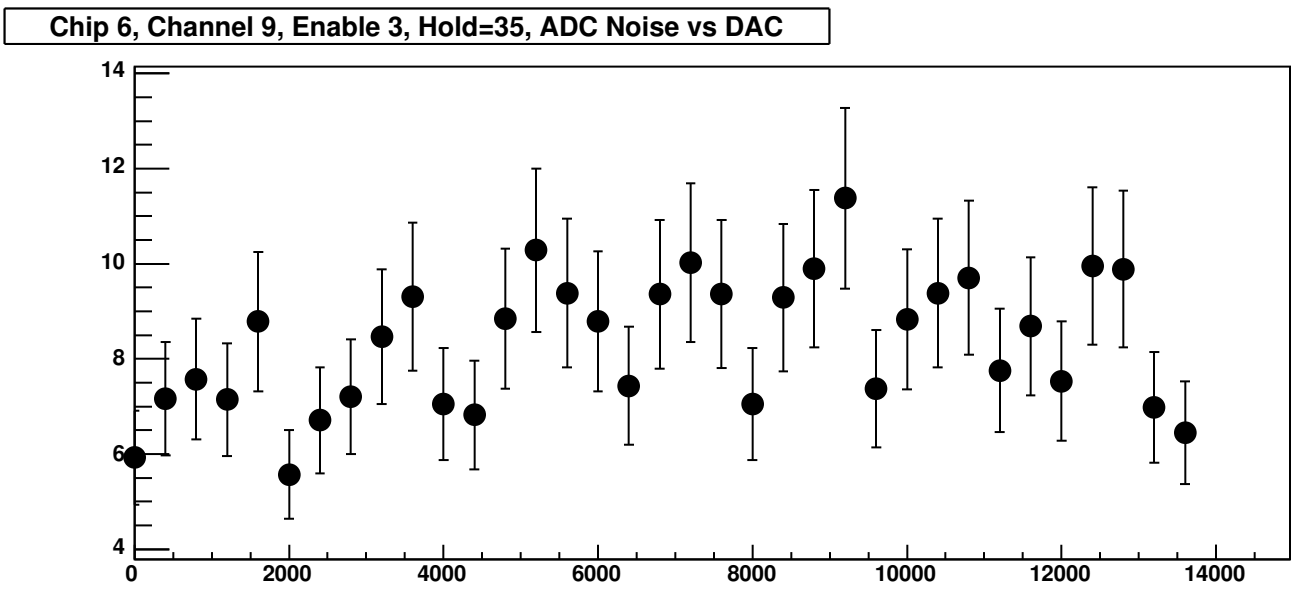


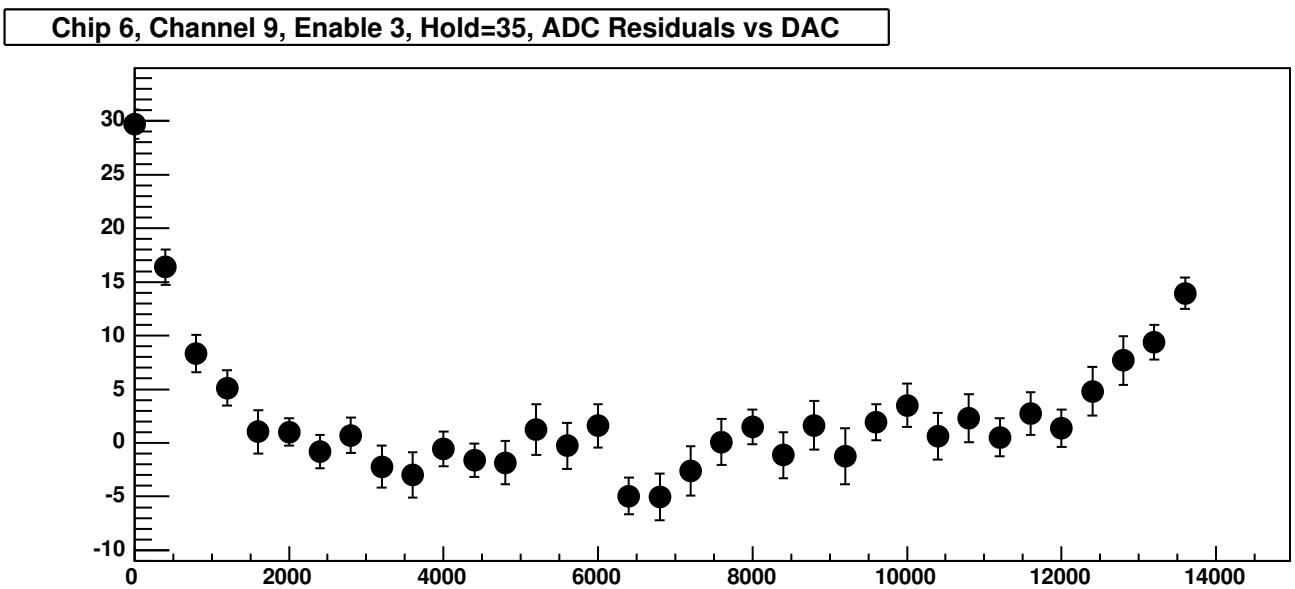

Chip 6, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.14 / 23
p0 -389.4 ± 0.8371
p1 0.02197 ± 0.0001291
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

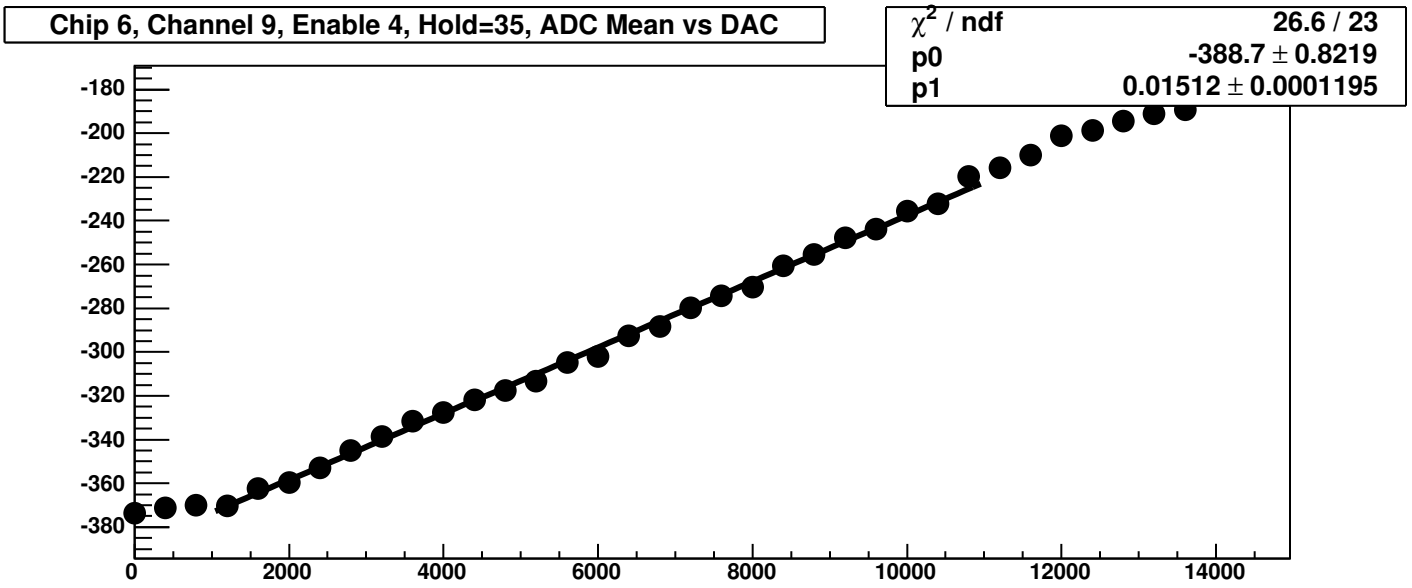

Chip 6, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

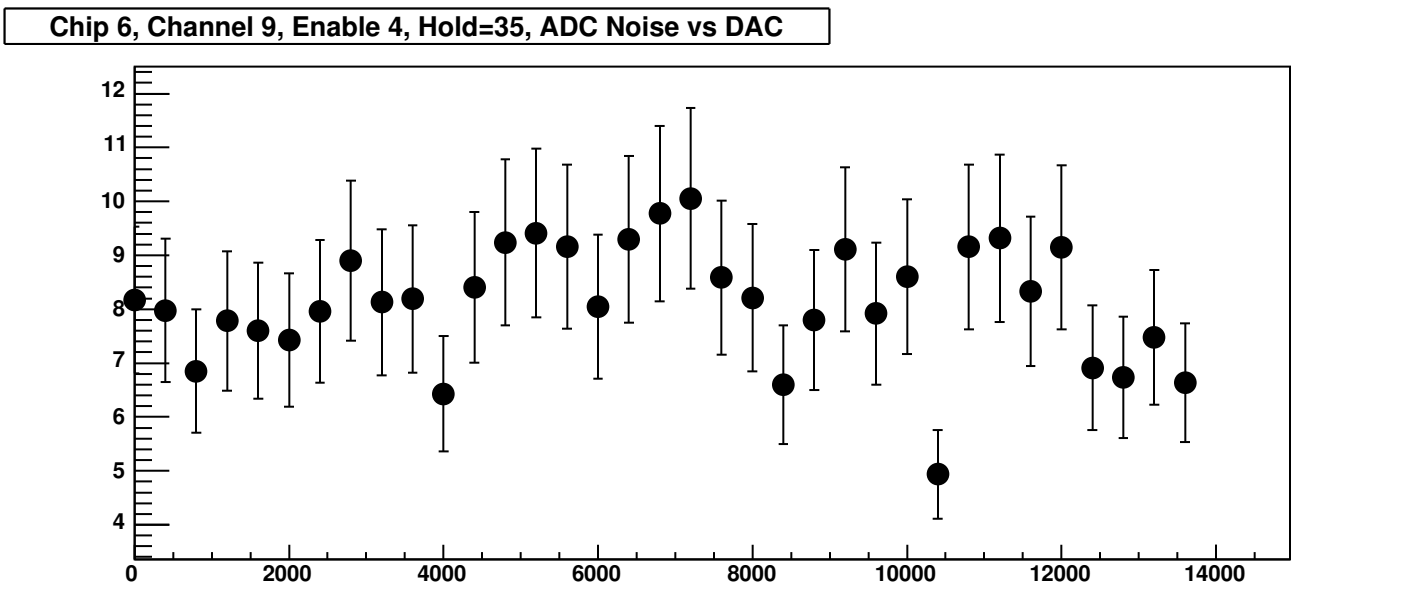

Chip 6, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

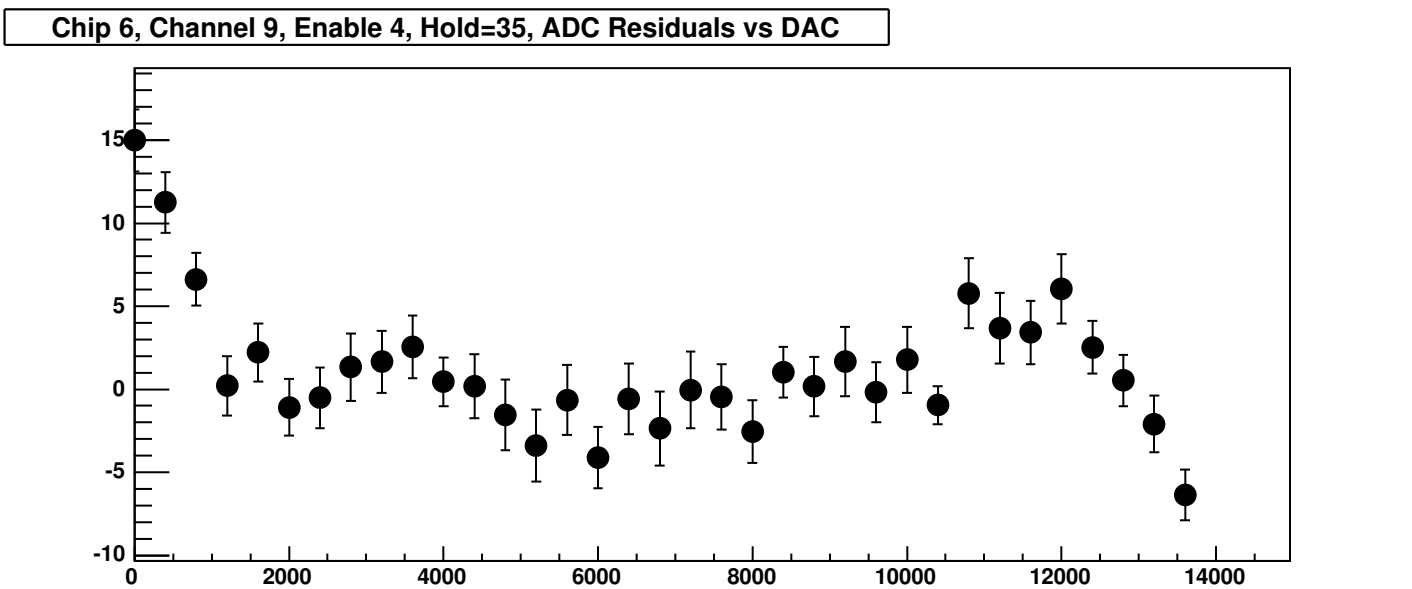

Chip 6, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.81 / 23
p0 -394 ± 0.7942
p1 0.03204 ± 0.0001283
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

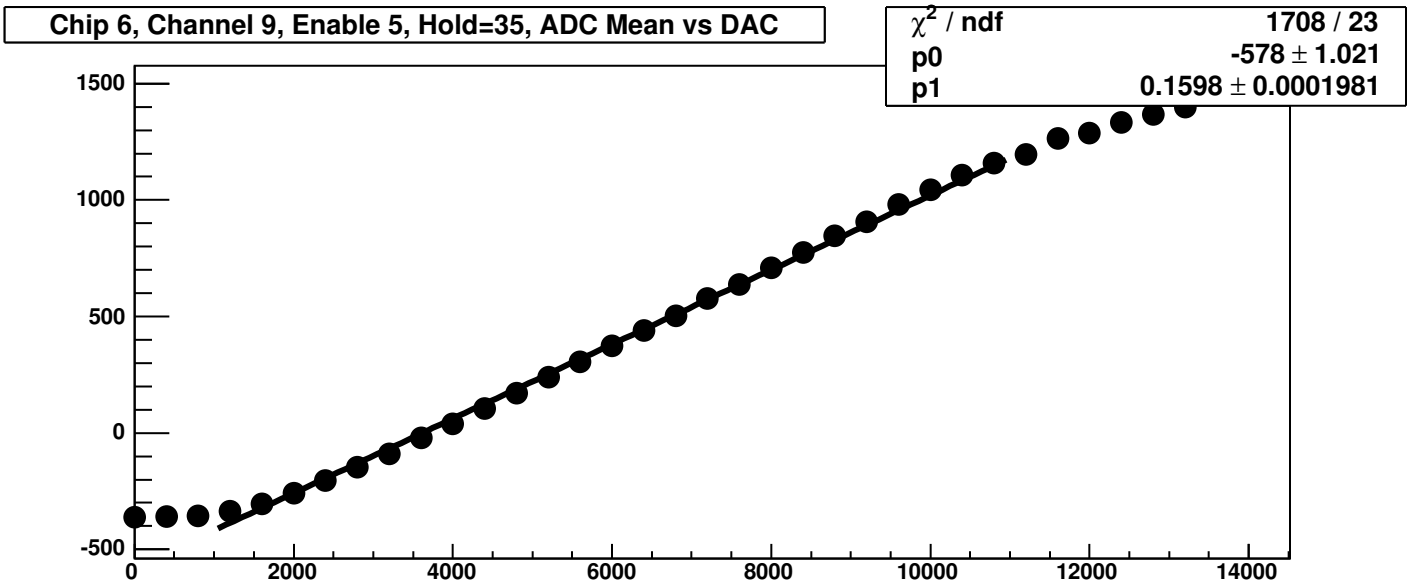

Chip 6, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

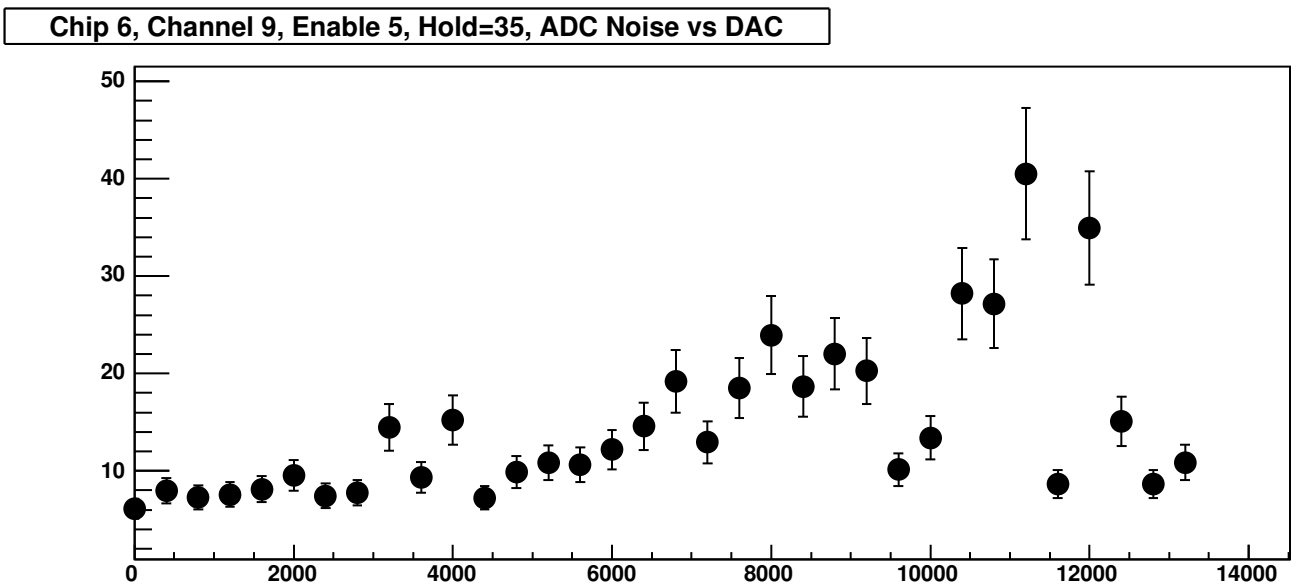

Chip 6, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

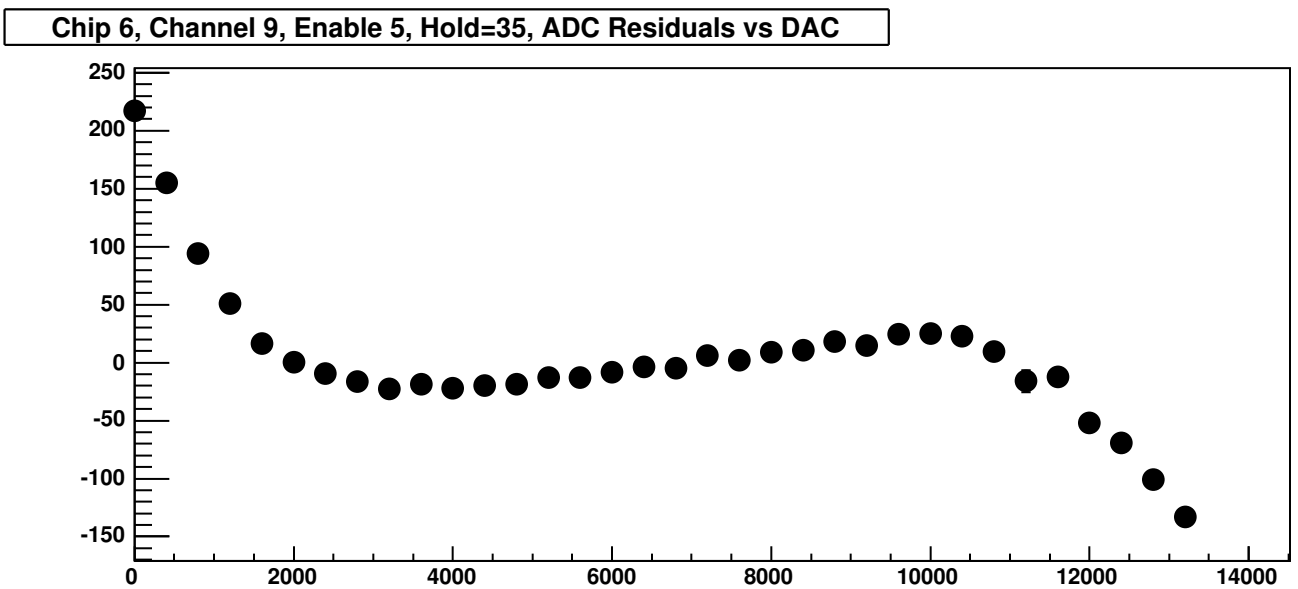

Chip 6, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.6 / 23
p0 -388.7 ± 0.8219
p1 0.01512 ± 0.0001195
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

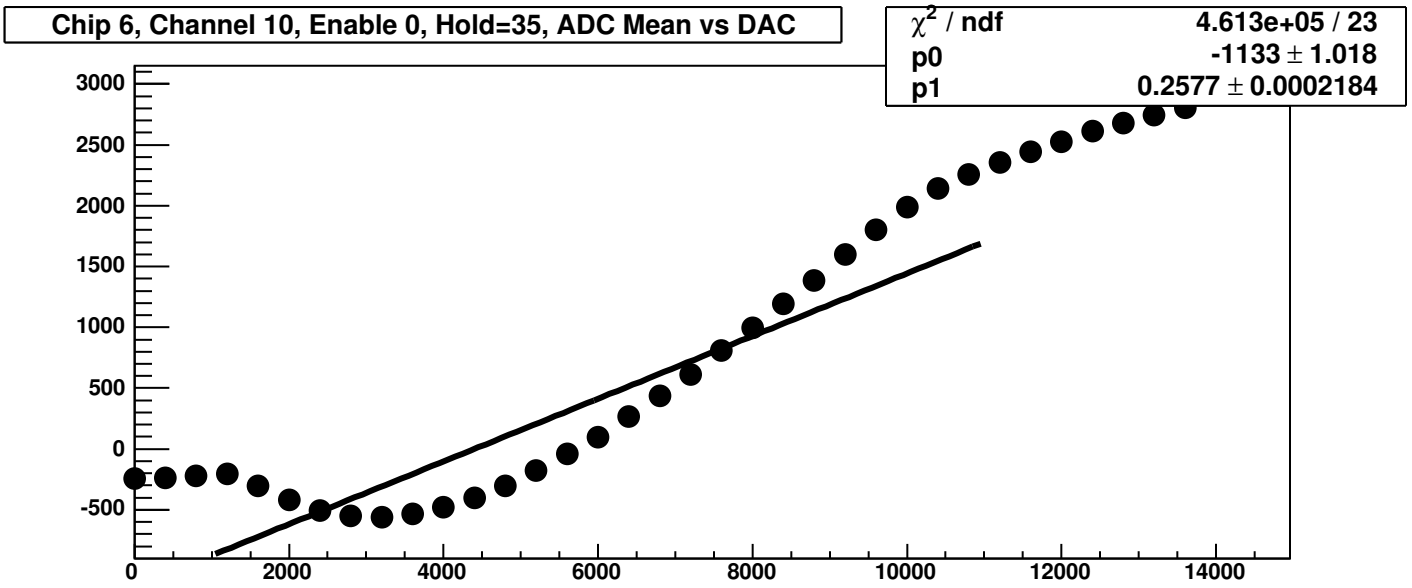

Chip 6, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

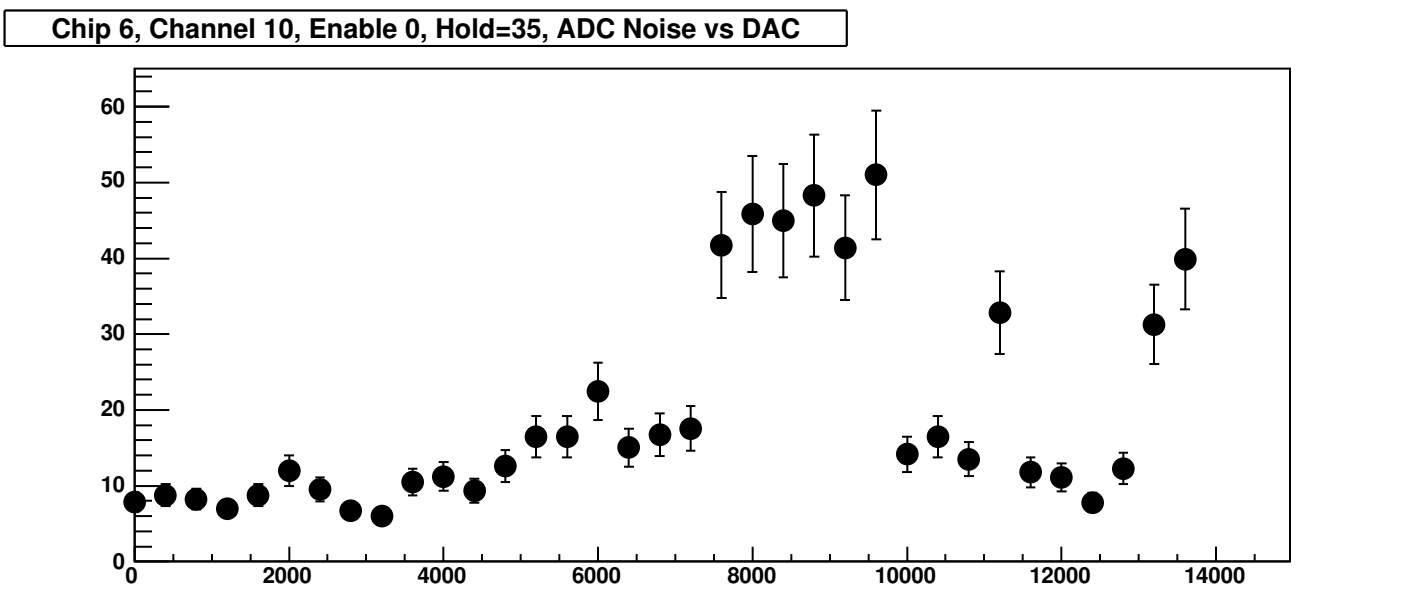

Chip 6, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

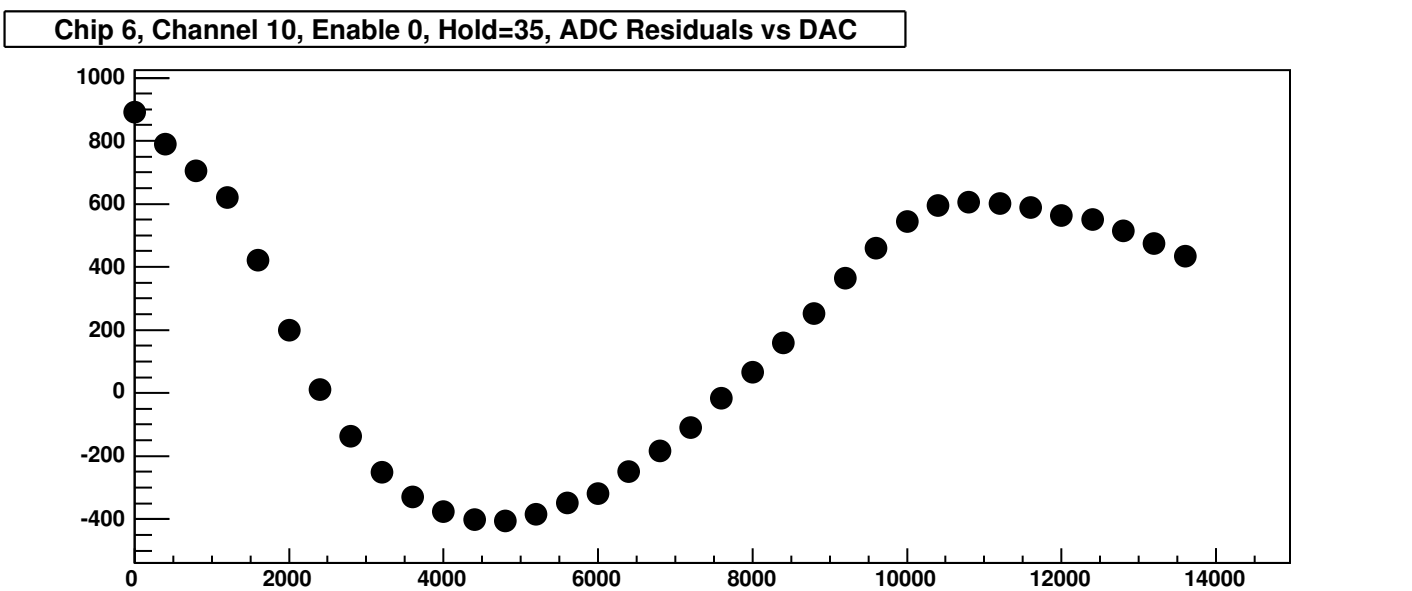

Chip 6, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1708 / 23
p0 -578 ± 1.021
p1 0.1598 ± 0.0001981
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

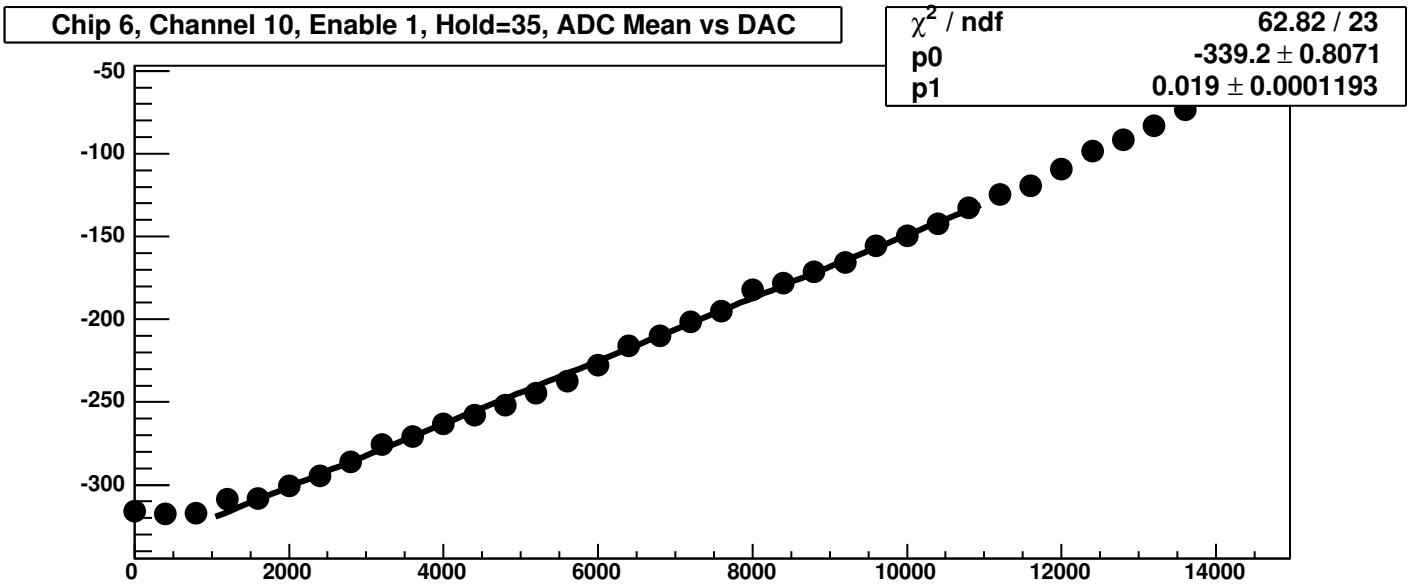

Chip 6, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

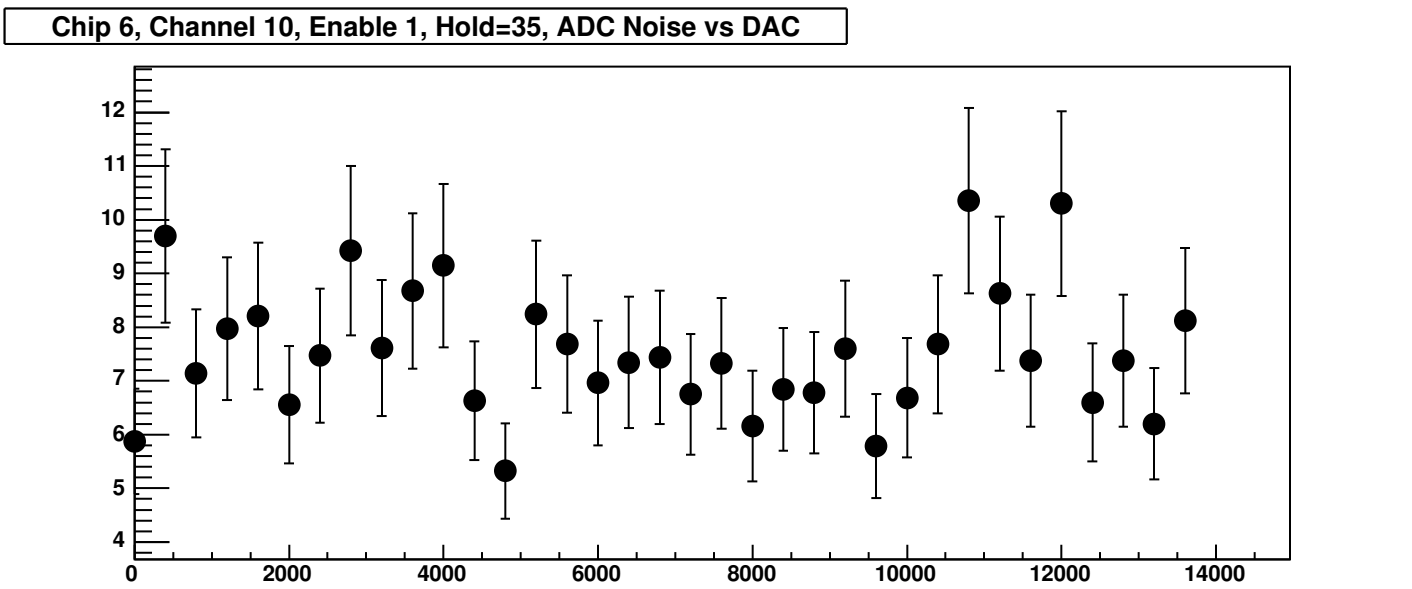

Chip 6, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

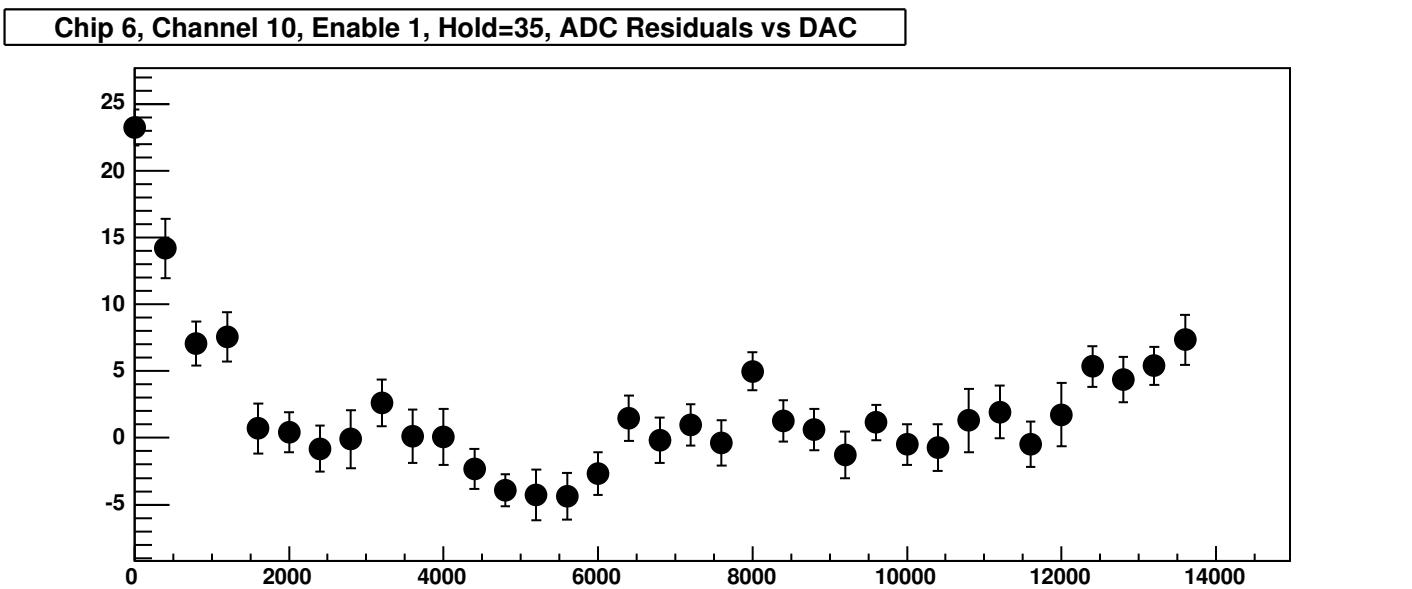

Chip 6, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.613e+05 / 23
p0 -1133 ± 1.018
p1 0.2577 ± 0.0002184
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

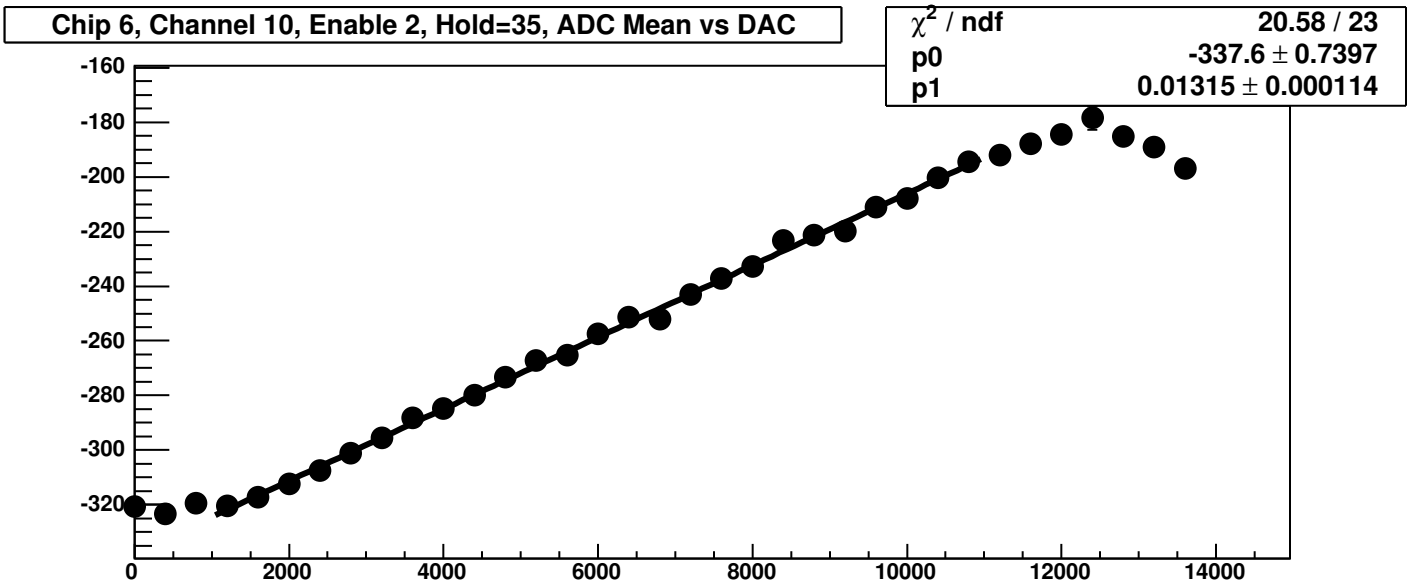

Chip 6, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

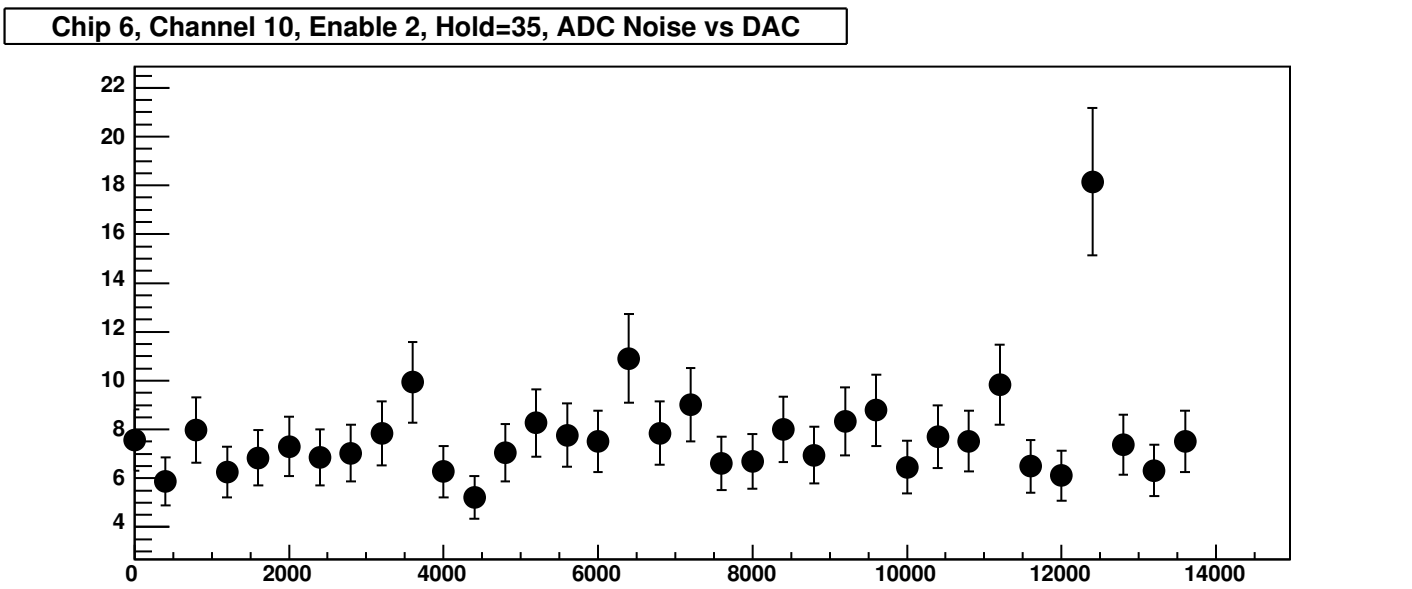

Chip 6, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

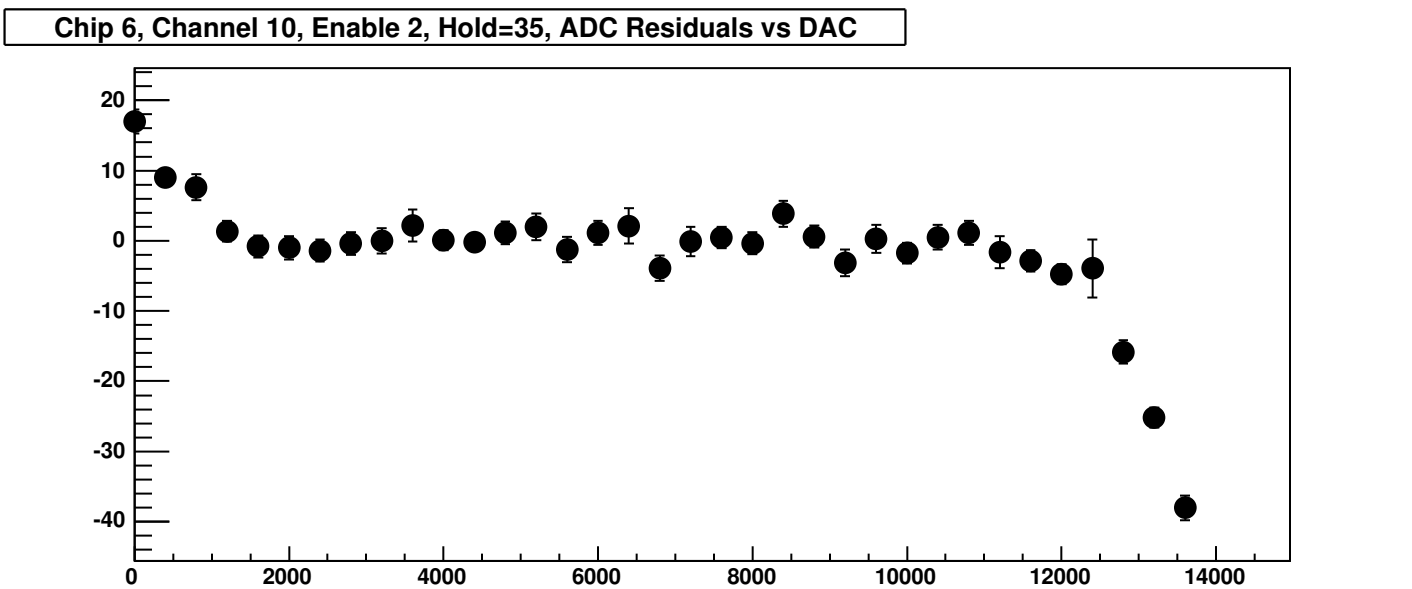

Chip 6, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 62.82 / 23
p0 -339.2 ± 0.8071
p1 0.019 ± 0.0001193
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

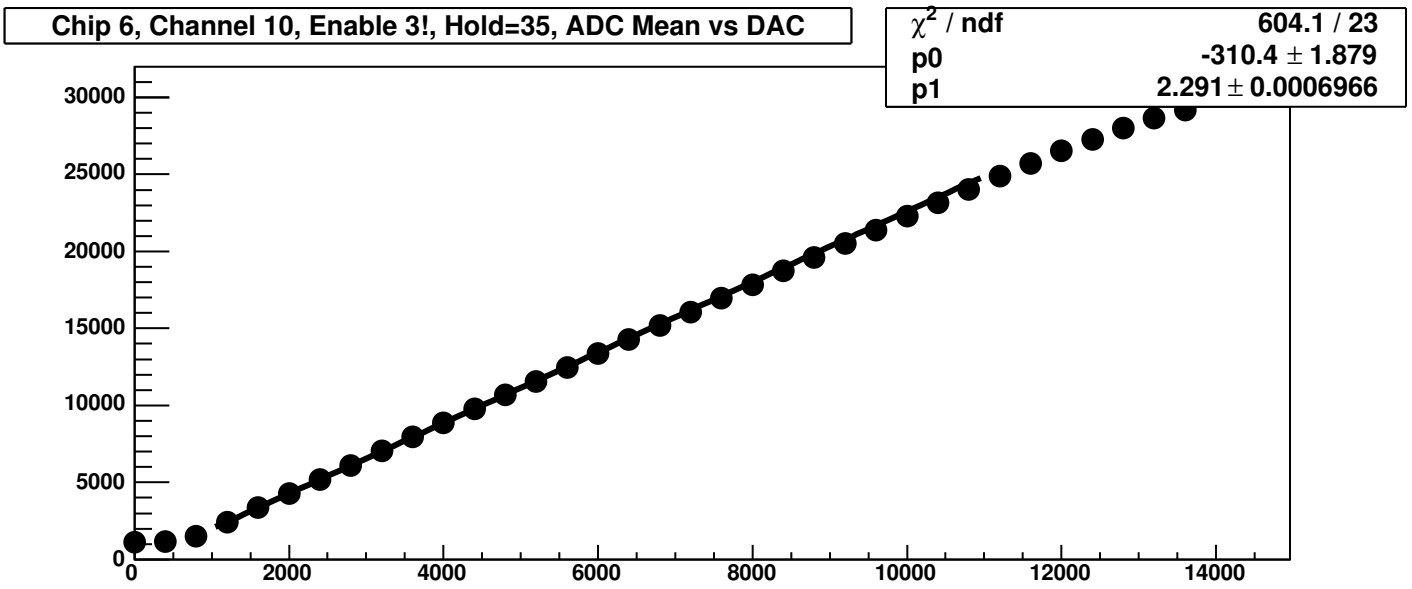

Chip 6, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

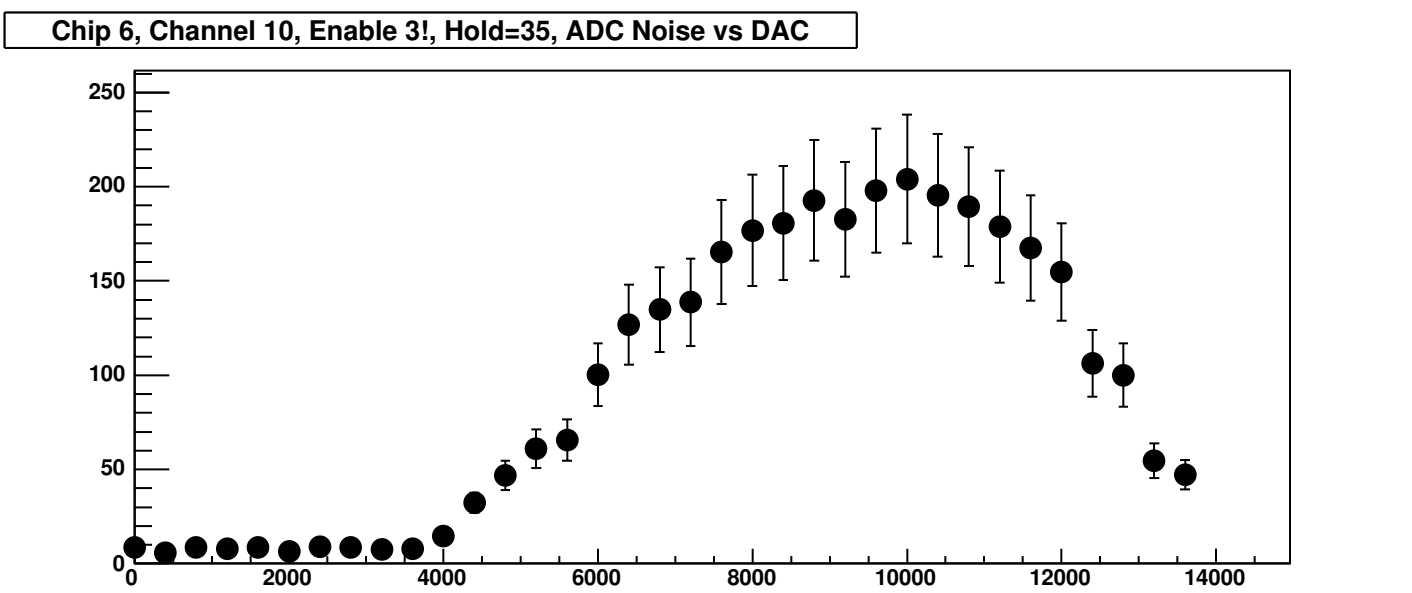

Chip 6, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

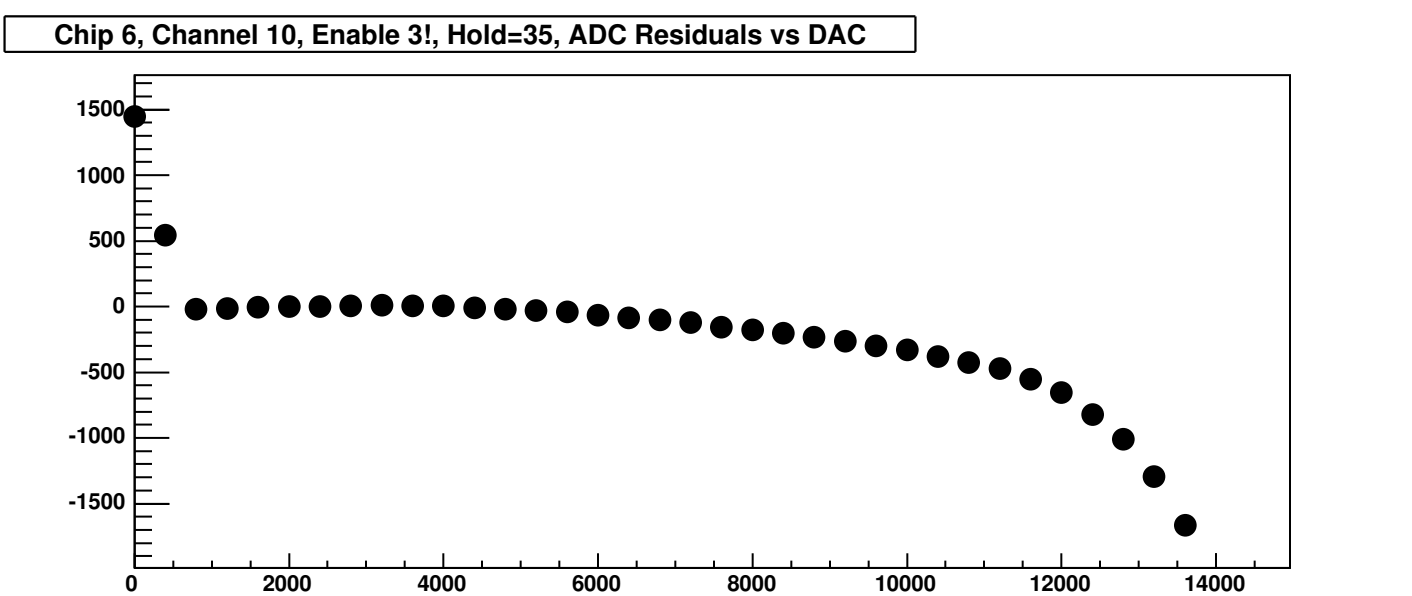

Chip 6, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.58 / 23
p0 -337.6 ± 0.7397
p1 0.01315 ± 0.000114
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

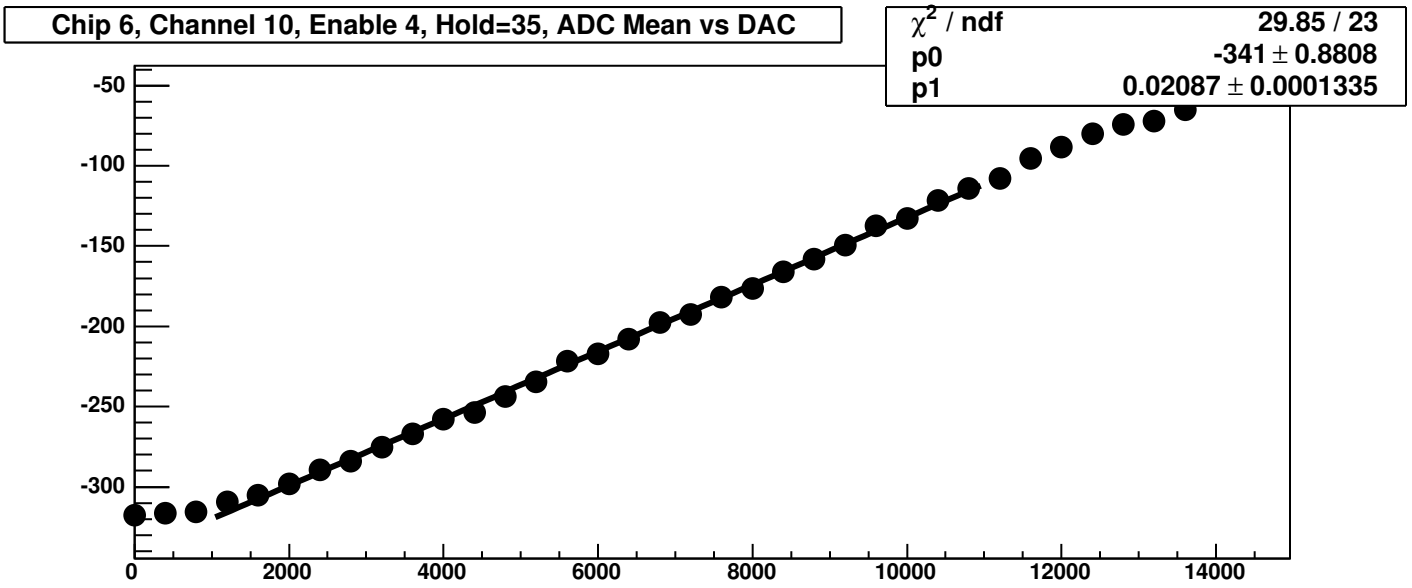

Chip 6, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

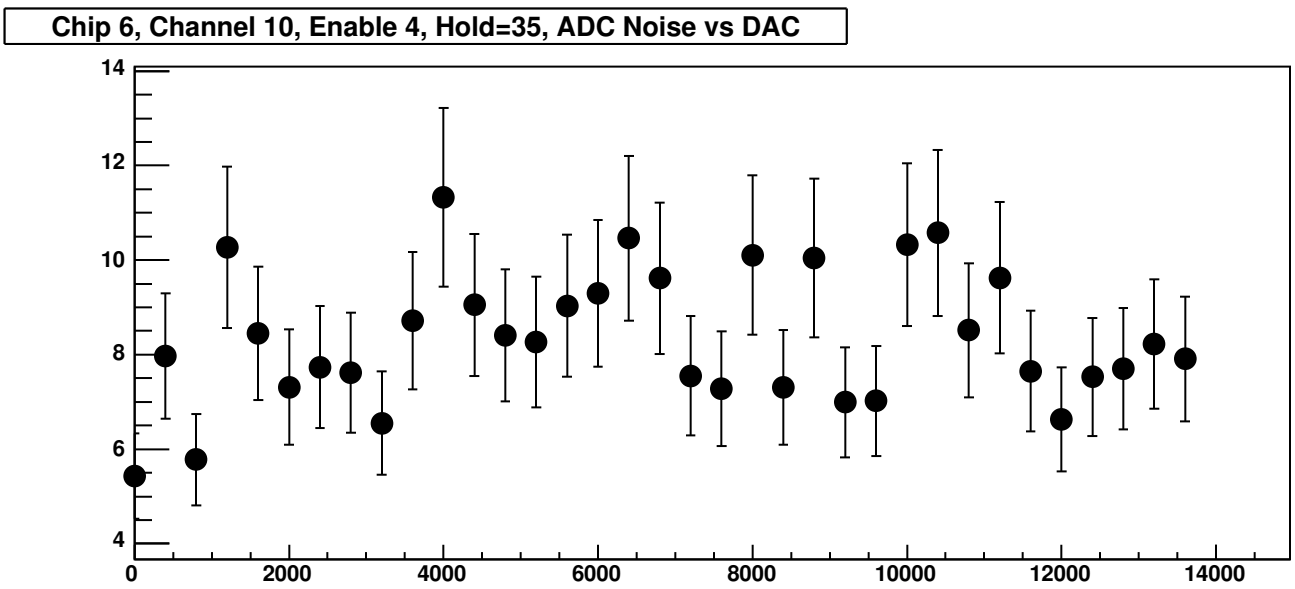

Chip 6, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

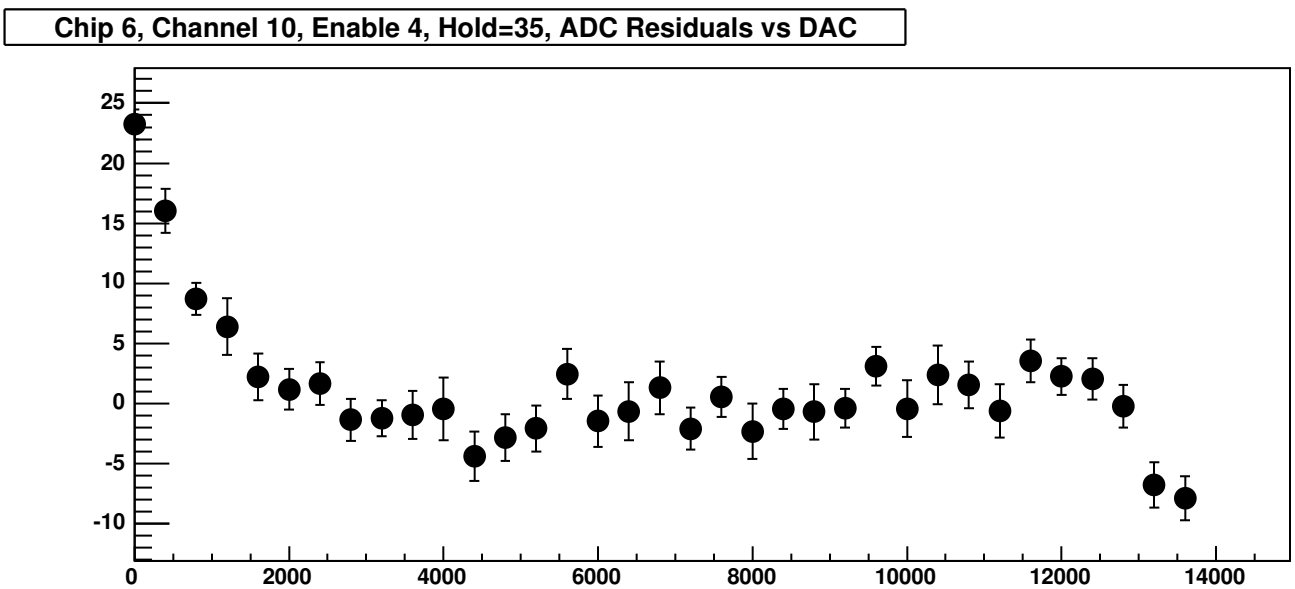

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 604.1 / 23
p0 -310.4 ± 1.879
p1 2.291 ± 0.0006966
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

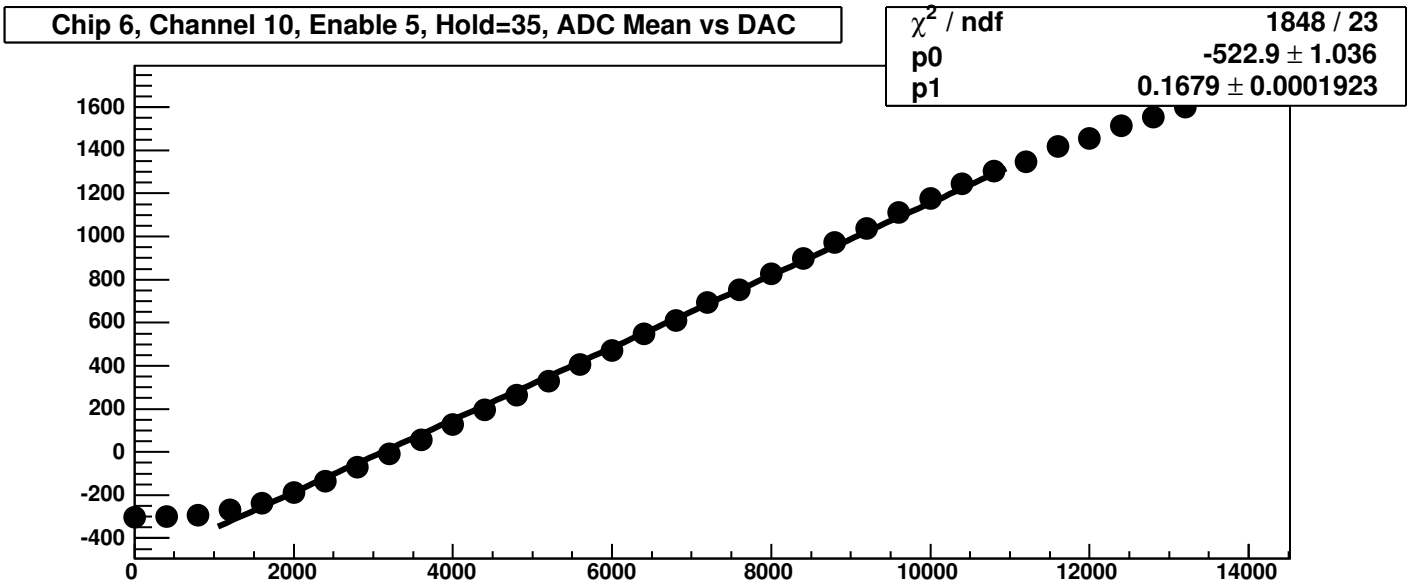

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

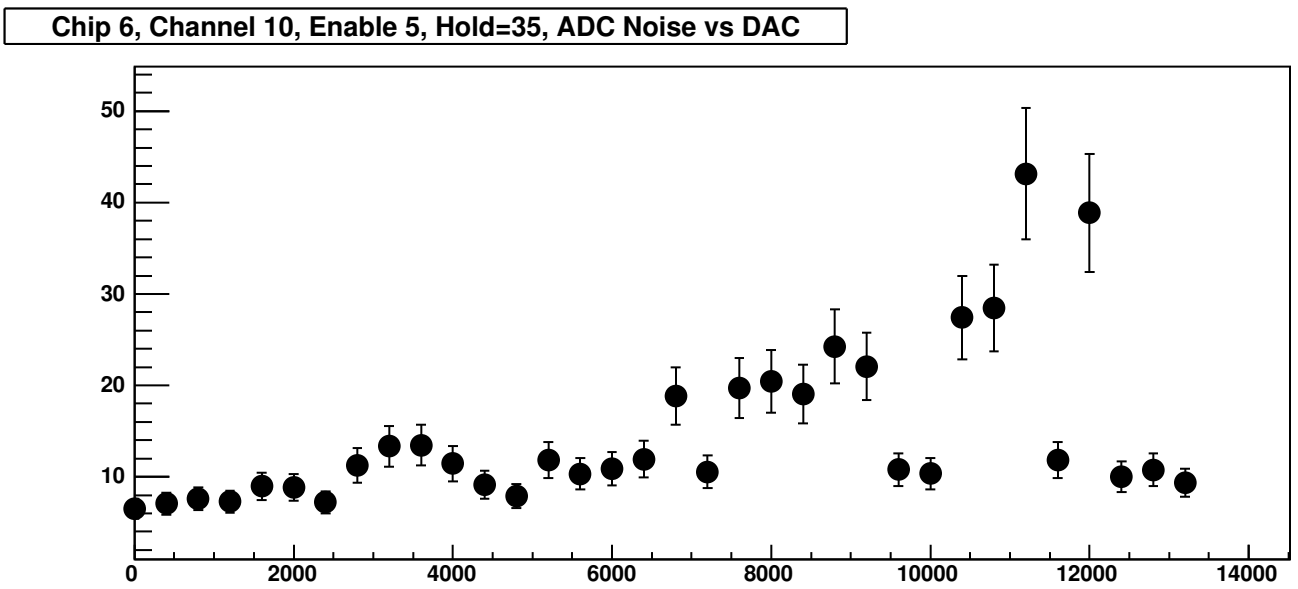

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

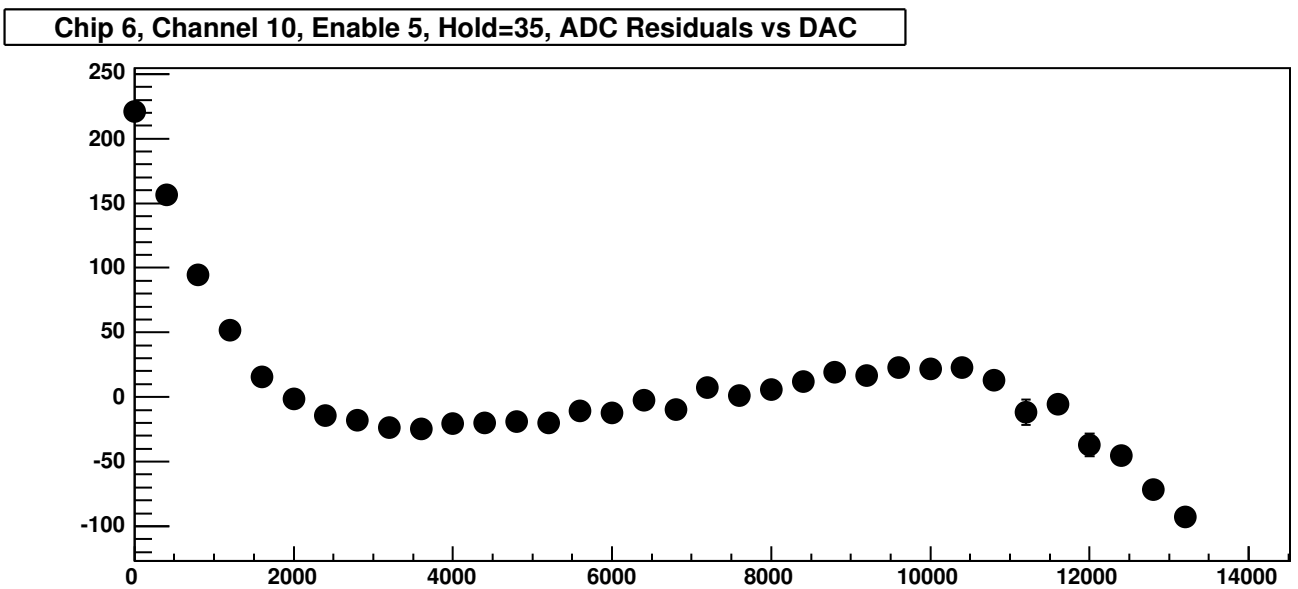

Chip 6, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.85 / 23
p0 -341 ± 0.8808
p1 0.02087 ± 0.0001335
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

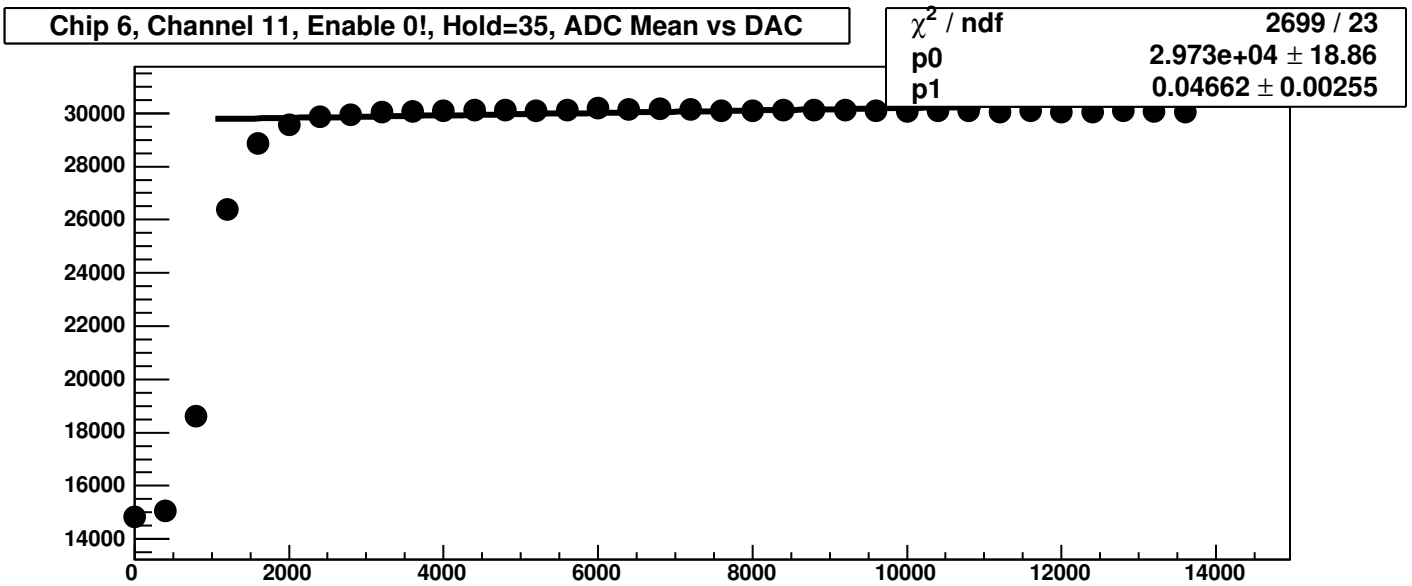

Chip 6, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

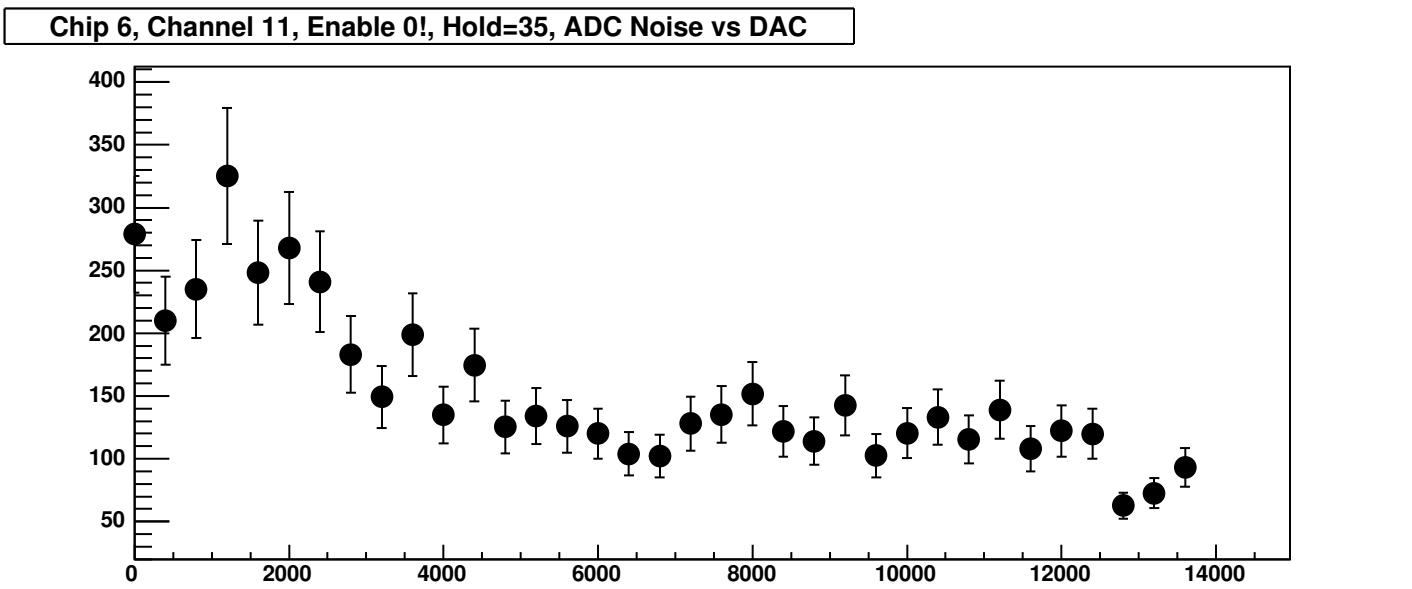

Chip 6, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

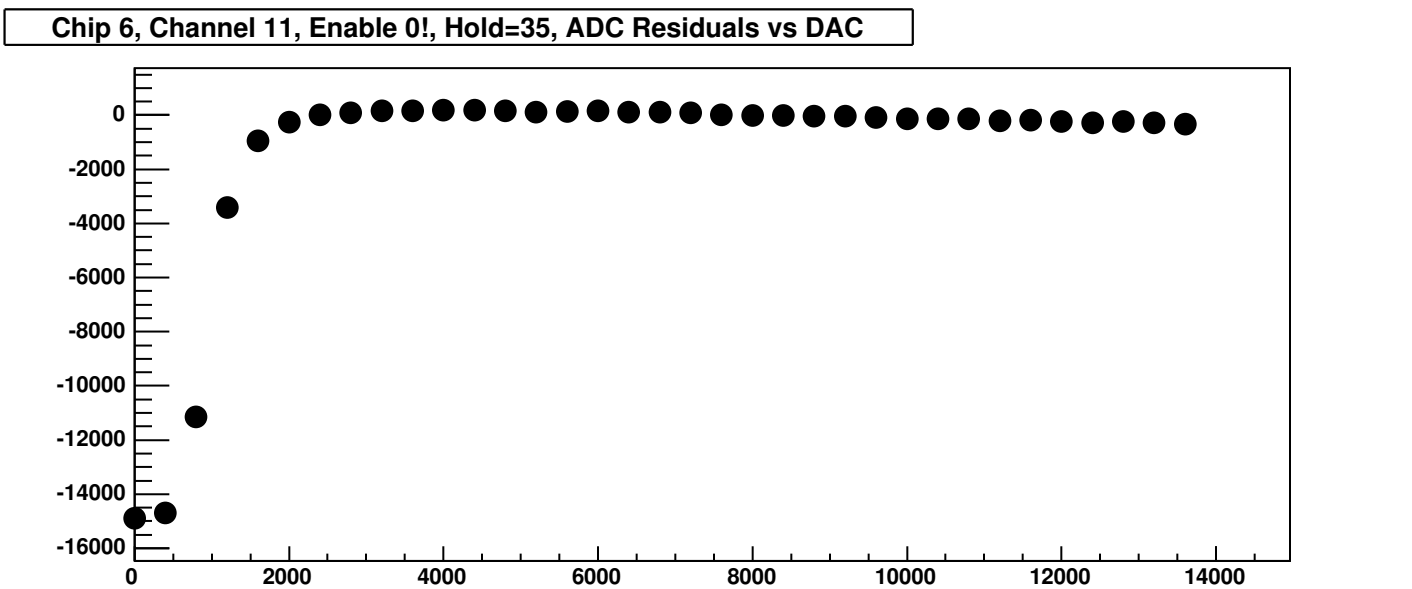

Chip 6, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1848 / 23
p0 -522.9 ± 1.036
p1 0.1679 ± 0.0001923
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

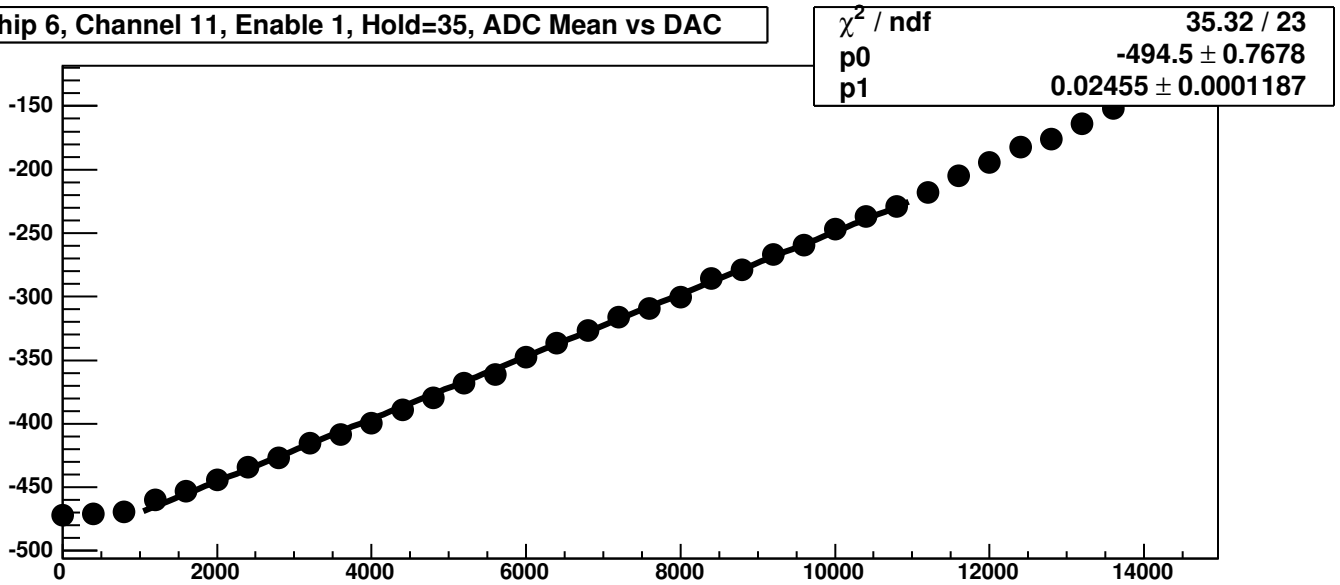

Chip 6, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2699 / 23
p0 2.973e+04 ± 18.86
p1 0.04662 ± 0.00255
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

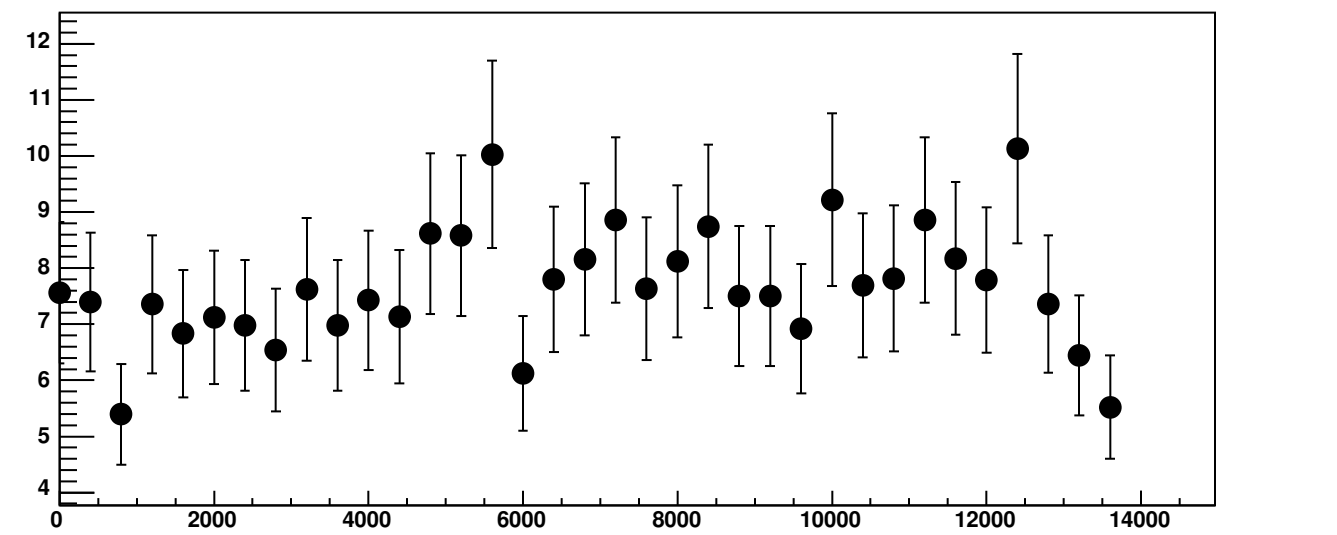

Chip 6, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

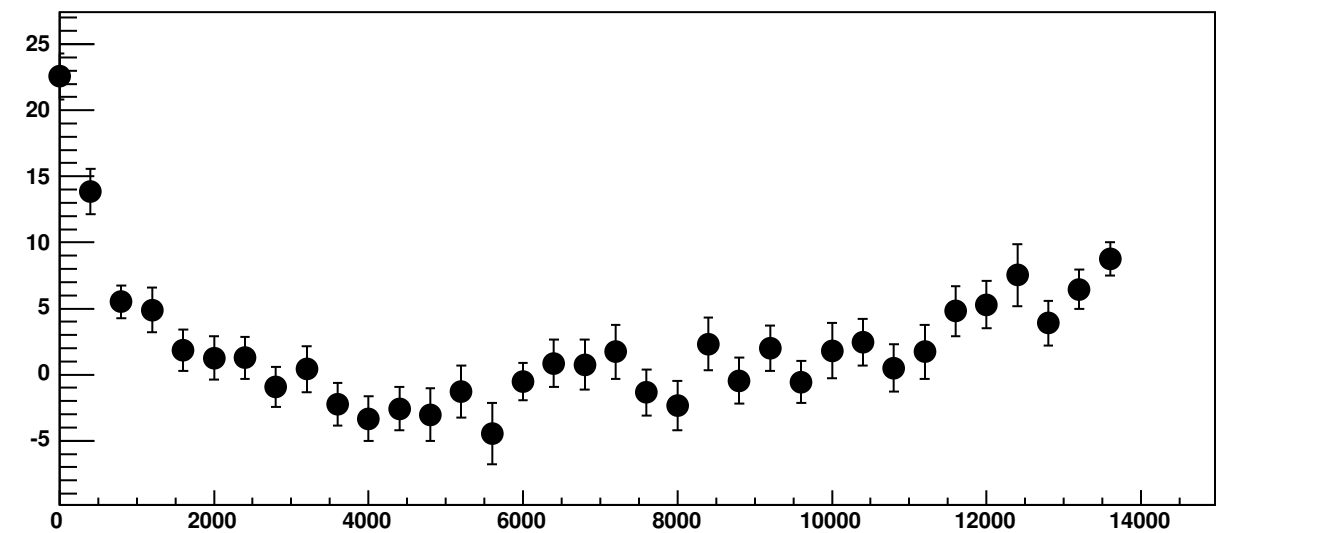
Chip 6, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

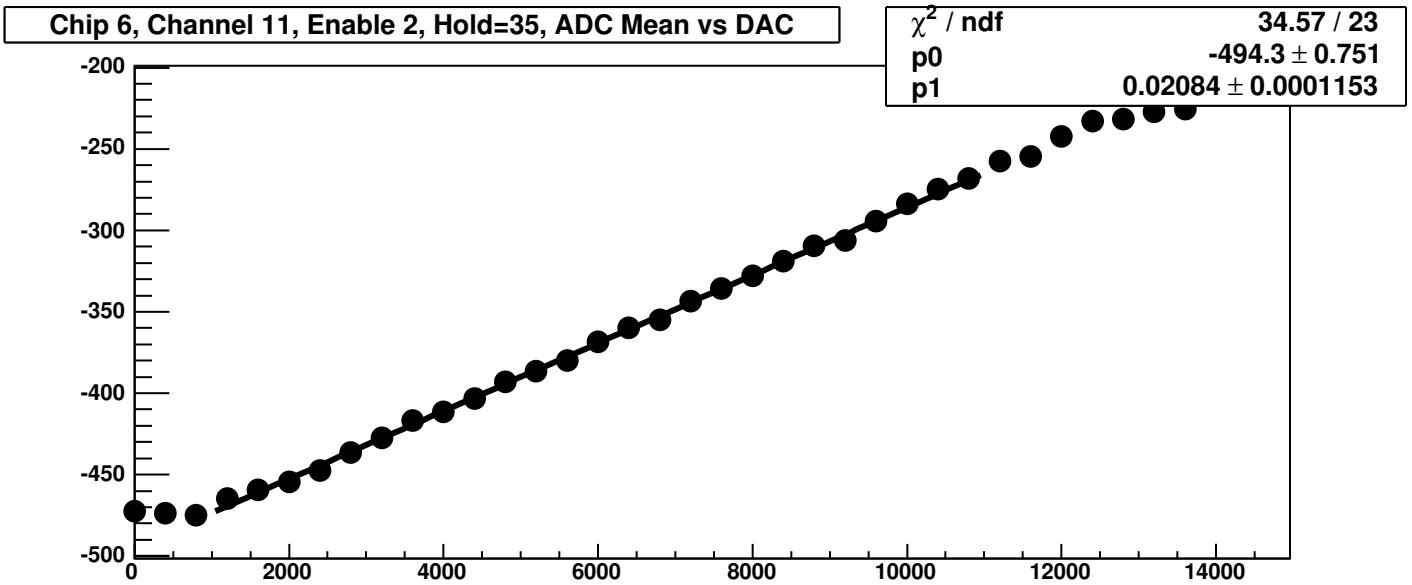


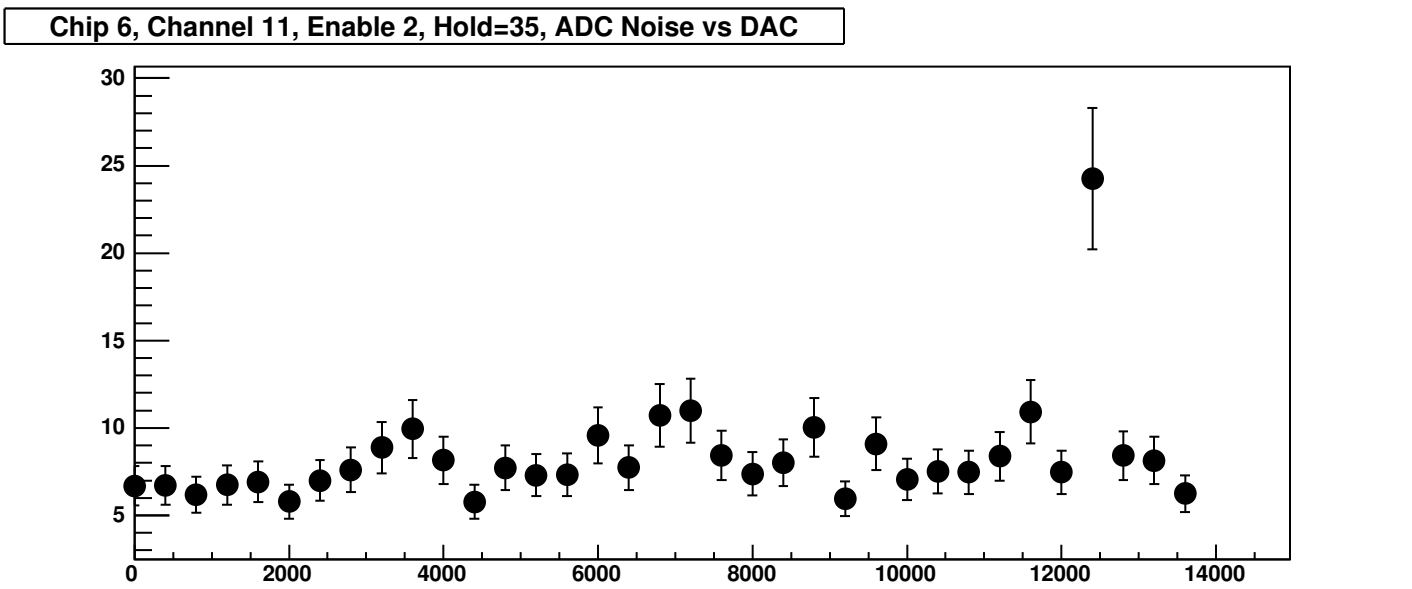
Chip 6, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

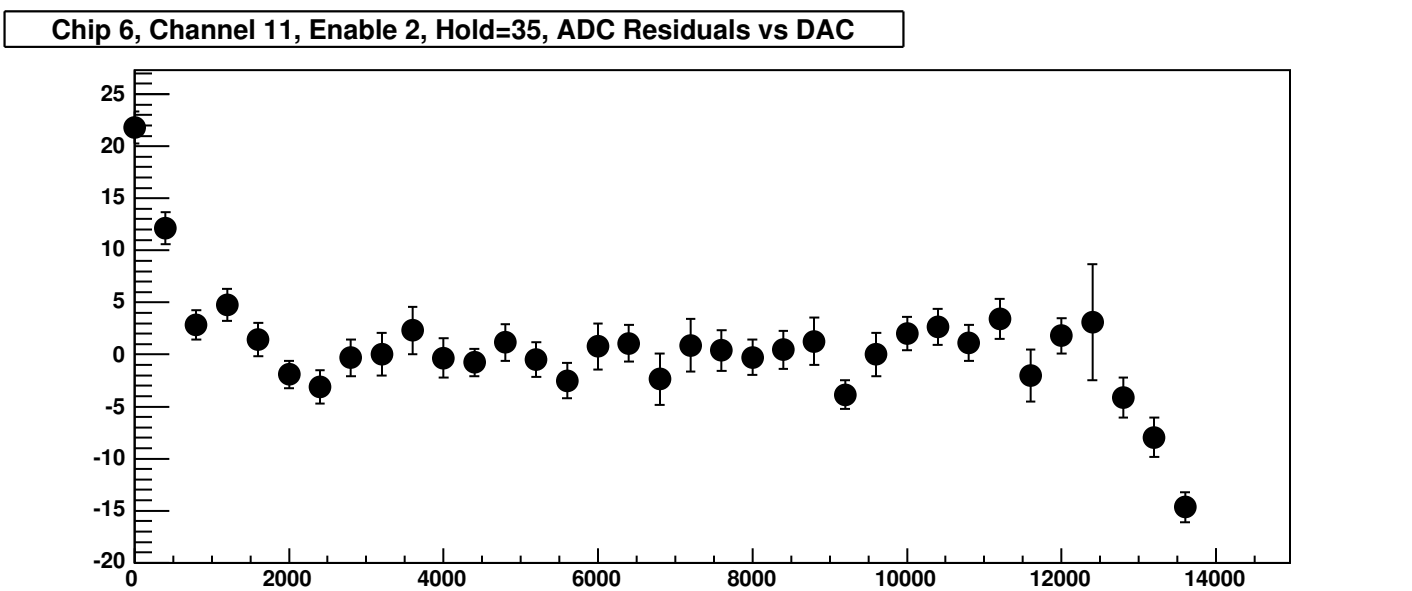


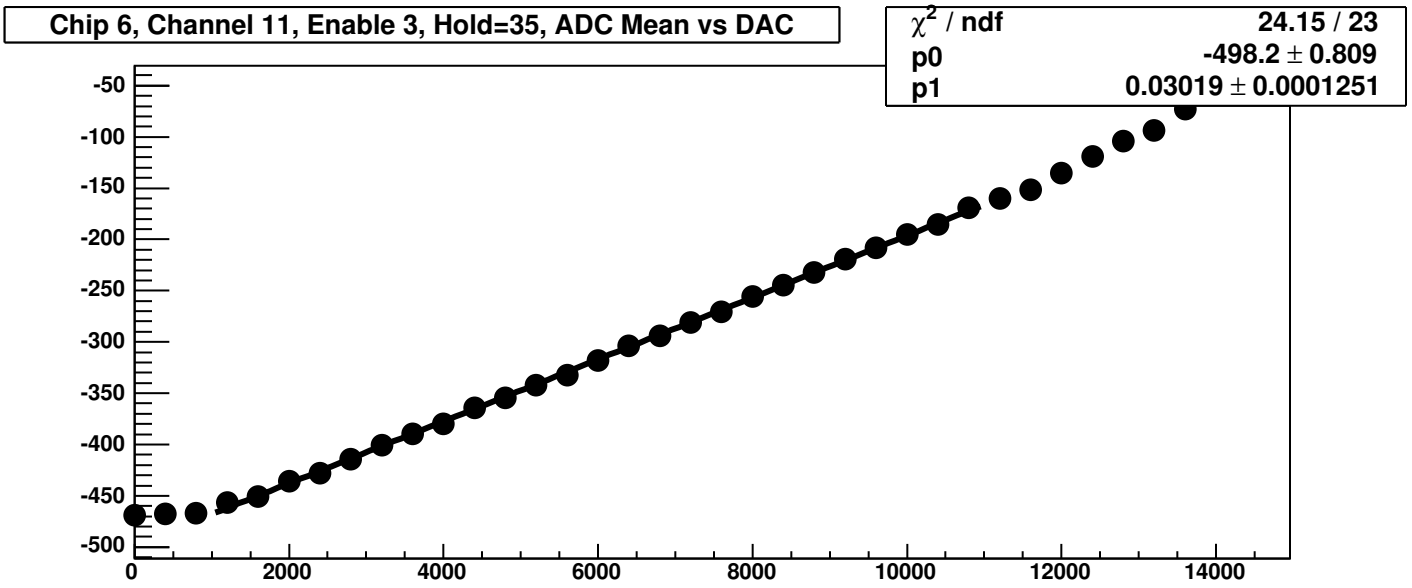
Chip 6, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

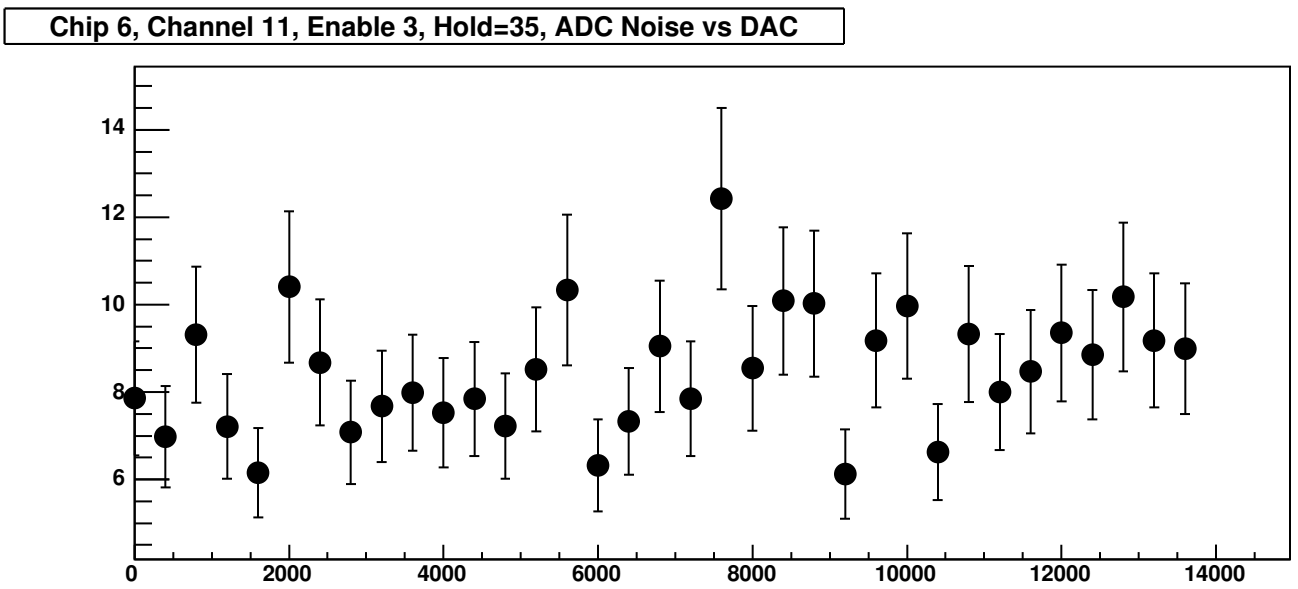


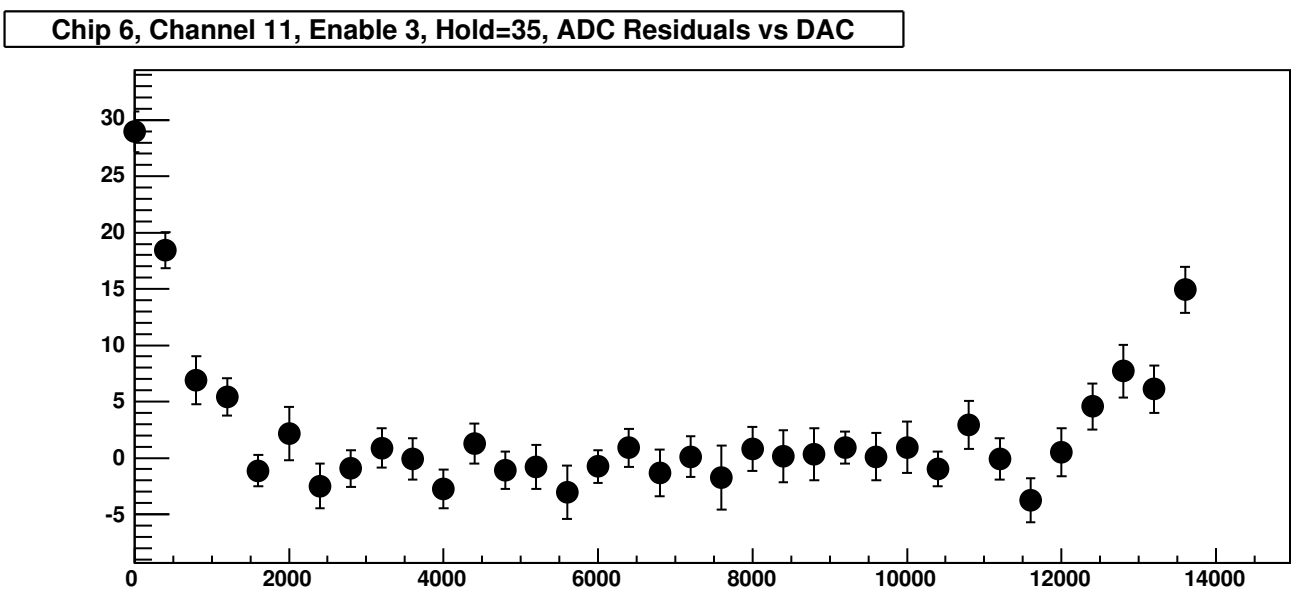

Chip 6, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.57 / 23
p0 -494.3 ± 0.751
p1 0.02084 ± 0.0001153
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

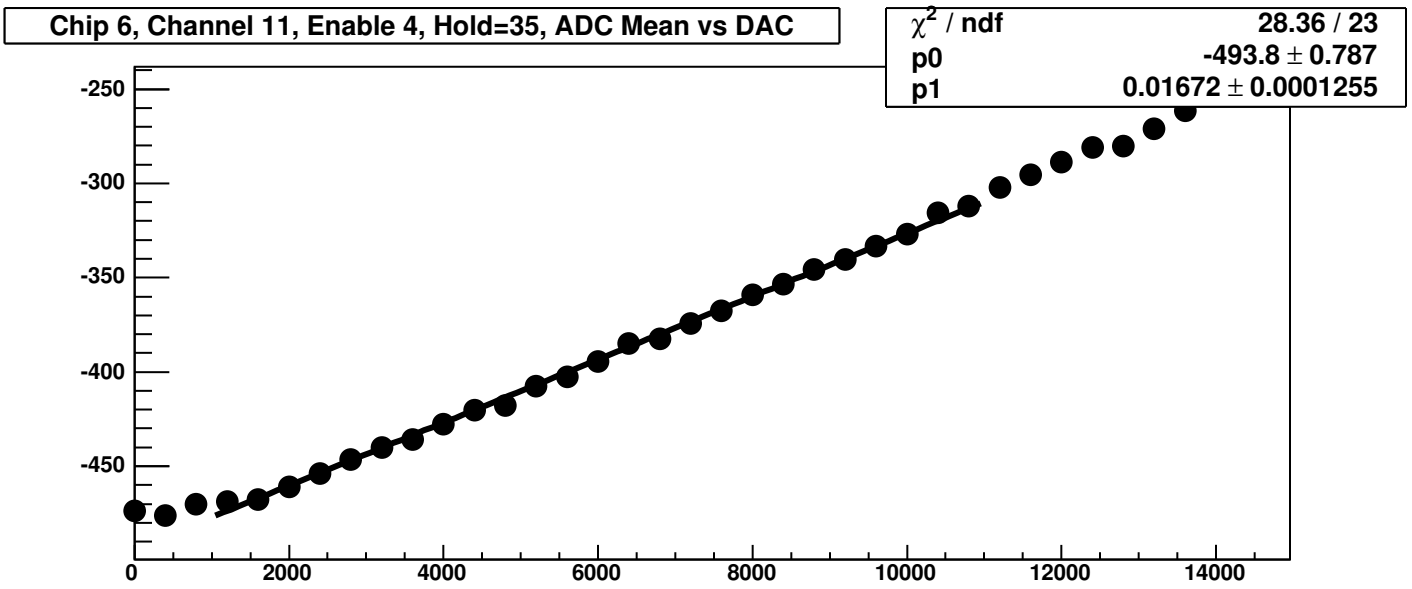

Chip 6, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

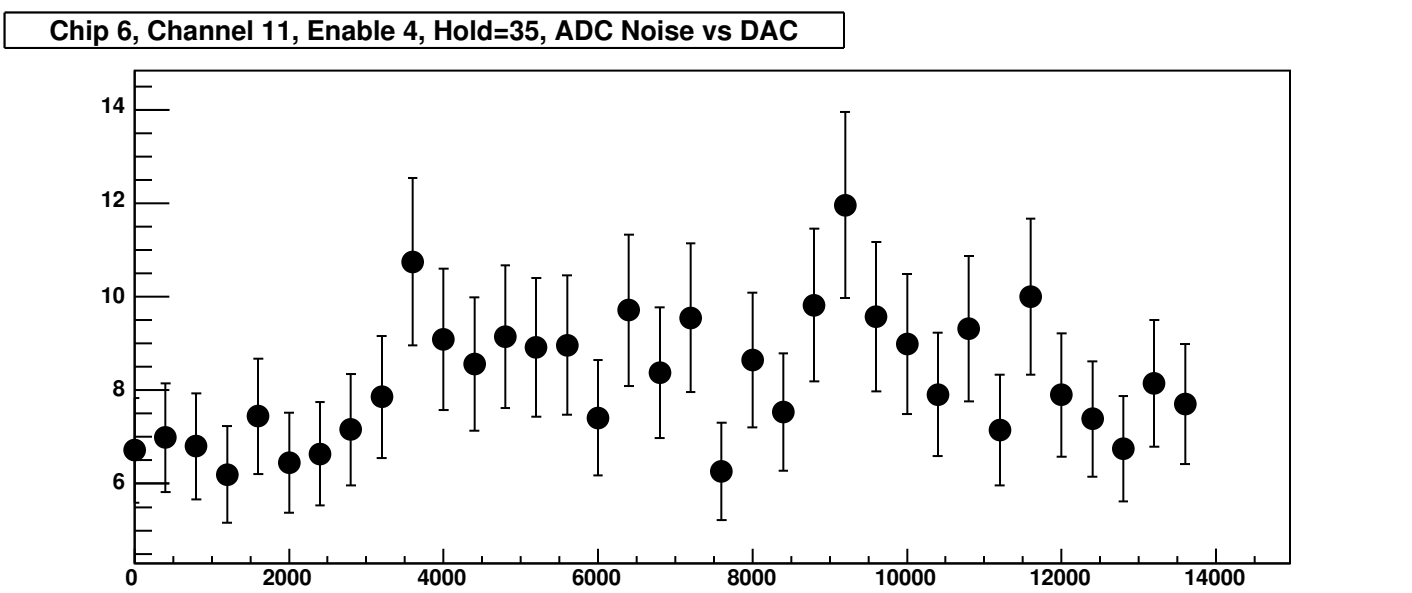

Chip 6, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

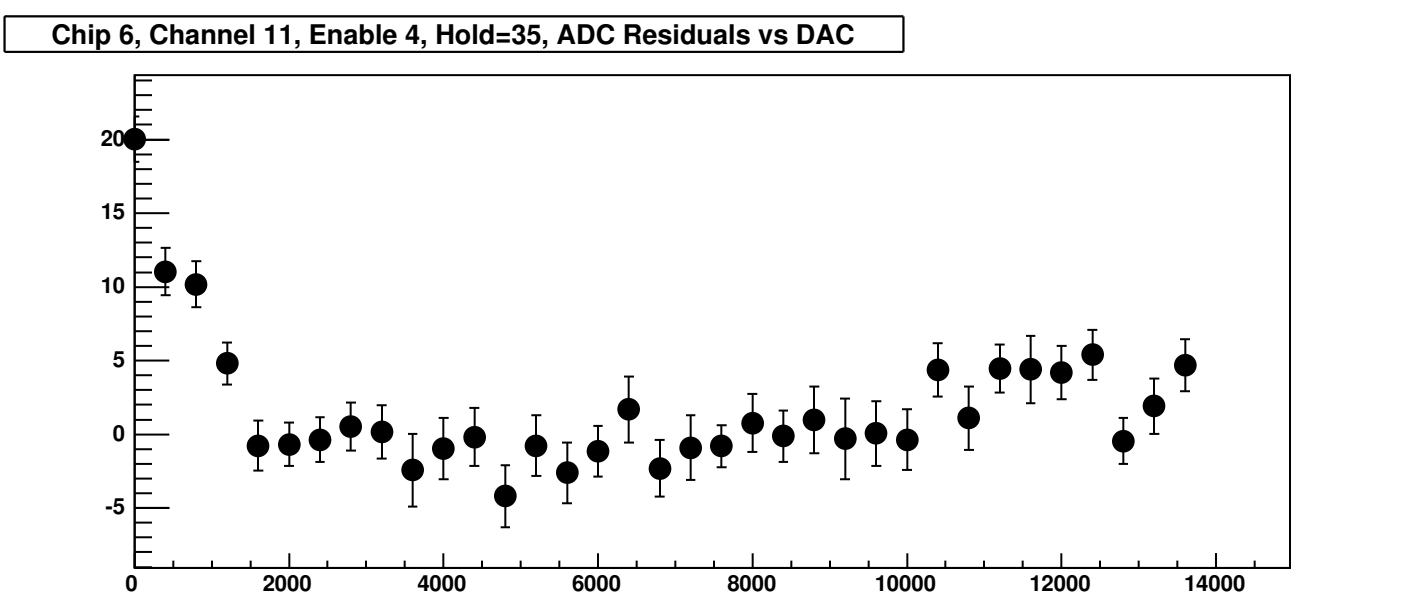

Chip 6, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.15 / 23
p0 -498.2 ± 0.809
p1 0.03019 ± 0.0001251
-50
-100
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

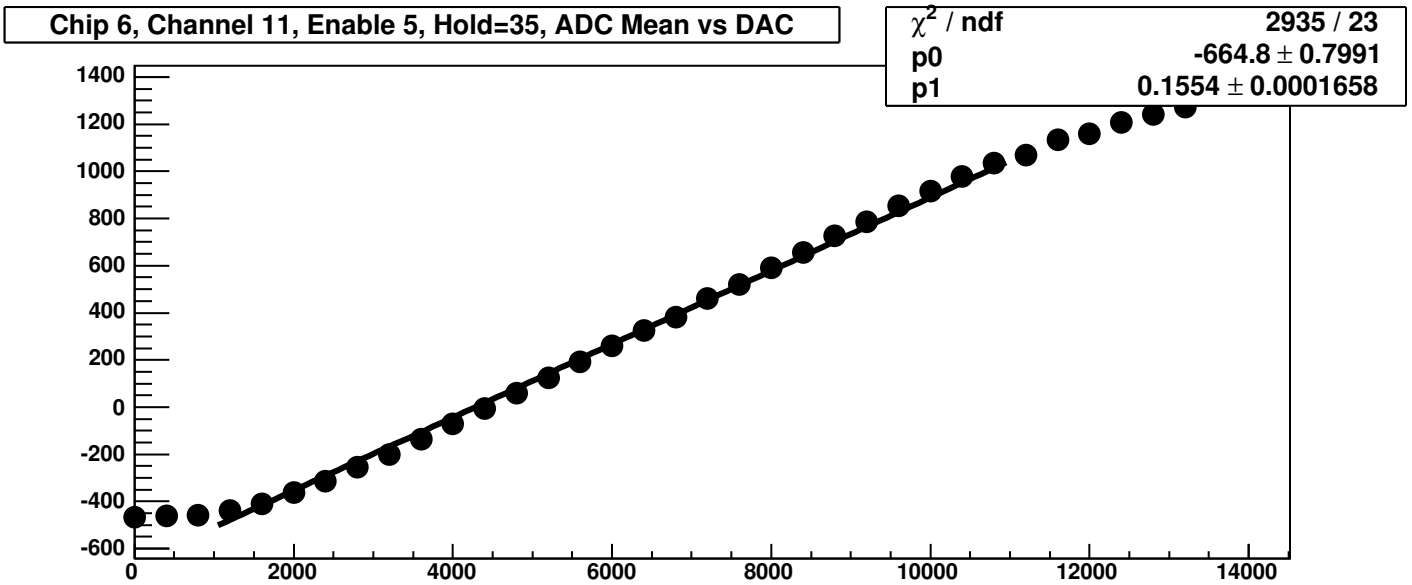

Chip 6, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

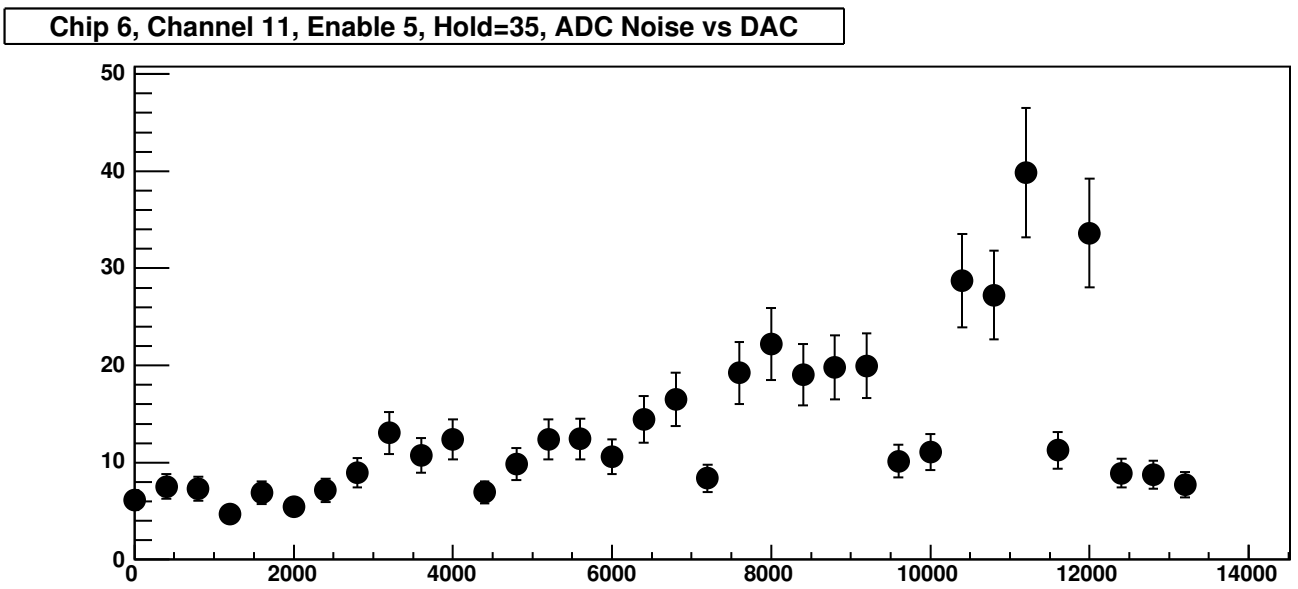

Chip 6, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

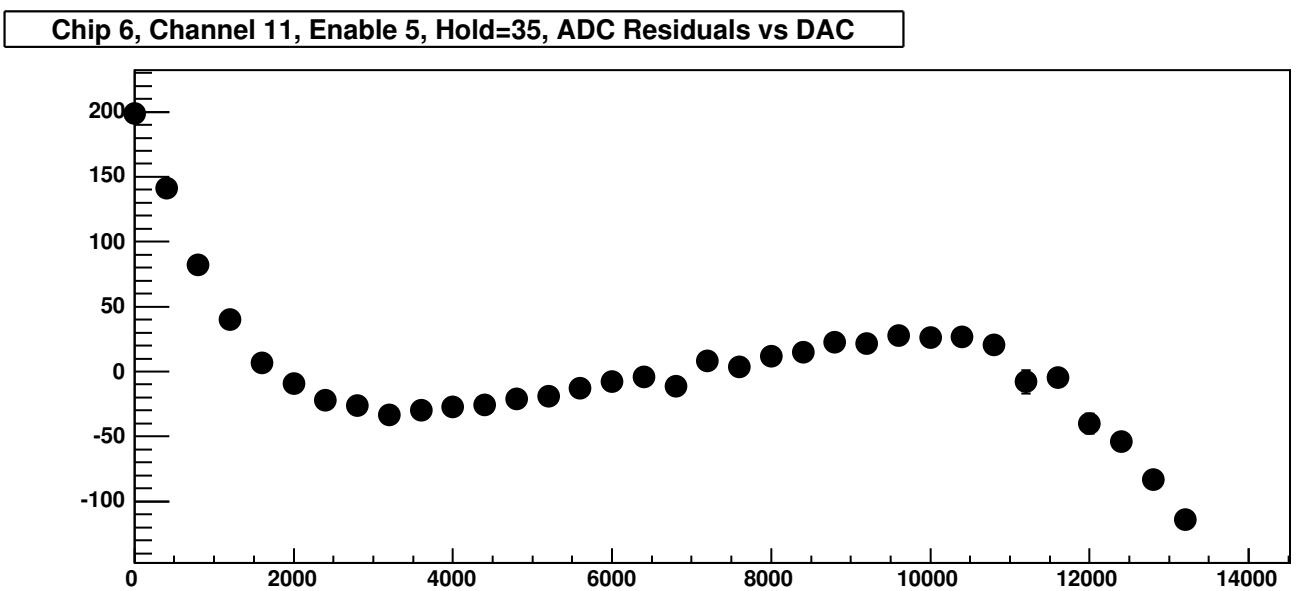

Chip 6, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.36 / 23
p0 -493.8 ± 0.787
p1 0.01672 ± 0.0001255
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

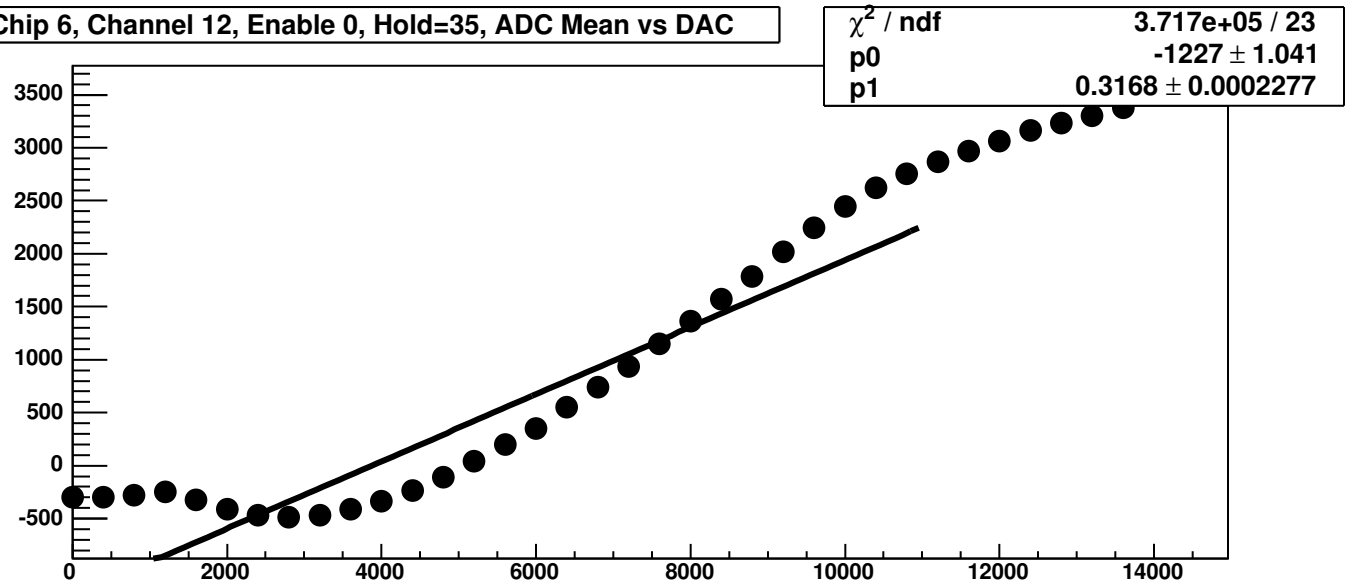

Chip 6, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2935 / 23
p0 -664.8 ± 0.7991
p1 0.1554 ± 0.0001658
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

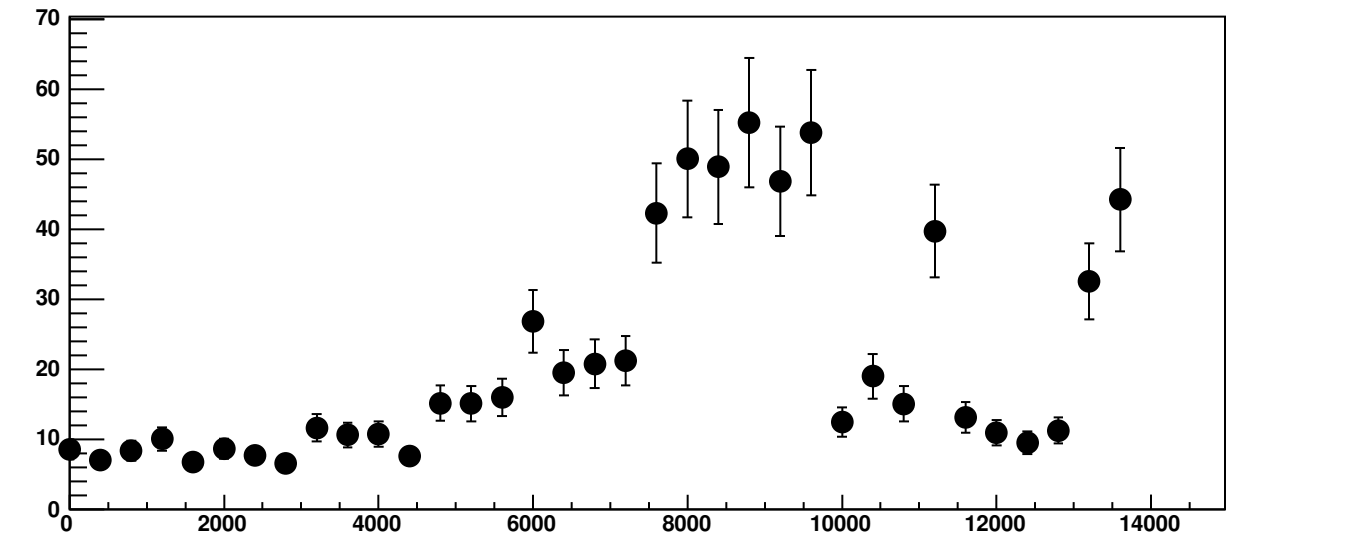

Chip 6, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

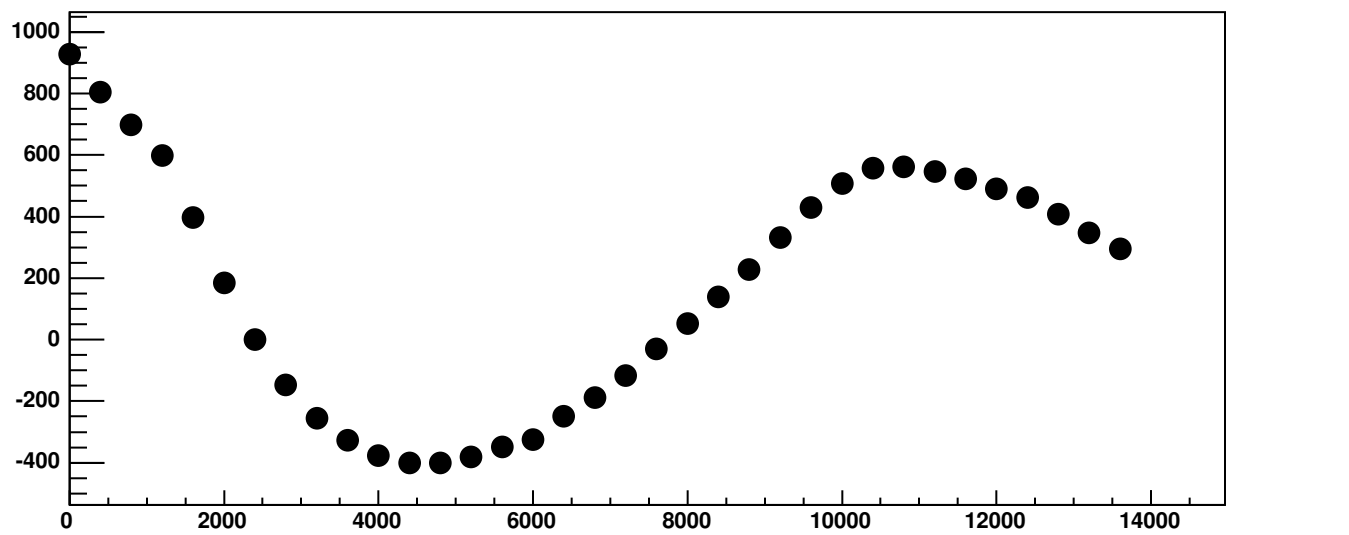
Chip 6, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

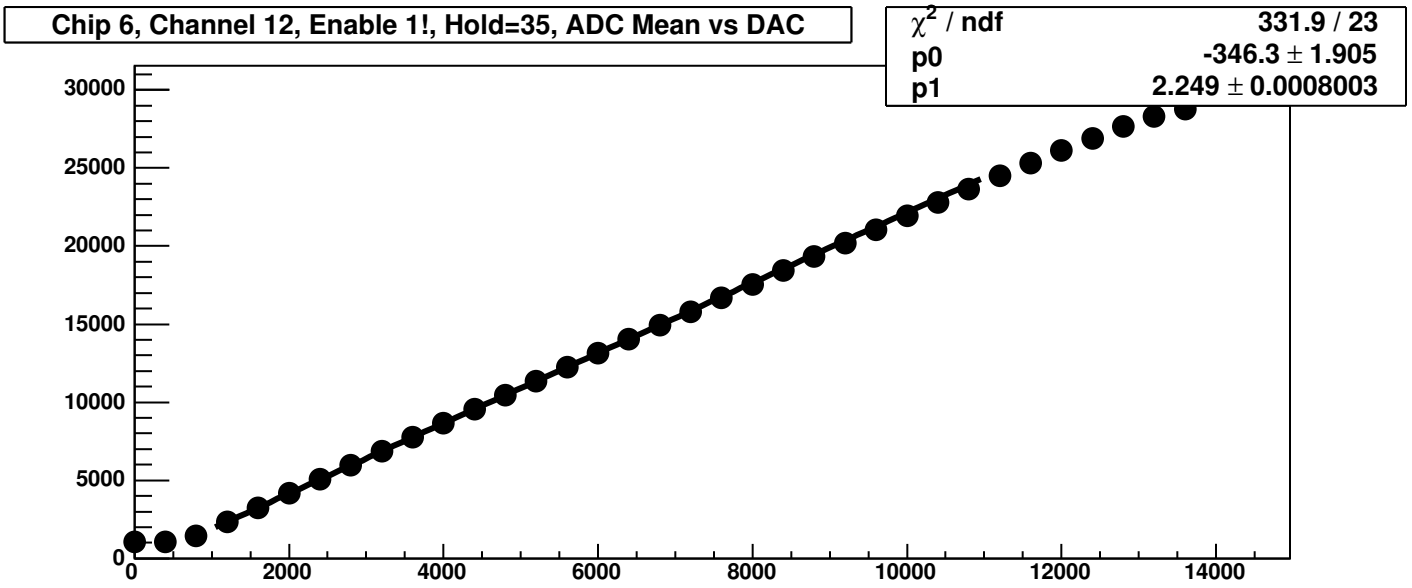


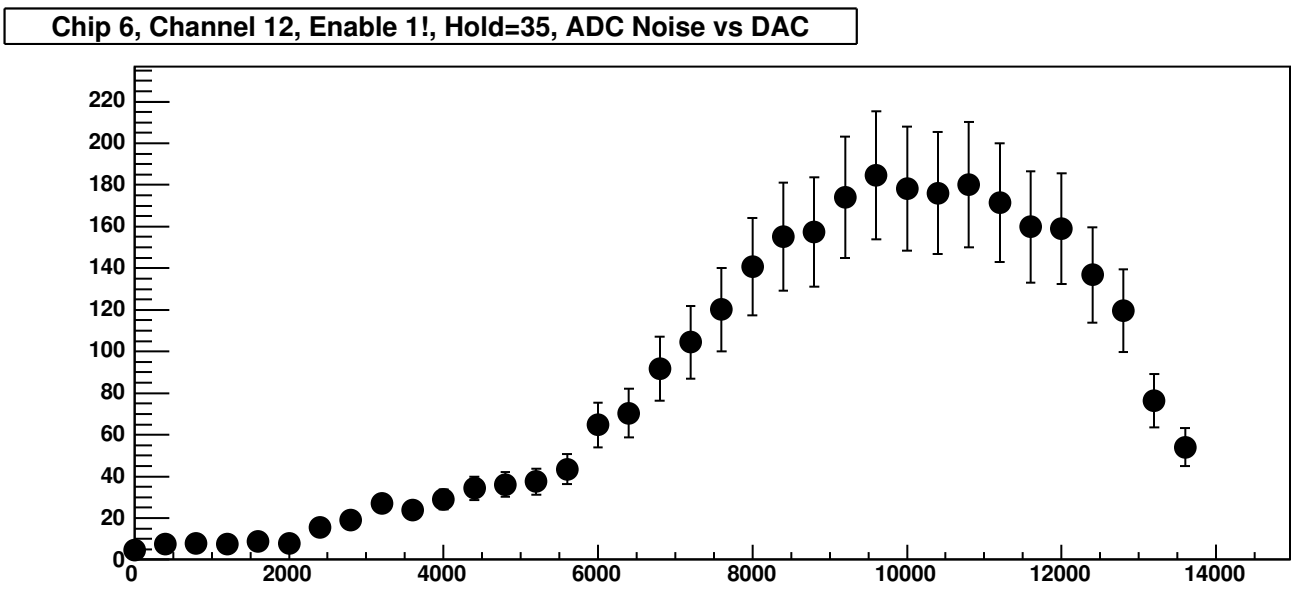
Chip 6, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

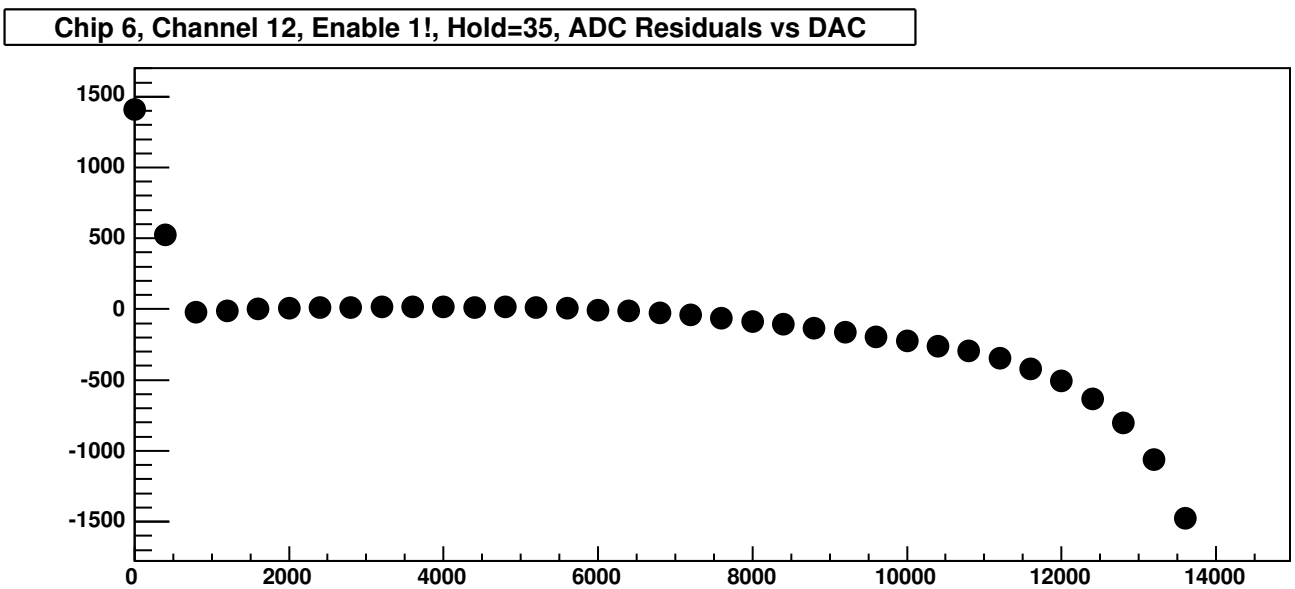


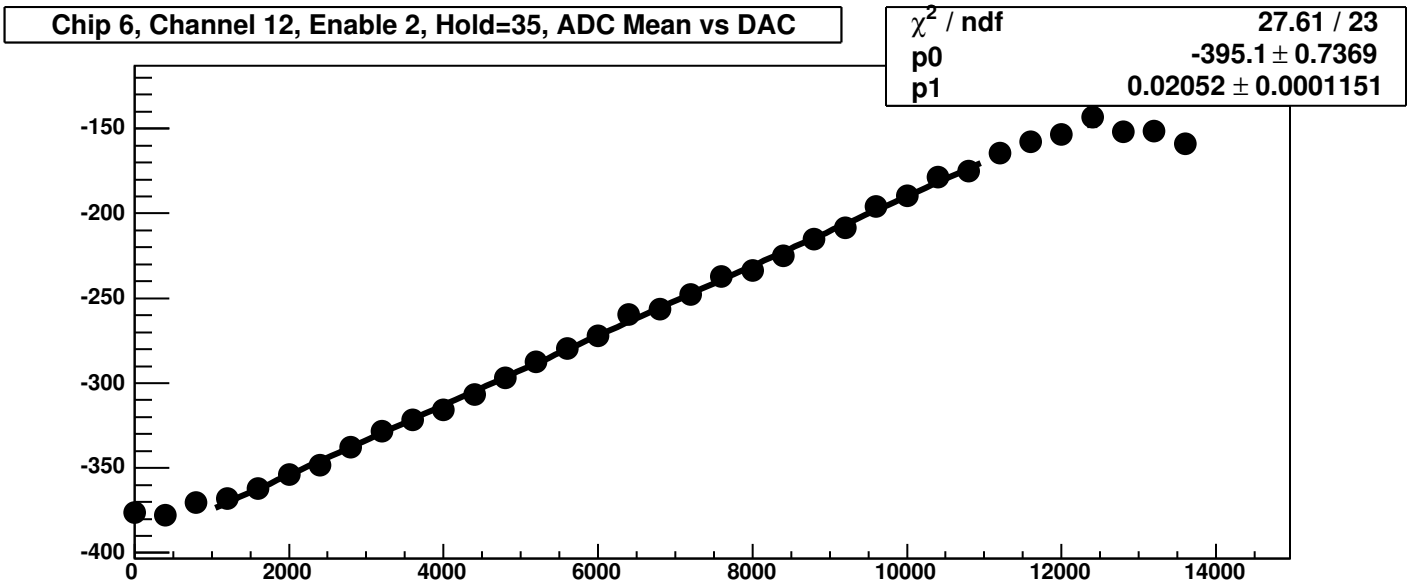
Chip 6, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

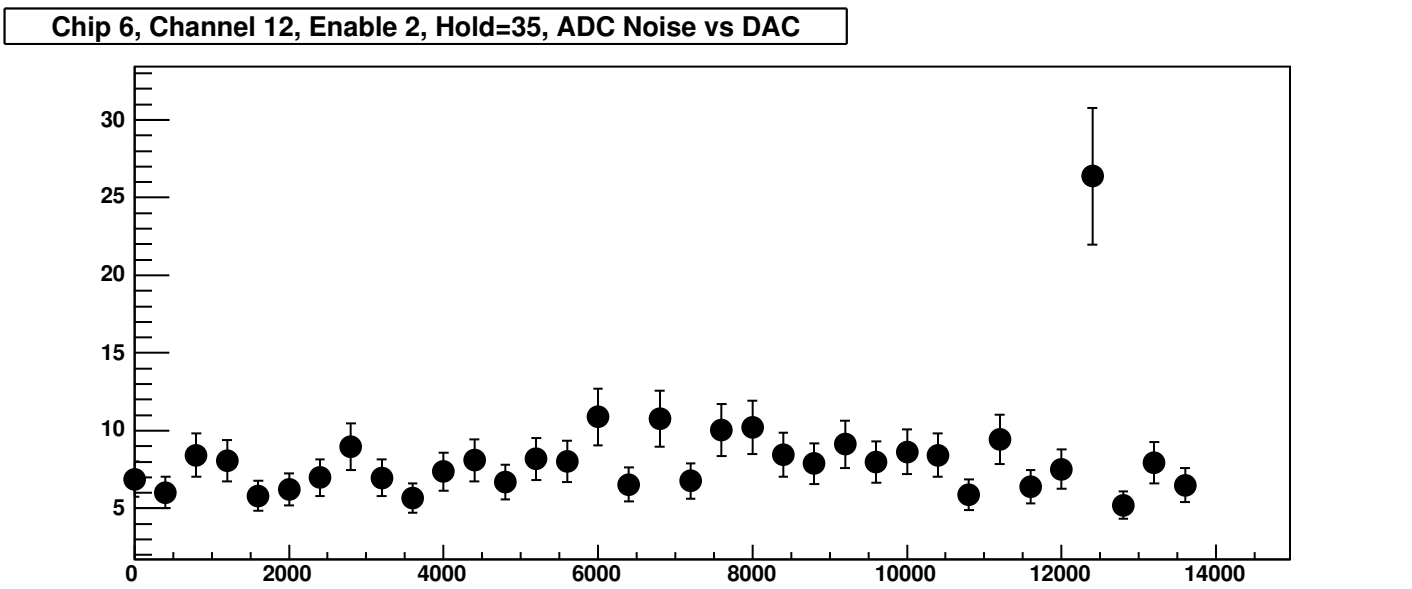


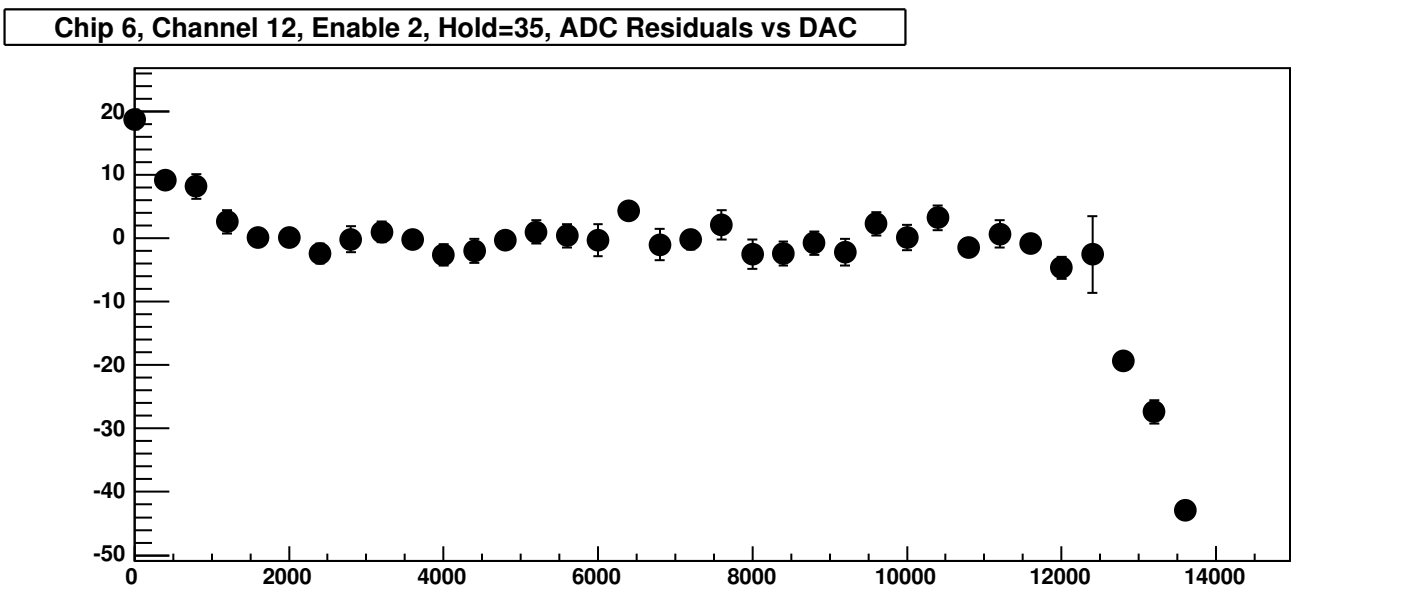

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 331.9 / 23
p0 -346.3 ± 1.905
p1 2.249 ± 0.0008003
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

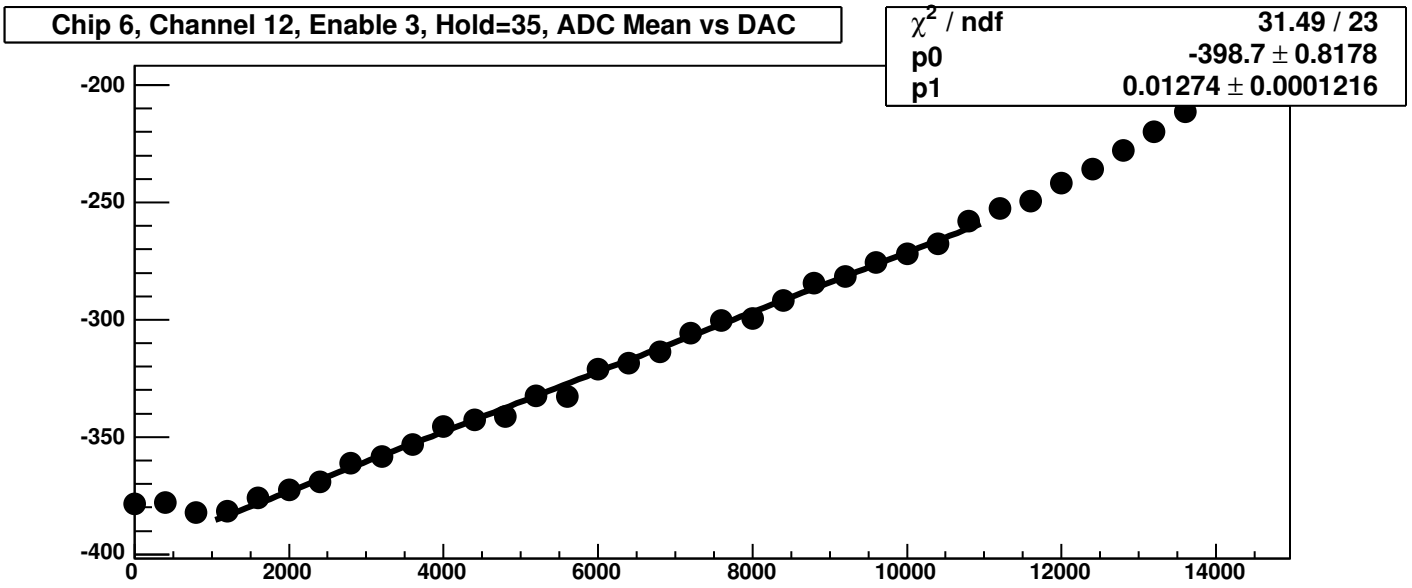

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

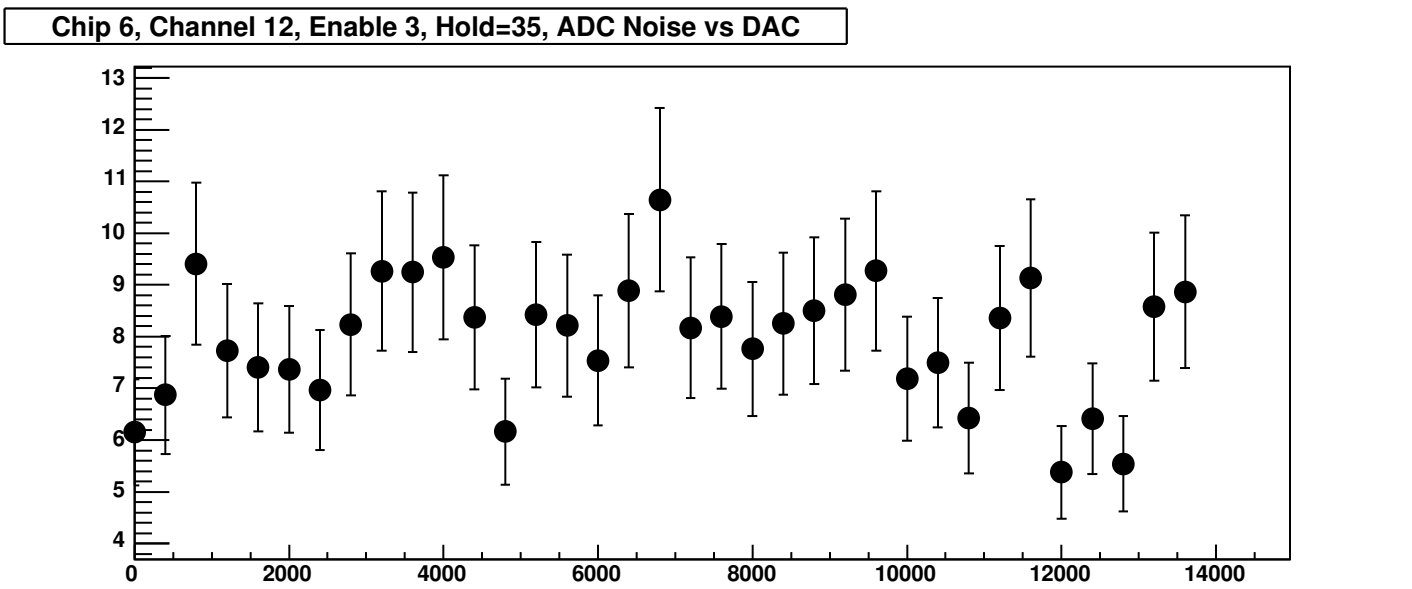

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

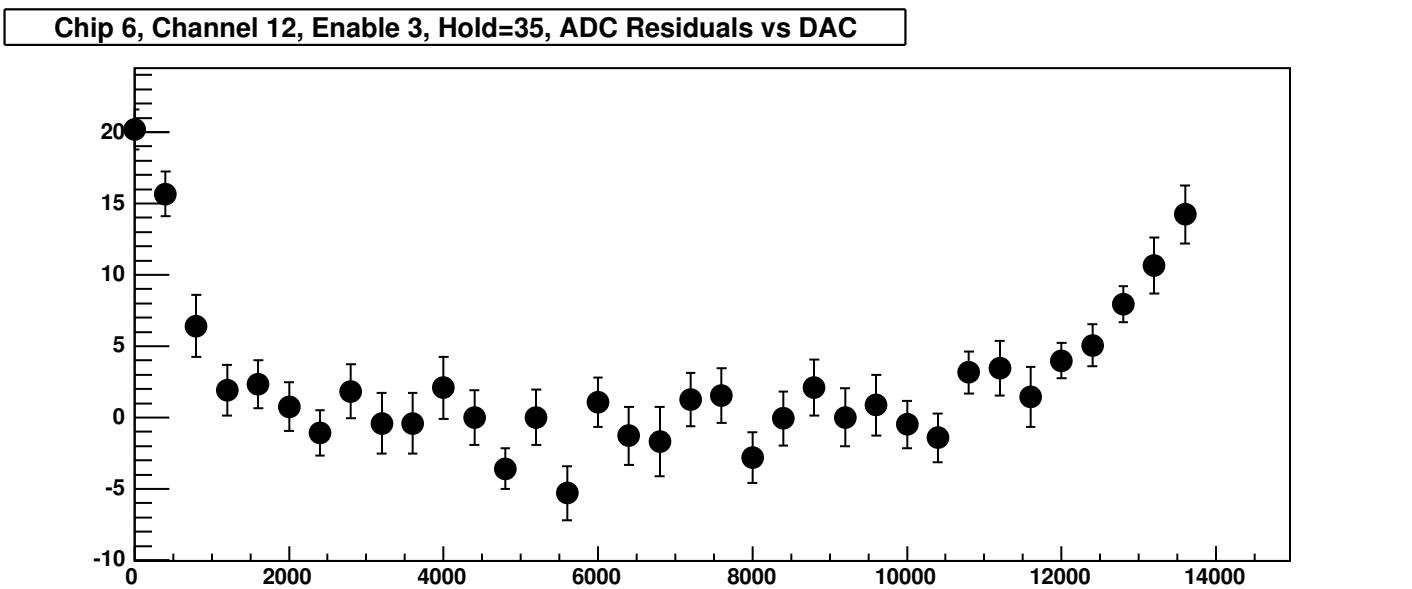

Chip 6, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.61 / 23
p0 -395.1 ± 0.7369
p1 0.02052 ± 0.0001151
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

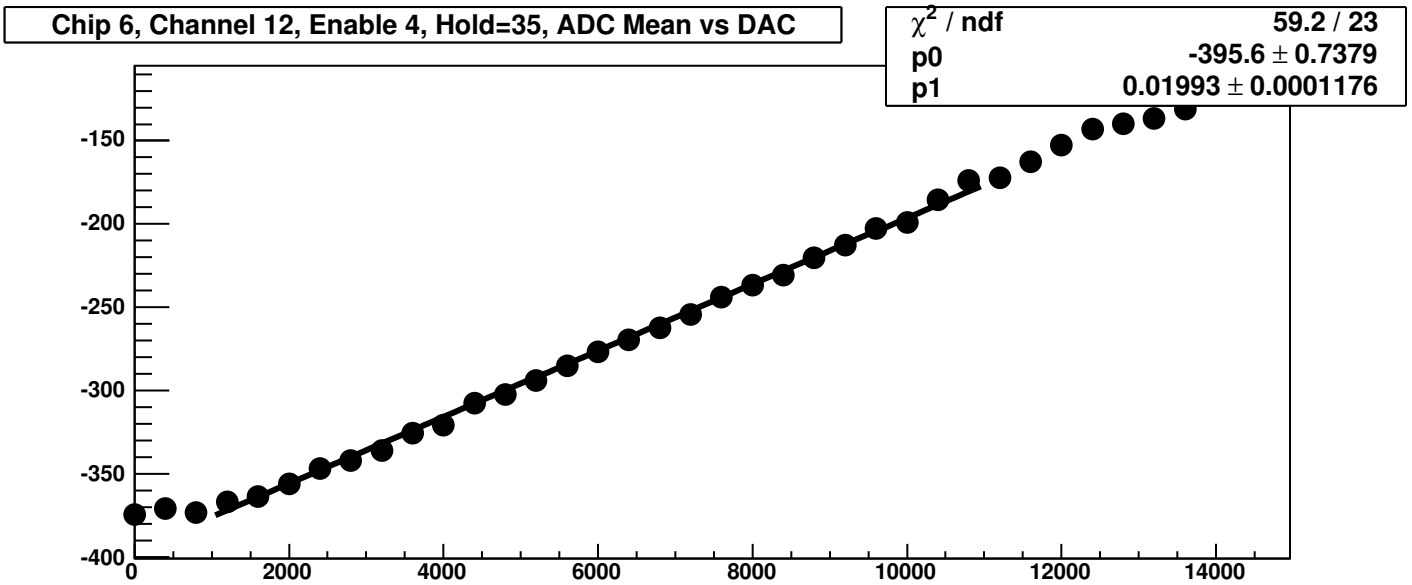

Chip 6, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

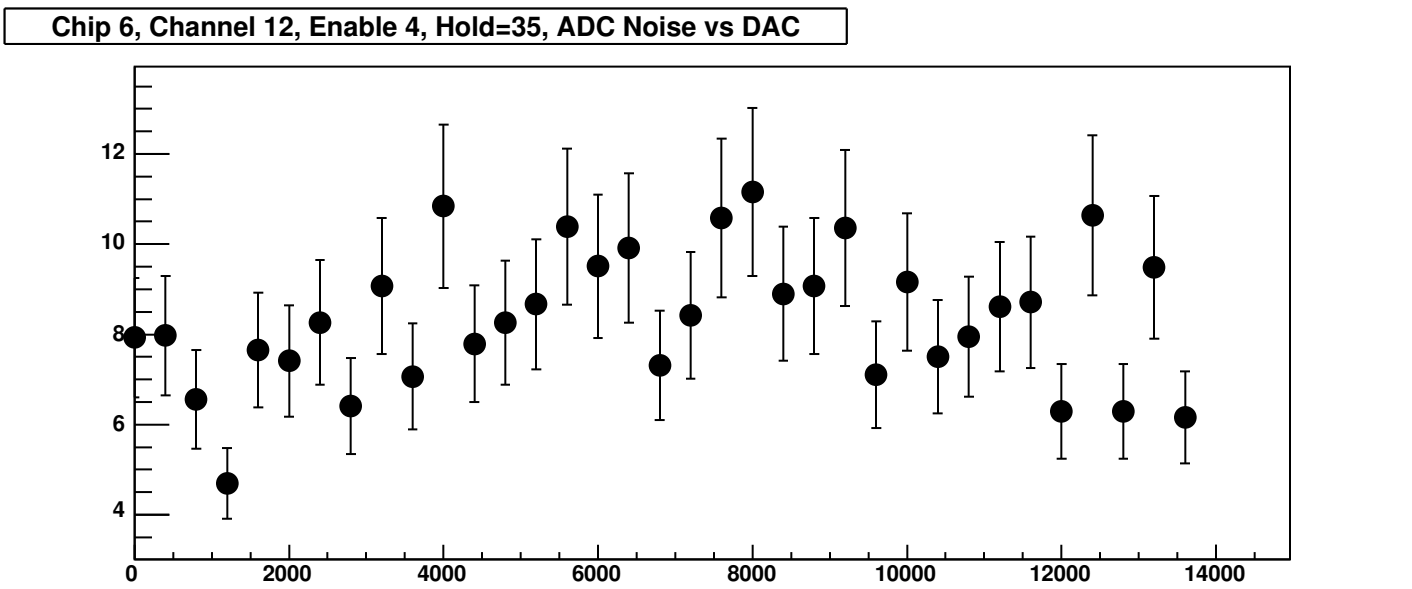

Chip 6, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

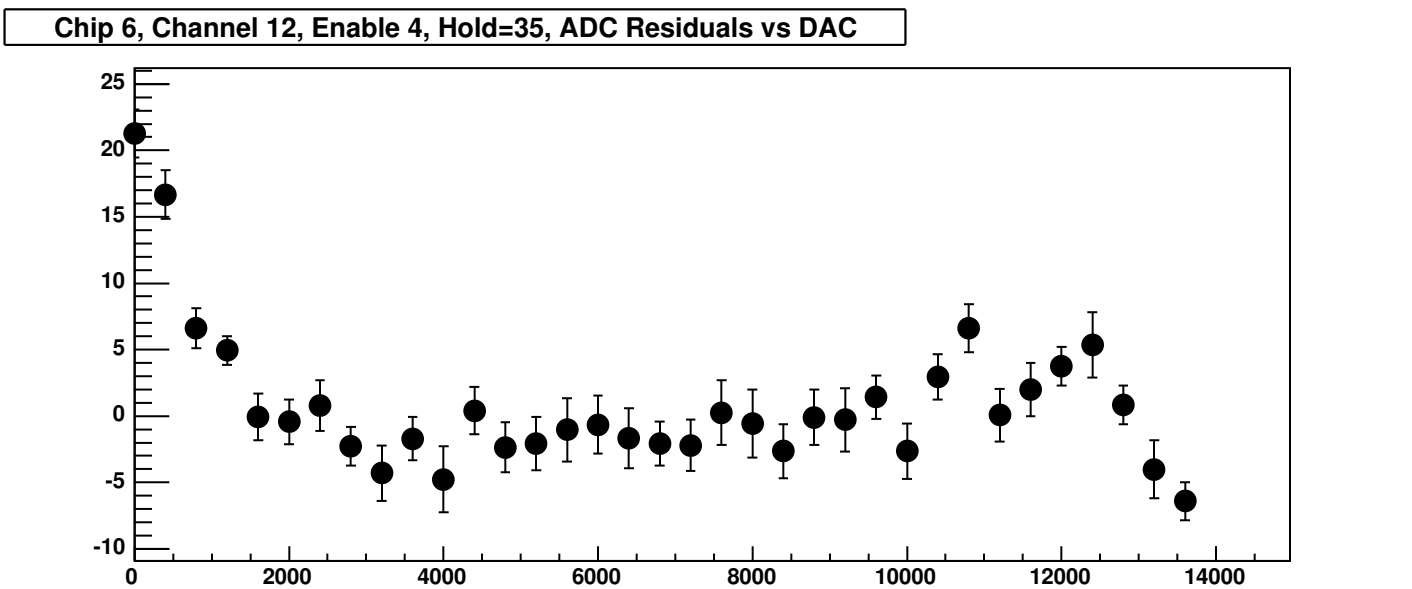

Chip 6, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.49 / 23
p0 -398.7 ± 0.8178
p1 0.01274 ± 0.0001216
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

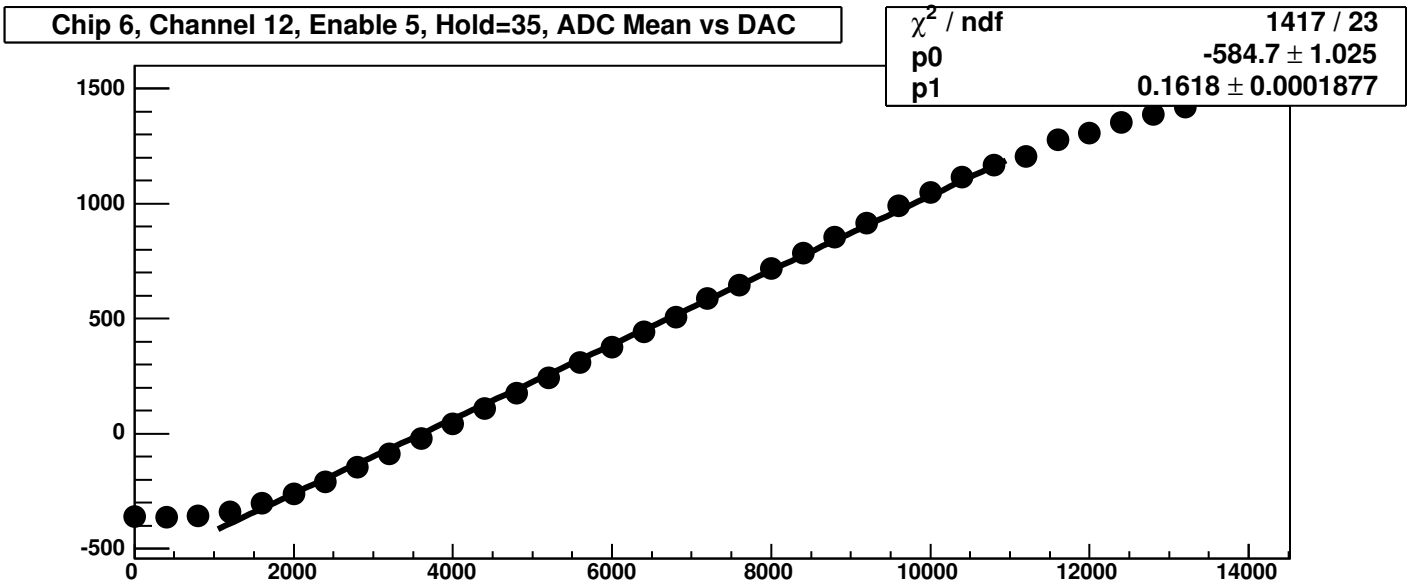

Chip 6, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

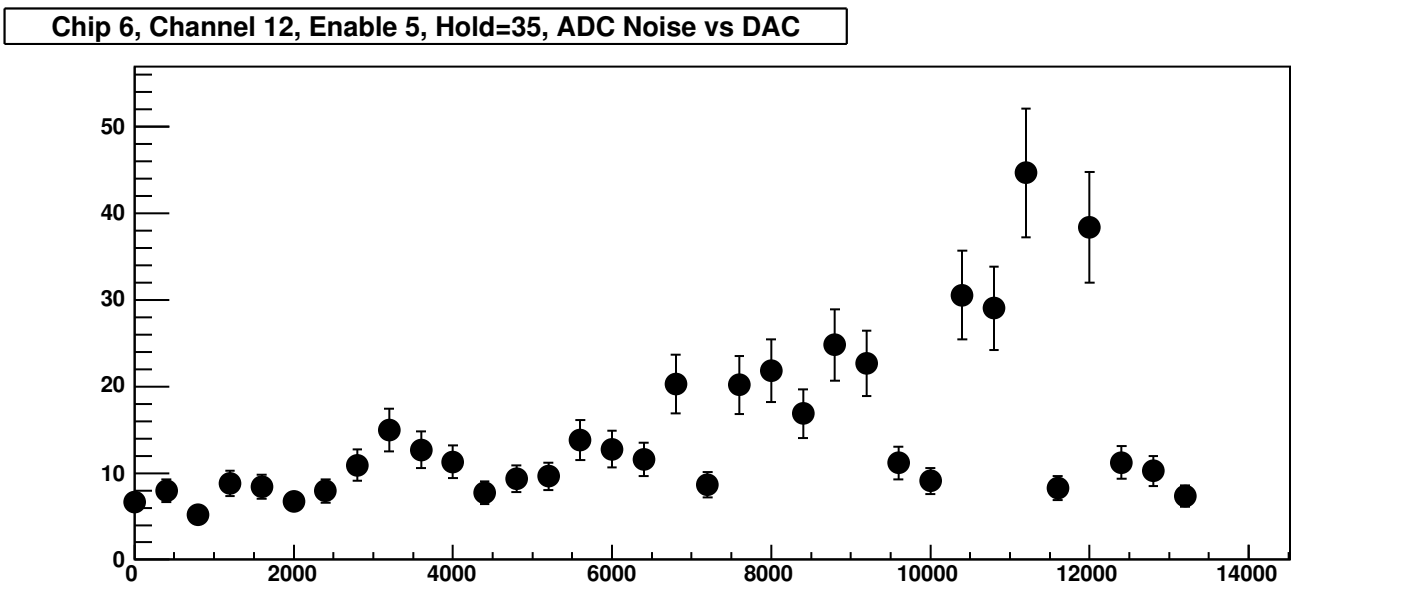

Chip 6, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

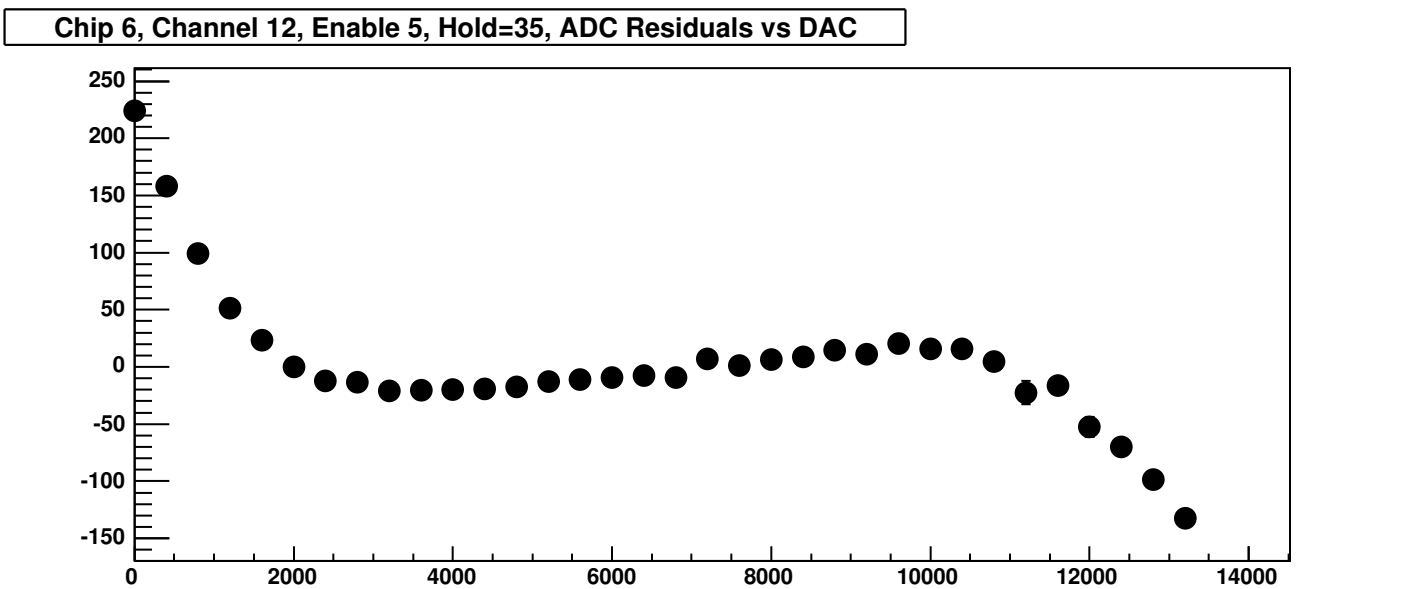

Chip 6, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 59.2 / 23
p0 -395.6 ± 0.7379
p1 0.01993 ± 0.0001176
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

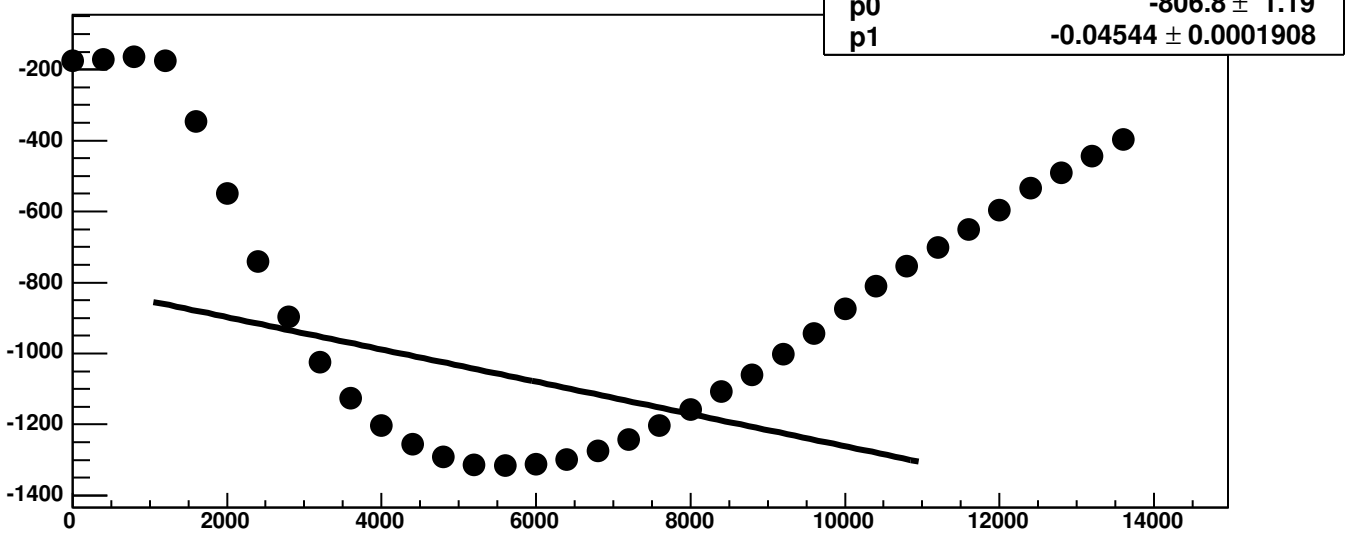

Chip 6, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1417 / 23
p0 -584.7 ± 1.025
p1 0.1618 ± 0.0001877
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

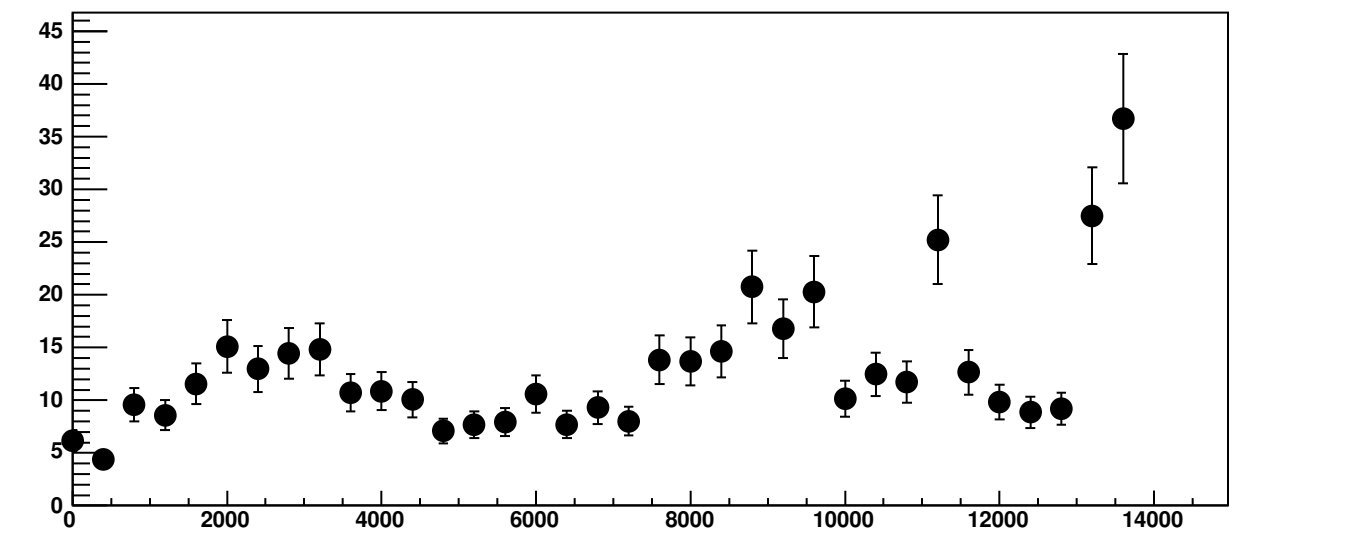

Chip 6, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

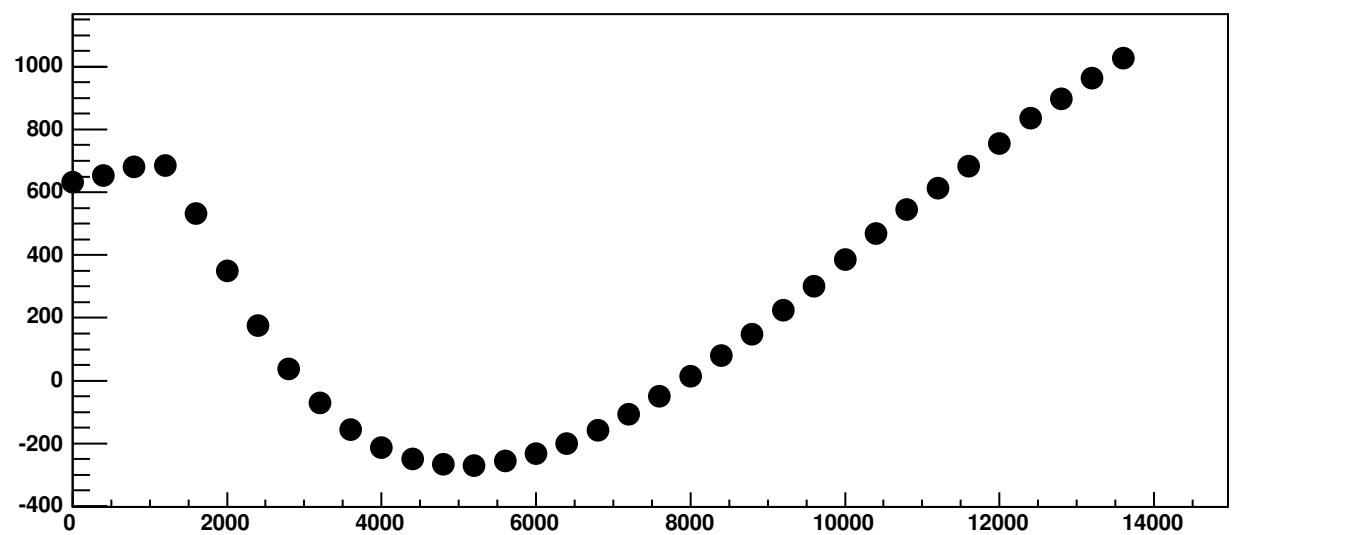
Chip 6, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

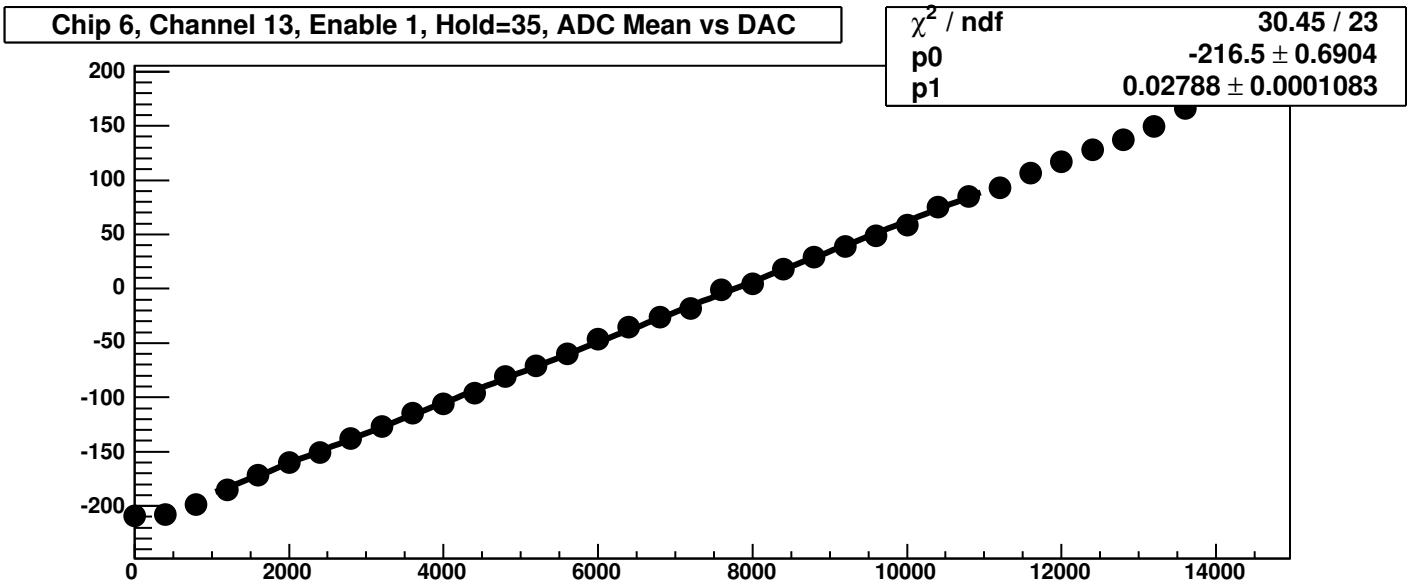


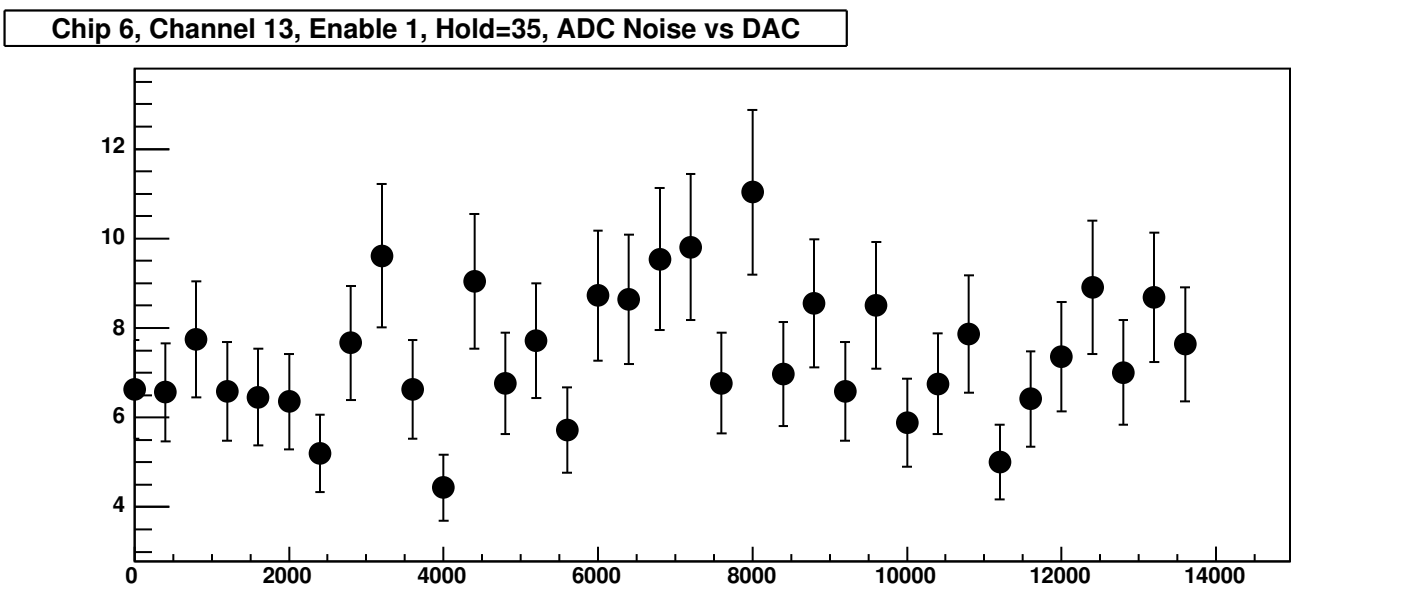
Chip 6, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

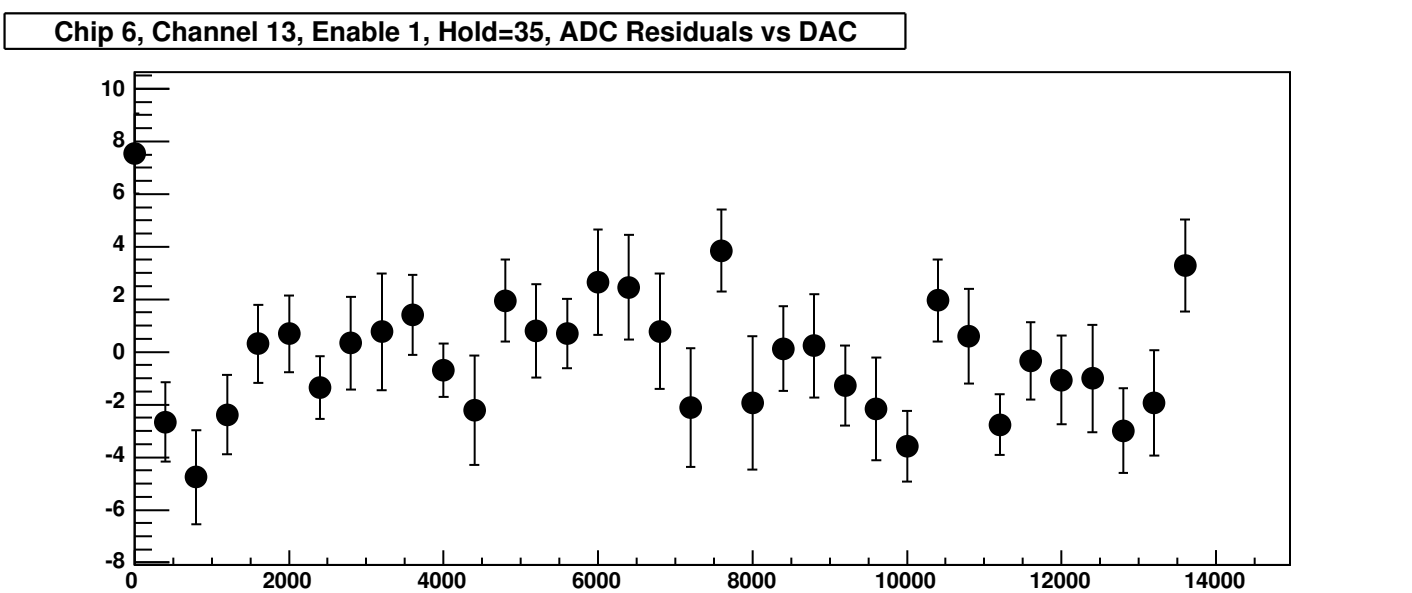


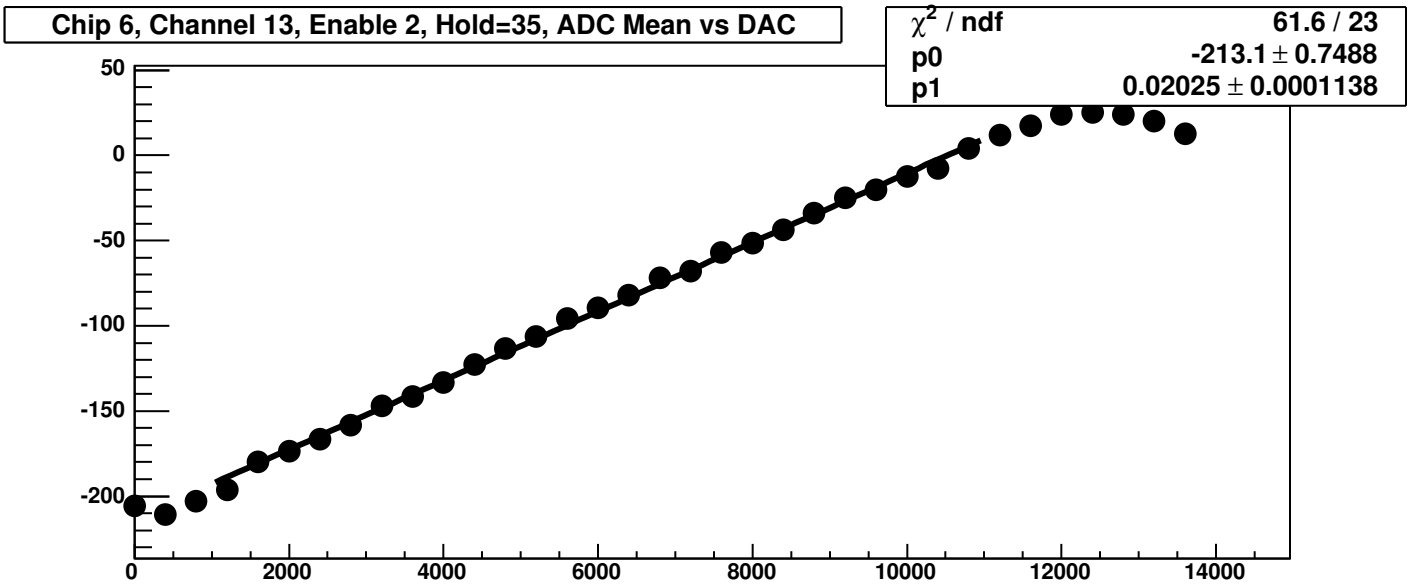
Chip 6, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

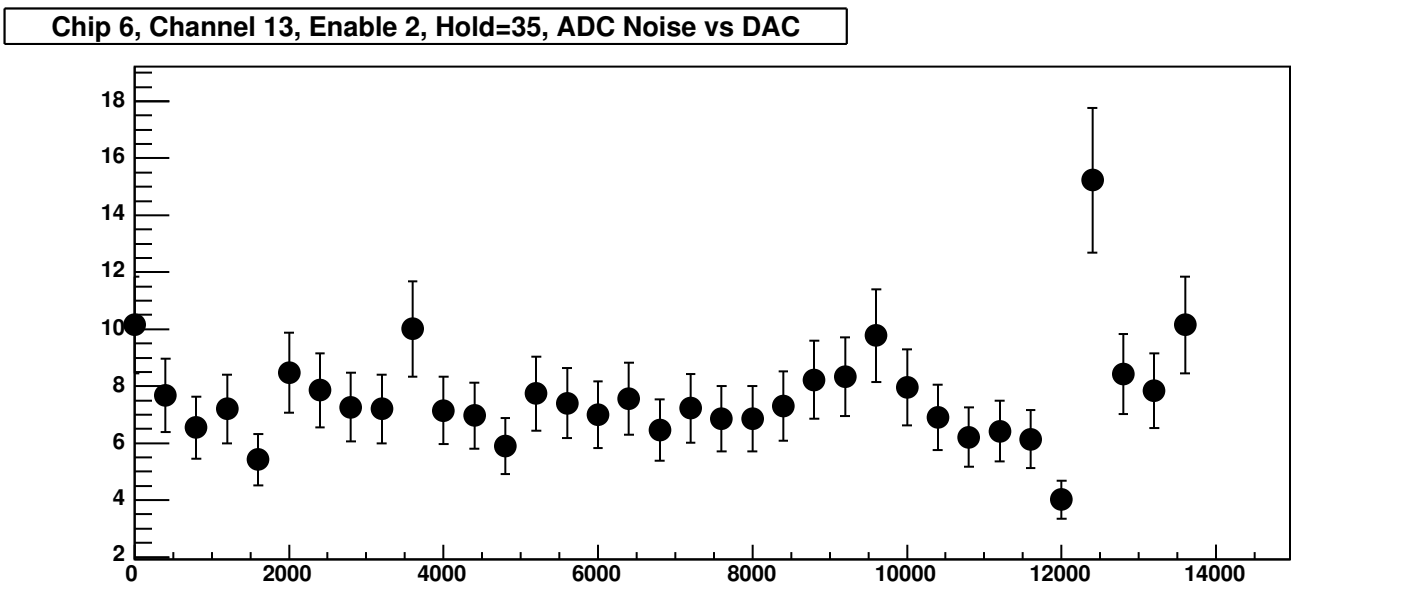


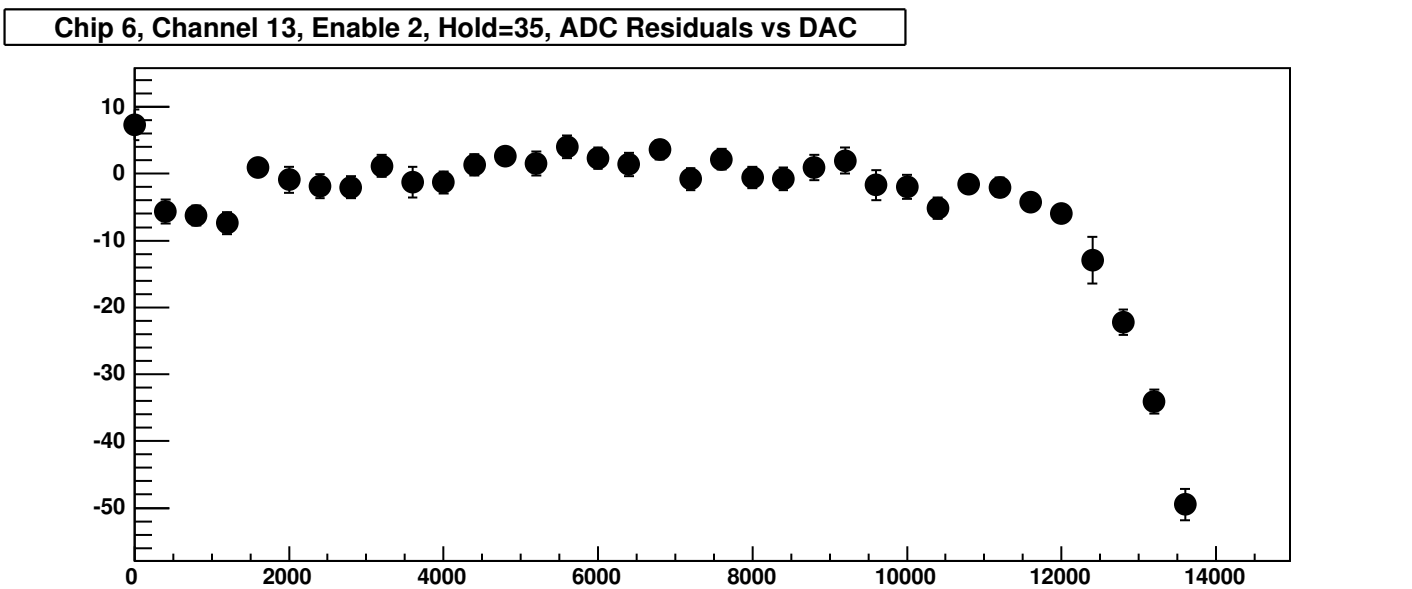

Chip 6, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.45 / 23
p0 -216.5 ± 0.6904
p1 0.02788 ± 0.0001083
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

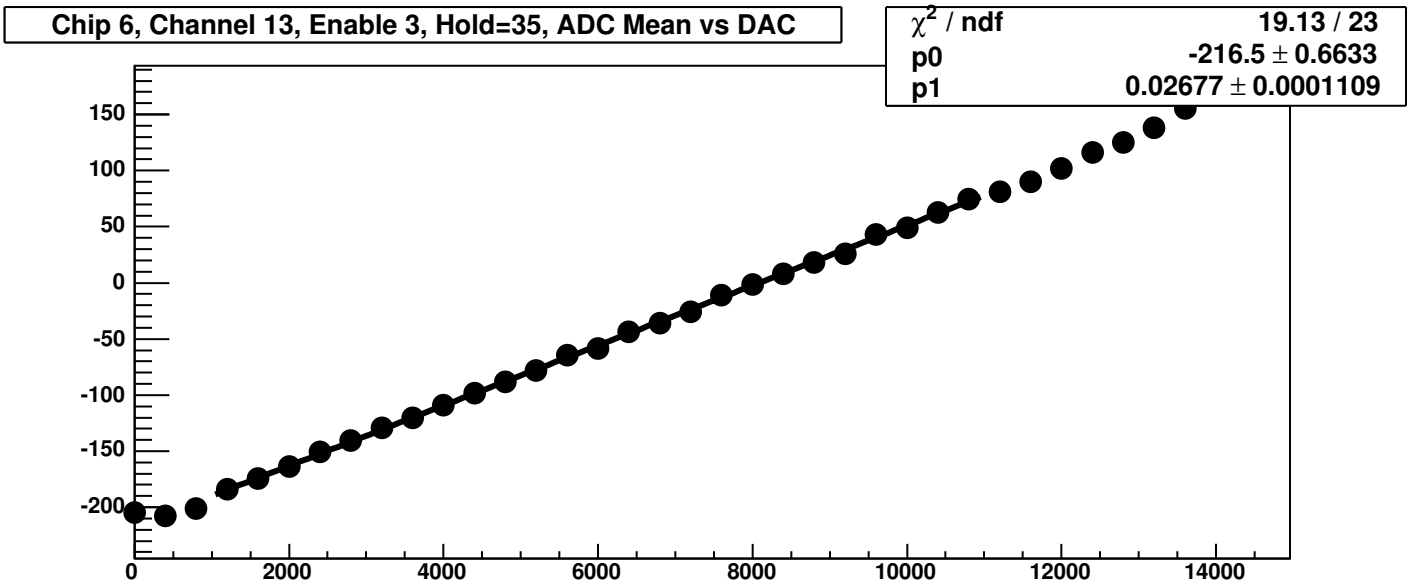

Chip 6, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

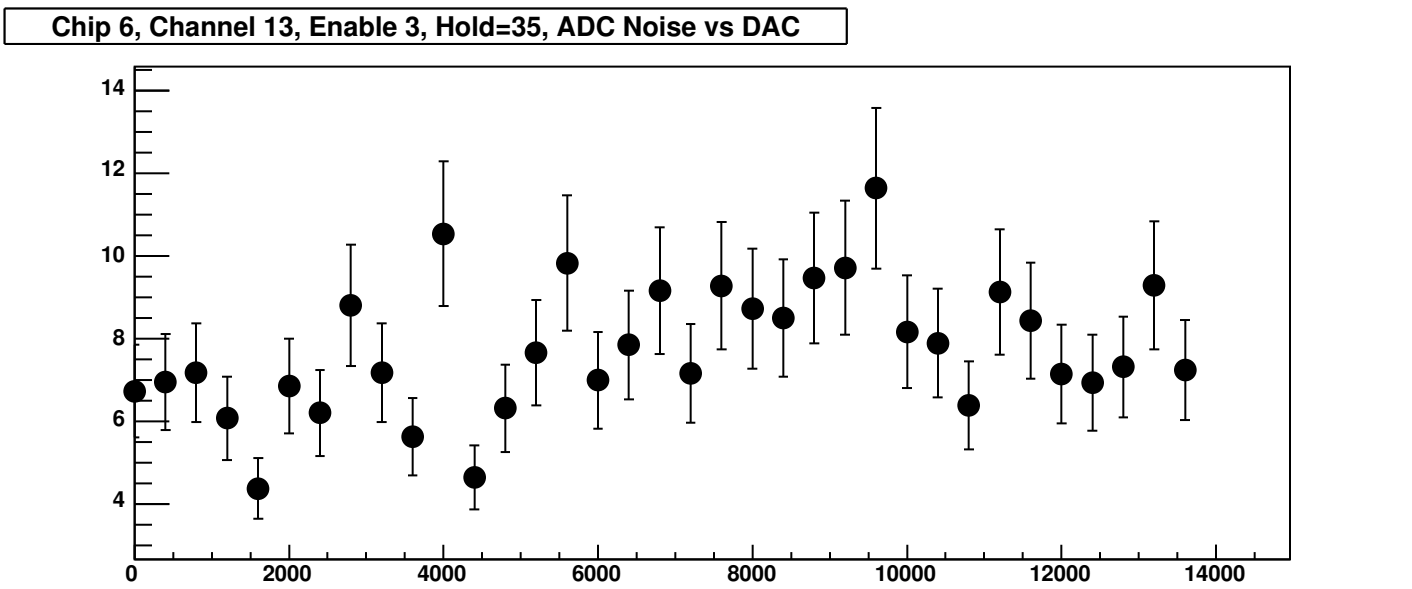

Chip 6, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

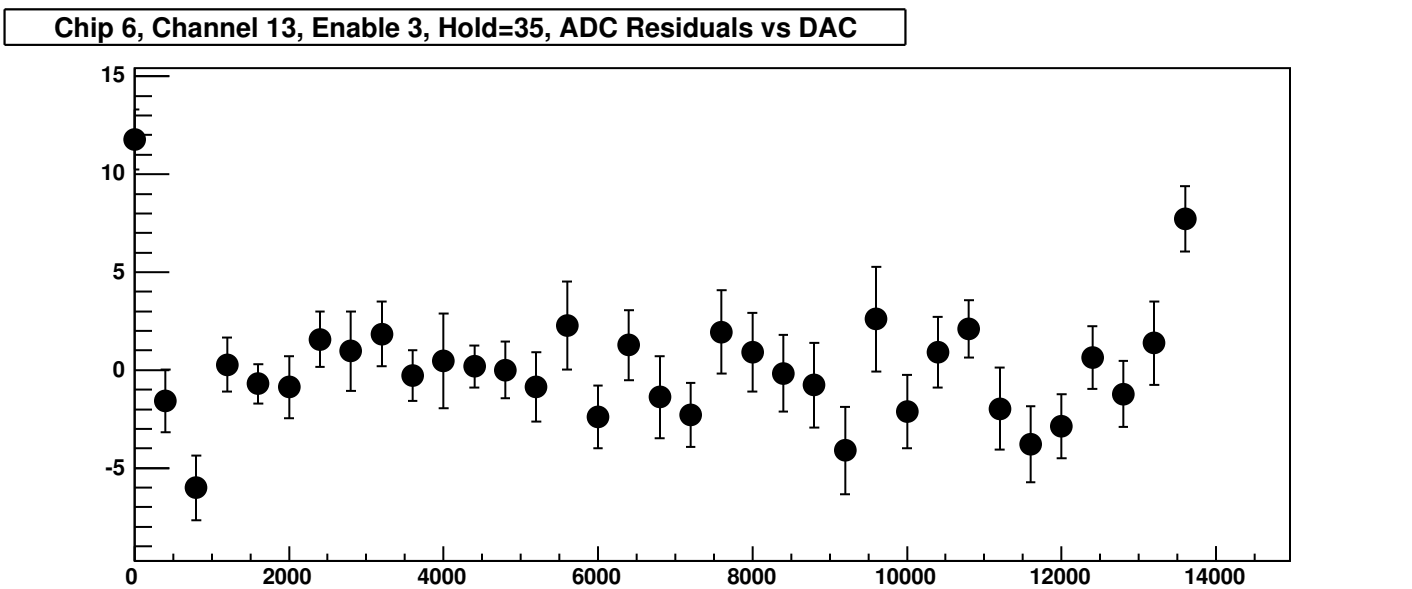

Chip 6, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 61.6 / 23
p0 -213.1 ± 0.7488
p1 0.02025 ± 0.0001138
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

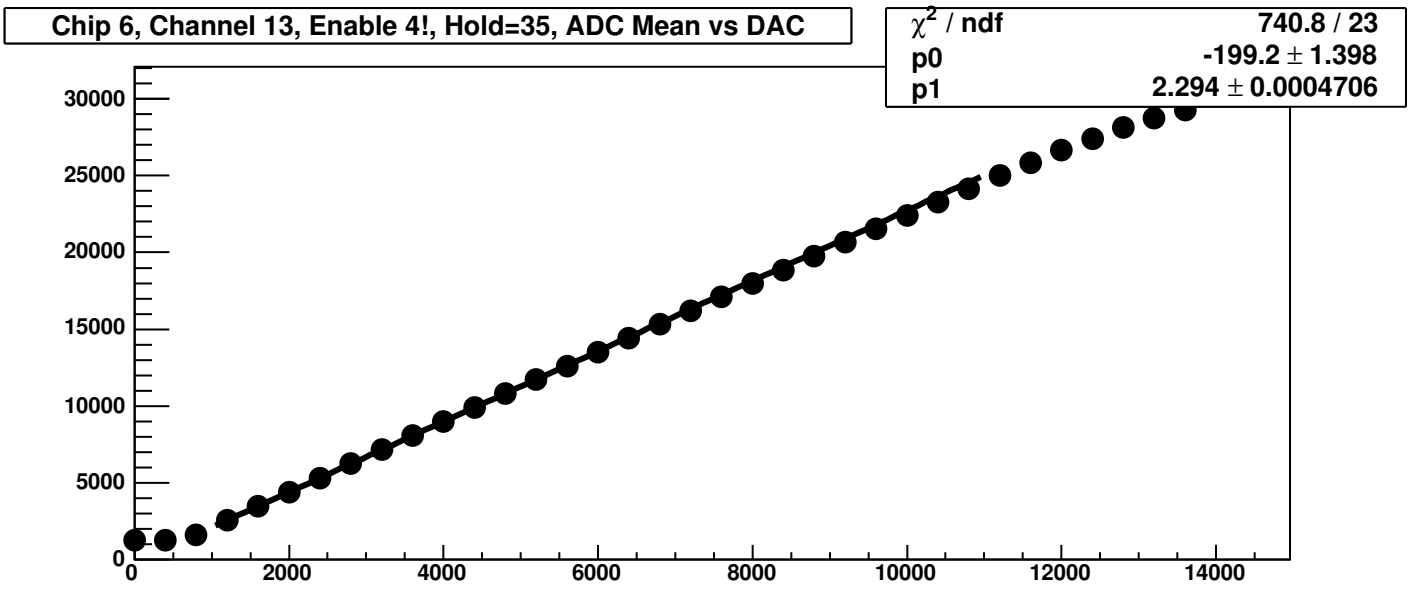

Chip 6, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

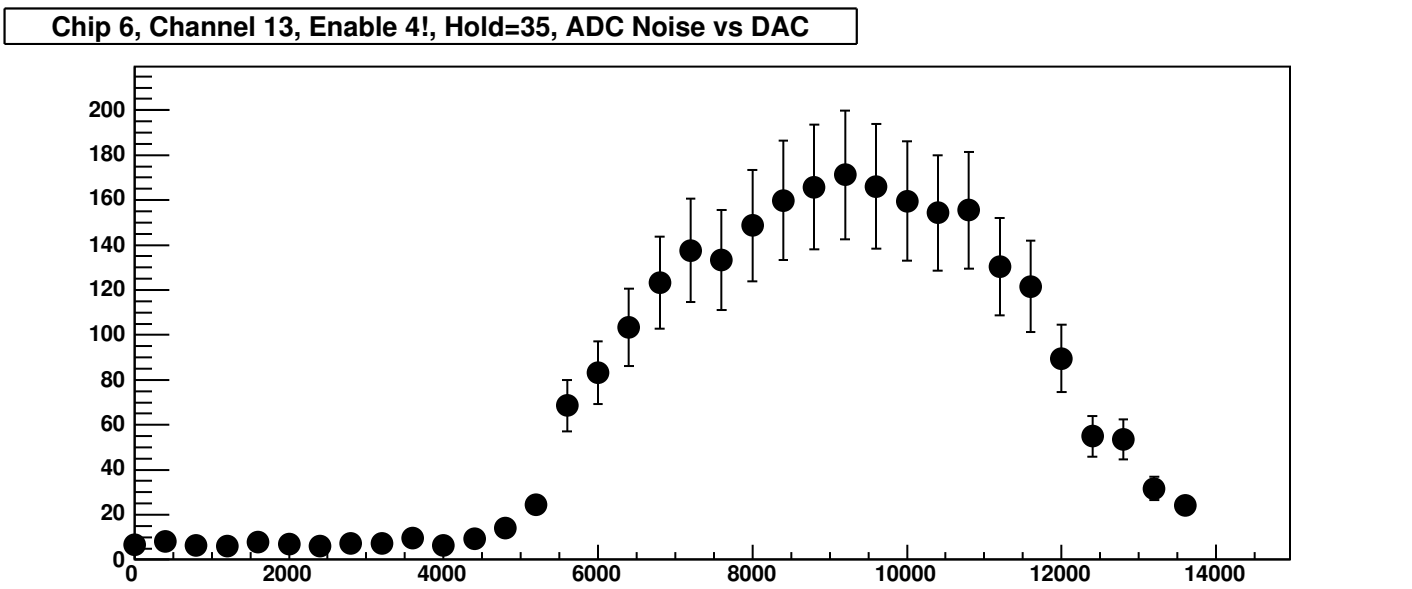

Chip 6, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

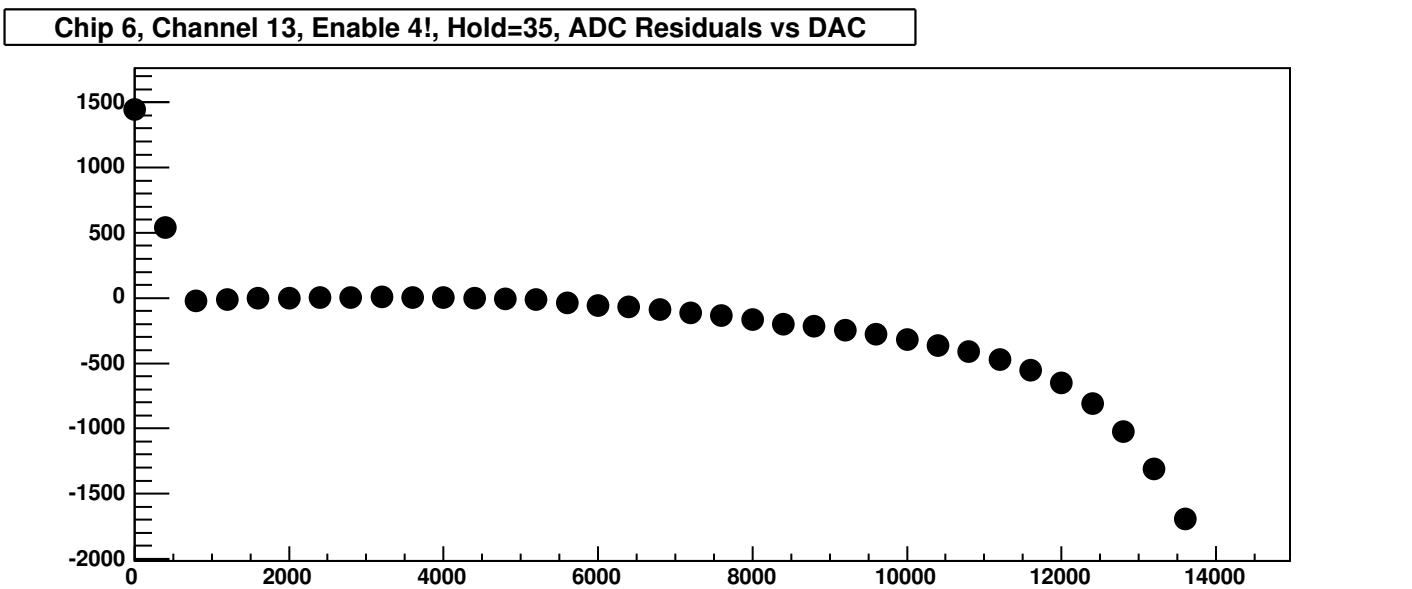

Chip 6, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.13 / 23
p0 -216.5 ± 0.6633
p1 0.02677 ± 0.0001109
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

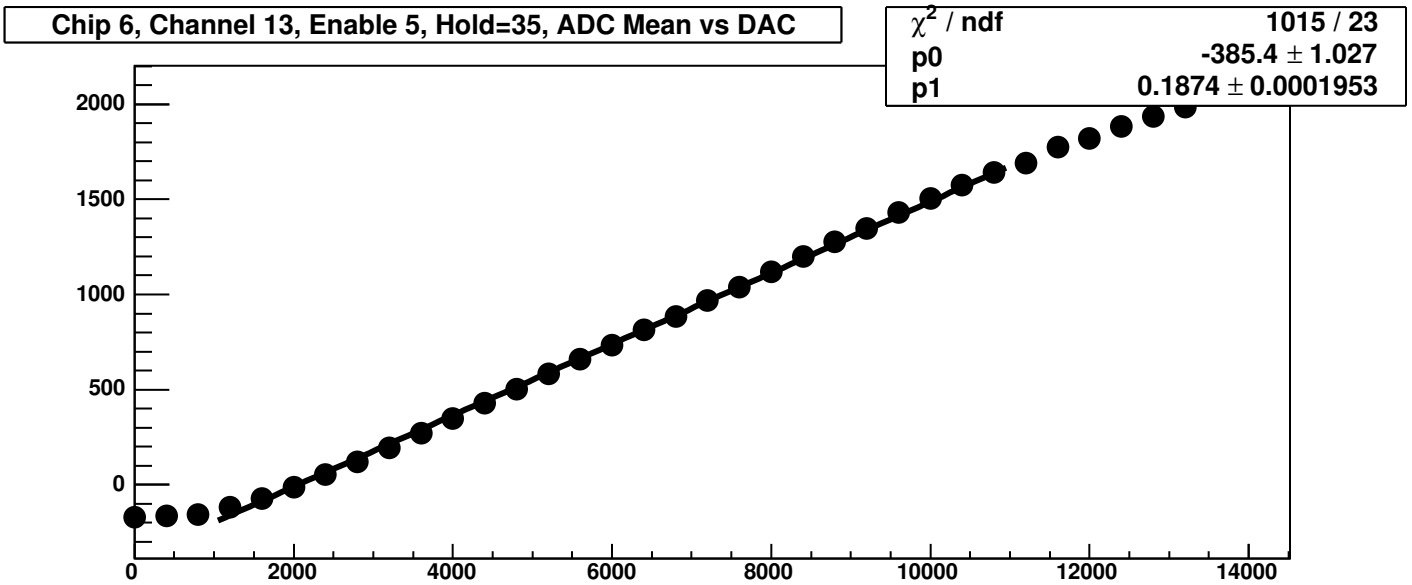

Chip 6, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

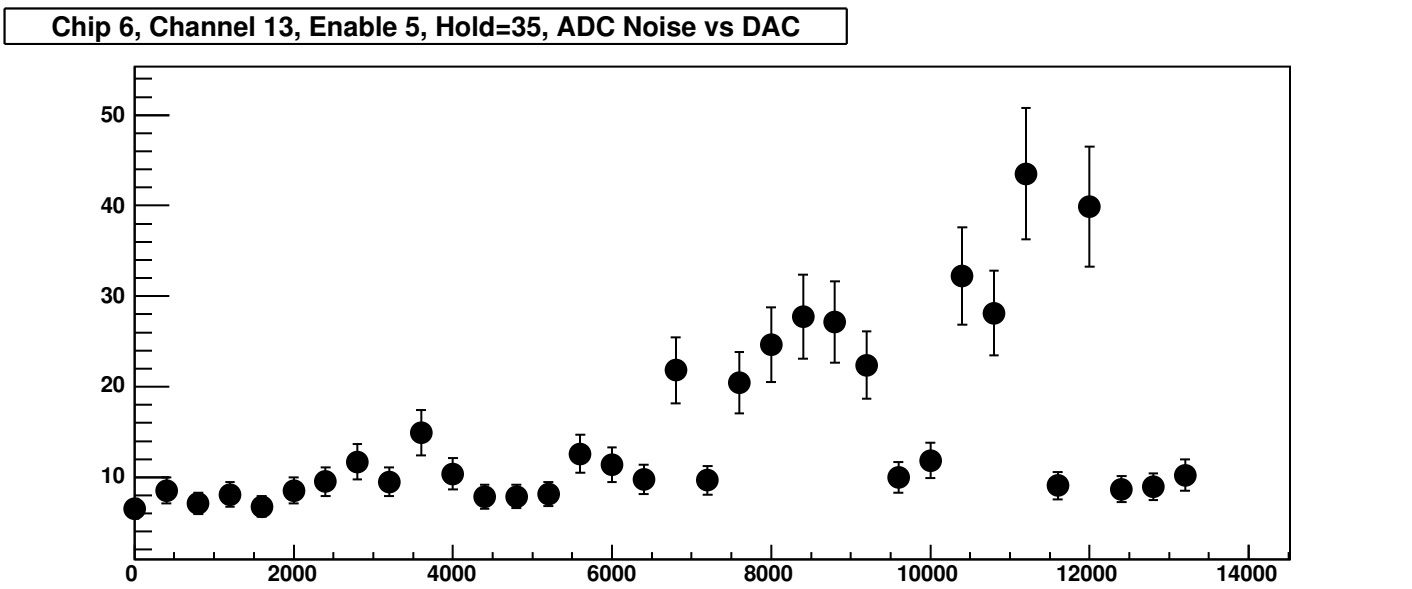

Chip 6, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

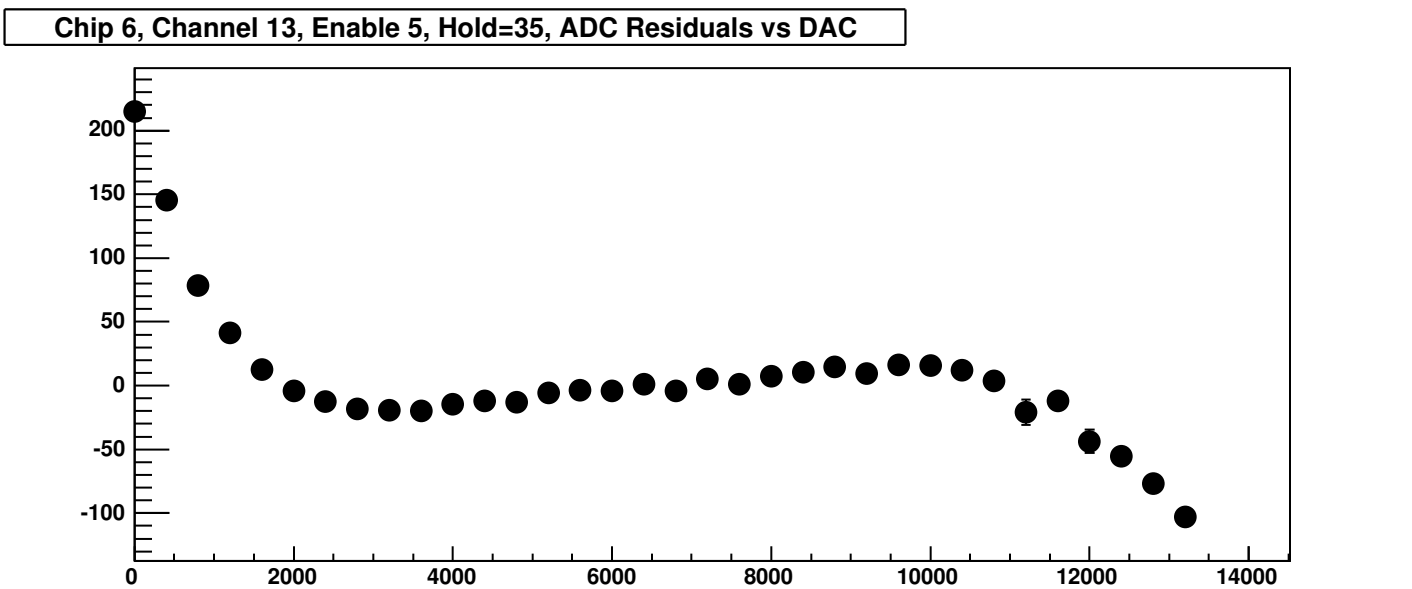

Chip 6, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 740.8 / 23
p0 -199.2 ± 1.398
p1 2.294 ± 0.0004706
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

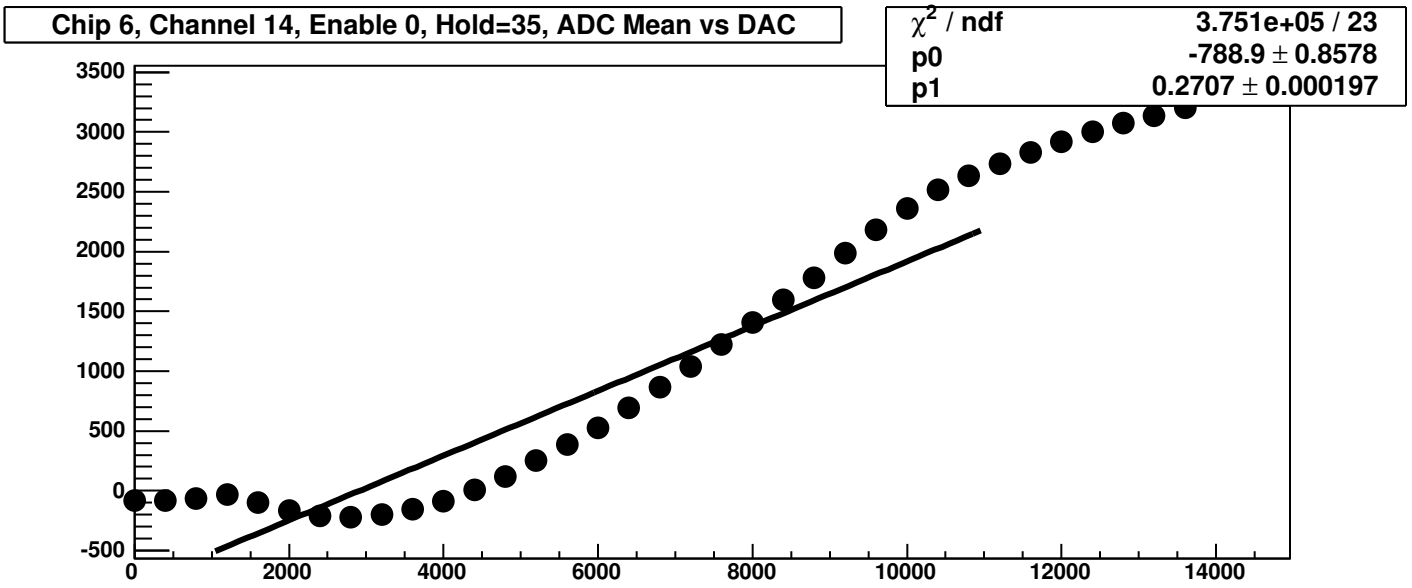

Chip 6, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

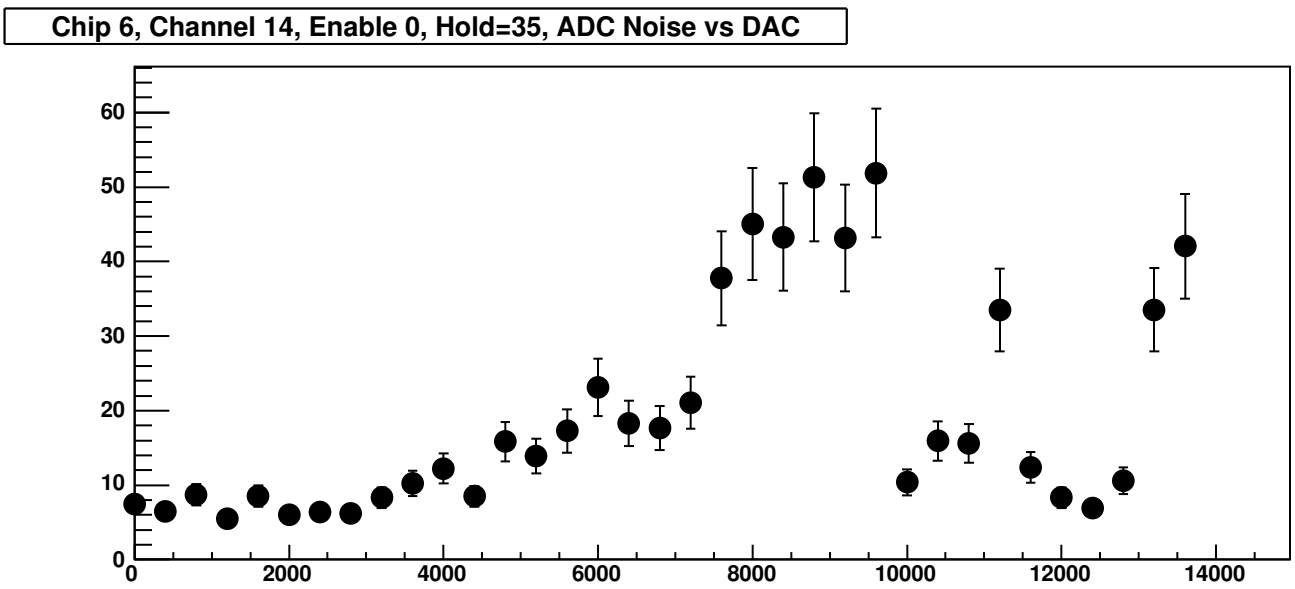

Chip 6, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

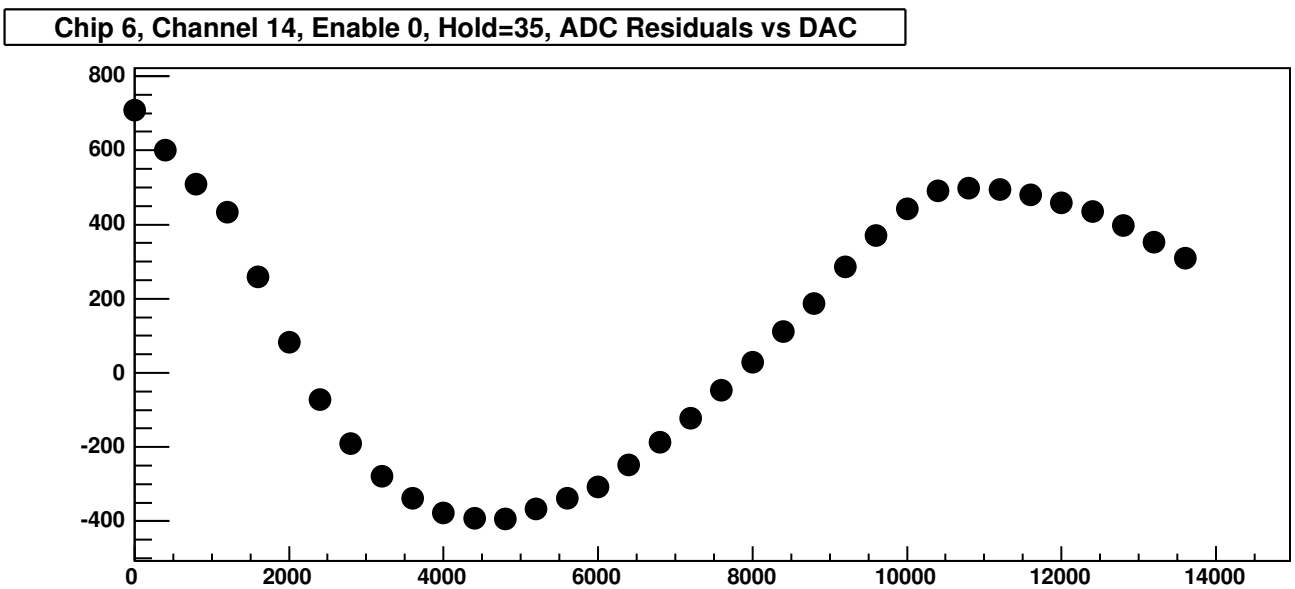

Chip 6, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1015 / 23
p0 -385.4 ± 1.027
p1 0.1874 ± 0.0001953
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

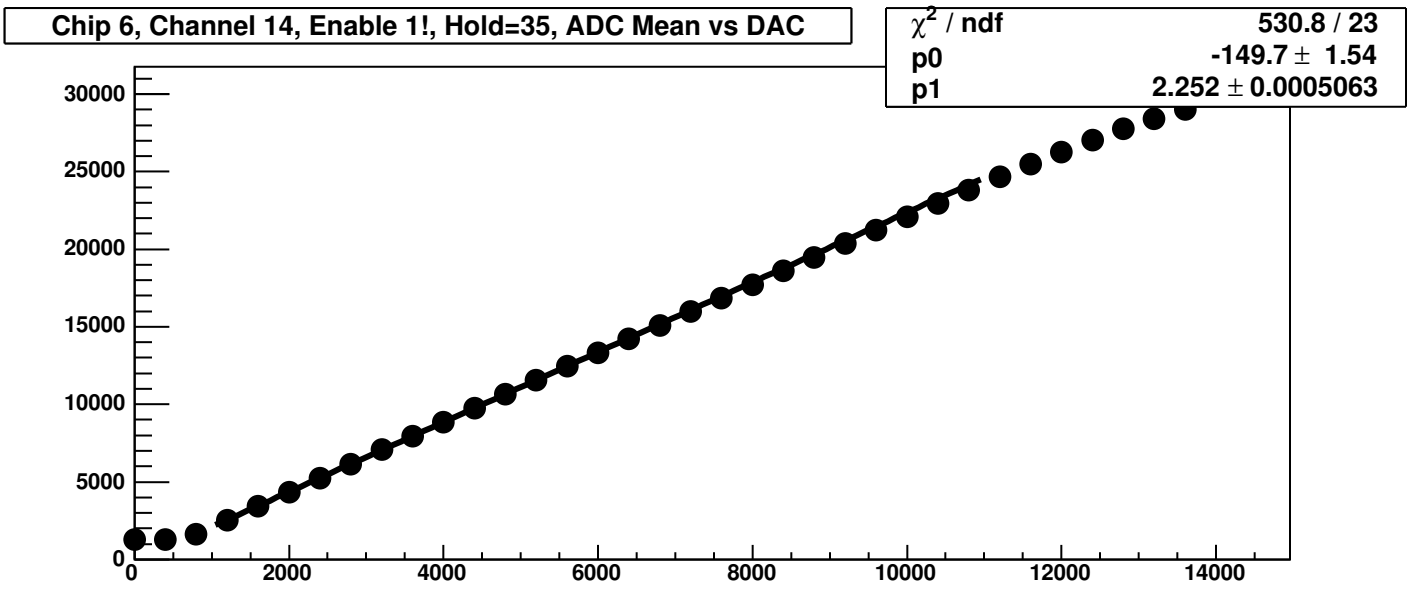

Chip 6, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

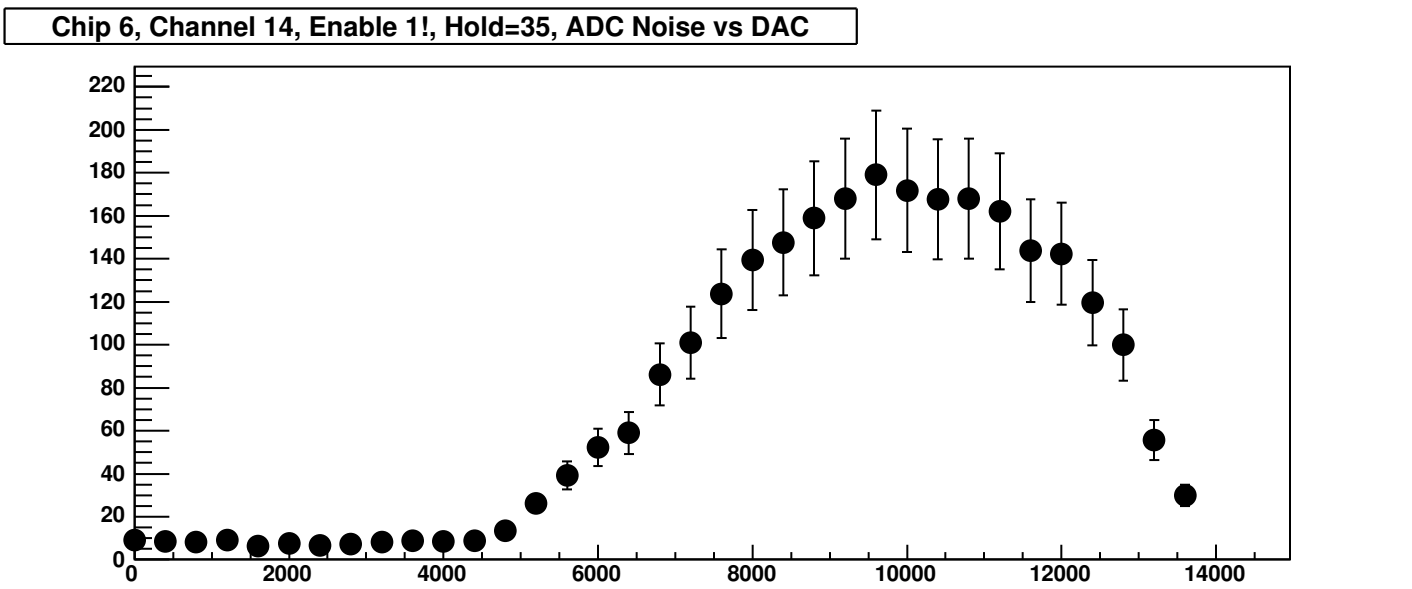

Chip 6, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

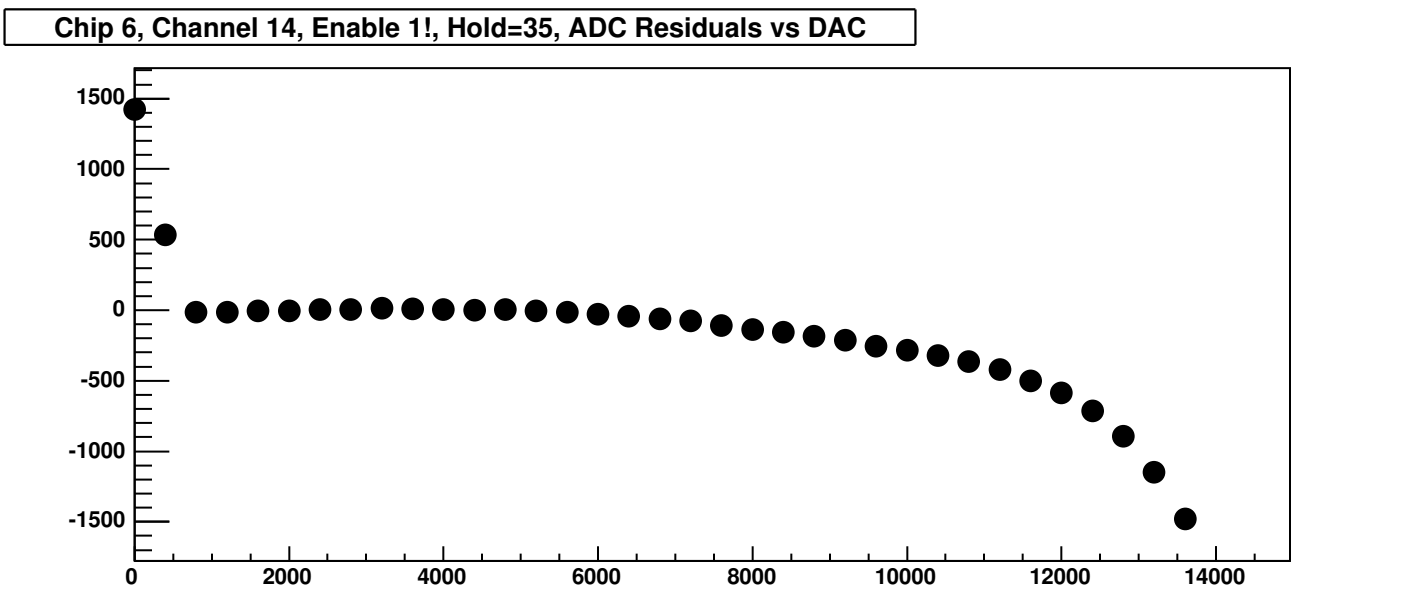

Chip 6, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.751e+05 / 23
p0 -788.9 ± 0.8578
p1 0.2707 ± 0.000197
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

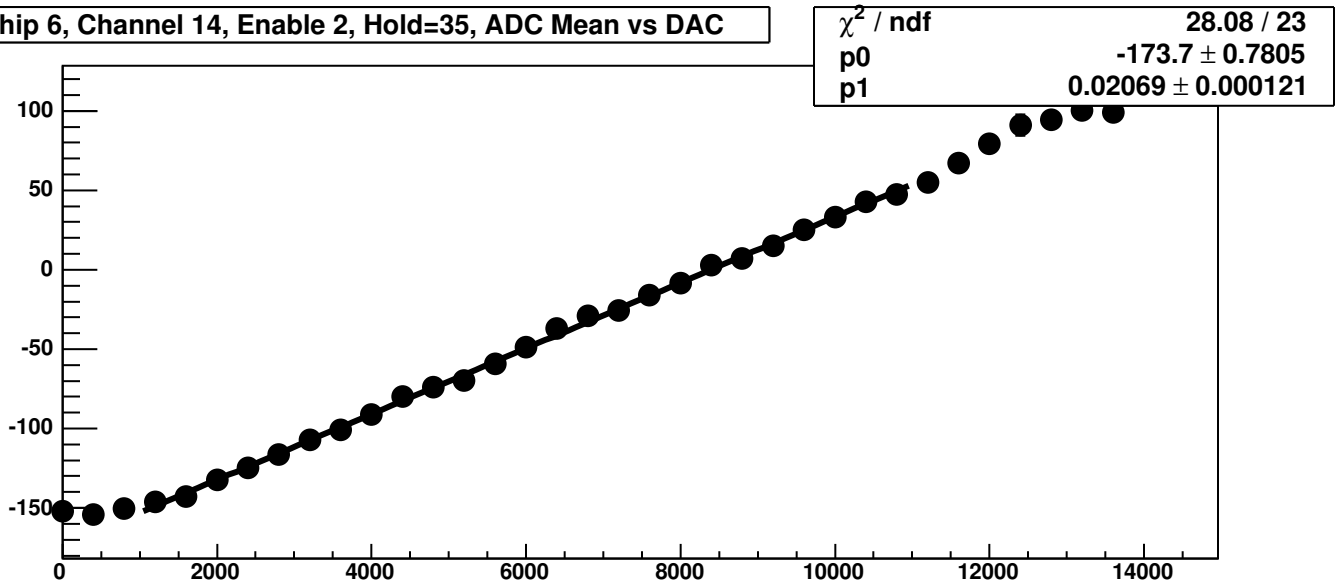

Chip 6, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 530.8 / 23
p0 -149.7 ± 1.54
p1 2.252 ± 0.0005063
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

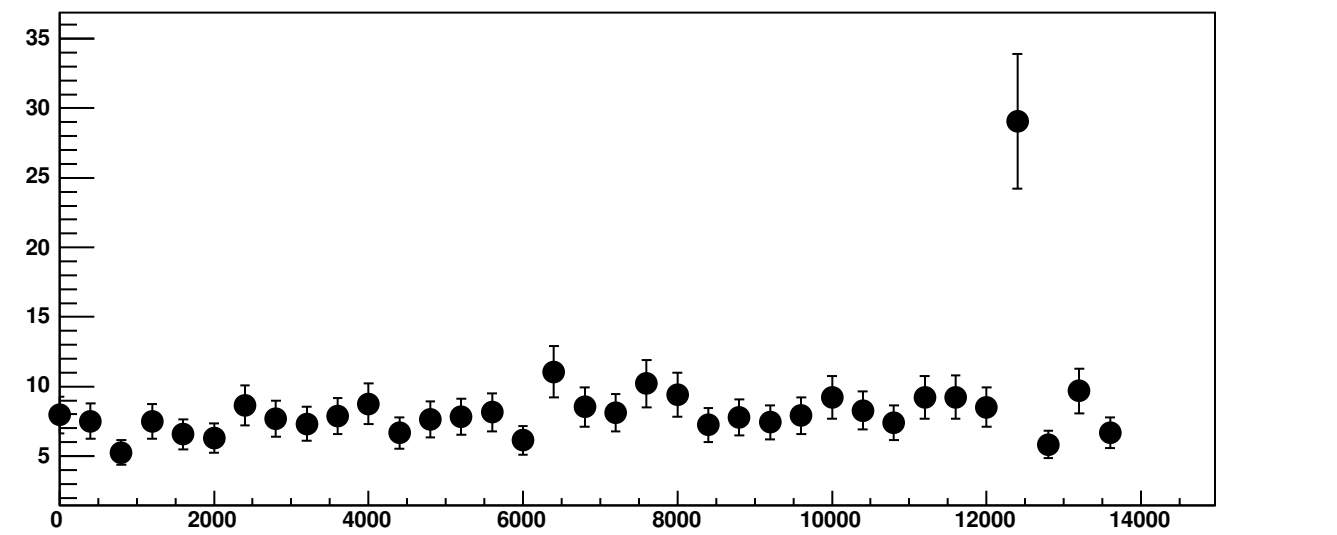

Chip 6, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

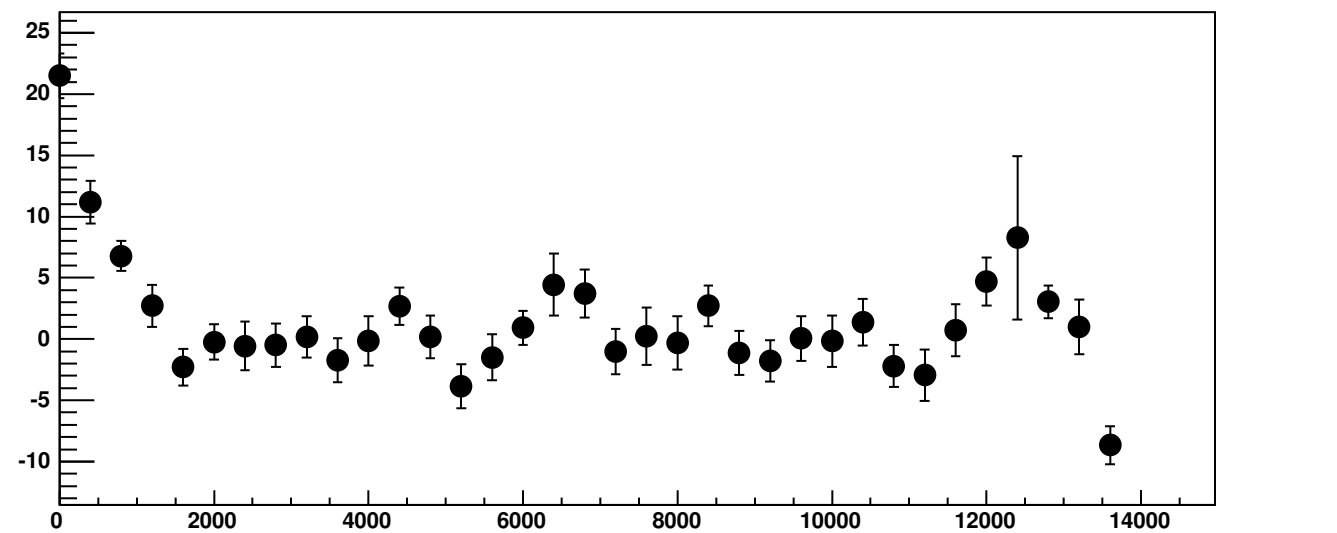
Chip 6, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



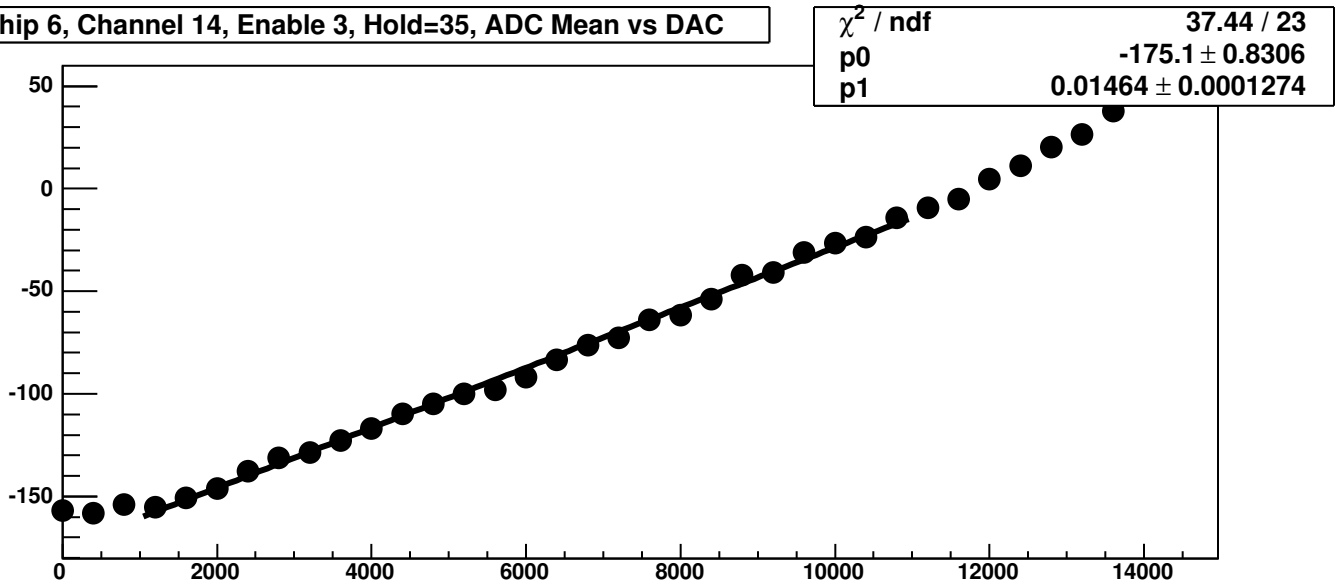
Chip 6, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



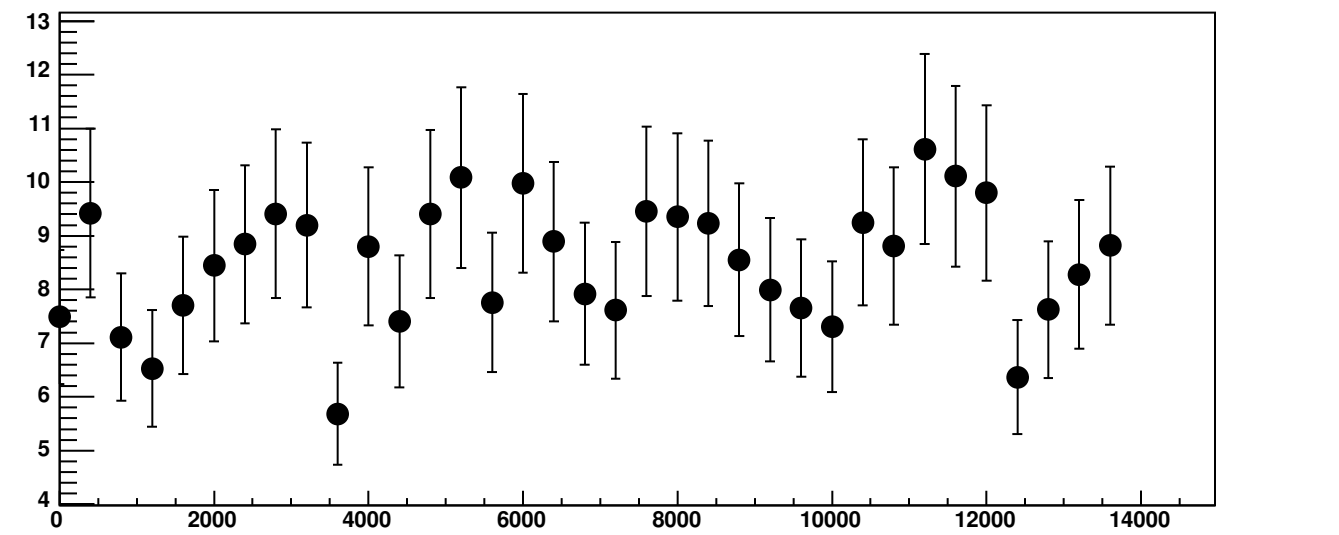
Chip 6, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



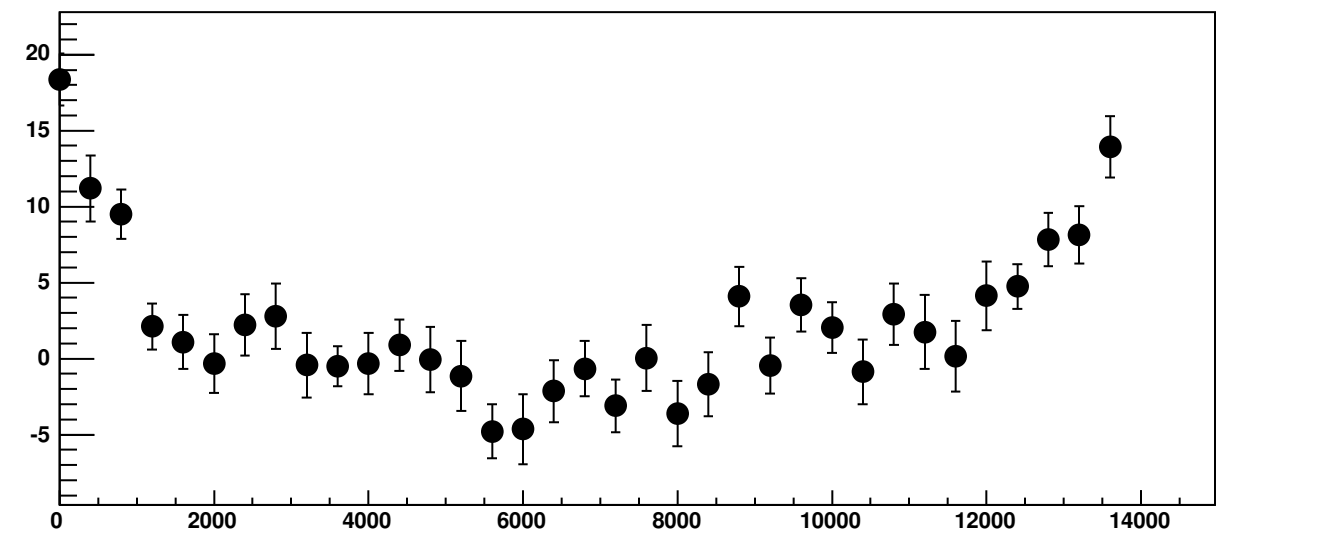
Chip 6, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



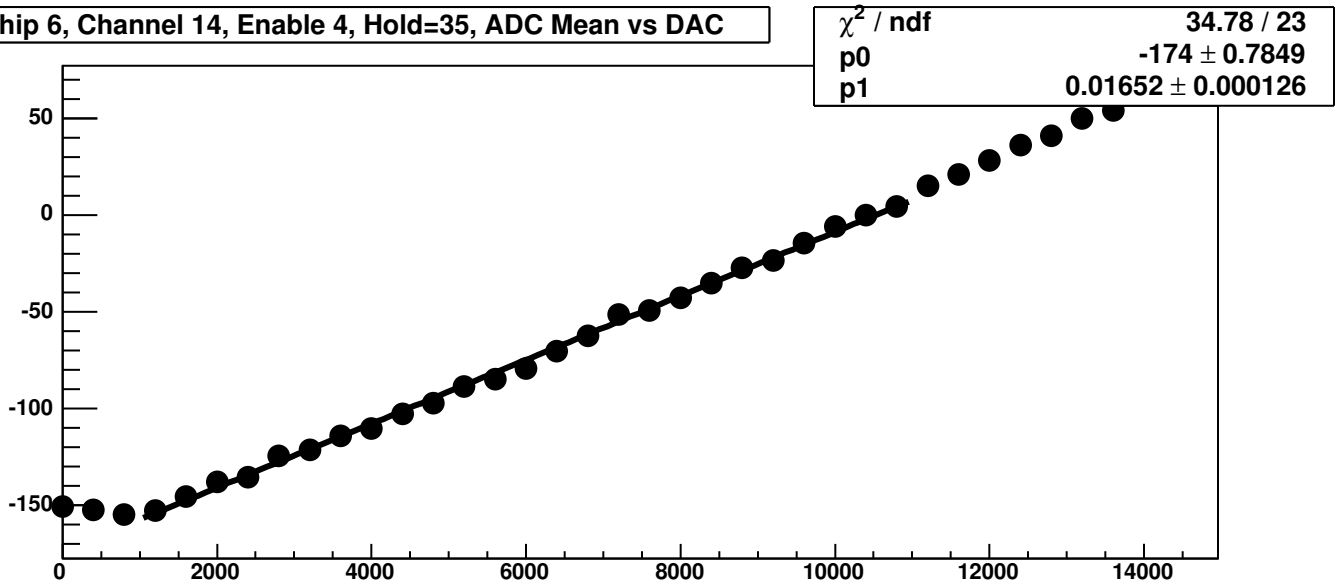
Chip 6, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



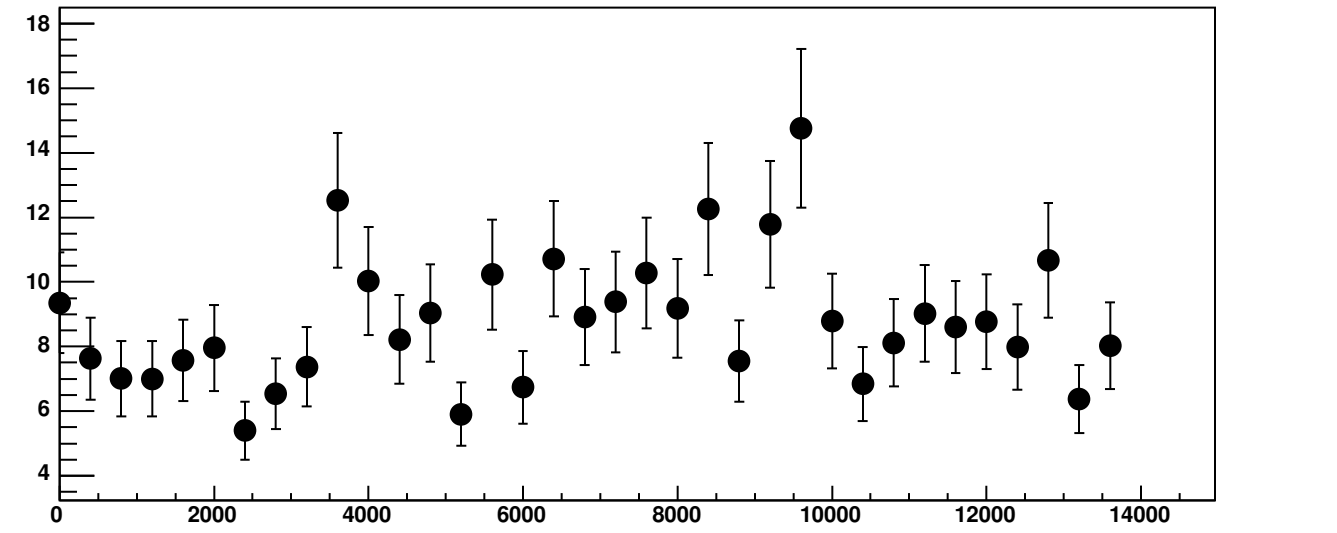
Chip 6, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



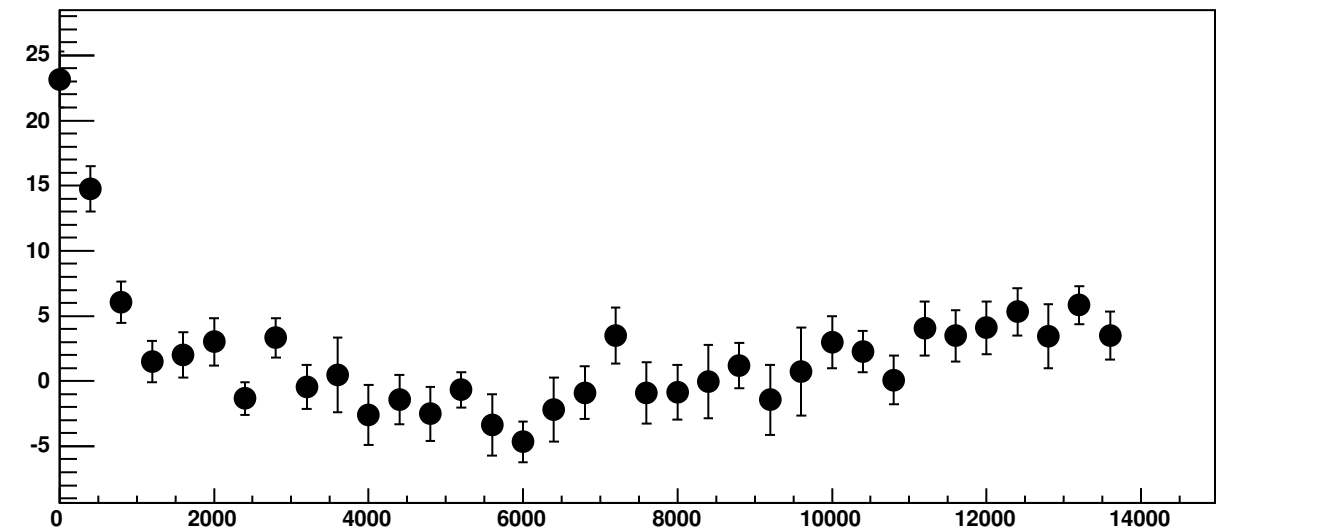
Chip 6, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

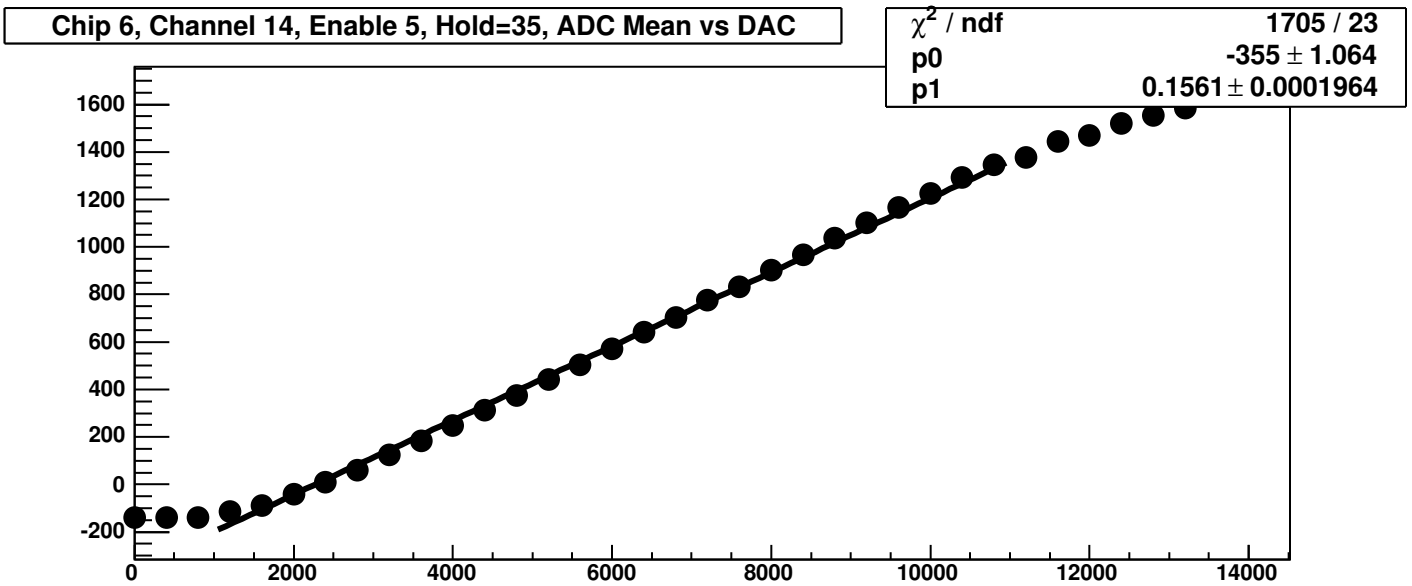


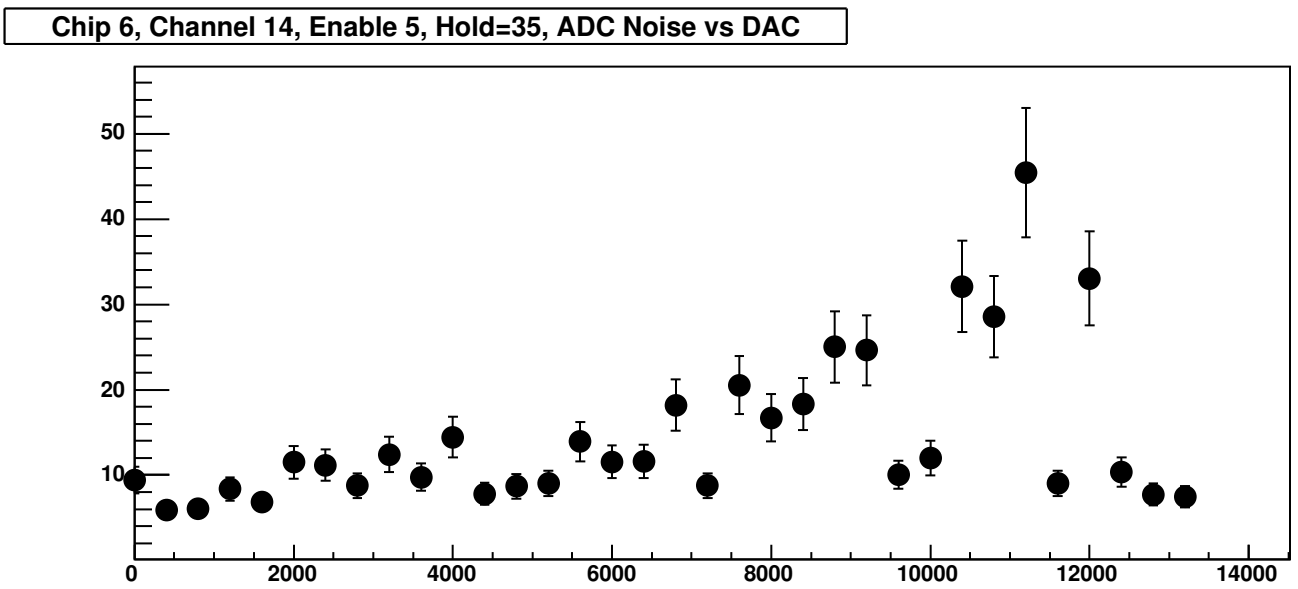
Chip 6, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

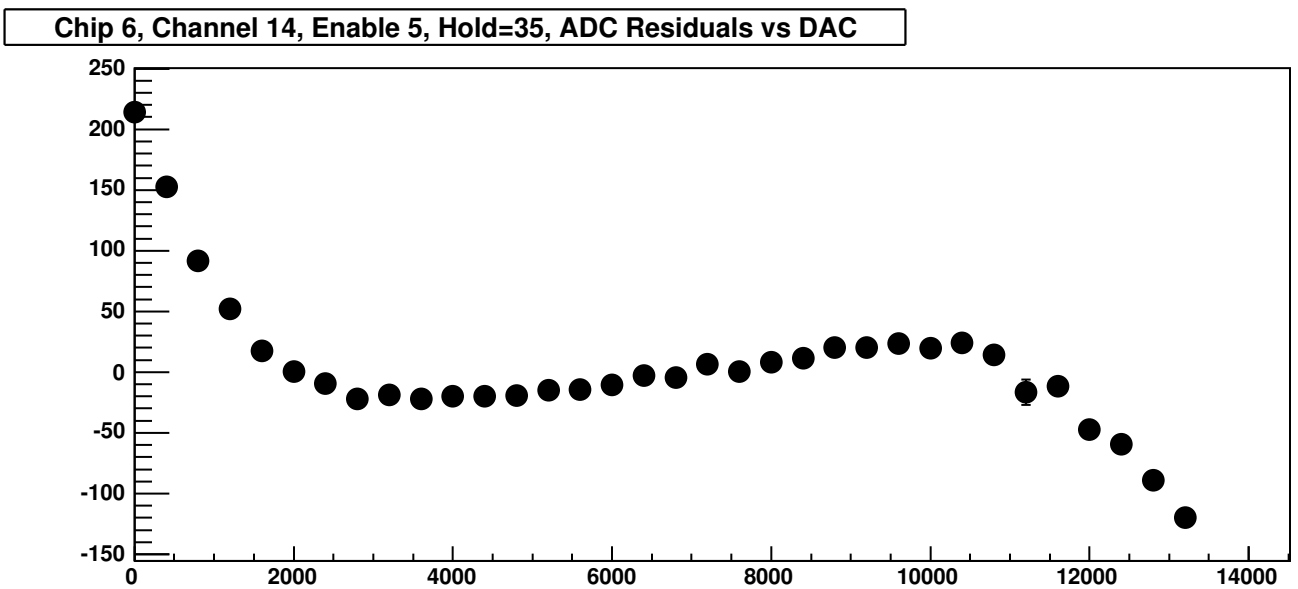


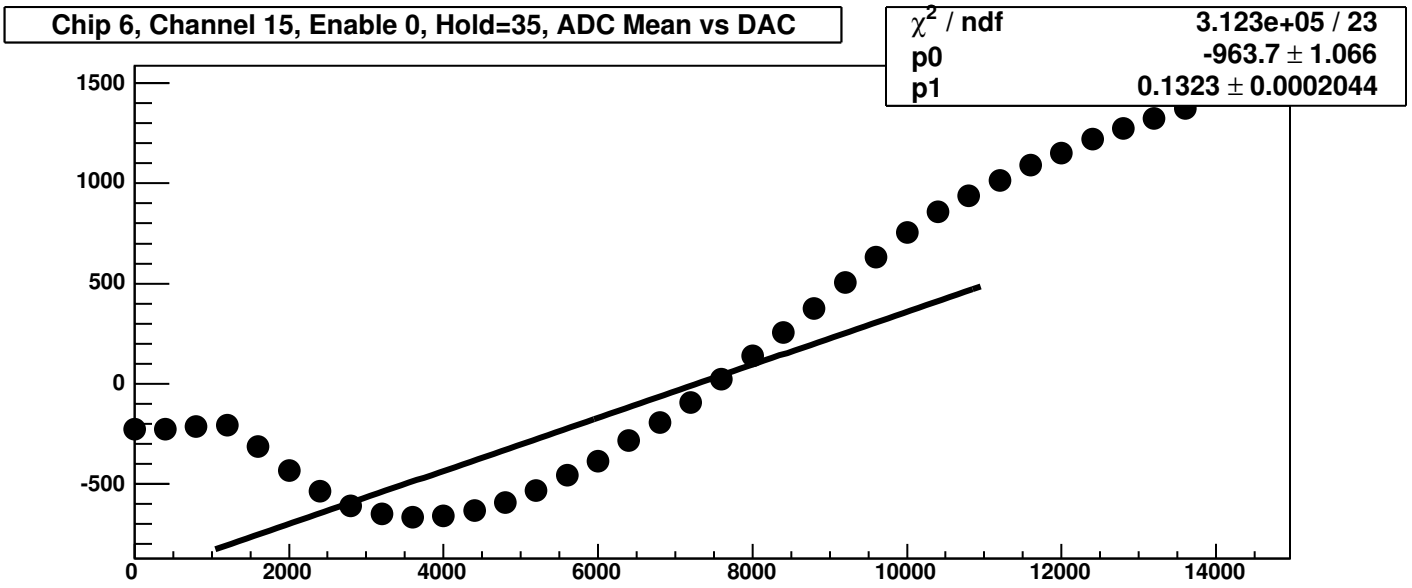
Chip 6, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

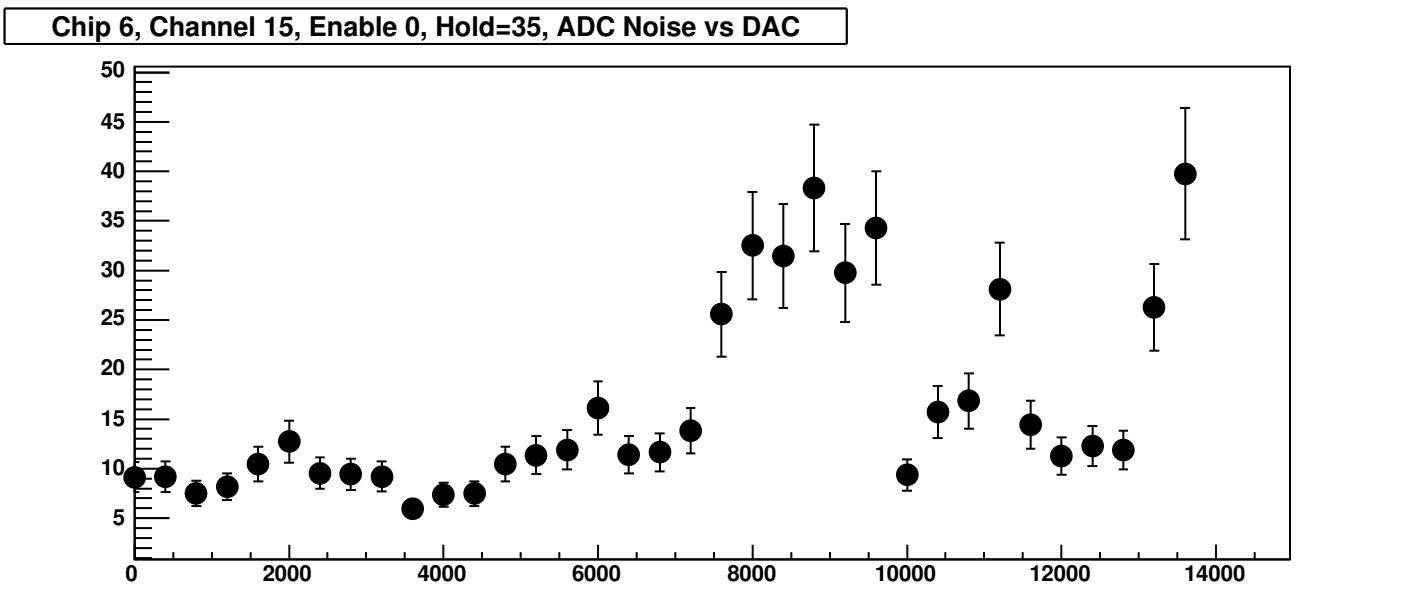


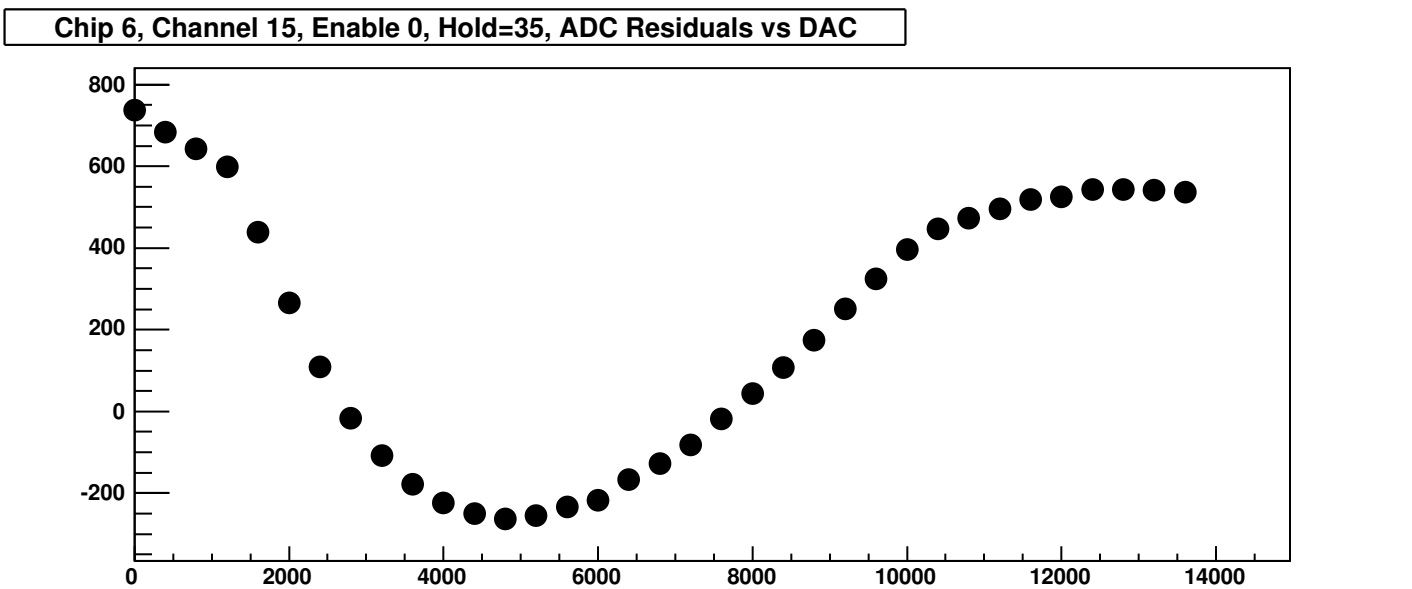

Chip 6, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1705 / 23
p0 -355 ± 1.064
p1 0.1561 ± 0.0001964
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

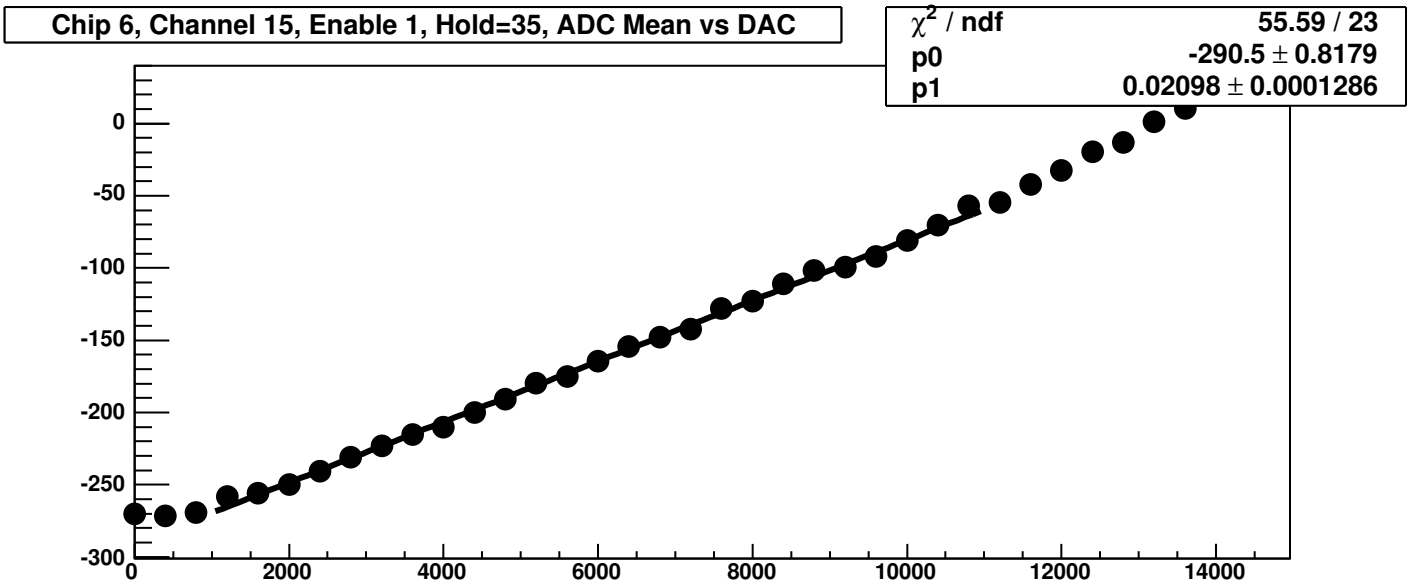

Chip 6, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

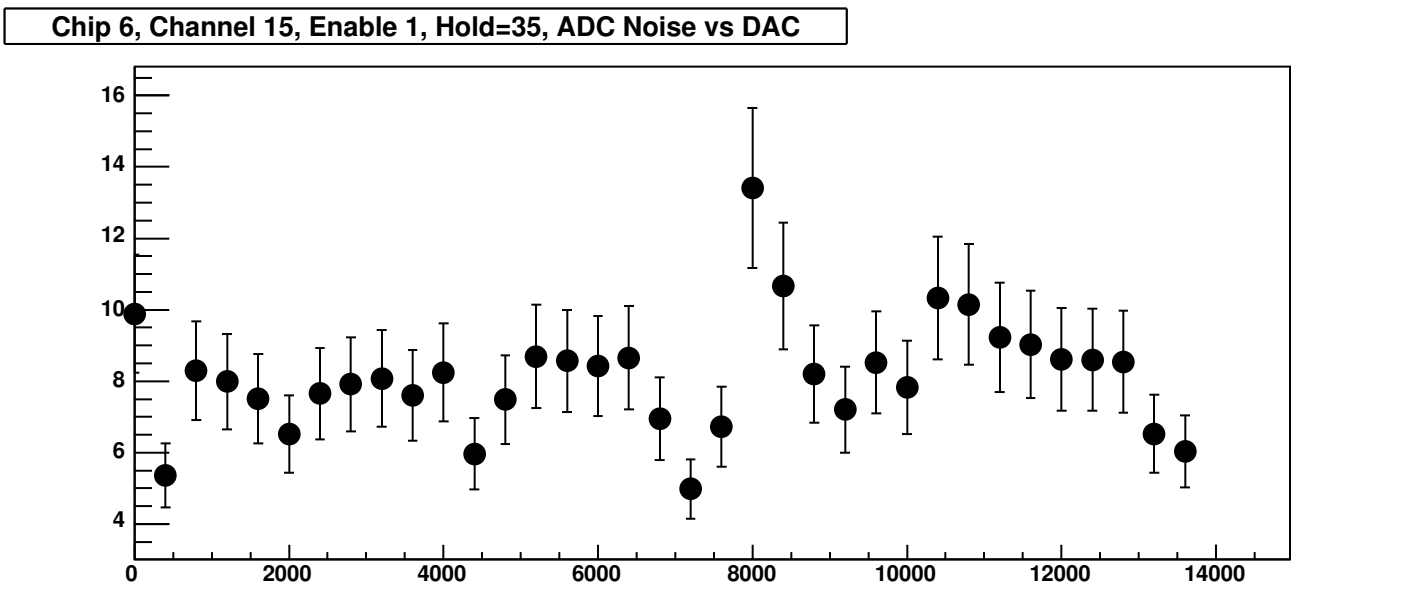

Chip 6, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

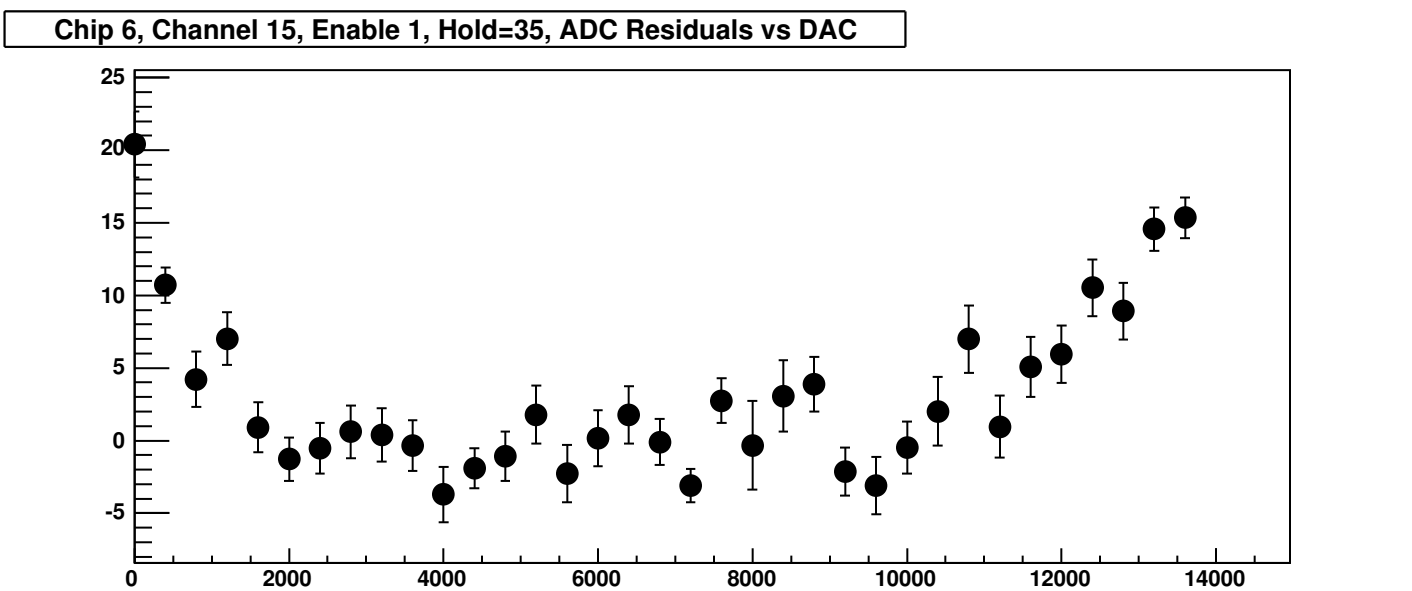

Chip 6, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.123e+05 / 23
p0 -963.7 ± 1.066
p1 0.1323 ± 0.0002044
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

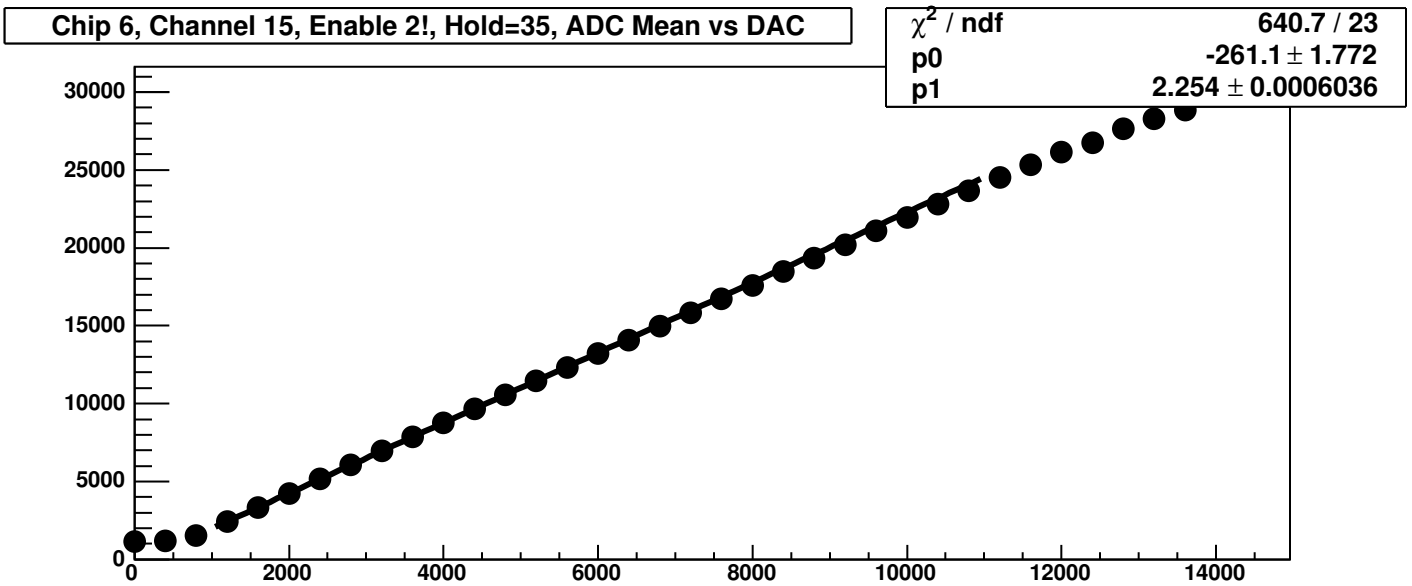

Chip 6, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

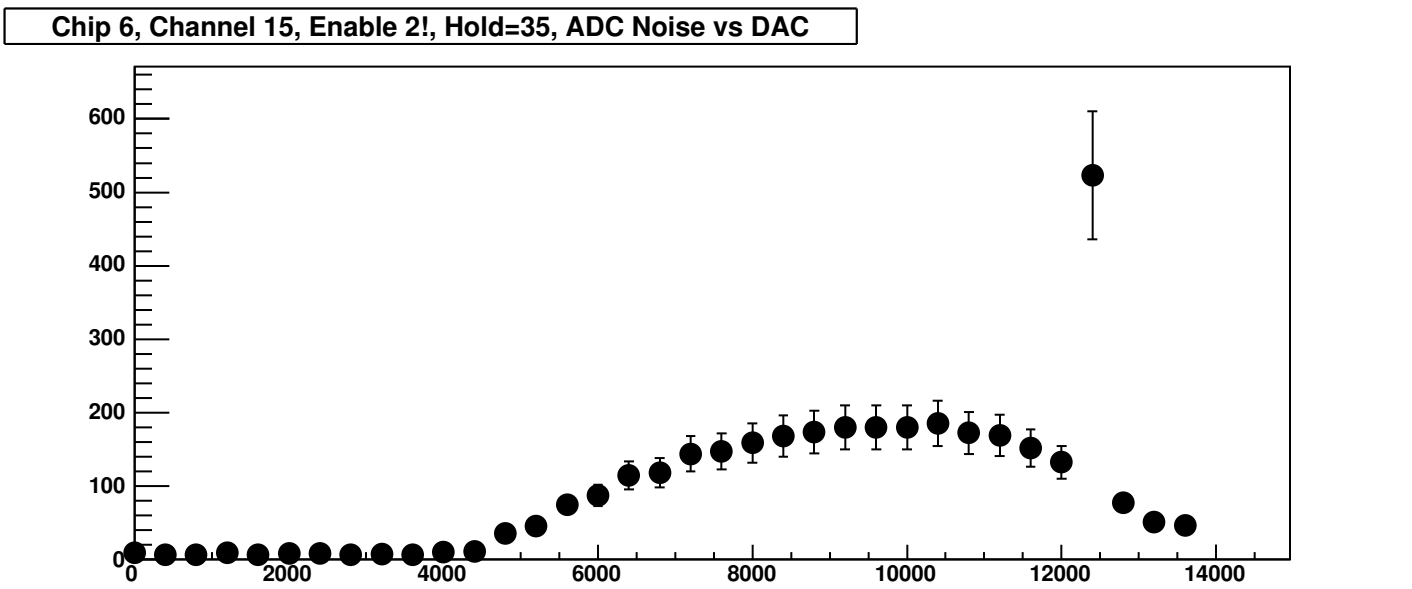

Chip 6, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

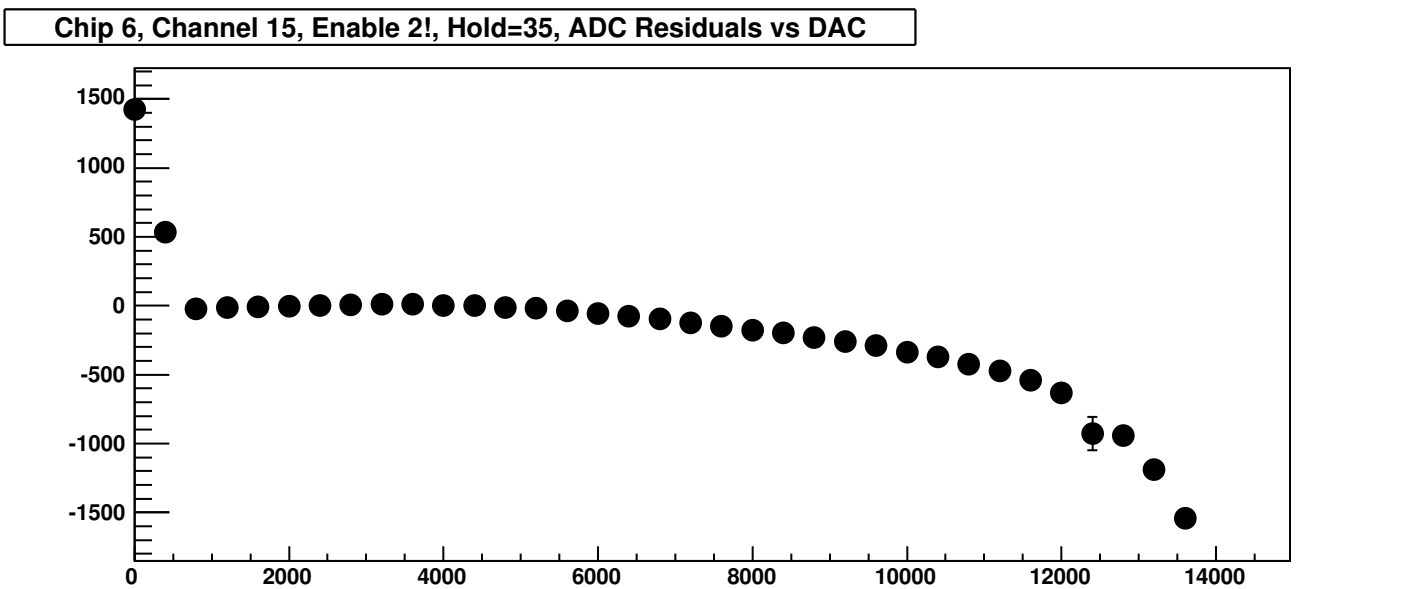

Chip 6, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.59 / 23
p0 -290.5 ± 0.8179
p1 0.02098 ± 0.0001286
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

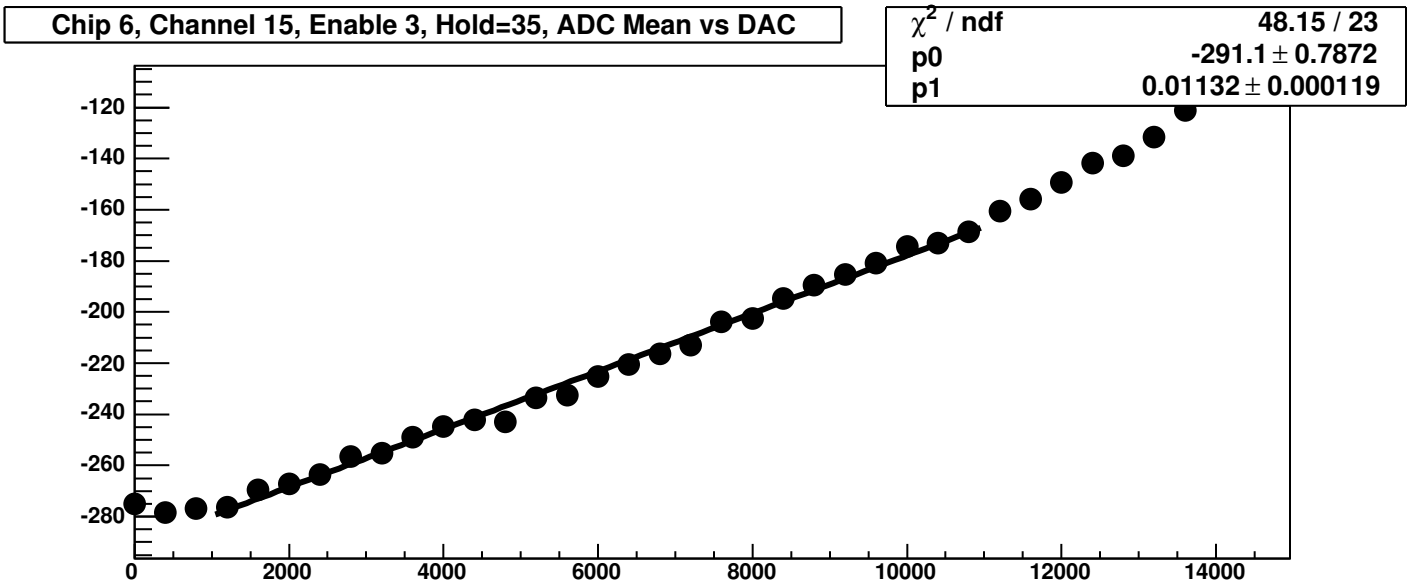

Chip 6, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

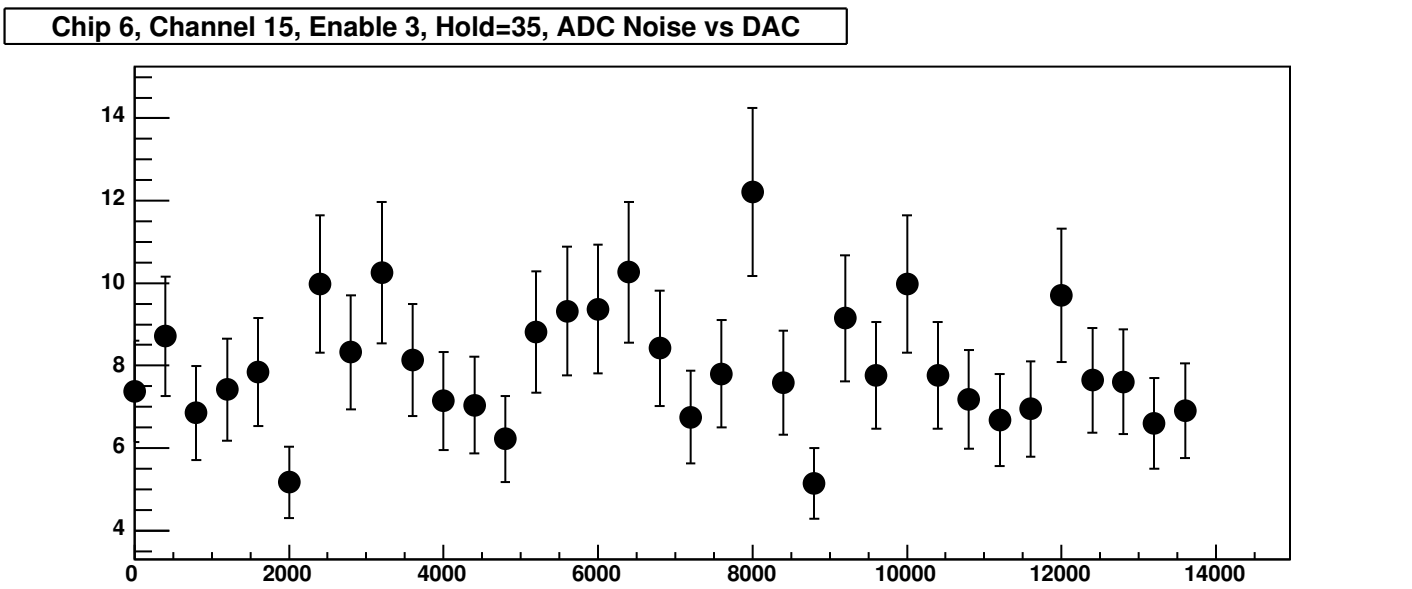

Chip 6, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

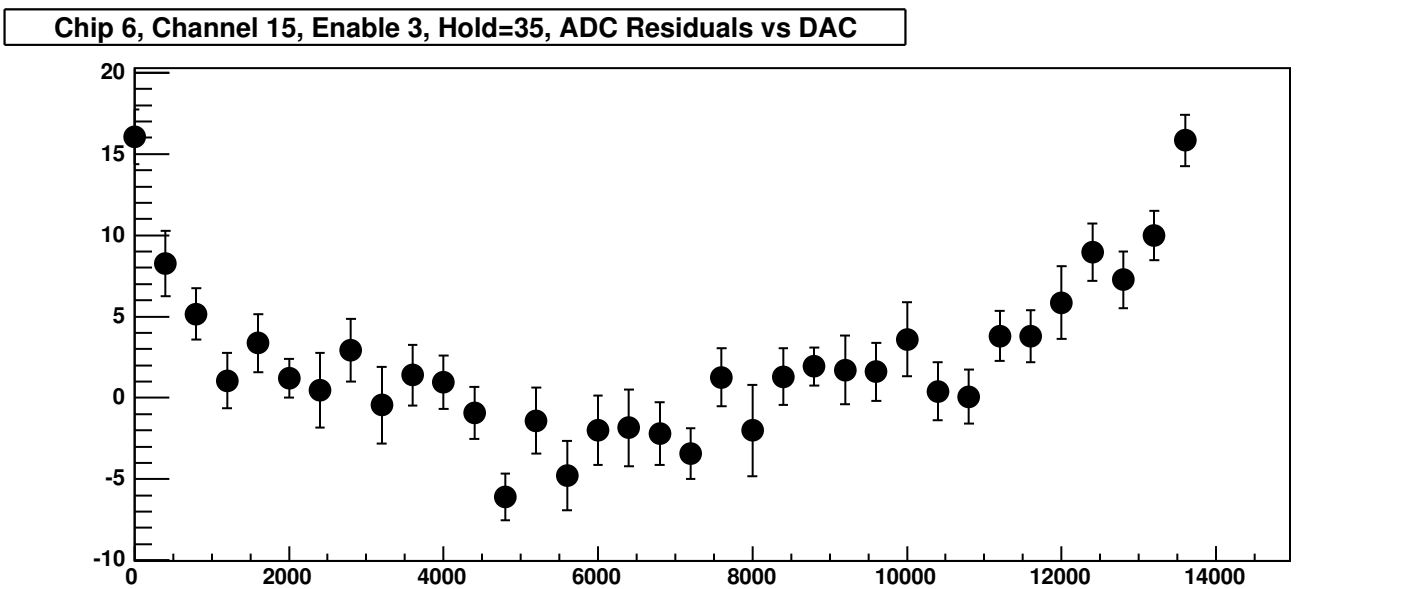

Chip 6, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 640.7 / 23
p0 -261.1 ± 1.772
p1 2.254 ± 0.0006036
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

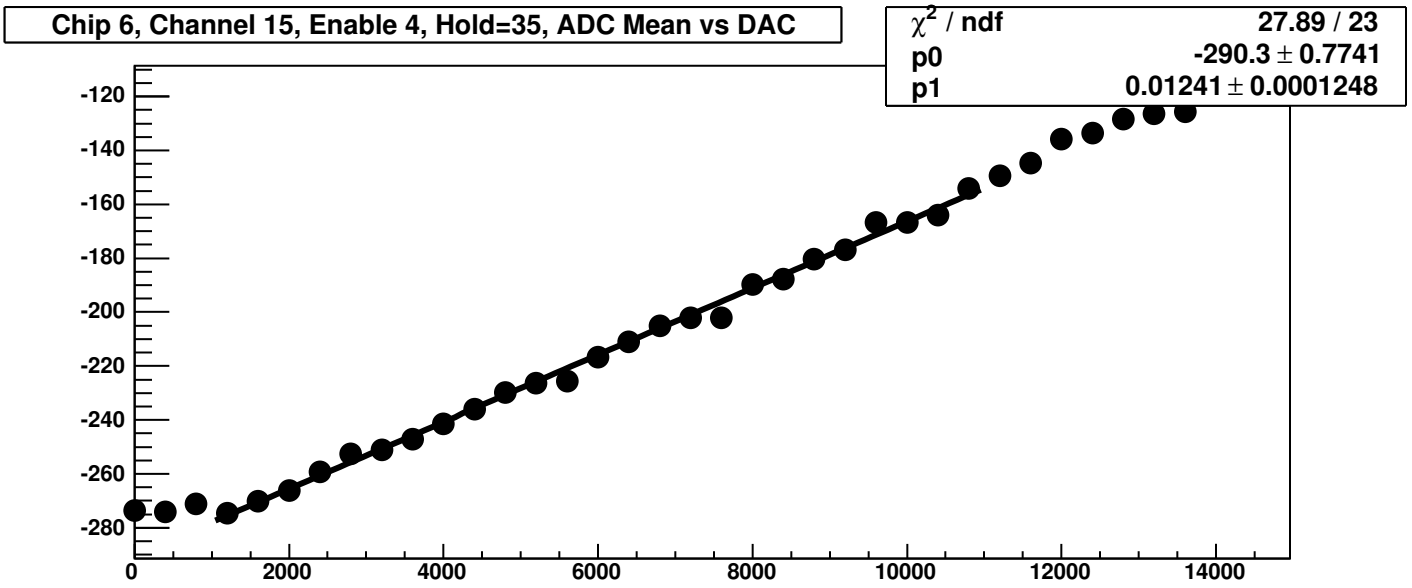

Chip 6, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

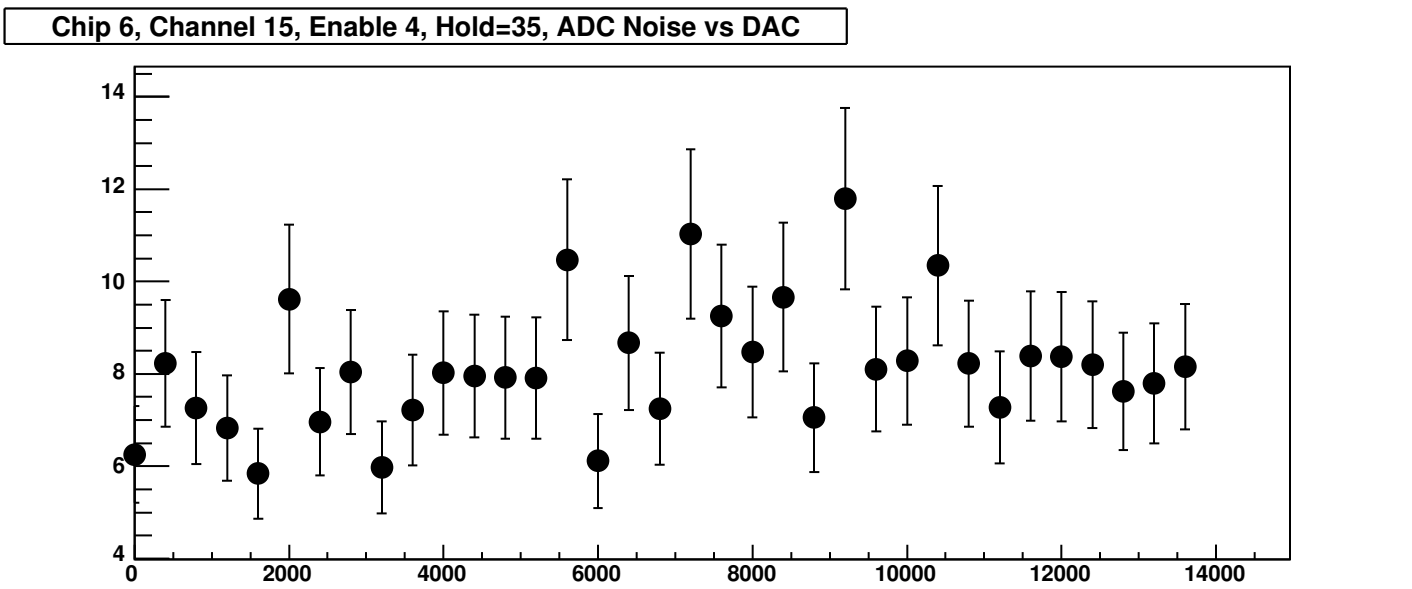

Chip 6, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

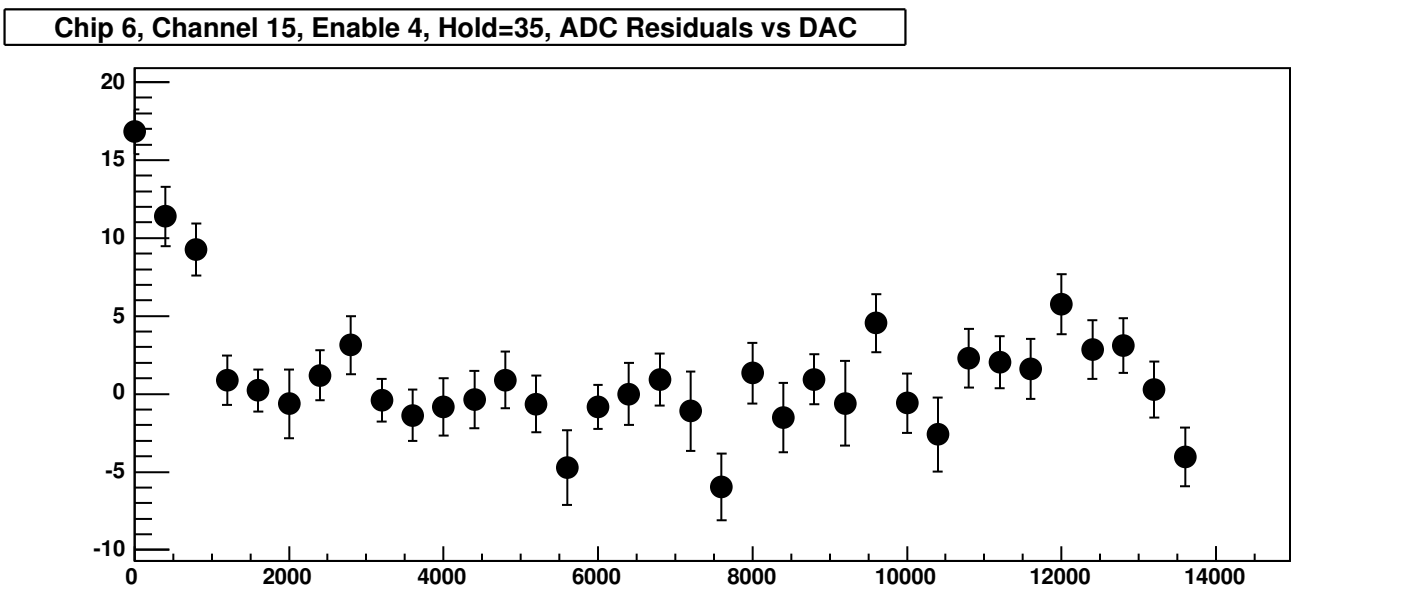

Chip 6, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 48.15 / 23
p0 -291.1 ± 0.7872
p1 0.01132 ± 0.000119
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

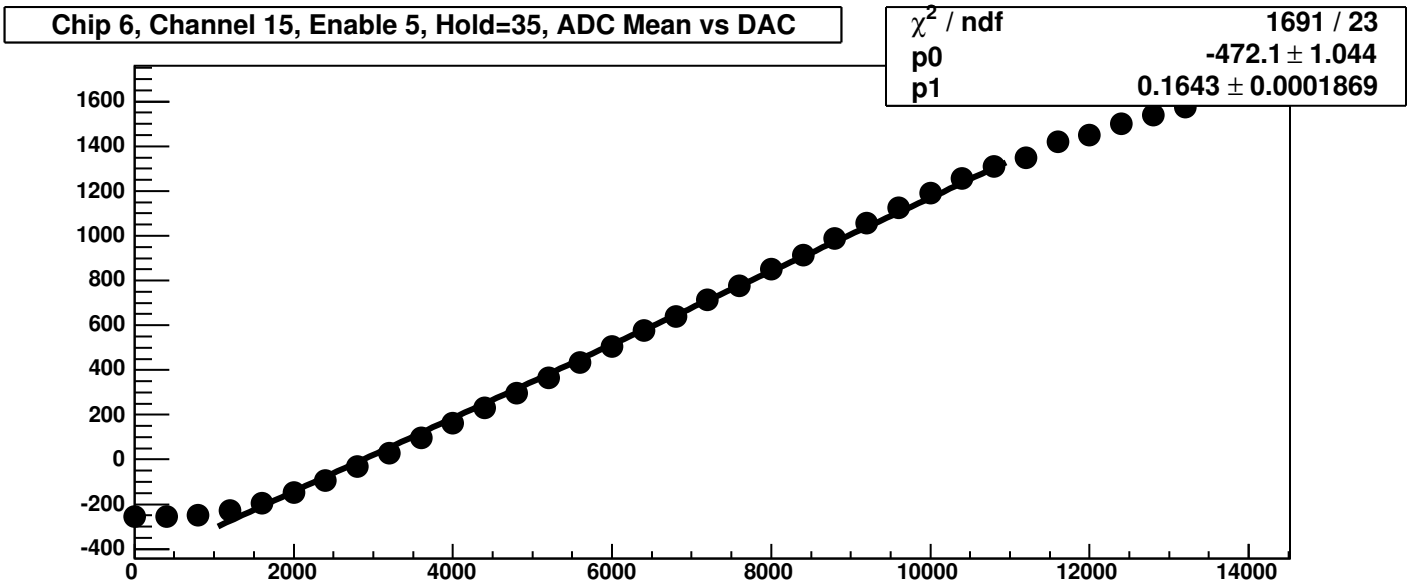

Chip 6, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

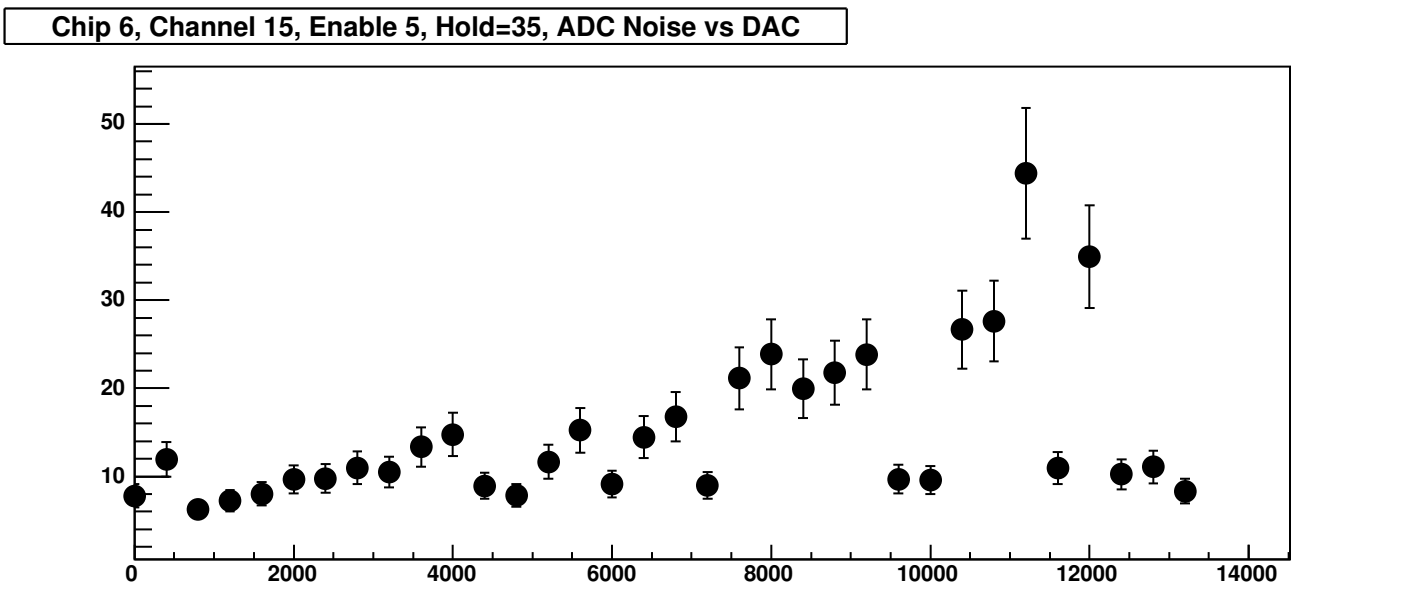

Chip 6, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

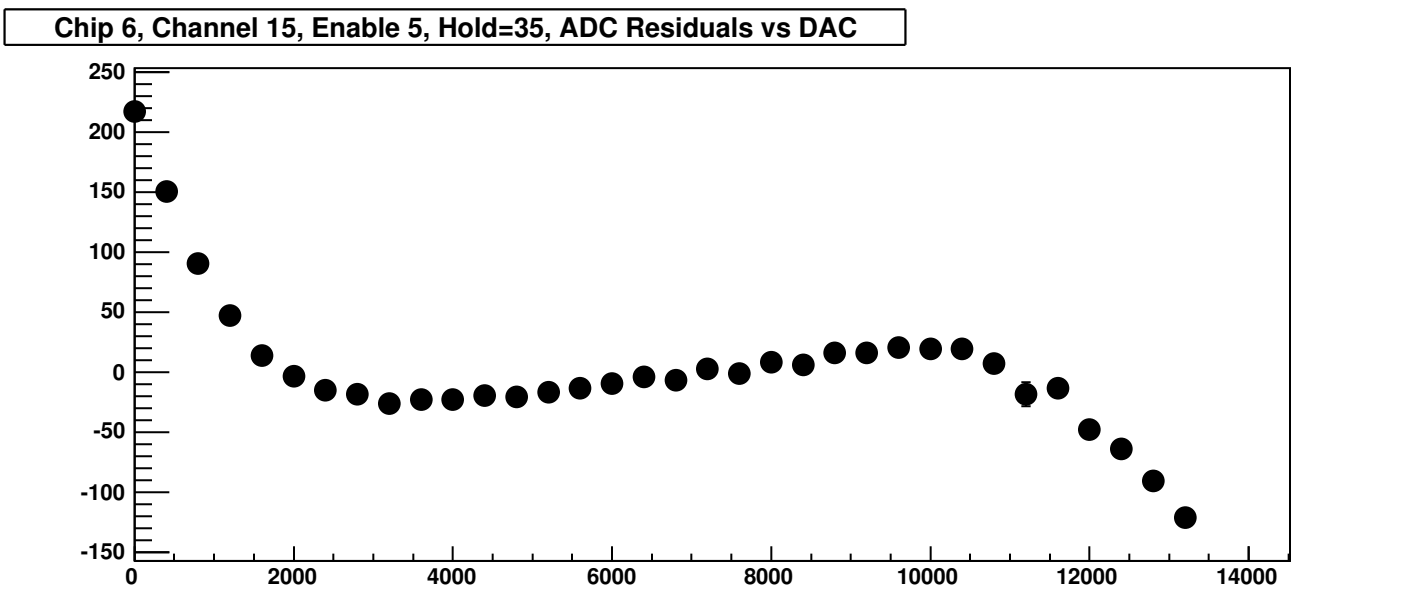

Chip 6, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.89 / 23
p0 -290.3 ± 0.7741
p1 0.01241 ± 0.0001248
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

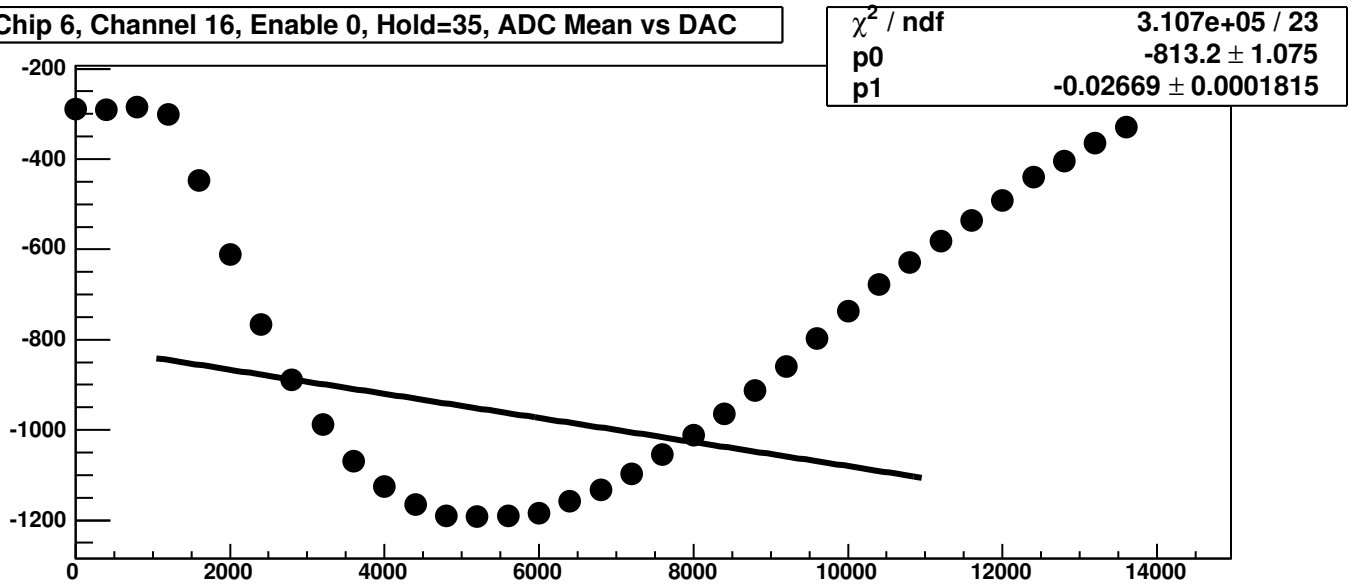

Chip 6, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1691 / 23
p0 -472.1 ± 1.044
p1 0.1643 ± 0.0001869
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

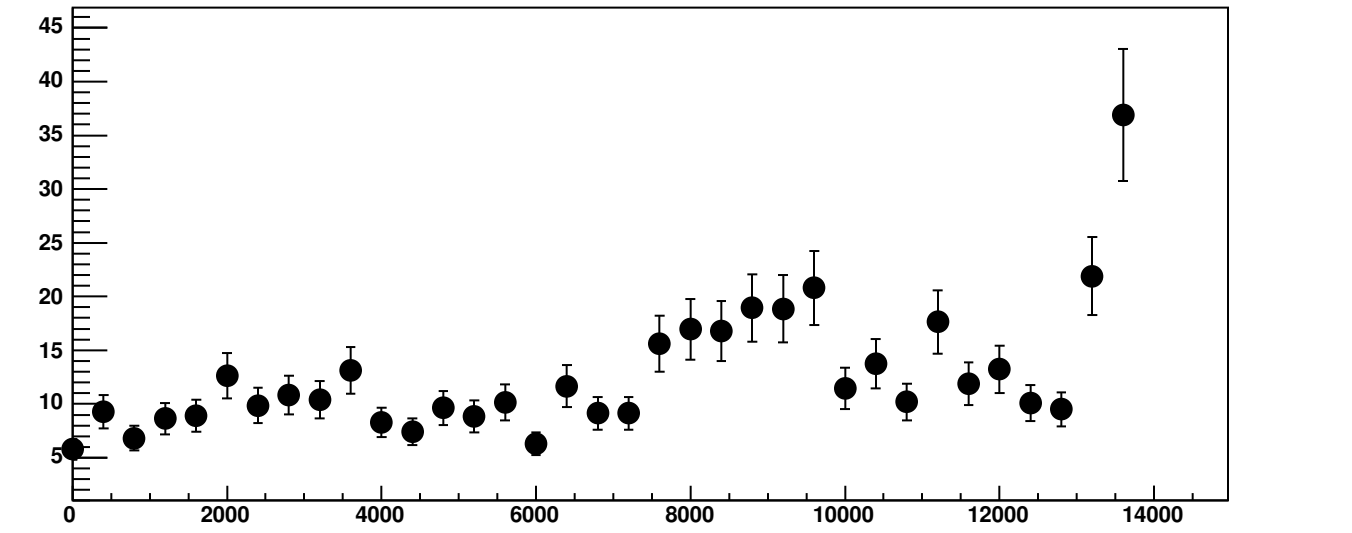

Chip 6, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

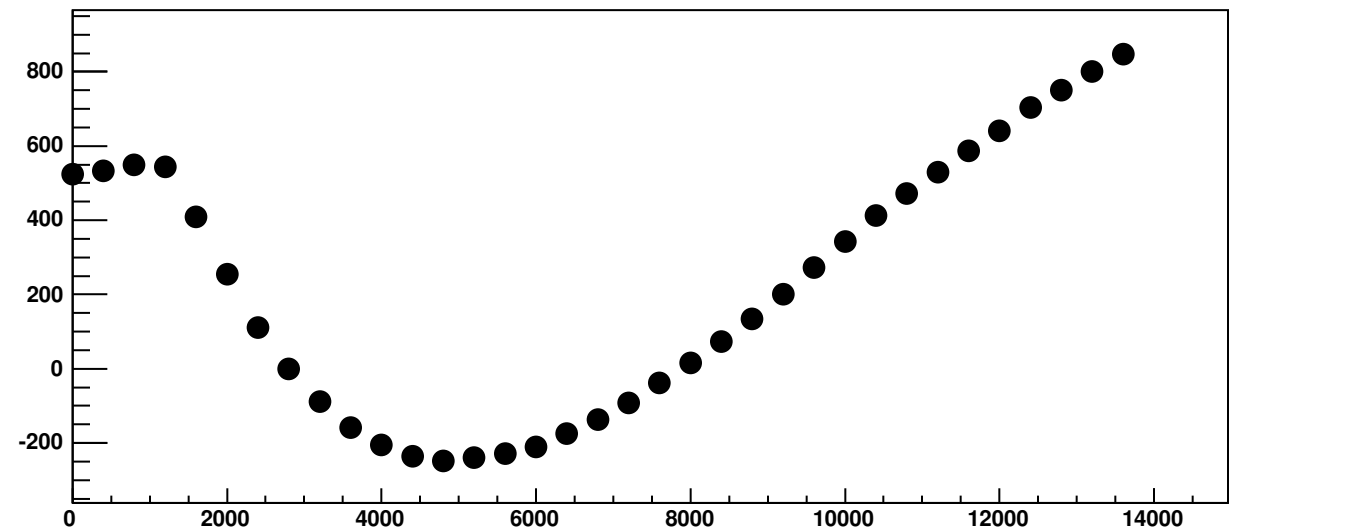
Chip 6, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

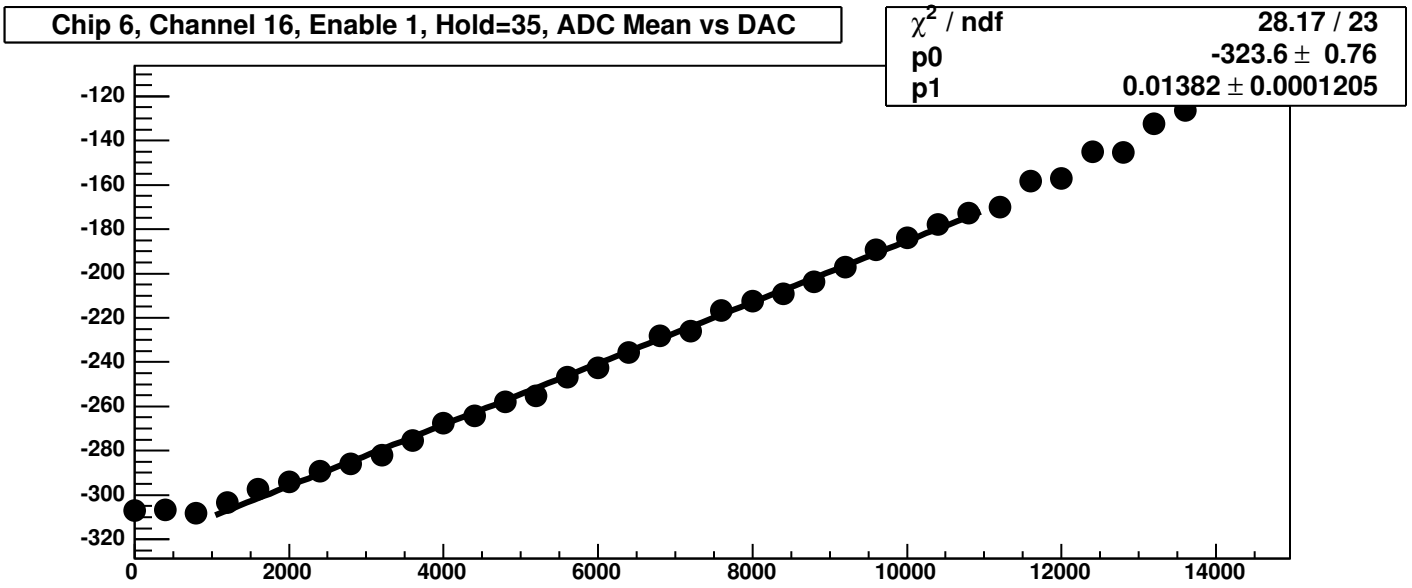


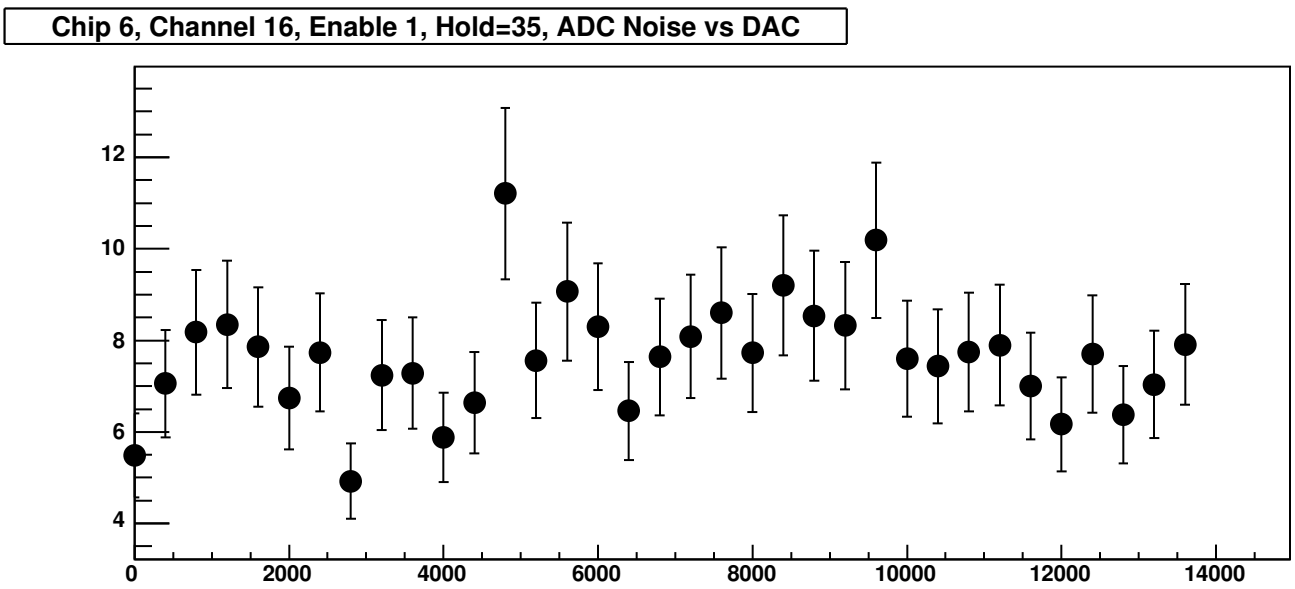
Chip 6, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

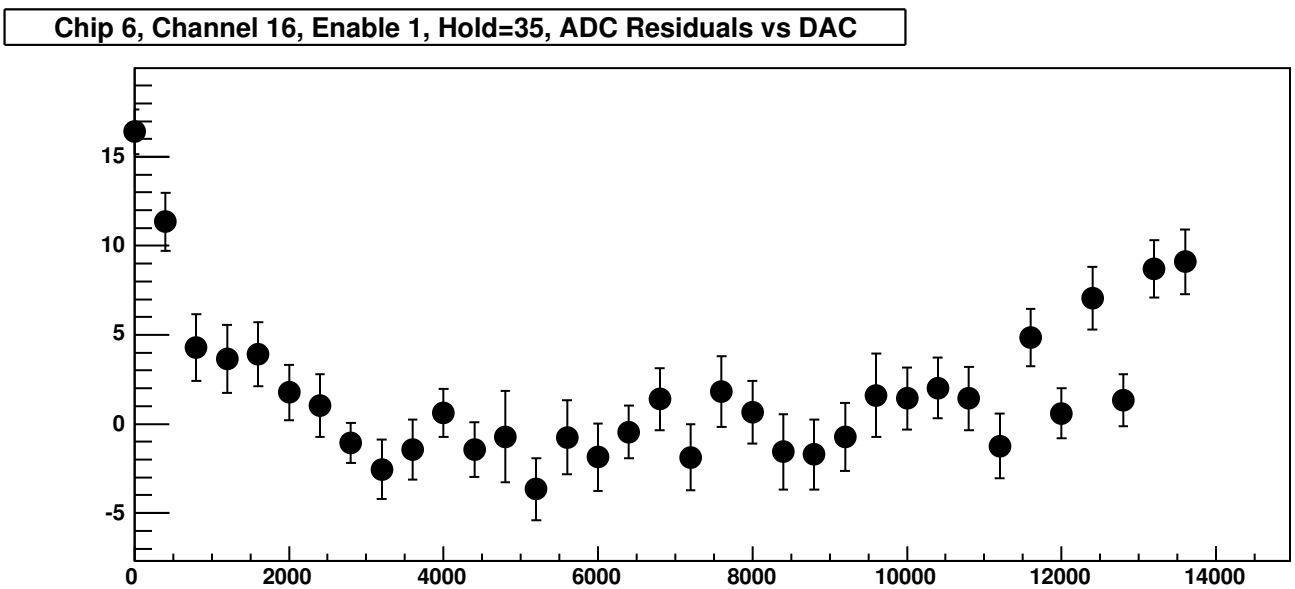


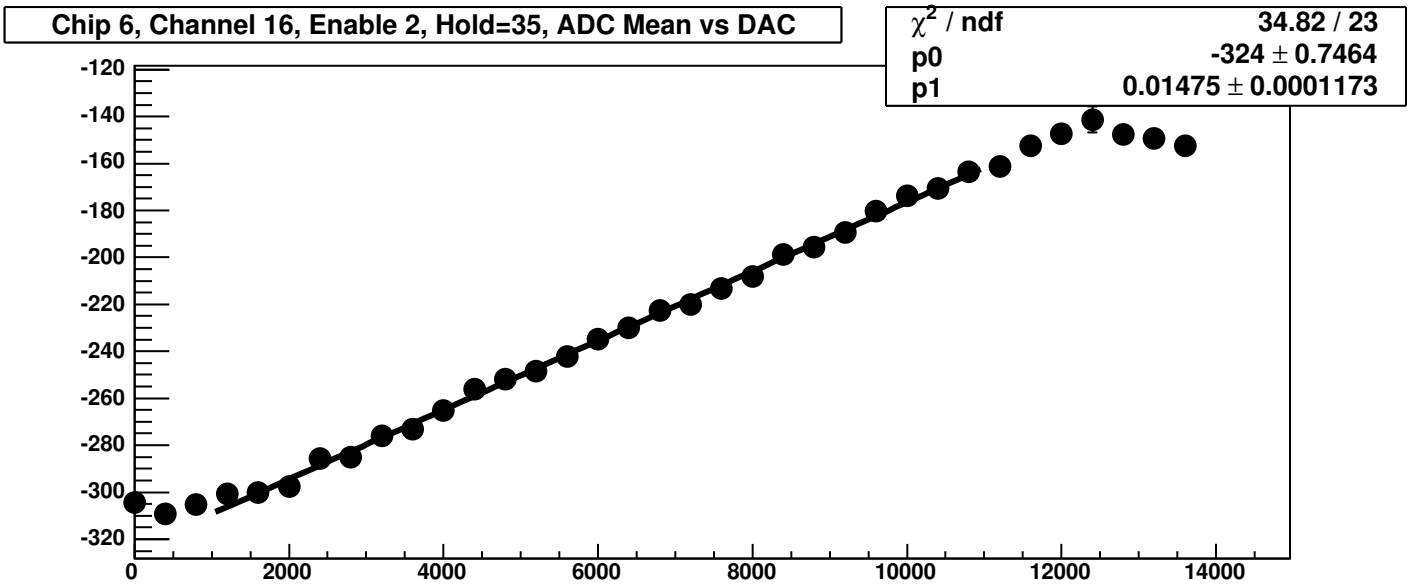
Chip 6, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

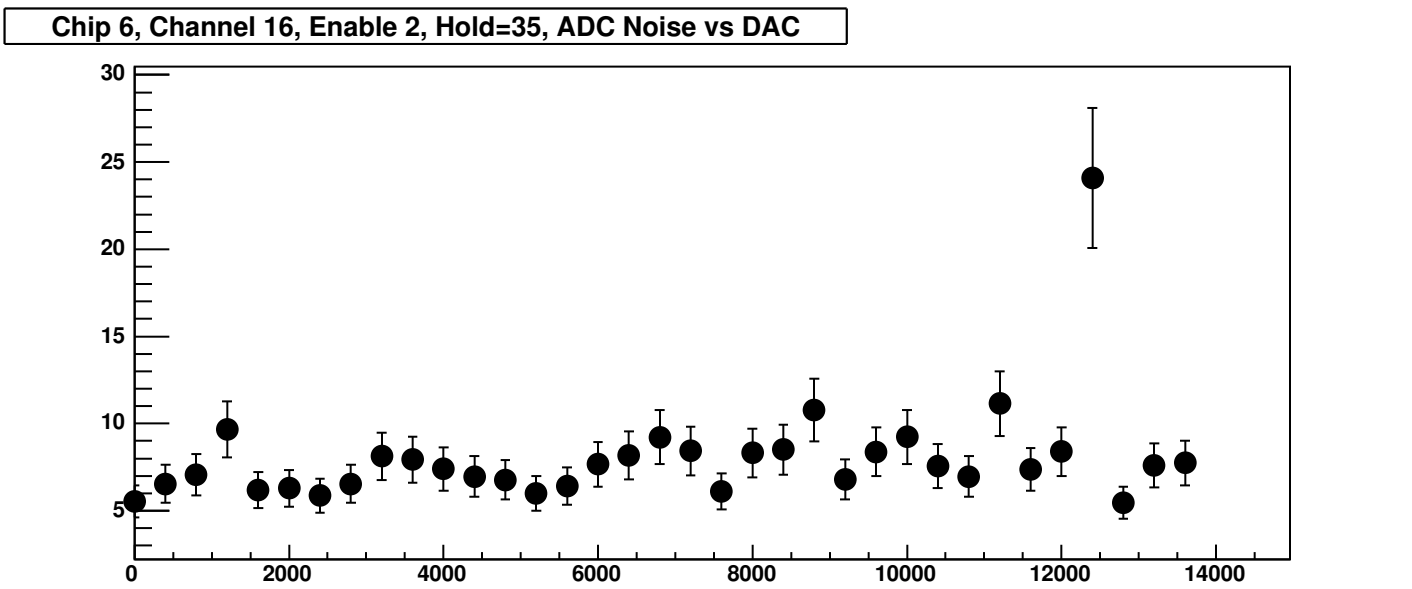


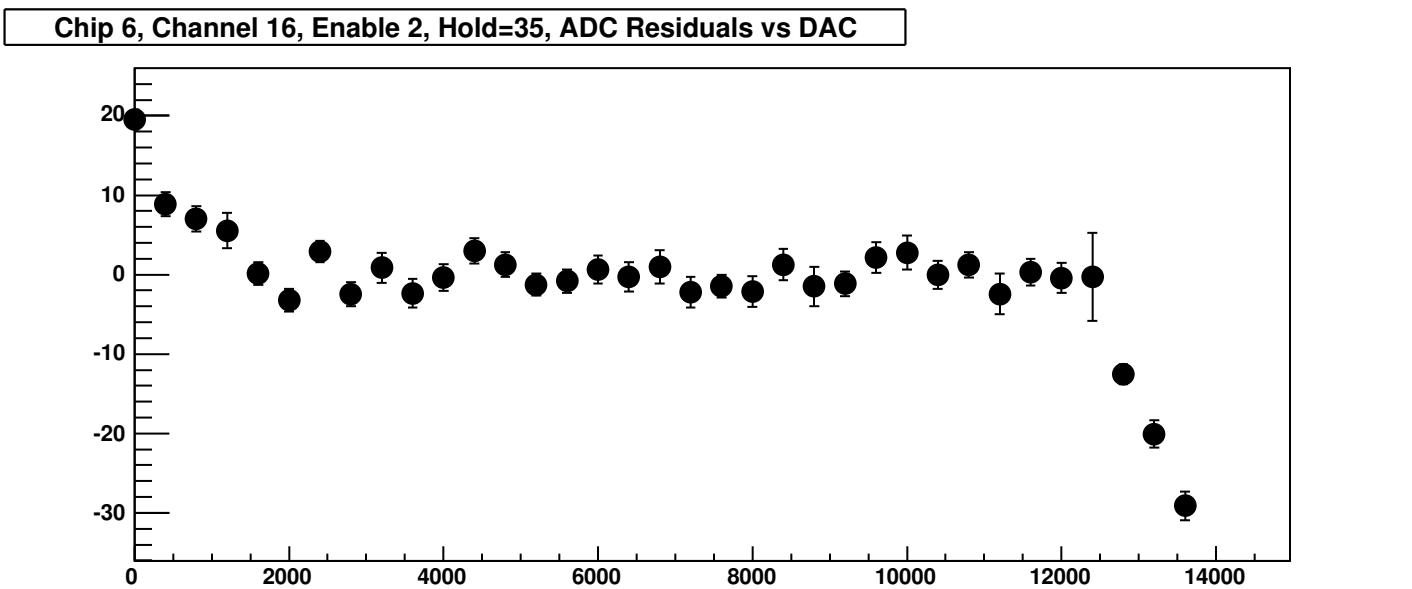

Chip 6, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.17 / 23
p0 -323.6 ± 0.76
p1 0.01382 ± 0.0001205
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

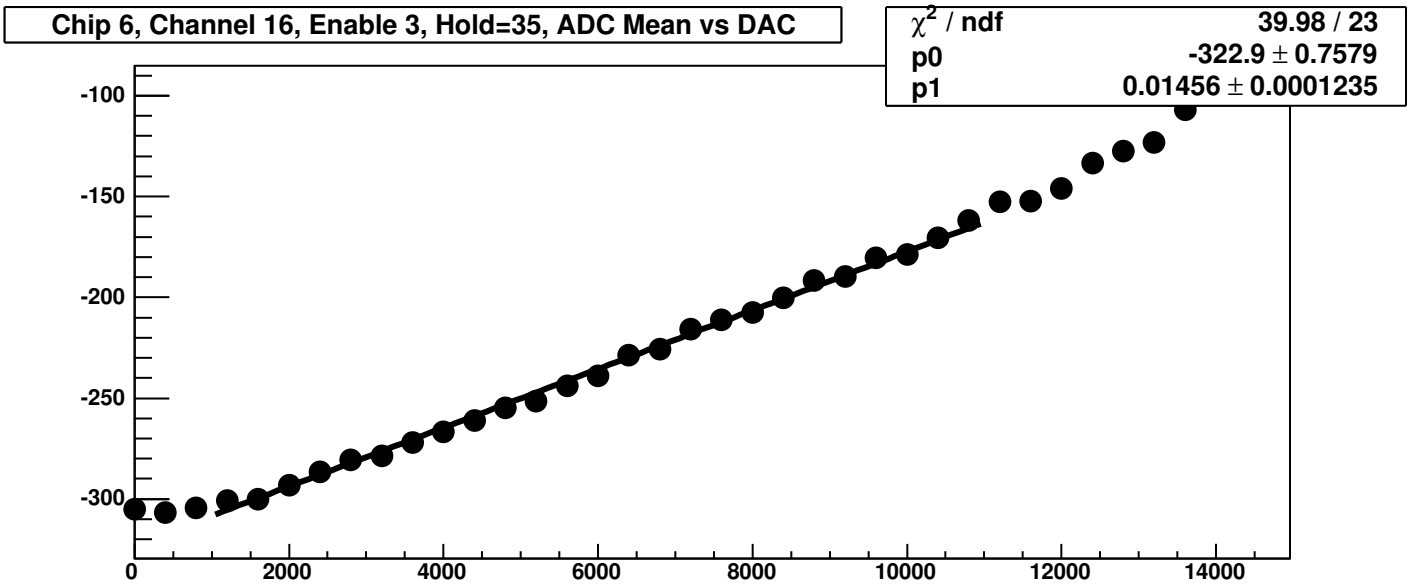

Chip 6, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

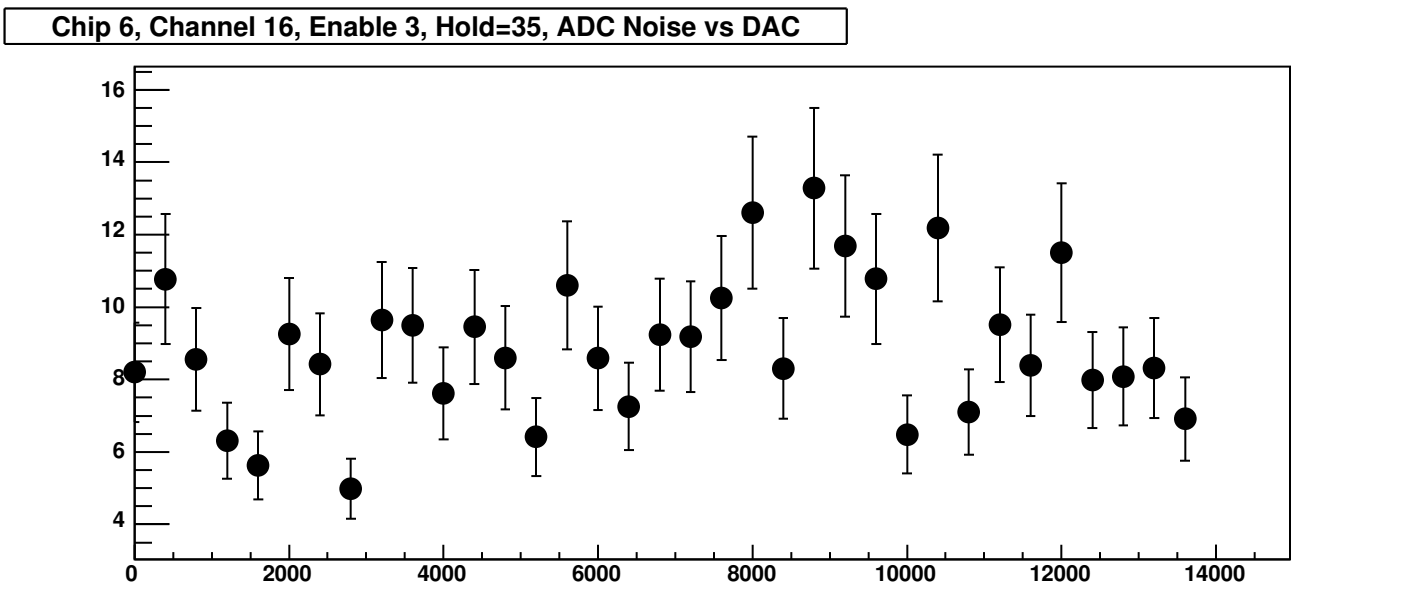

Chip 6, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

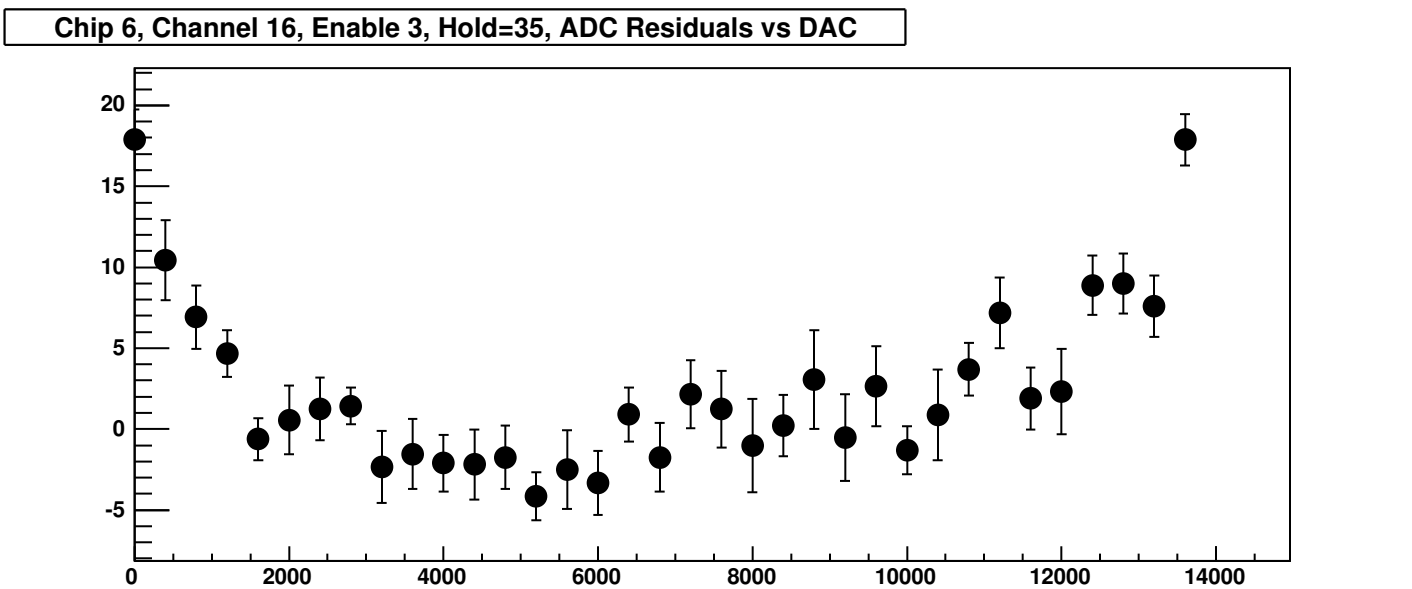

Chip 6, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.82 / 23
p0 -324 ± 0.7464
p1 0.01475 ± 0.0001173
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

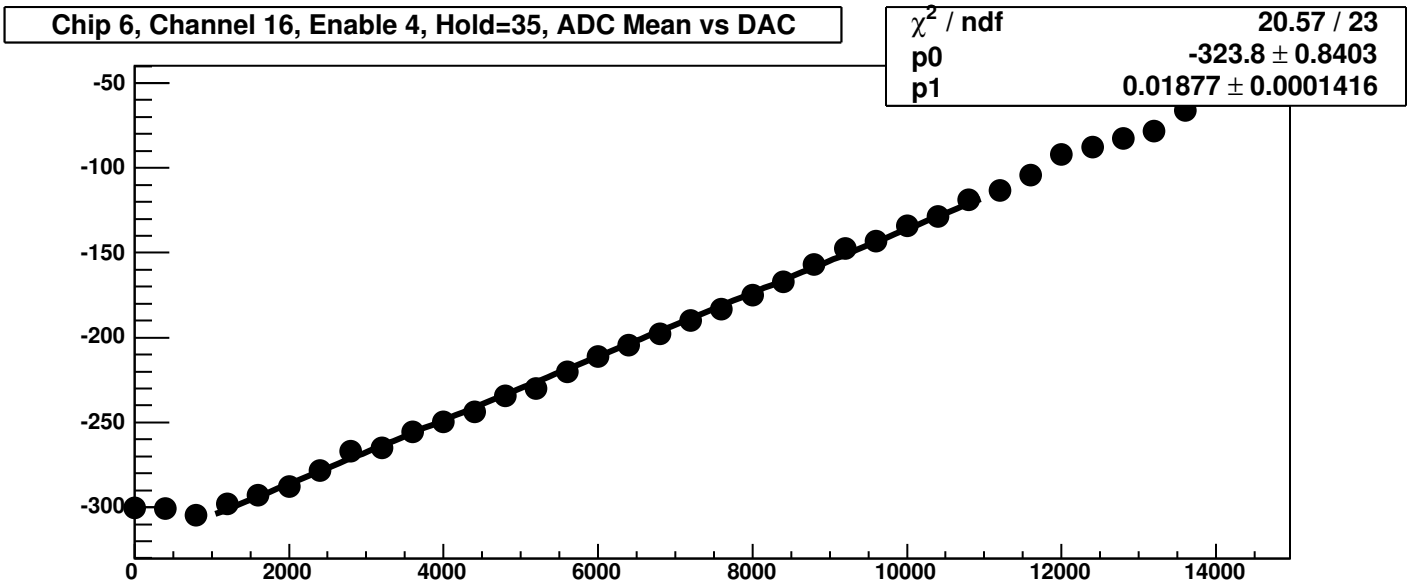

Chip 6, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

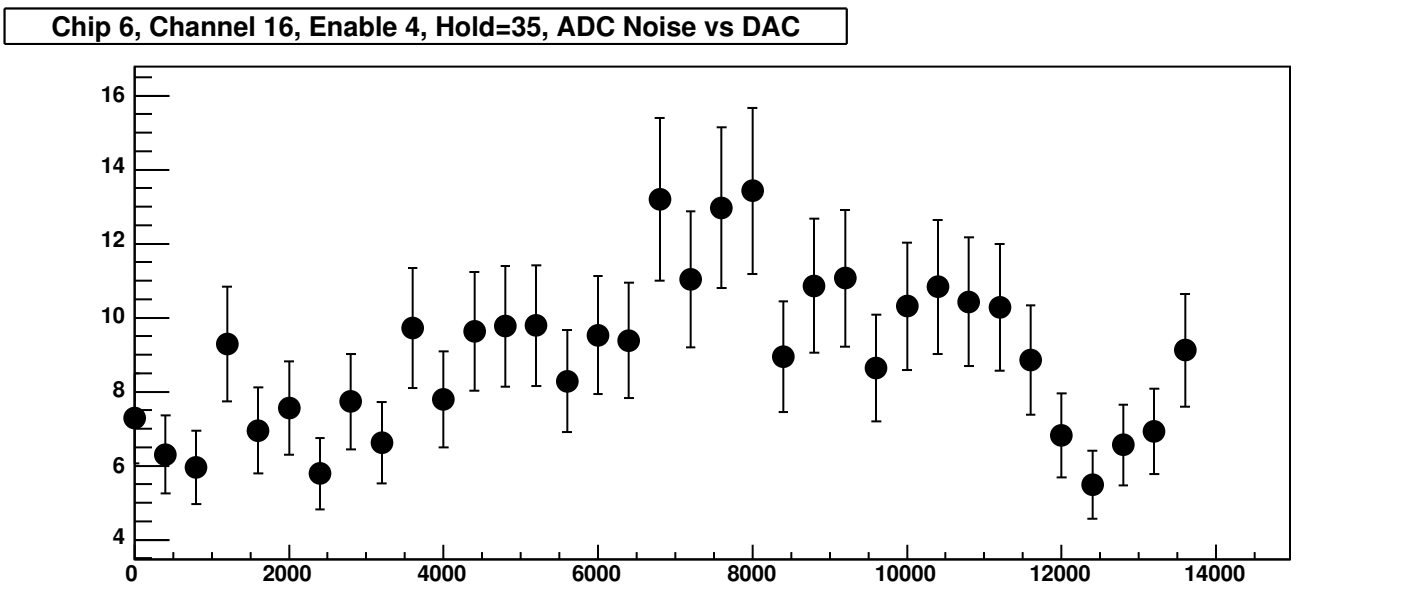

Chip 6, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

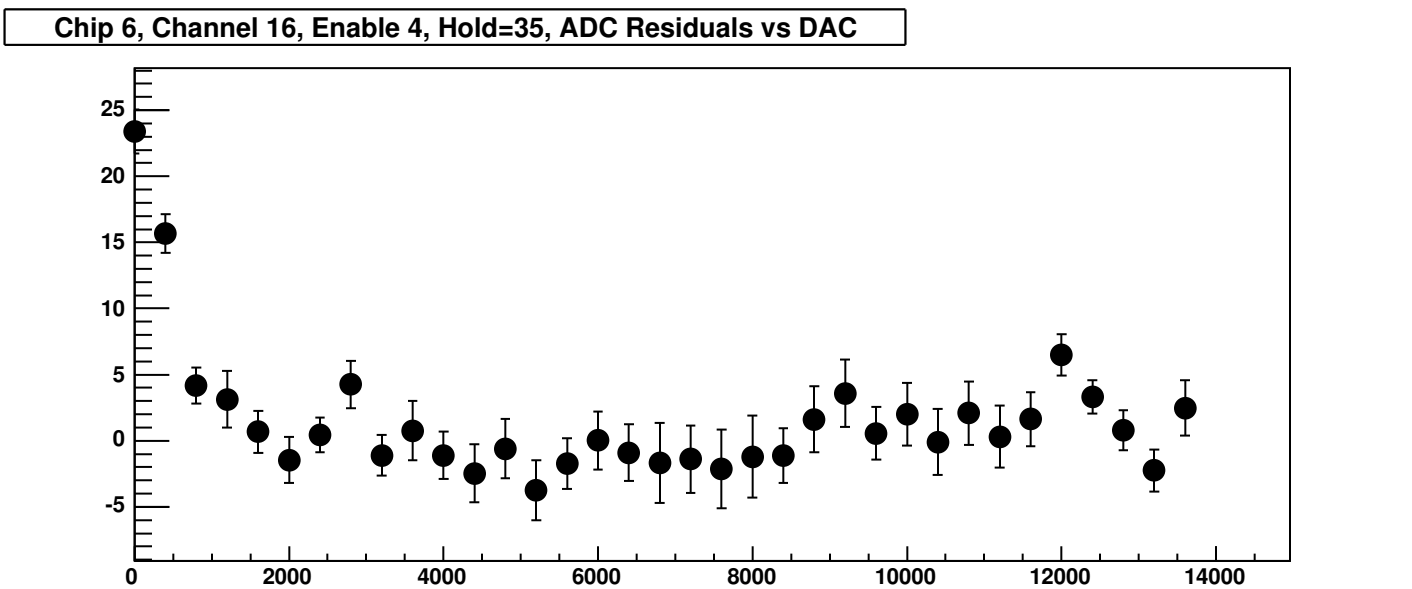

Chip 6, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.98 / 23
p0 -322.9 ± 0.7579
p1 0.01456 ± 0.0001235
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

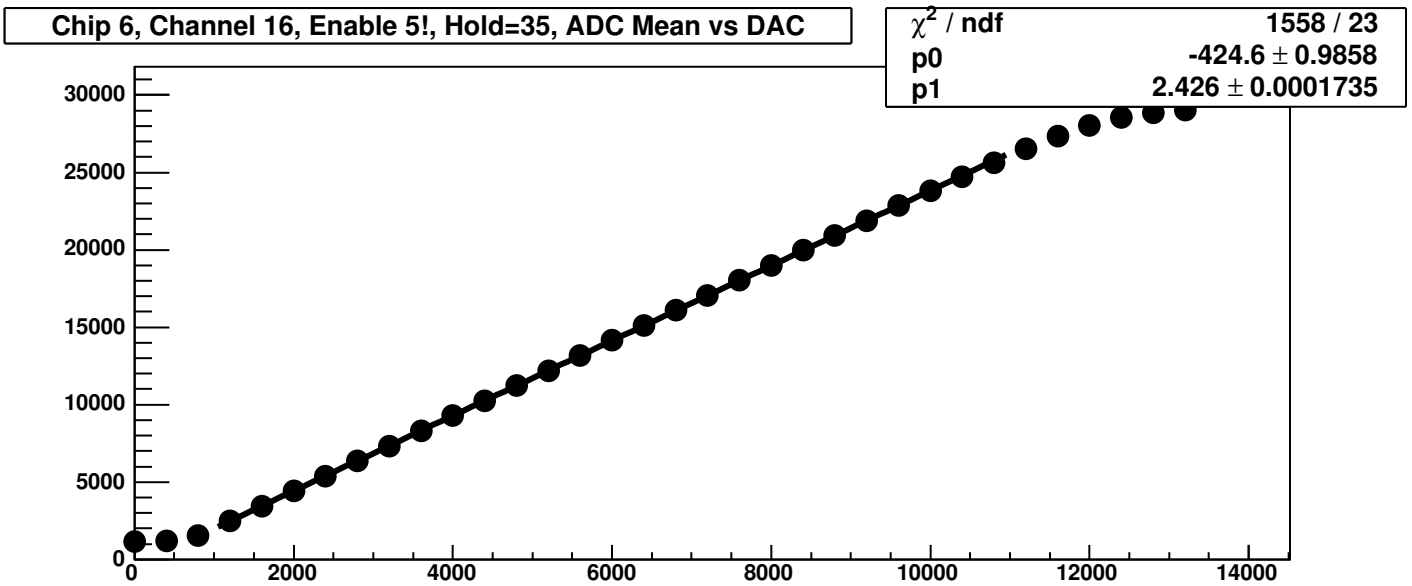

Chip 6, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

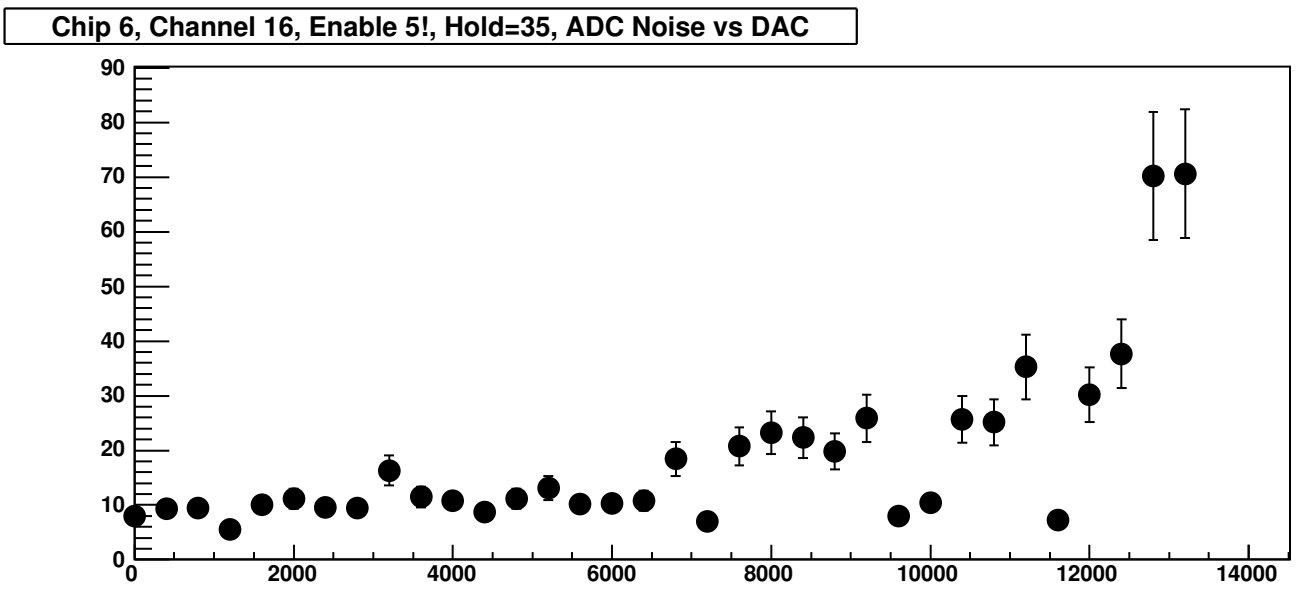

Chip 6, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

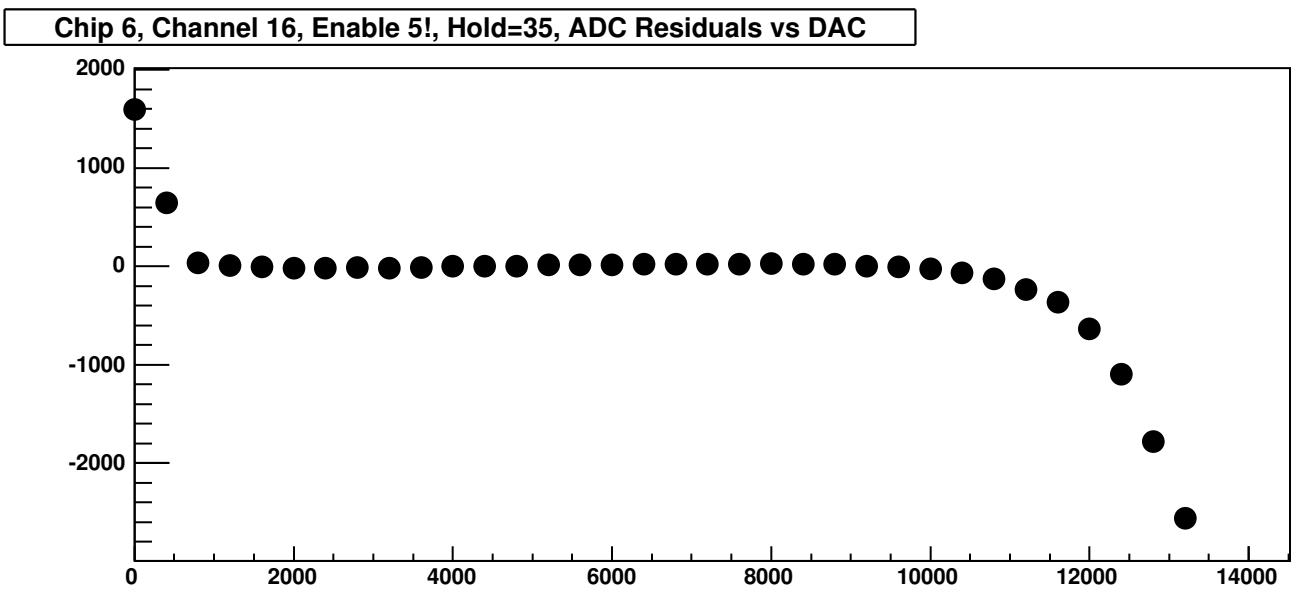

Chip 6, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.57 / 23
p0 -323.8 ± 0.8403
p1 0.01877 ± 0.0001416
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

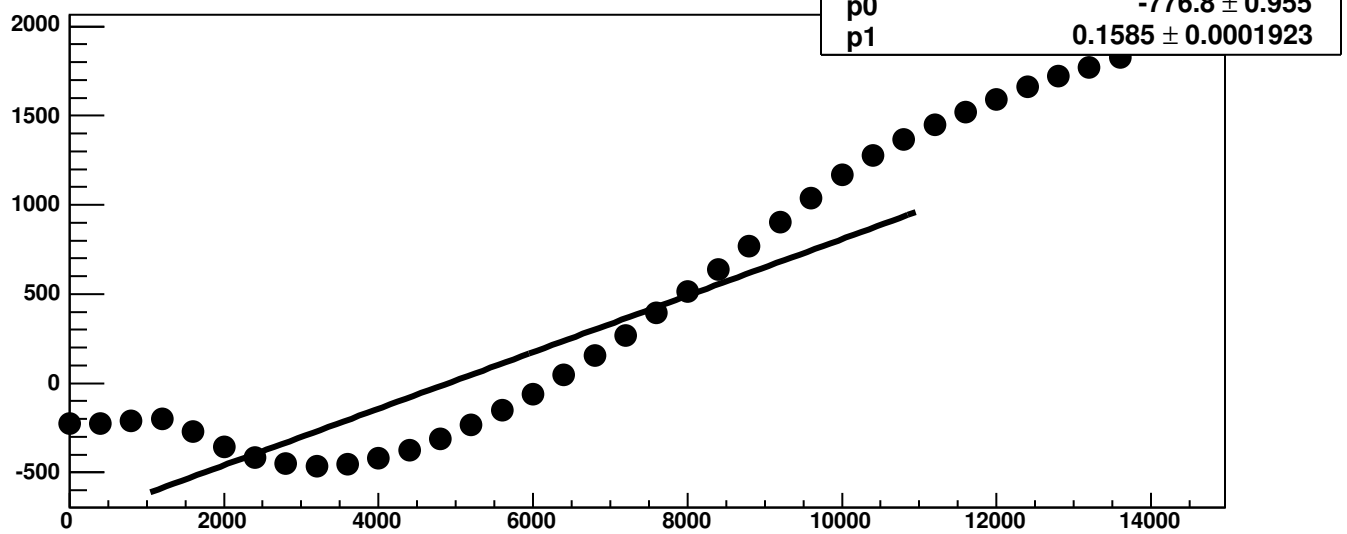

Chip 6, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1558 / 23
p0 -424.6 ± 0.9858
p1 2.426 ± 0.0001735
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

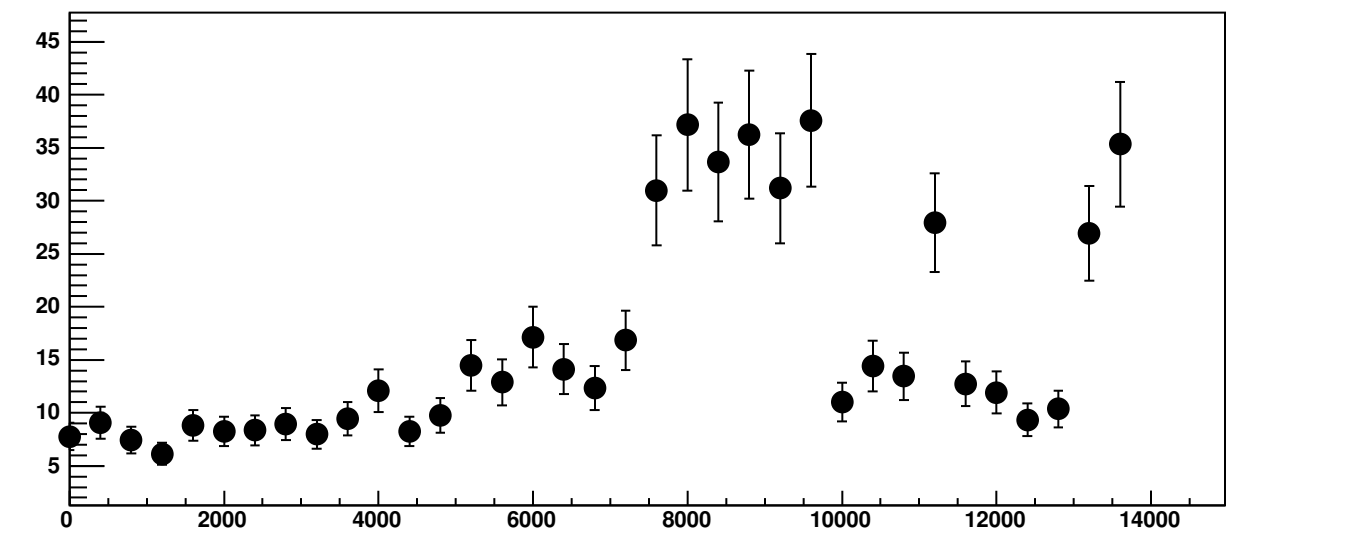

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
0
2000
4000
6000
8000
10000
12000
14000

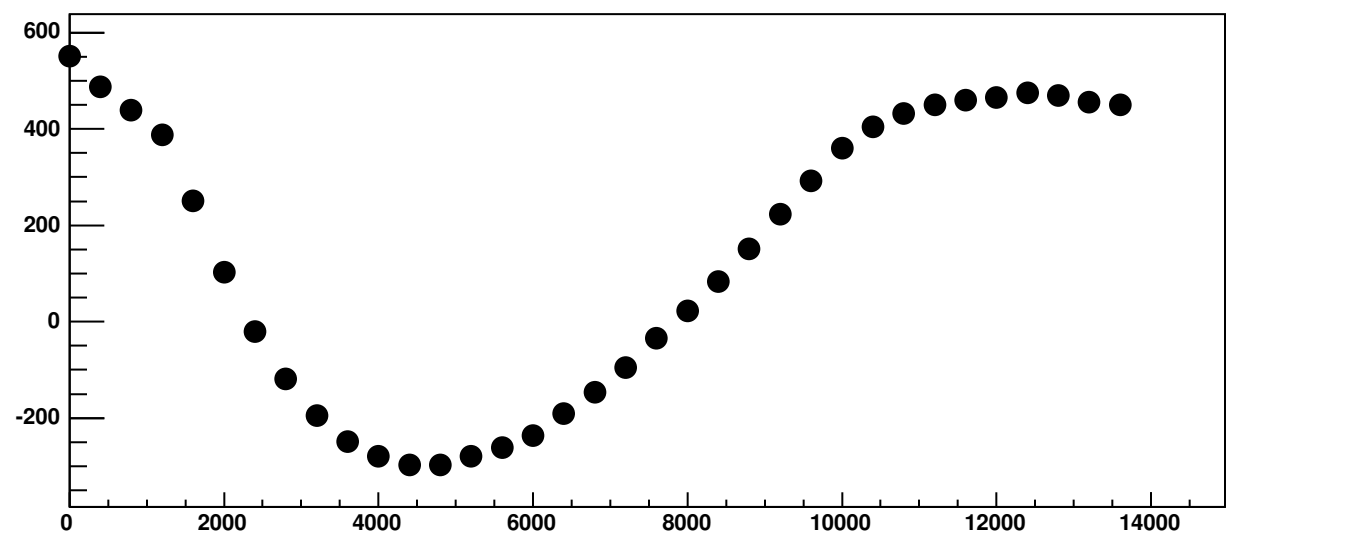
Chip 6, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



Chip 6, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

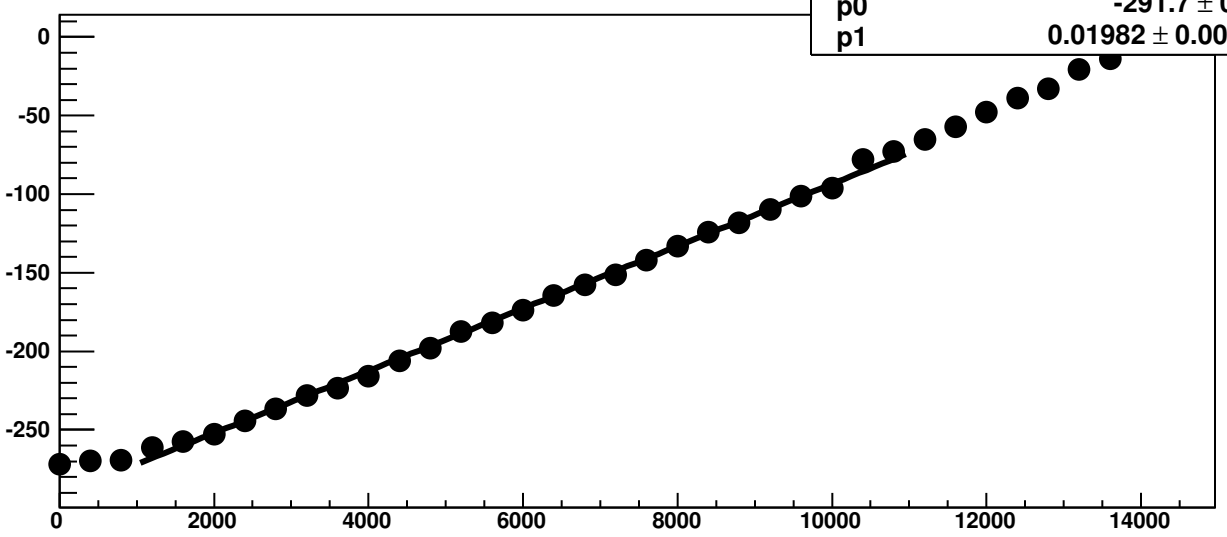


Chip 6, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

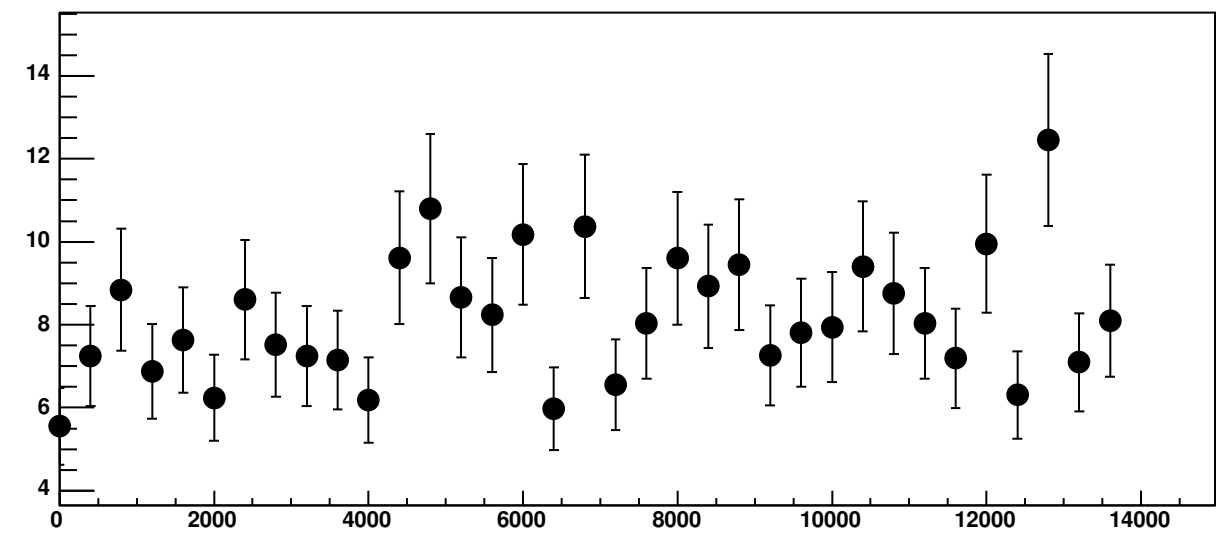


Chip 6, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

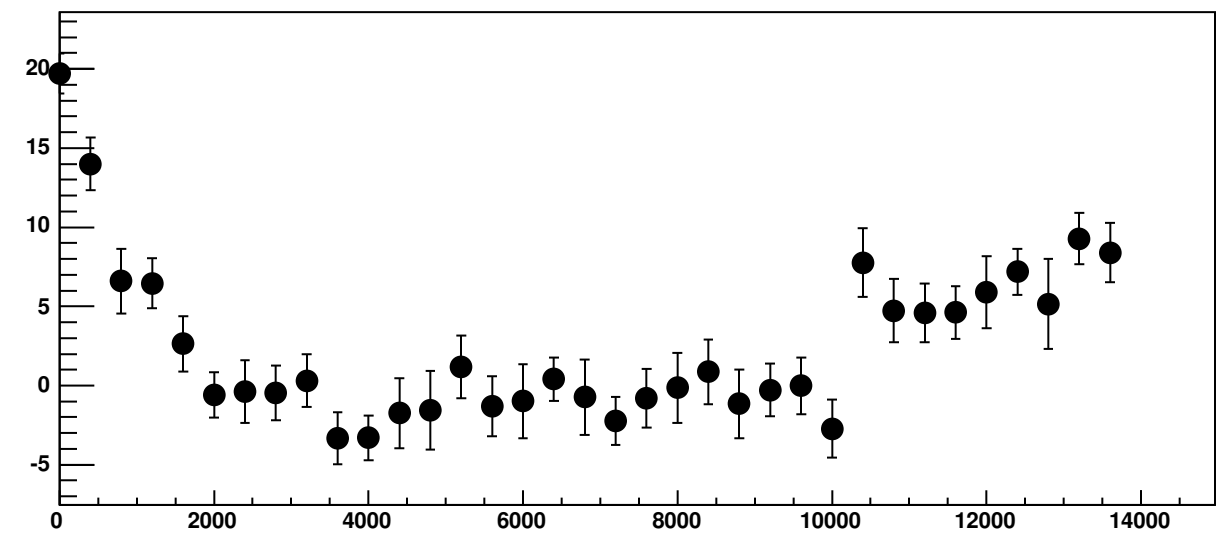
χ^2 / ndf	54.73 / 23
p0	-291.7 ± 0.7902
p1	0.01982 ± 0.0001239

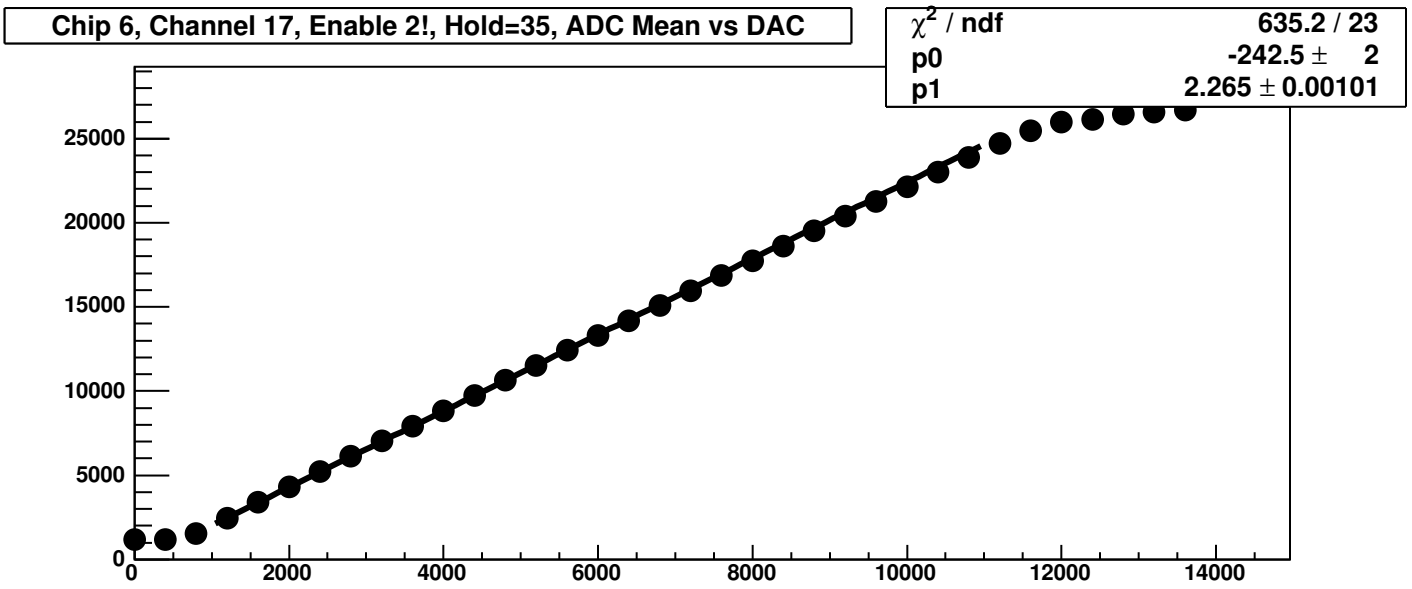


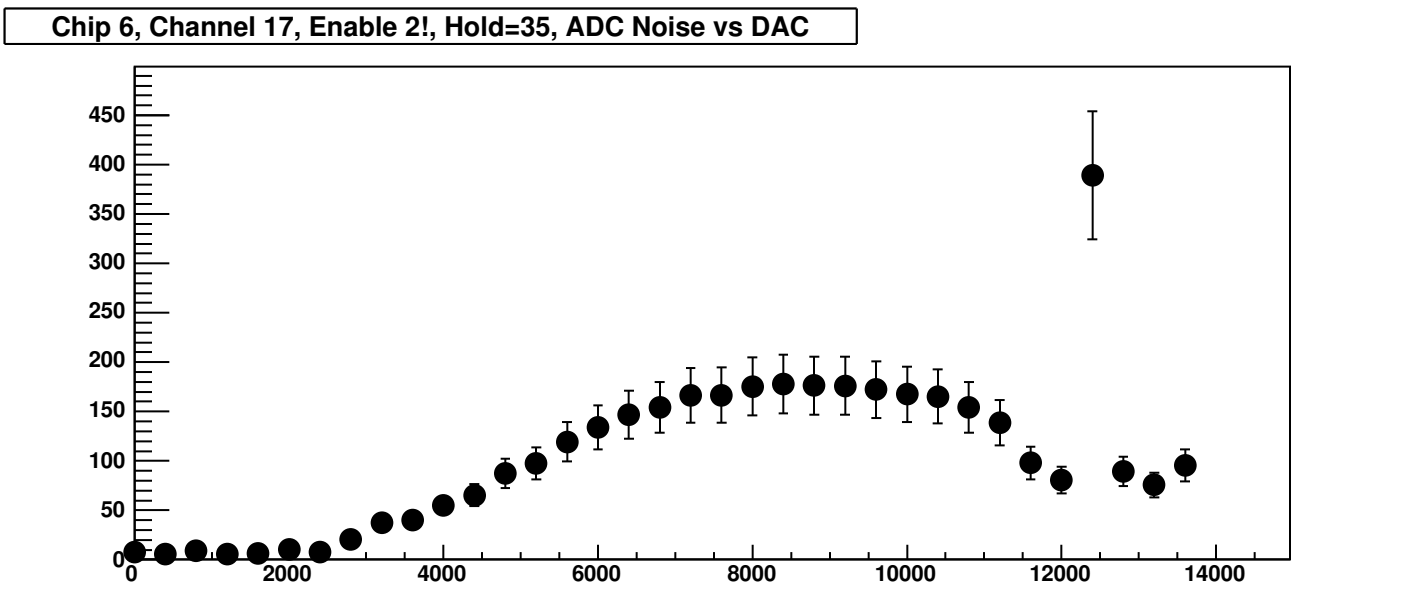
Chip 6, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

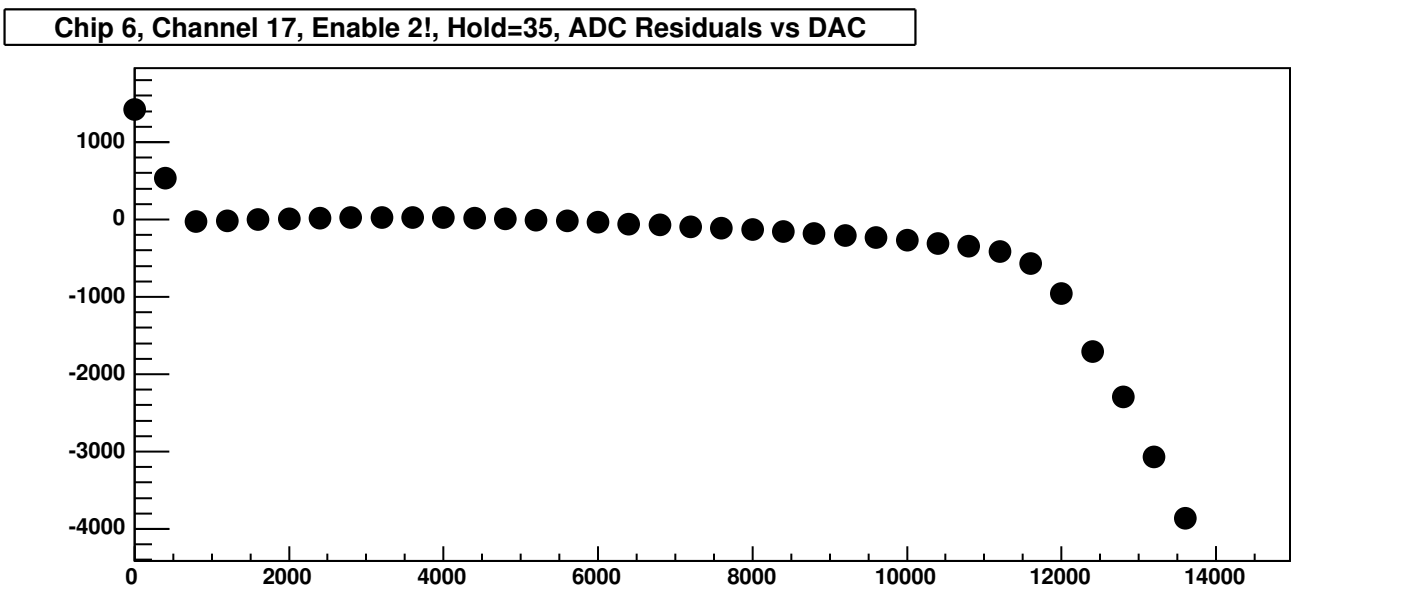


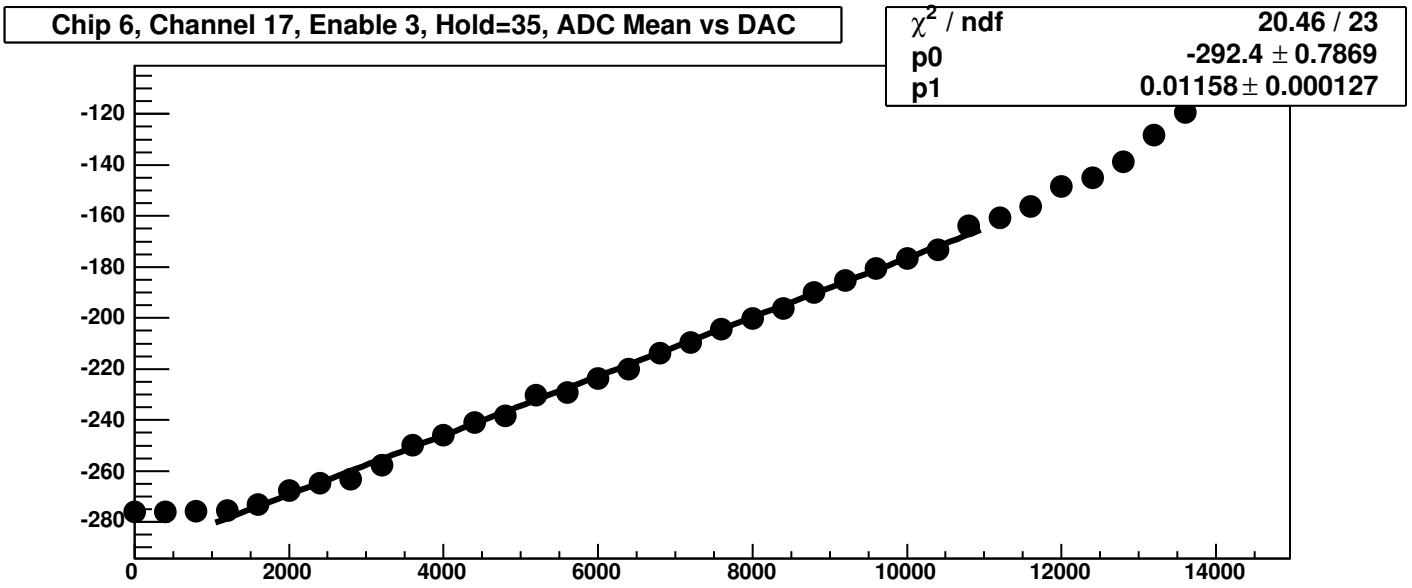
Chip 6, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

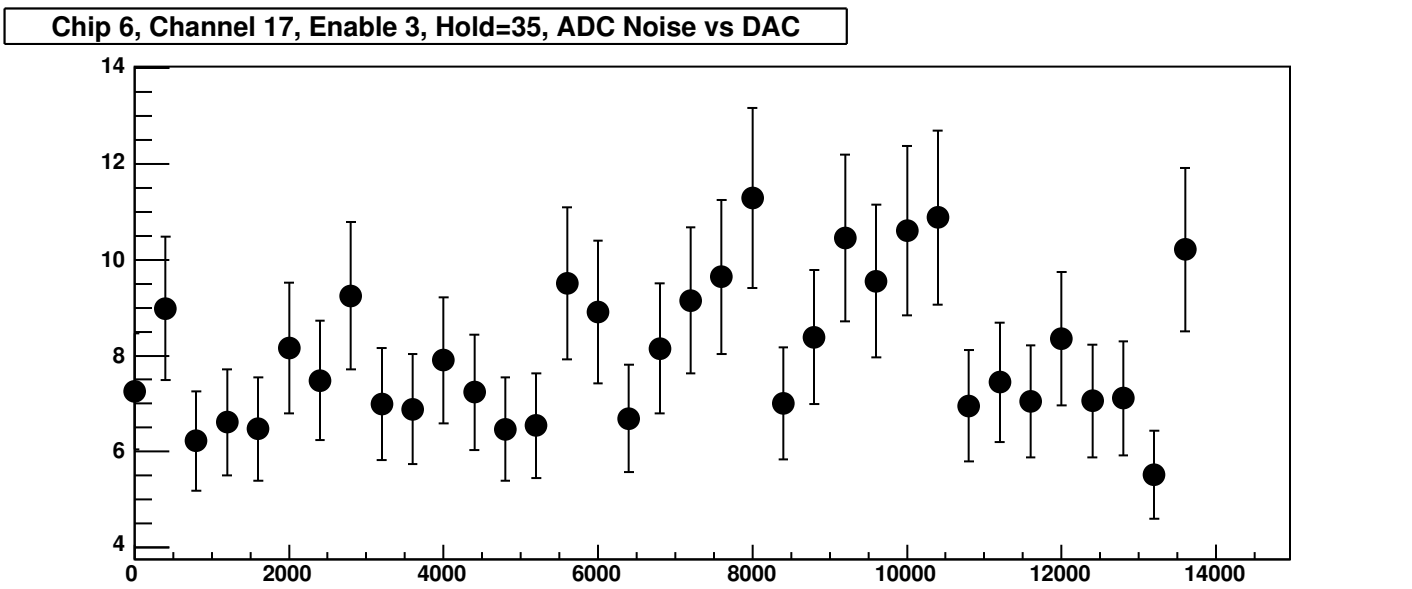


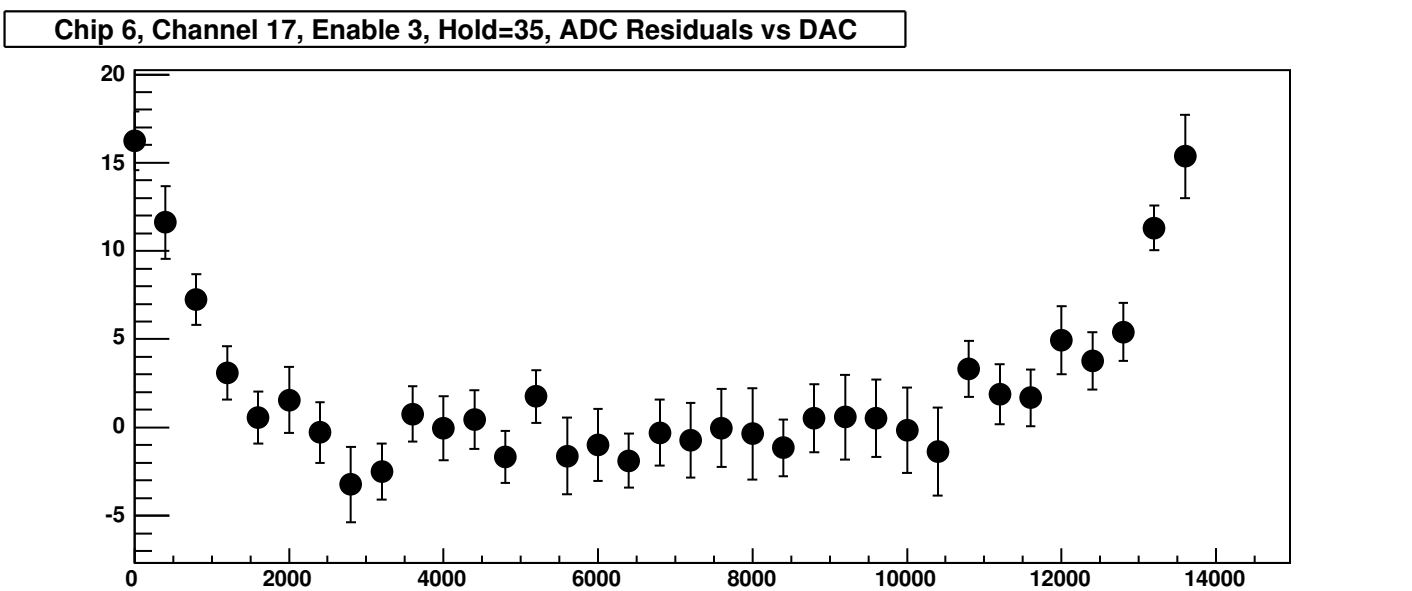

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 635.2 / 23
p0 -242.5 ± 2
p1 2.265 ± 0.00101
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

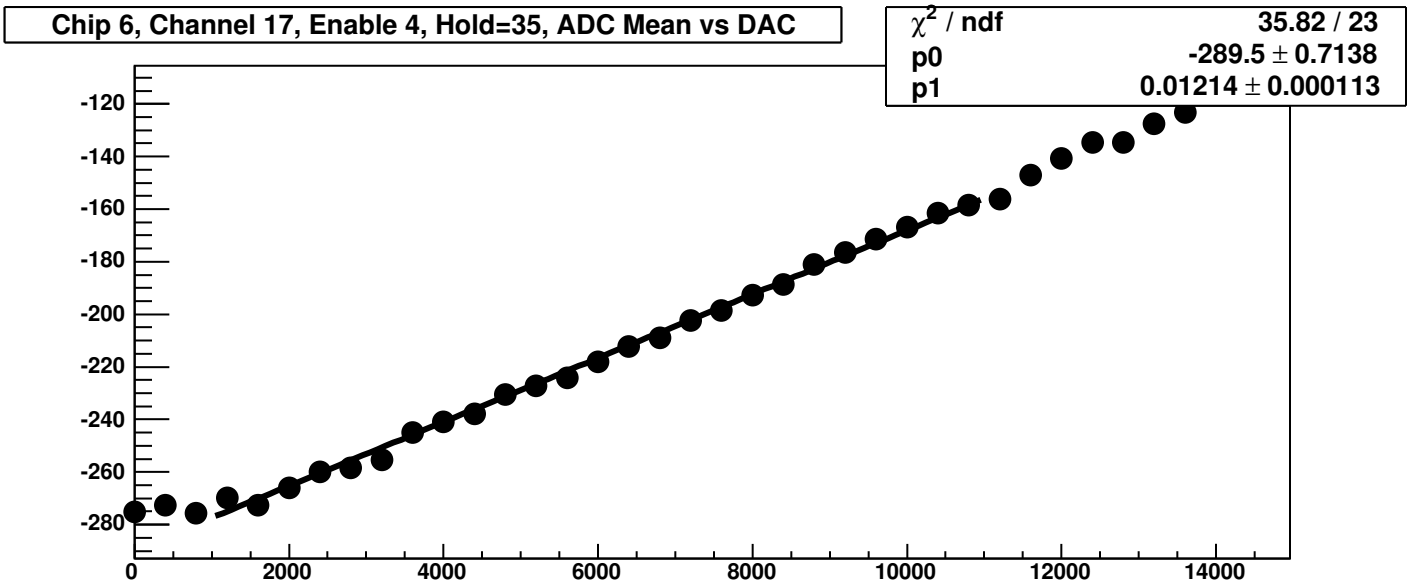

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

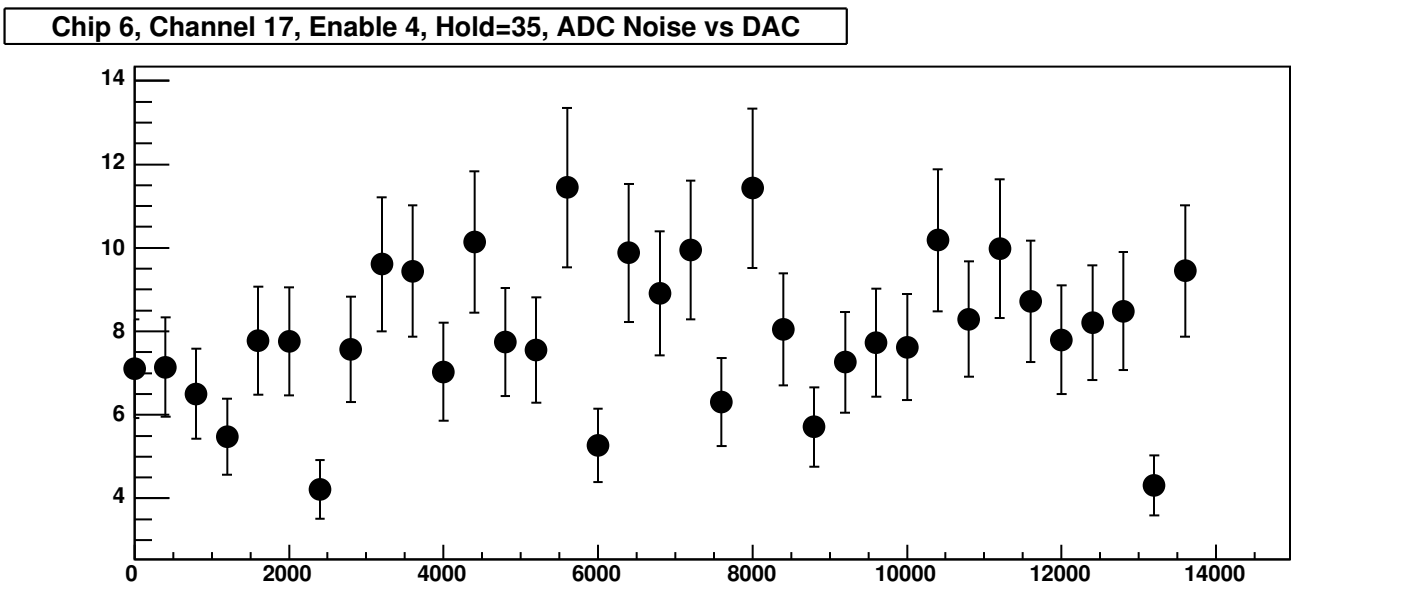

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

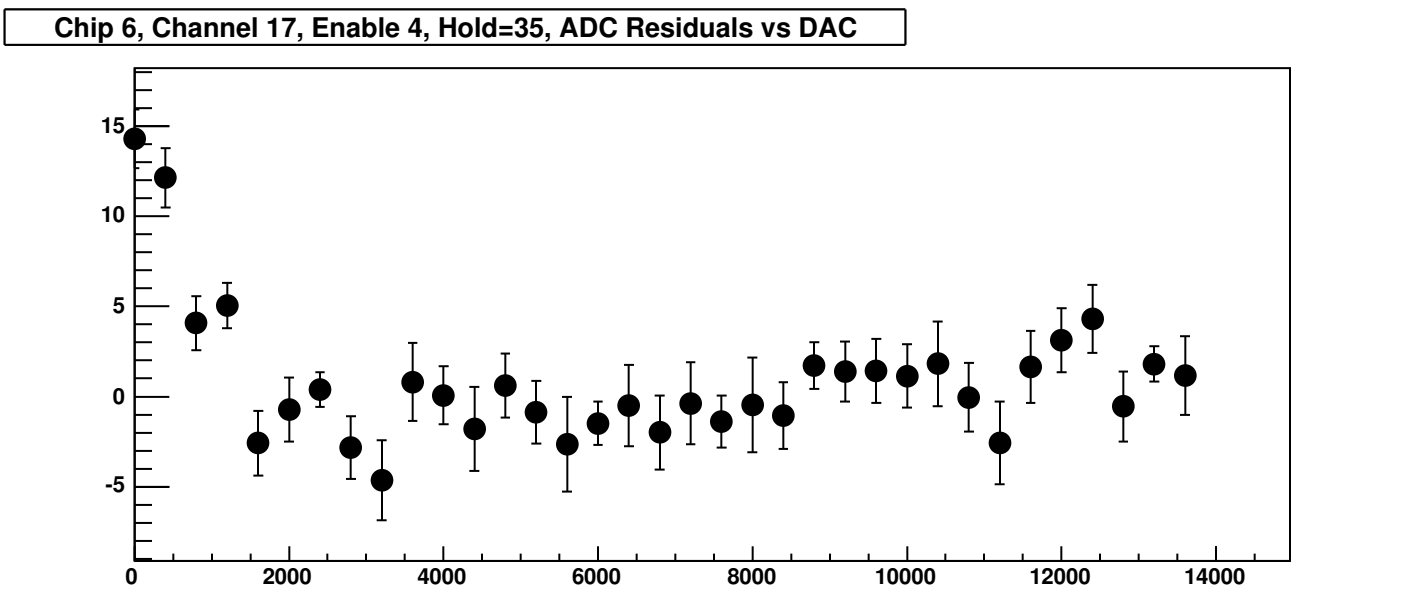

Chip 6, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.46 / 23
p0 -292.4 ± 0.7869
p1 0.01158 ± 0.000127
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

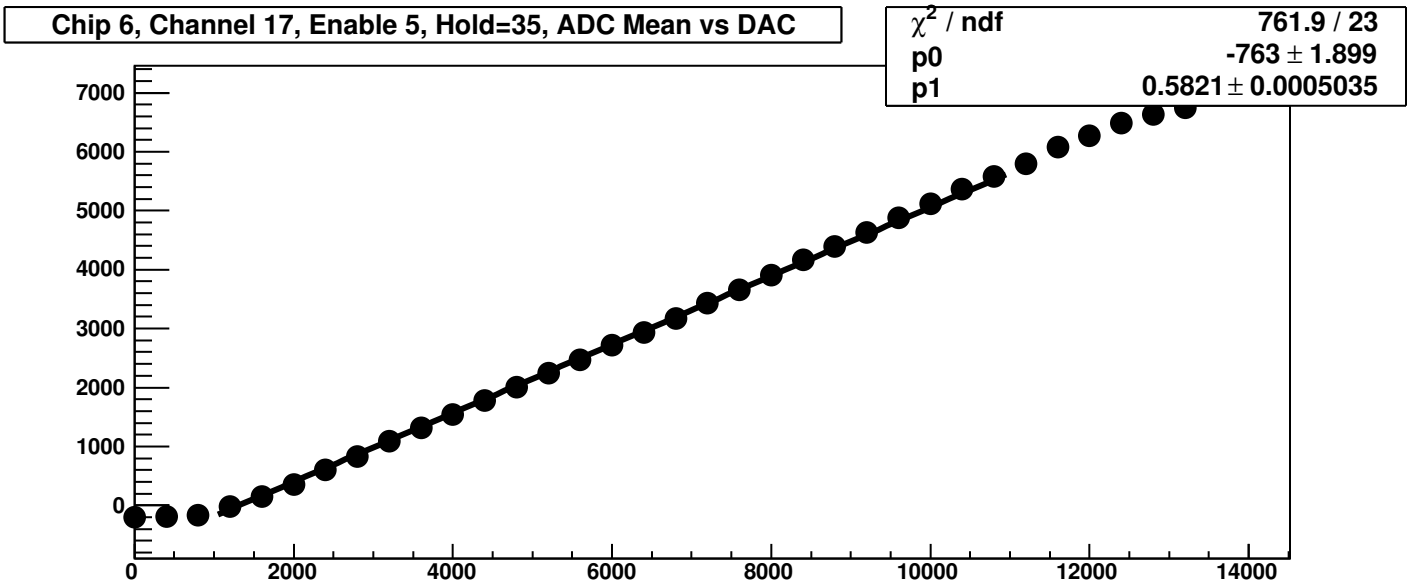

Chip 6, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

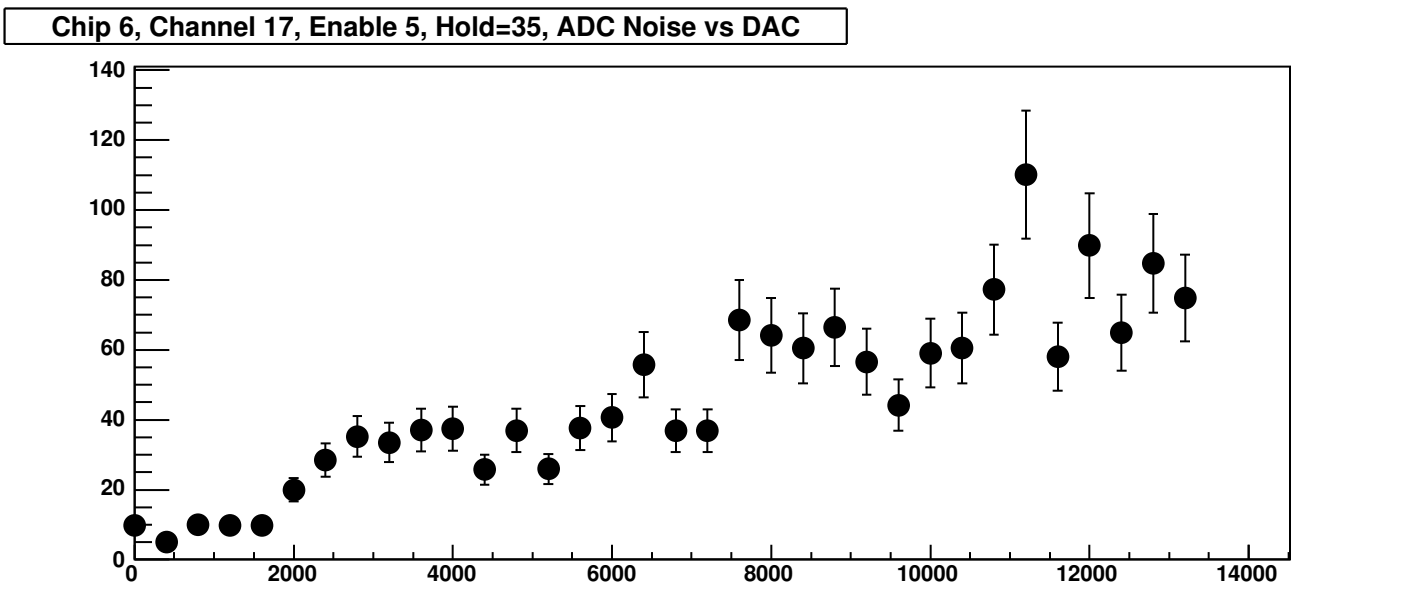

Chip 6, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

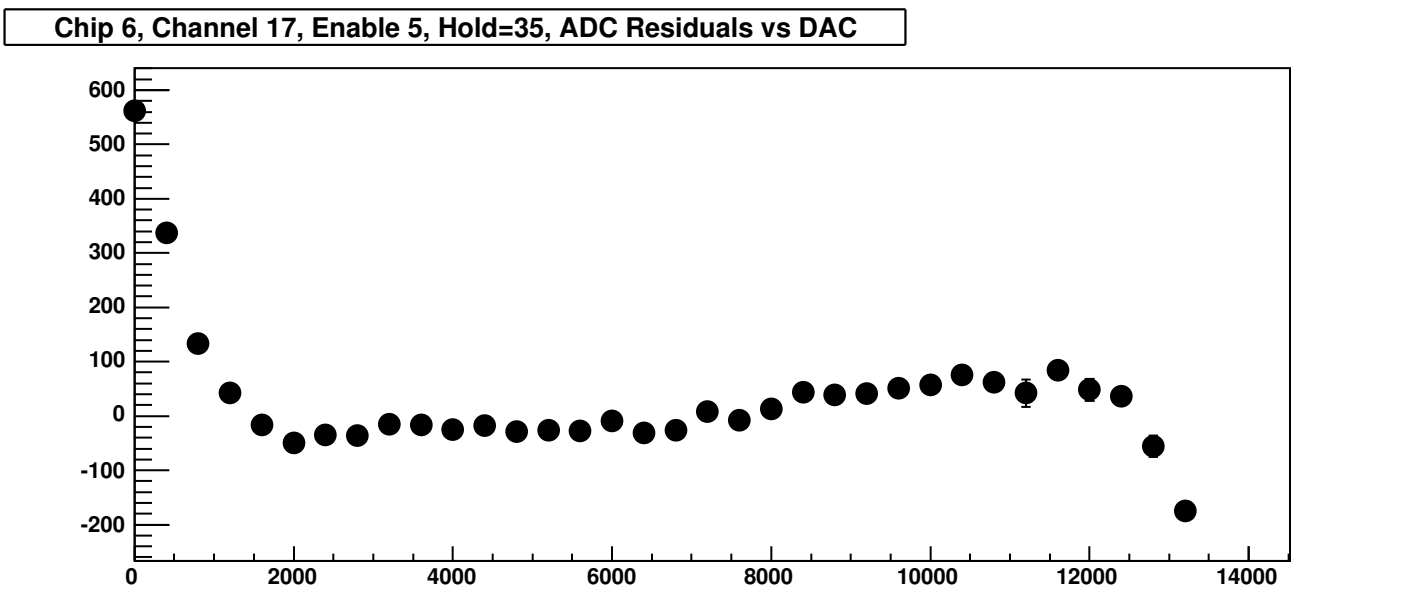

Chip 6, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.82 / 23
p0 -289.5 ± 0.7138
p1 0.01214 ± 0.000113
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

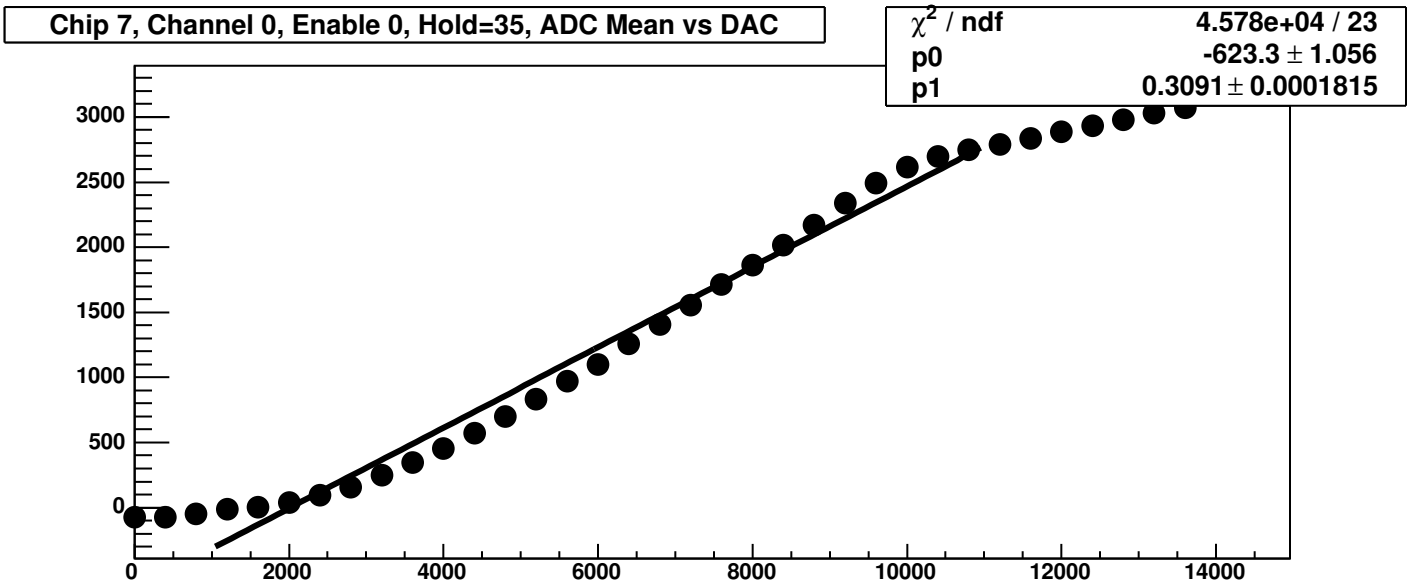

Chip 6, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

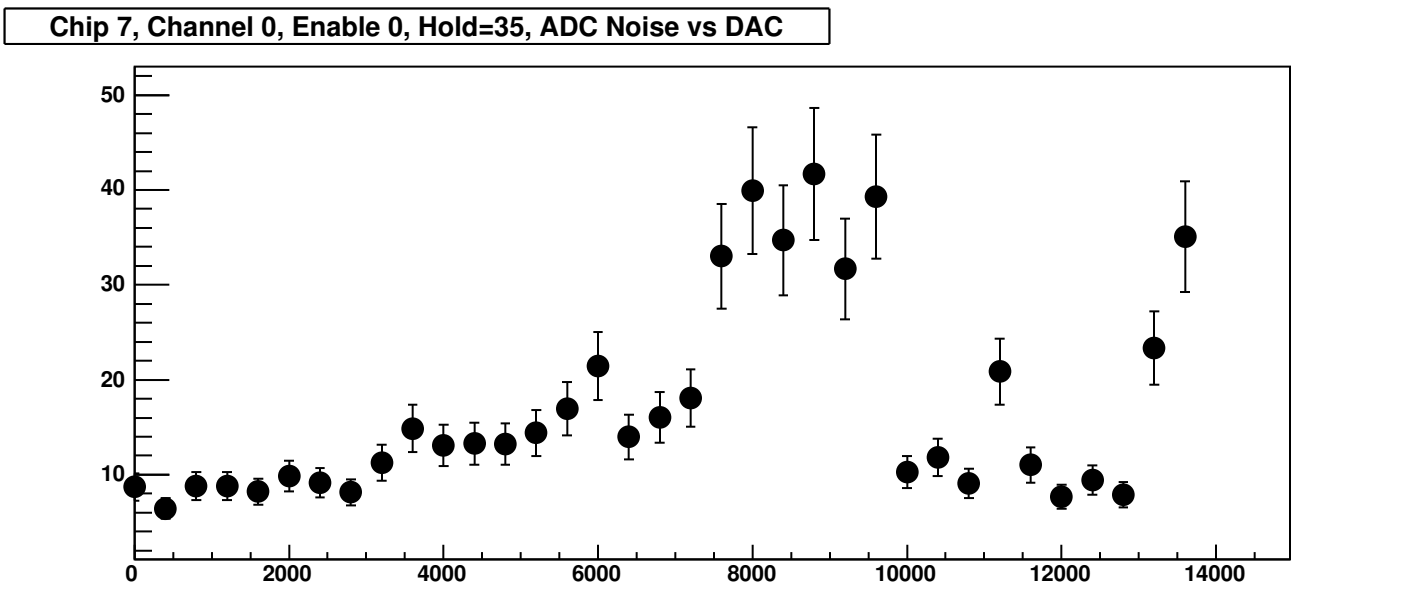

Chip 6, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

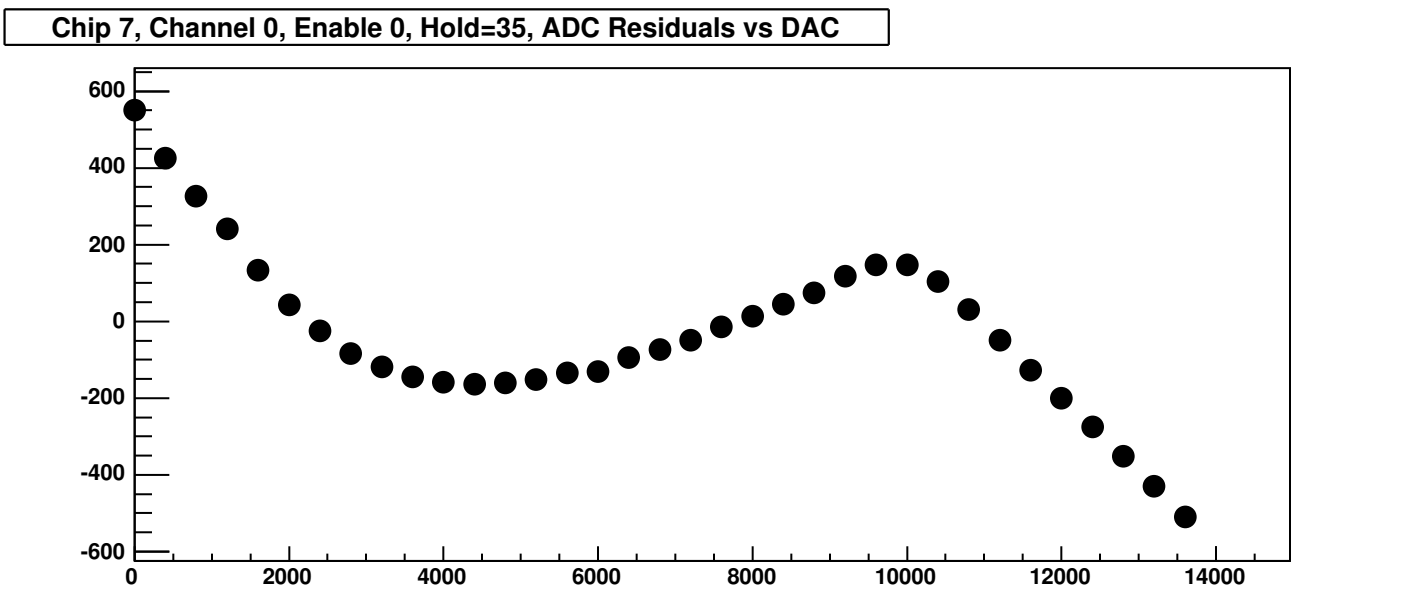

Chip 6, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 761.9 / 23
p0 -763 ± 1.899
p1 0.5821 ± 0.0005035
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

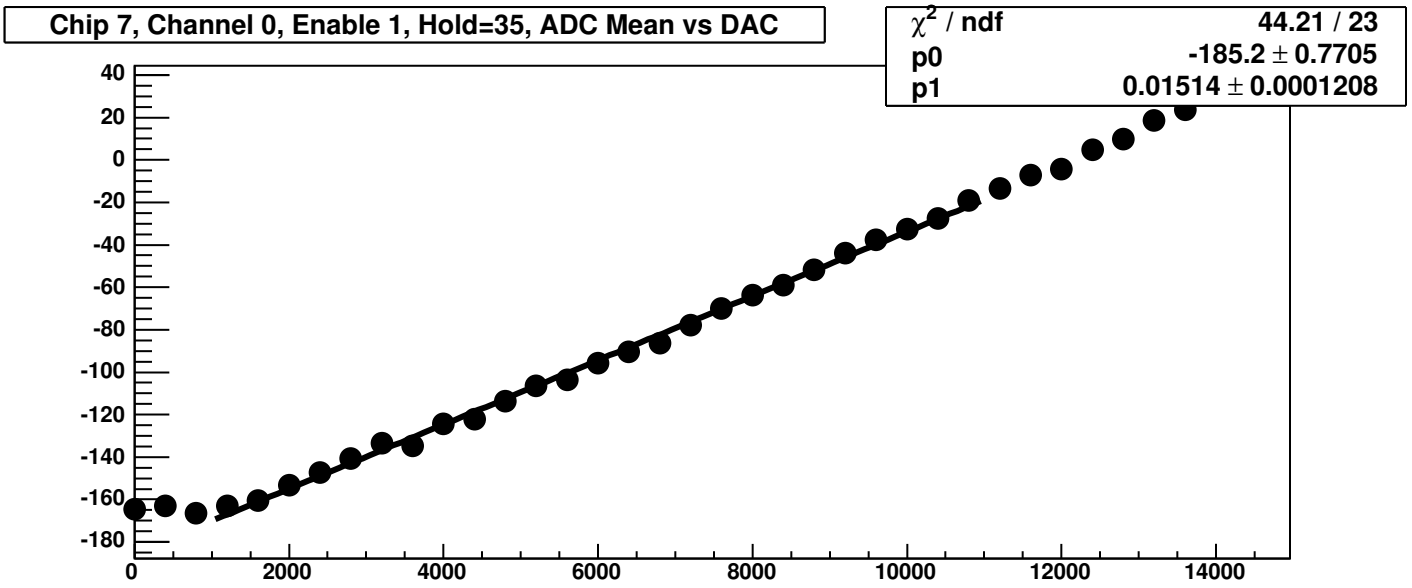

Chip 6, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

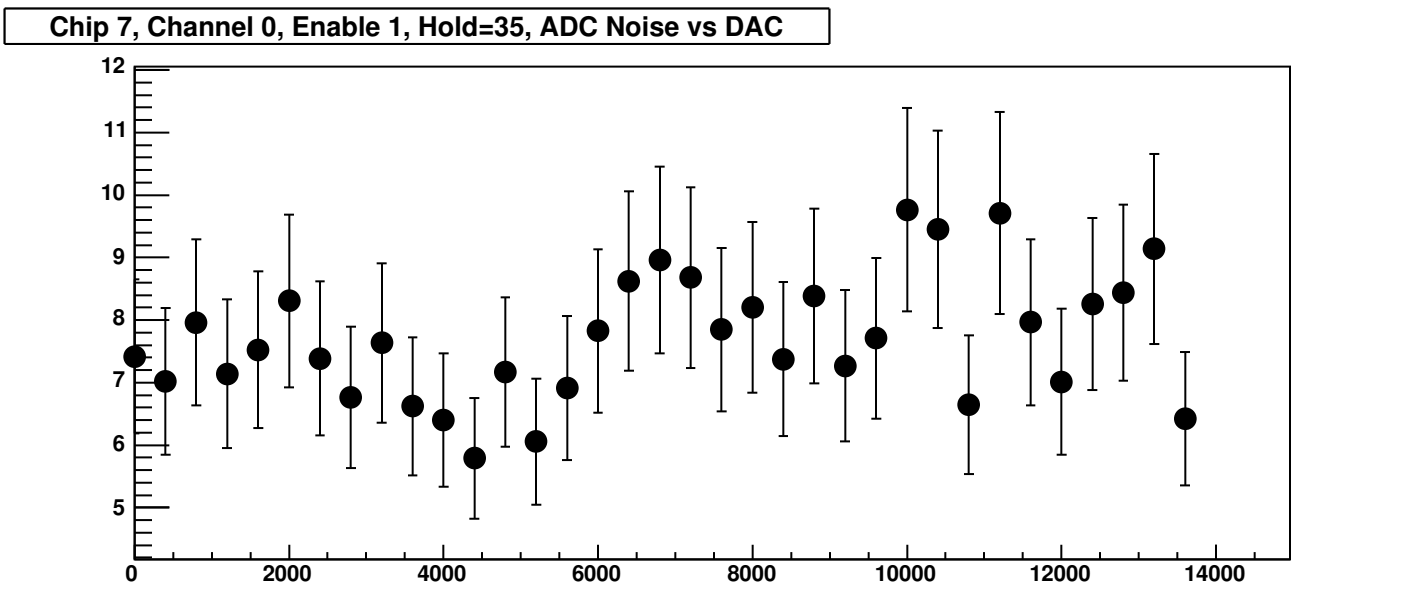

Chip 6, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

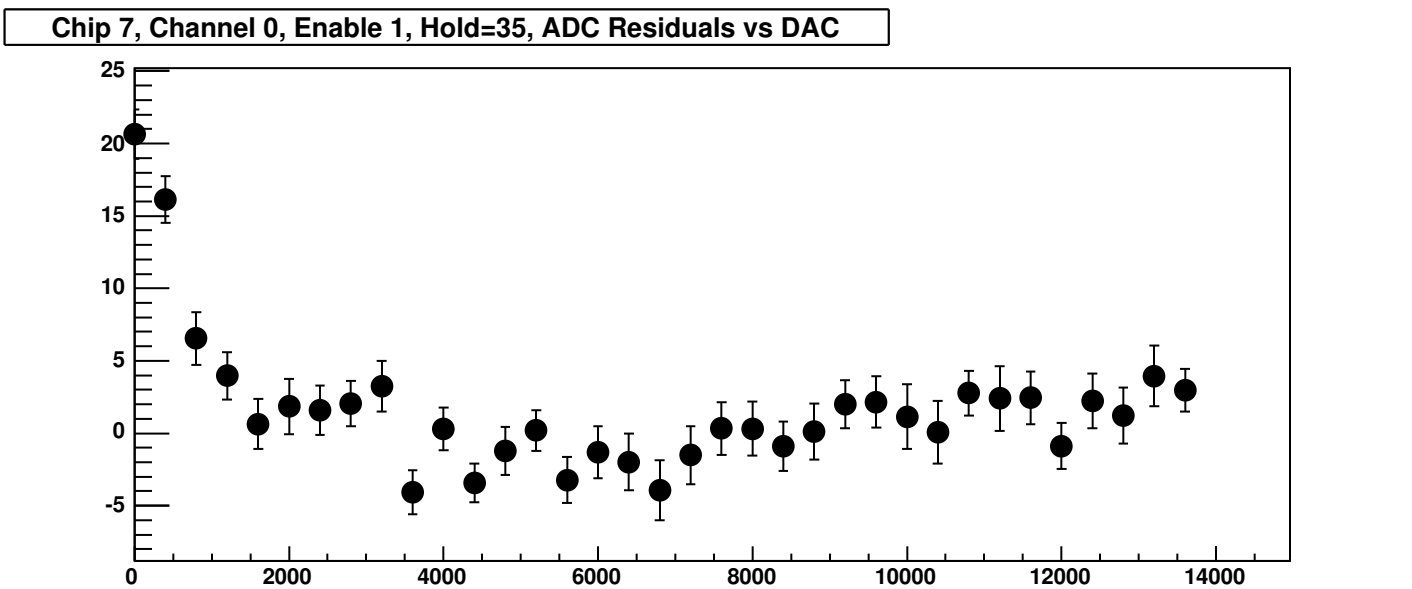

Chip 7, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.578e+04 / 23
p0 -623.3 ± 1.056
p1 0.3091 ± 0.0001815
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

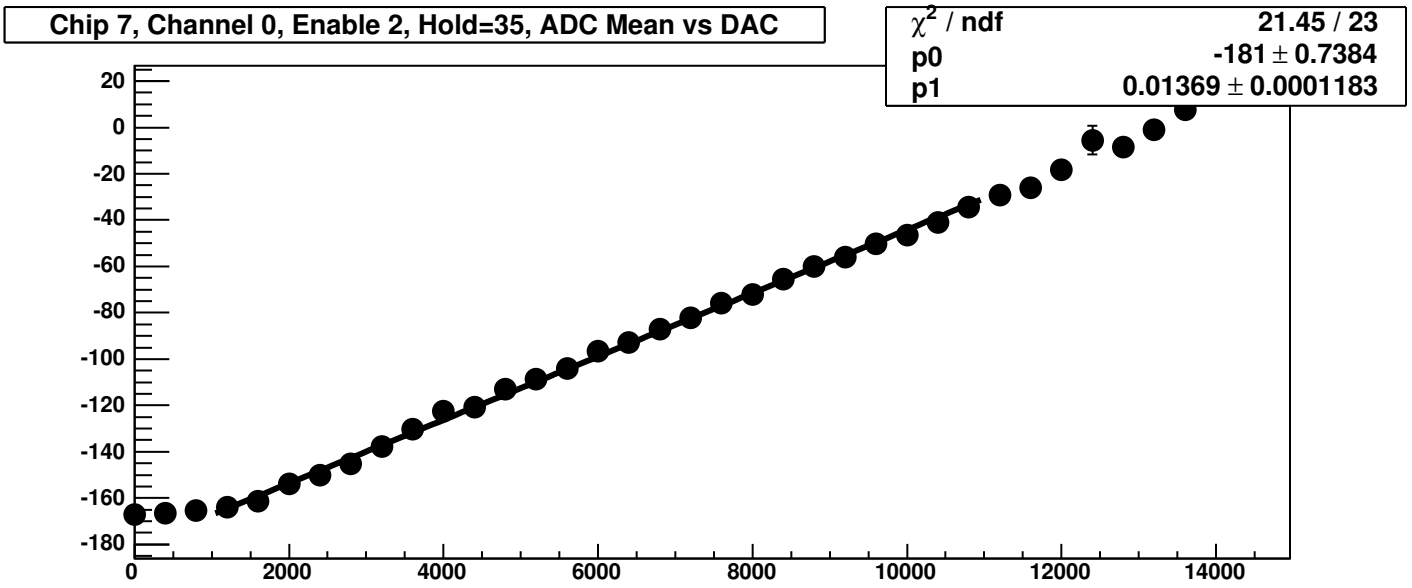

Chip 7, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

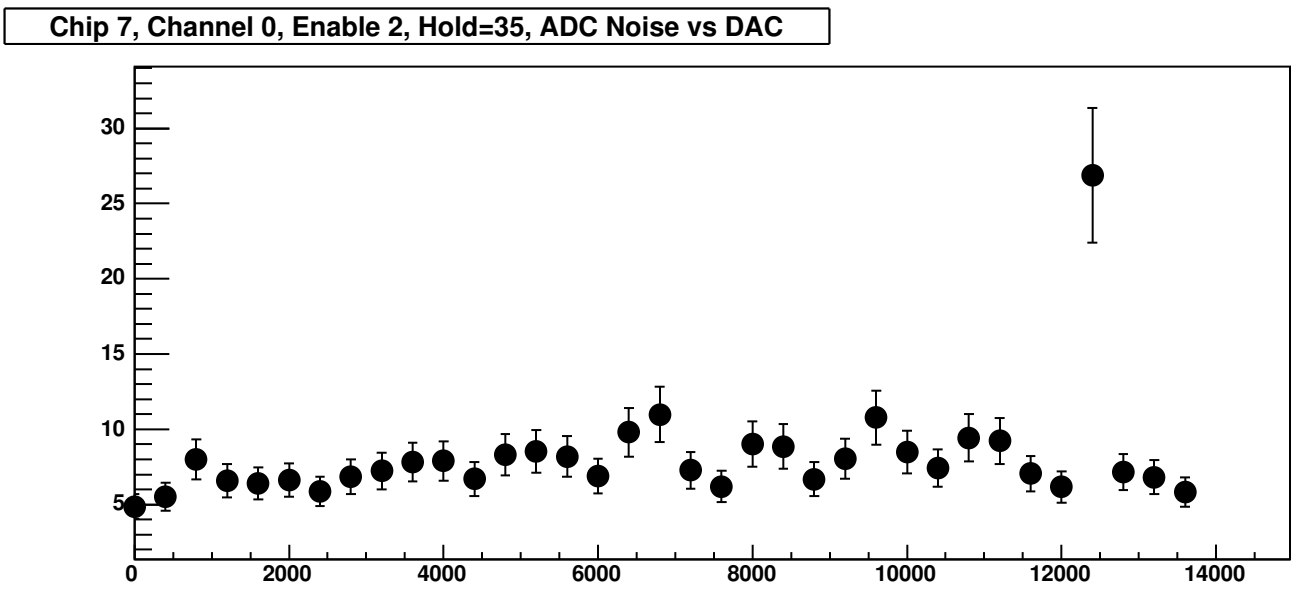

Chip 7, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

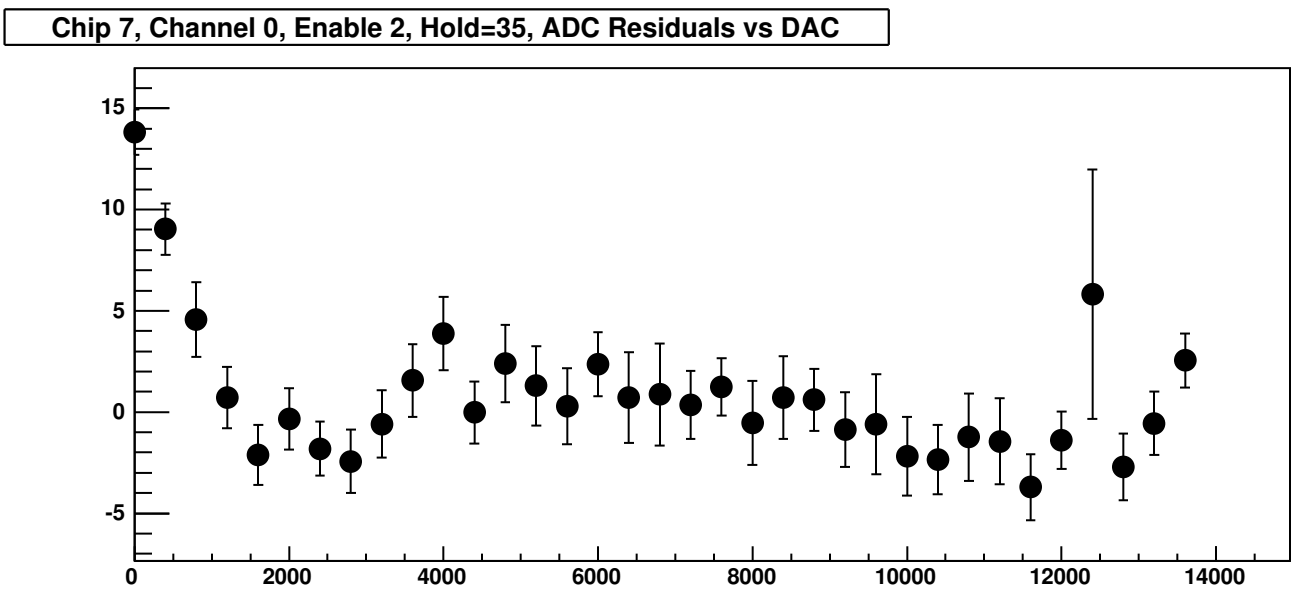

Chip 7, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.21 / 23
p0 -185.2 ± 0.7705
p1 0.01514 ± 0.0001208
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

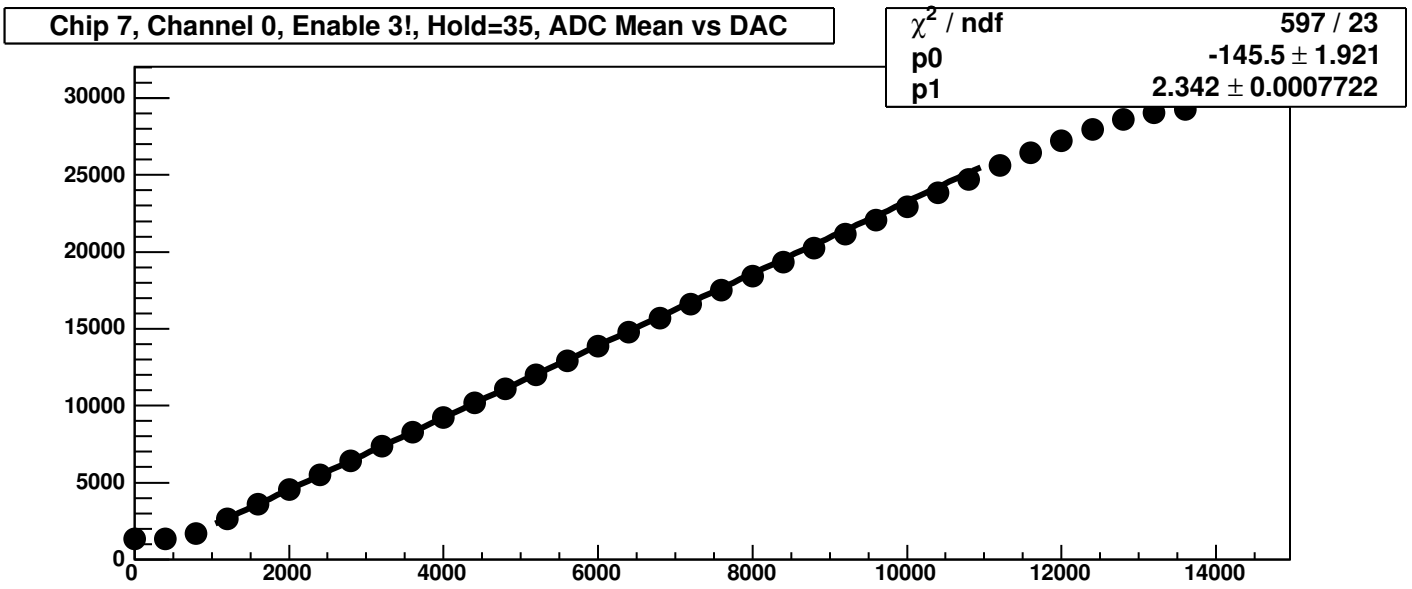

Chip 7, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

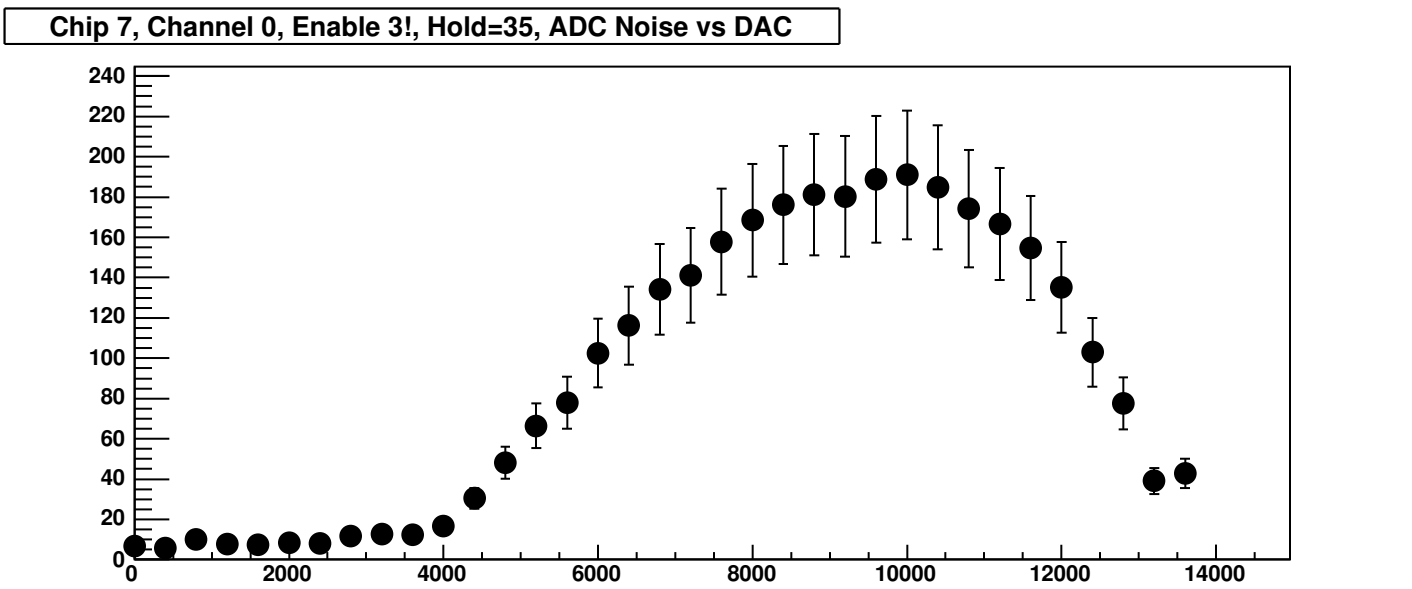

Chip 7, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

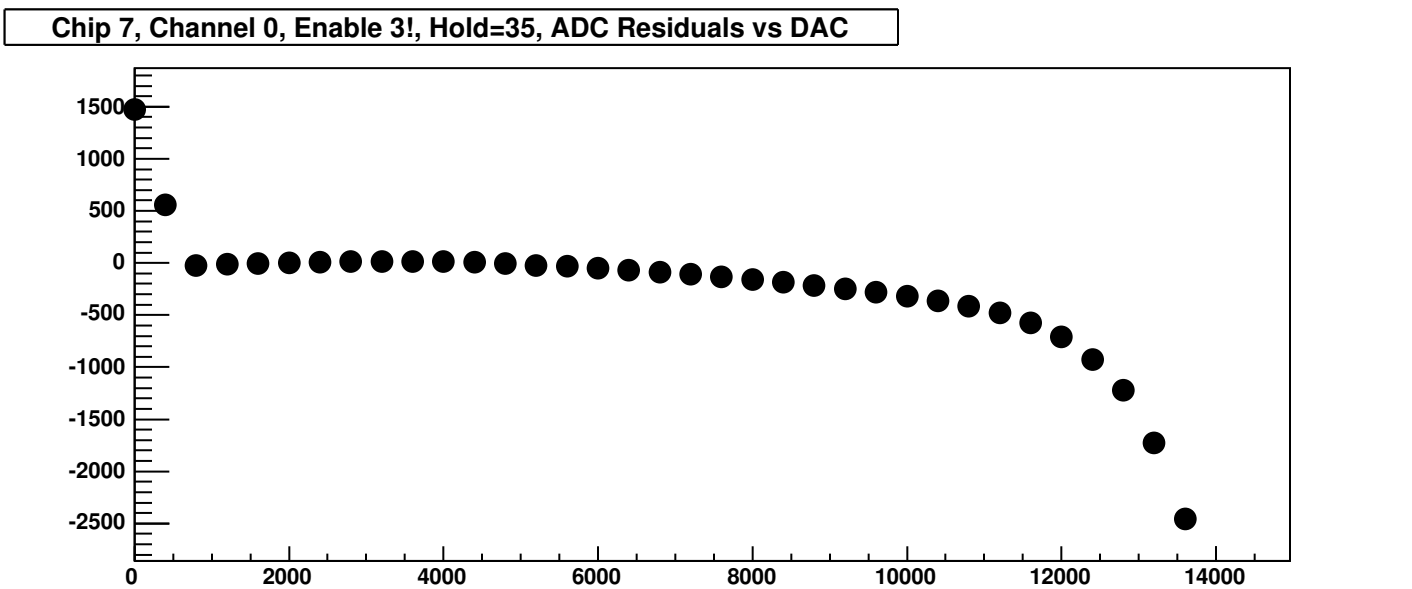

Chip 7, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.45 / 23
p0 -181 ± 0.7384
p1 0.01369 ± 0.0001183
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

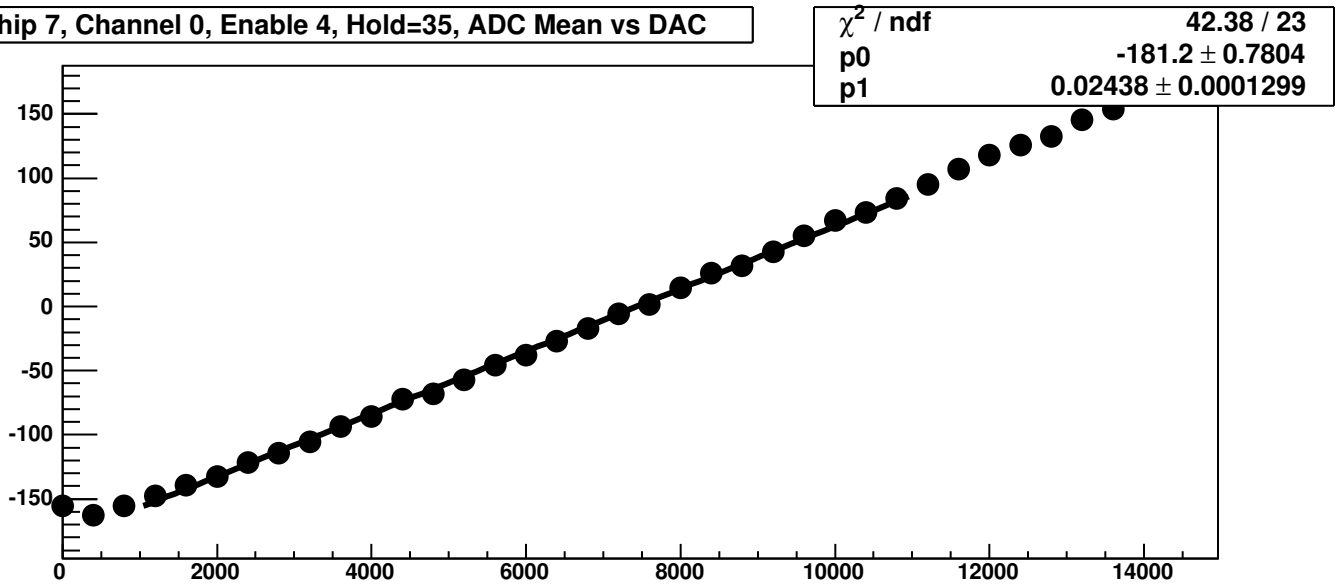

Chip 7, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 597 / 23
p0 -145.5 ± 1.921
p1 2.342 ± 0.0007722
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

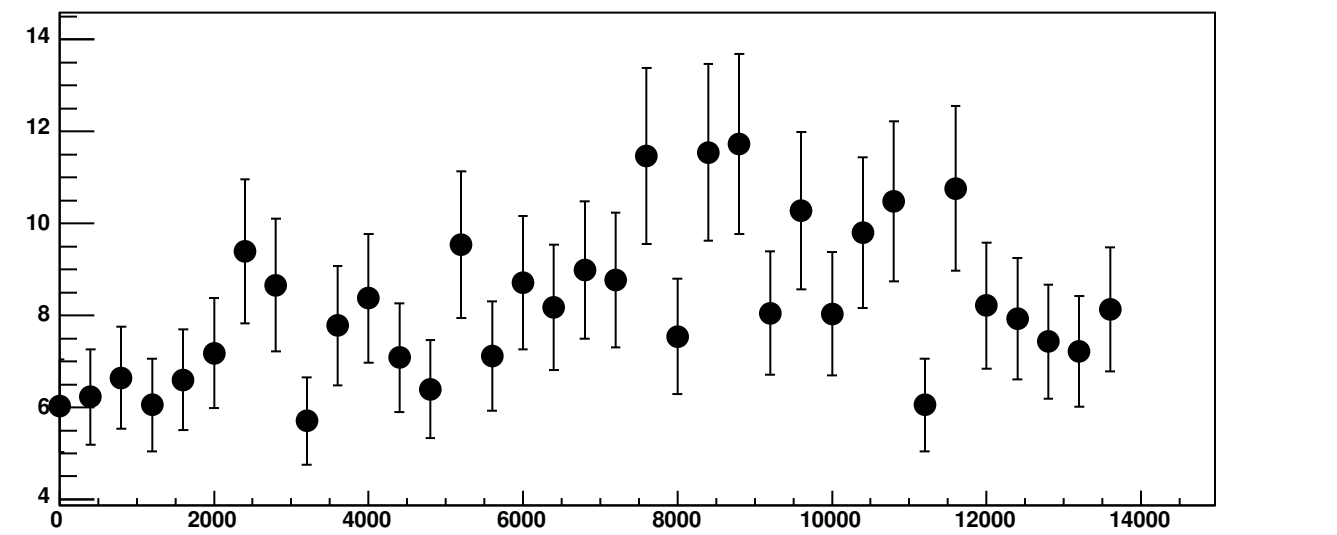

Chip 7, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

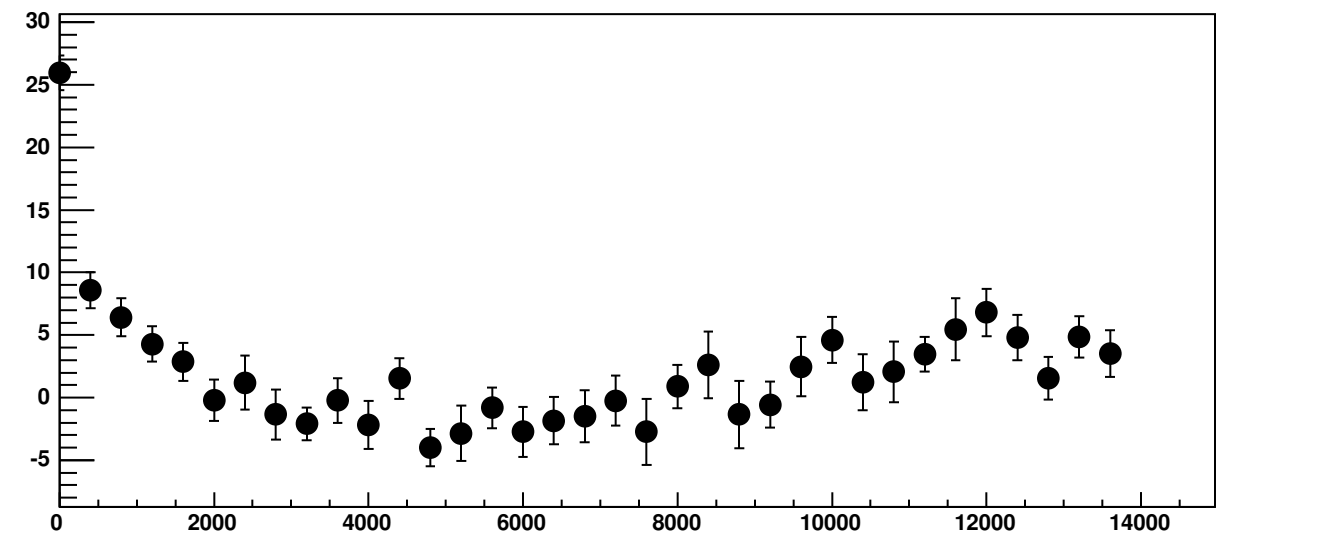
Chip 7, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

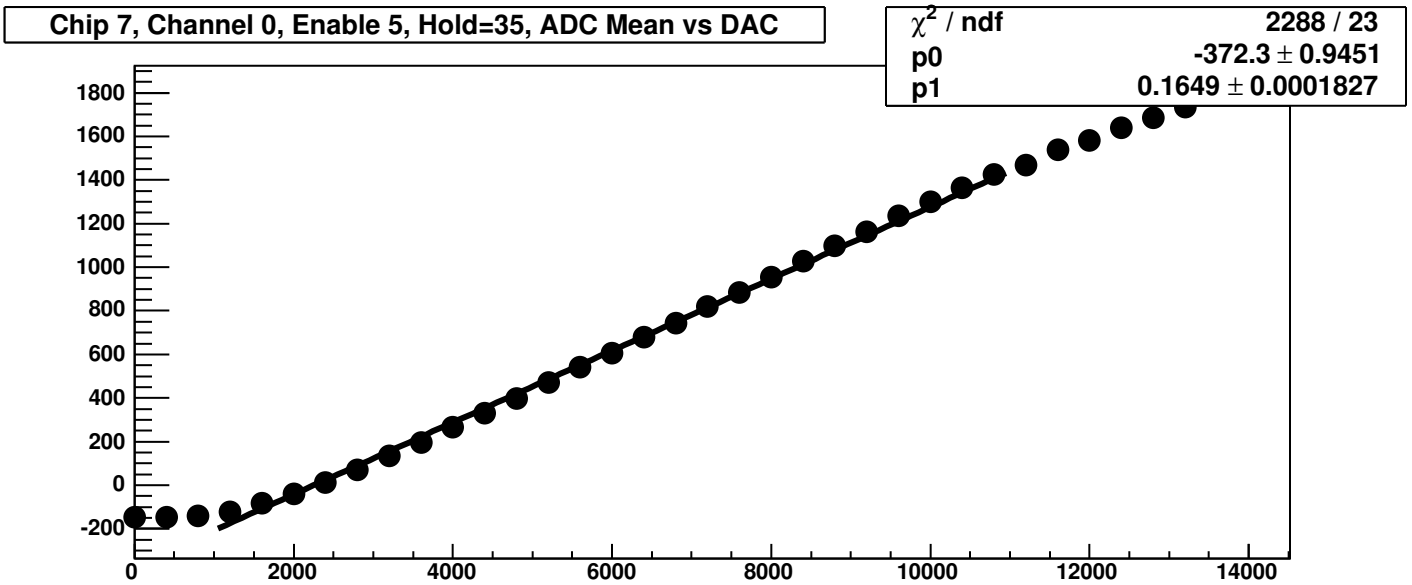


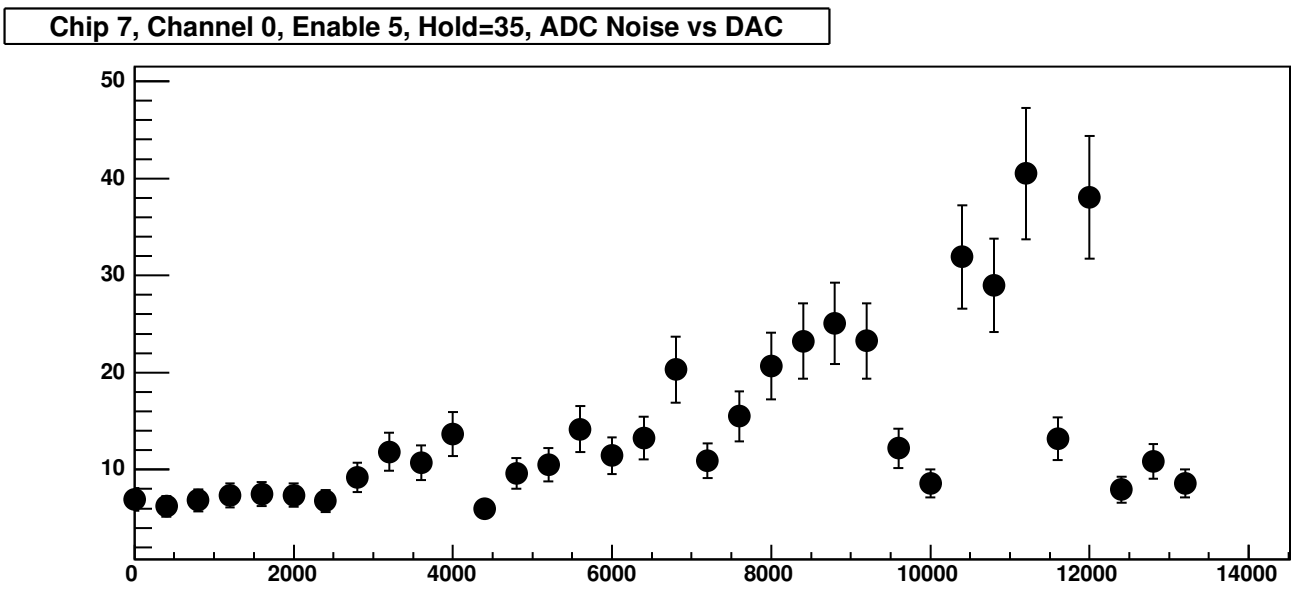
Chip 7, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

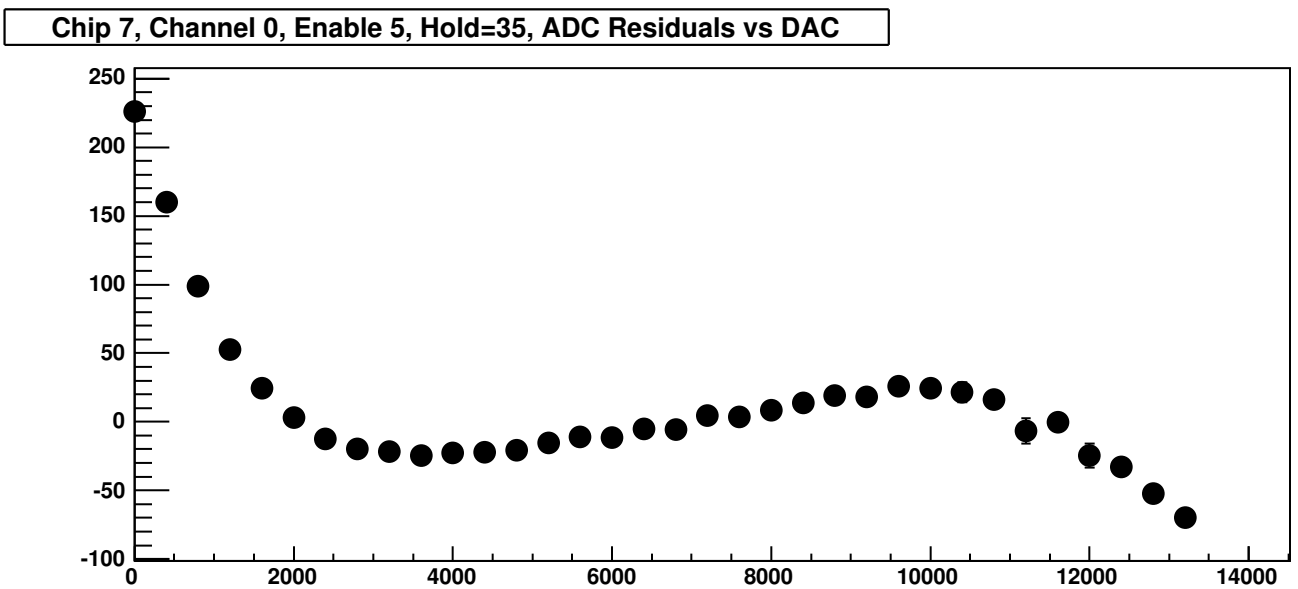


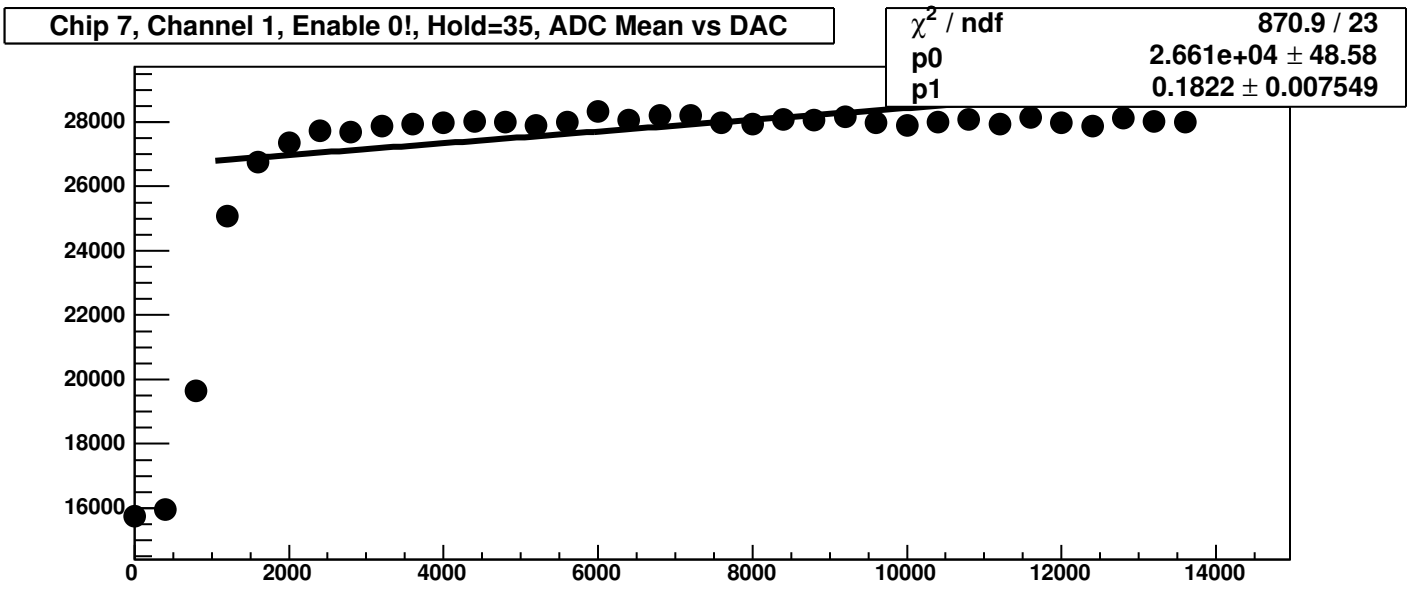
Chip 7, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

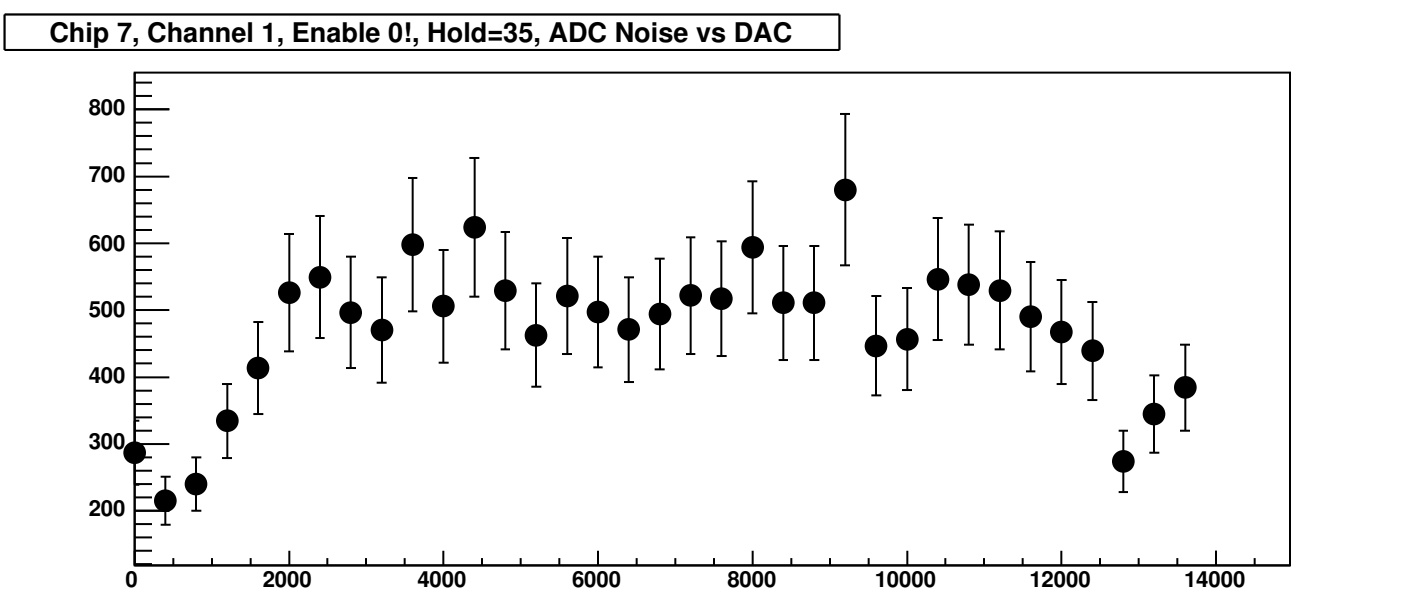


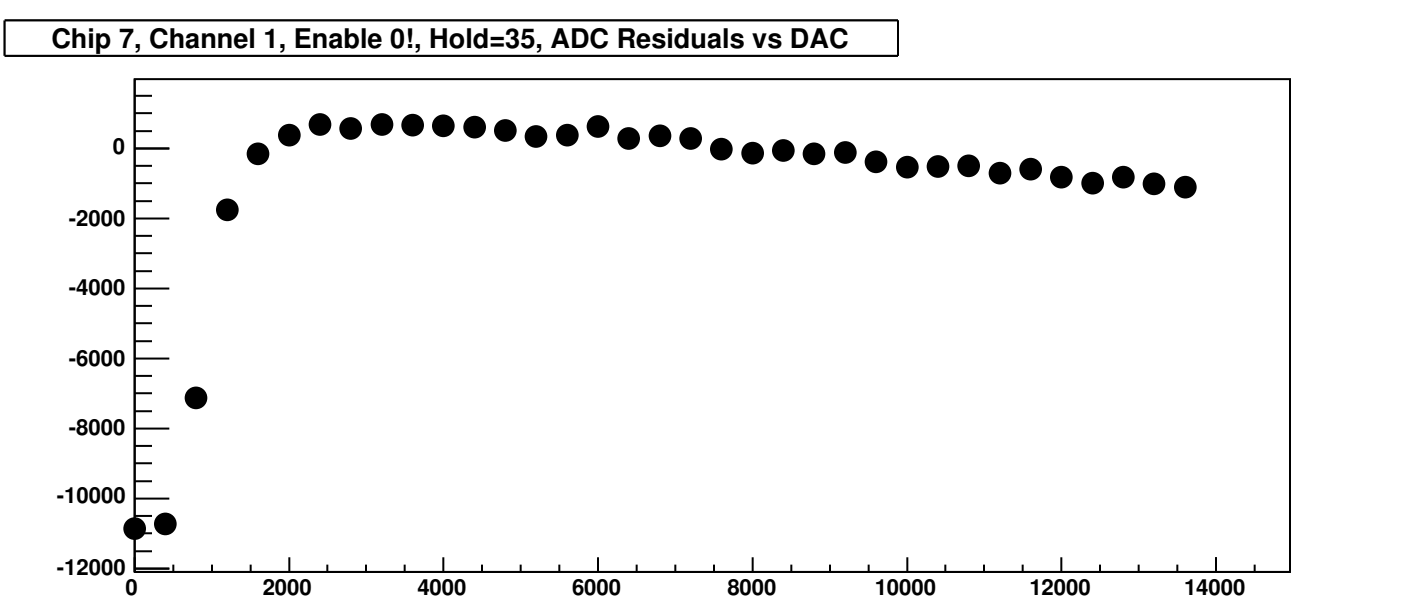

Chip 7, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2288 / 23
p0 -372.3 ± 0.9451
p1 0.1649 ± 0.0001827
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

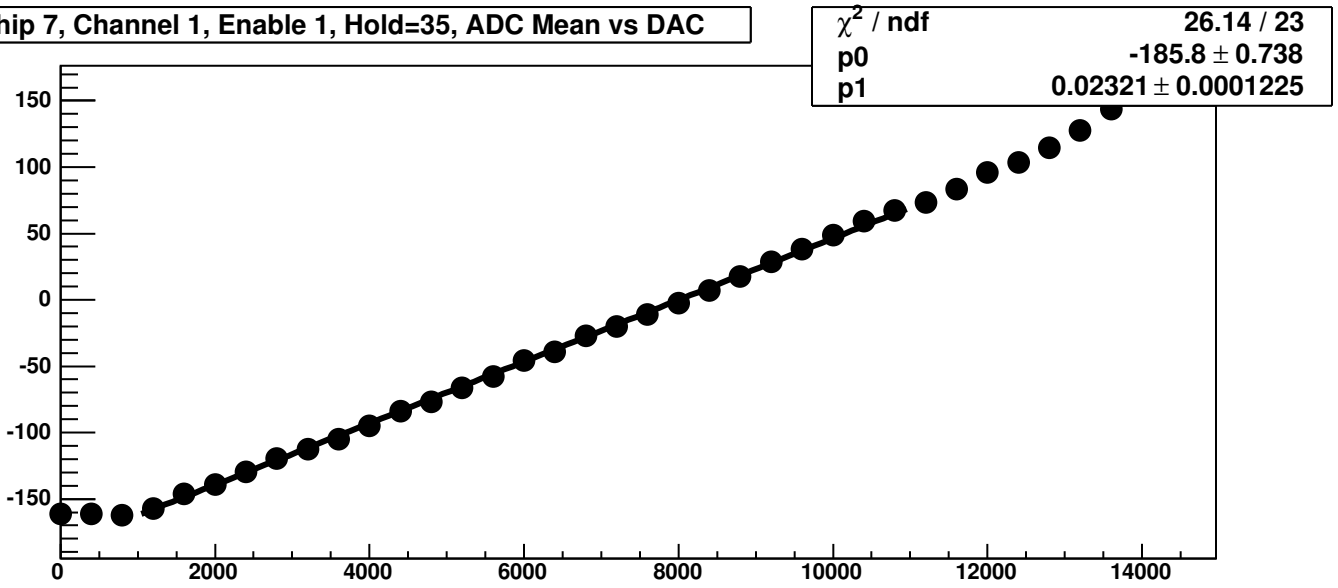

Chip 7, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 870.9 / 23
p0 2.661e+04 ± 48.58
p1 0.1822 ± 0.007549
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

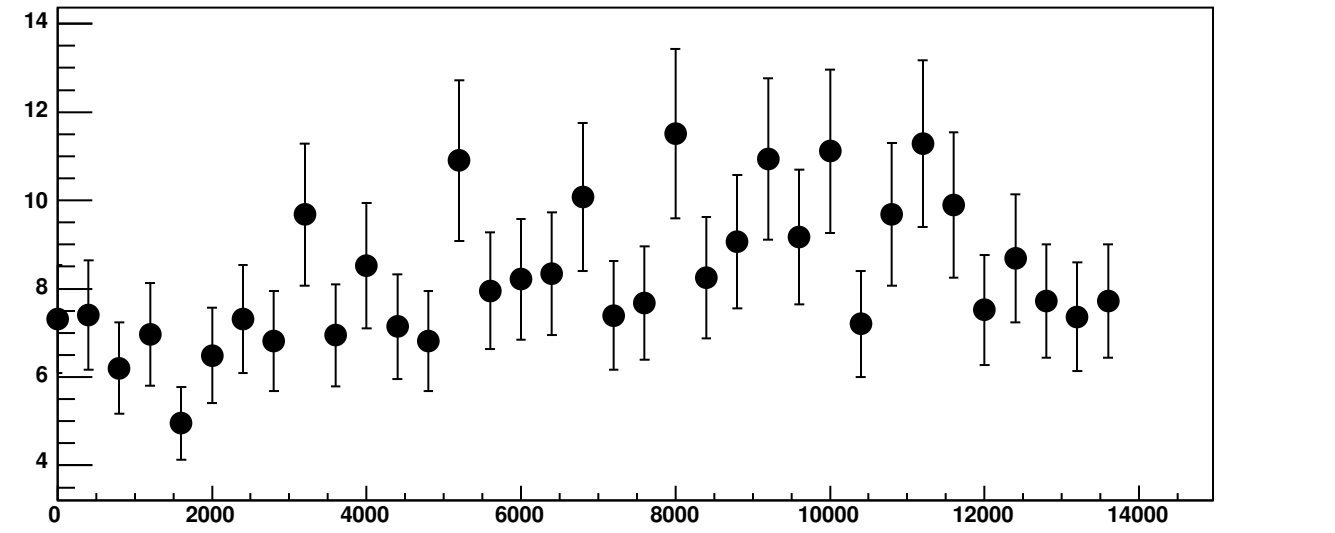

Chip 7, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

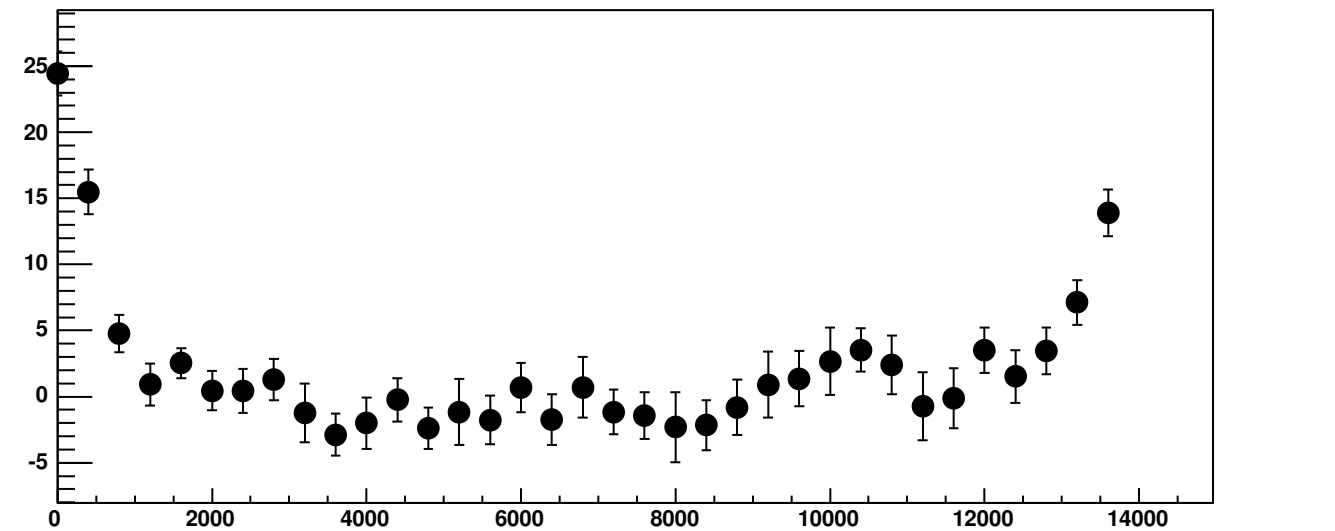
Chip 7, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



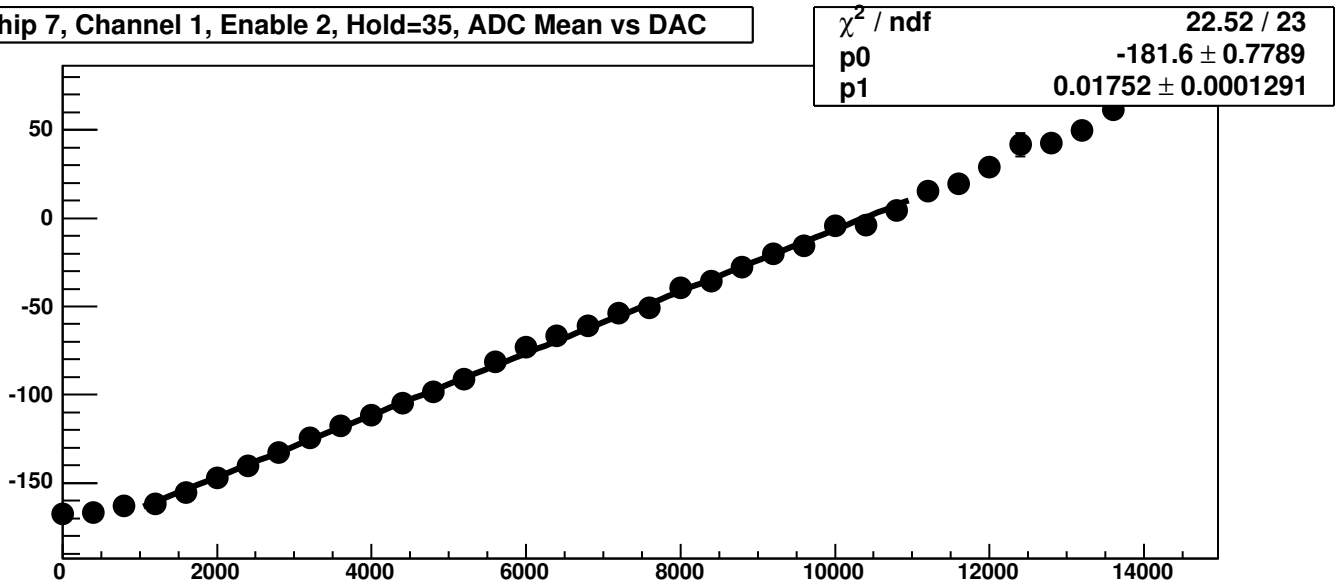
Chip 7, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



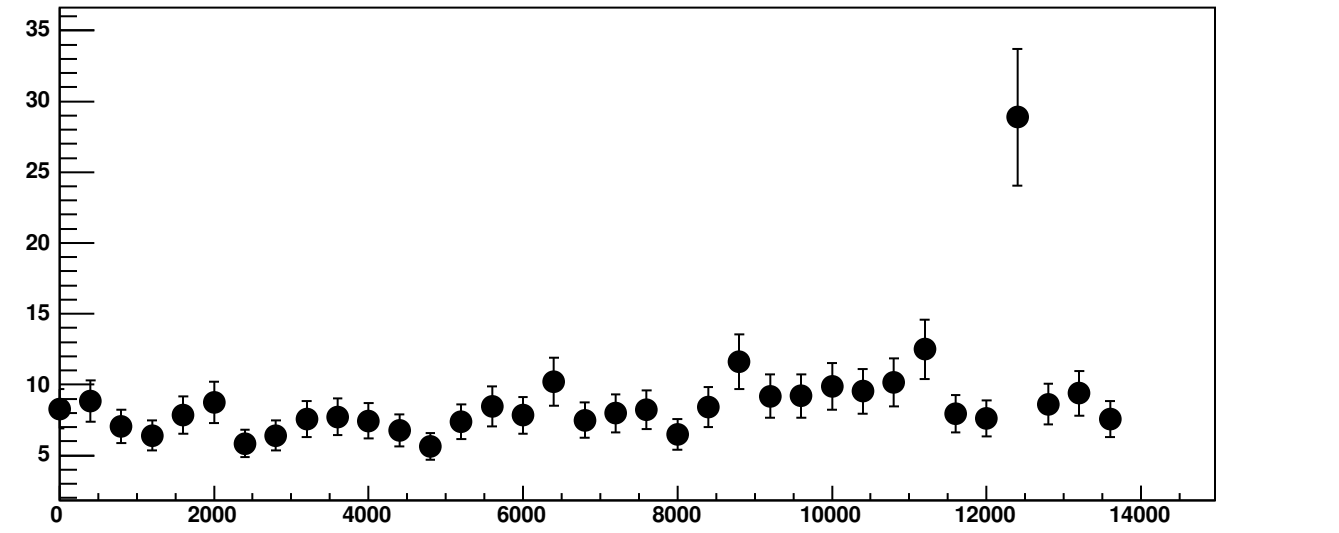
Chip 7, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



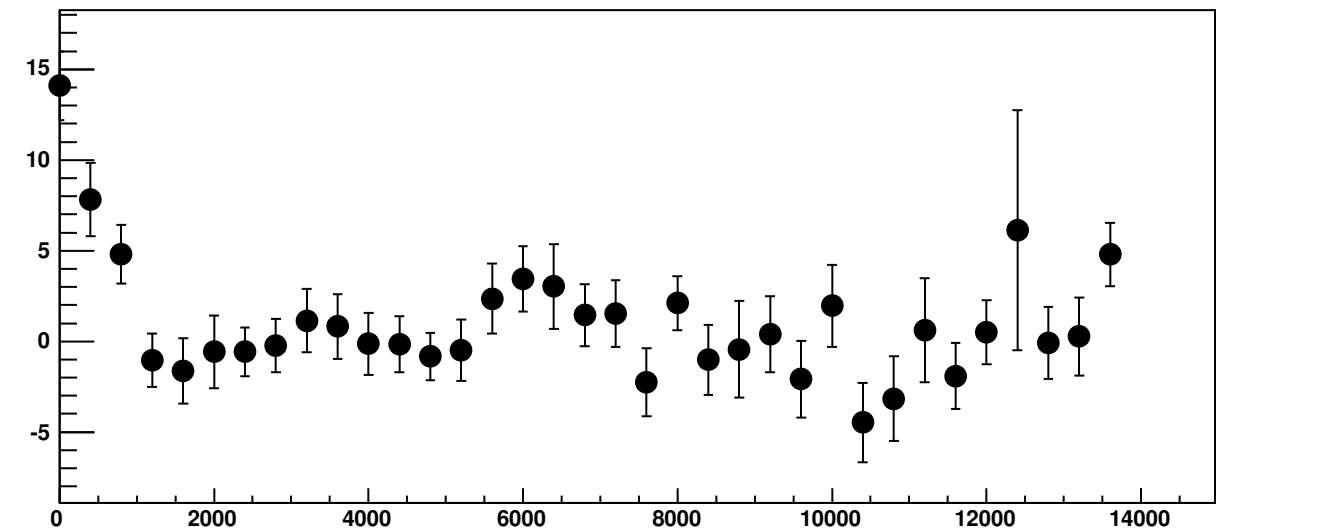
Chip 7, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

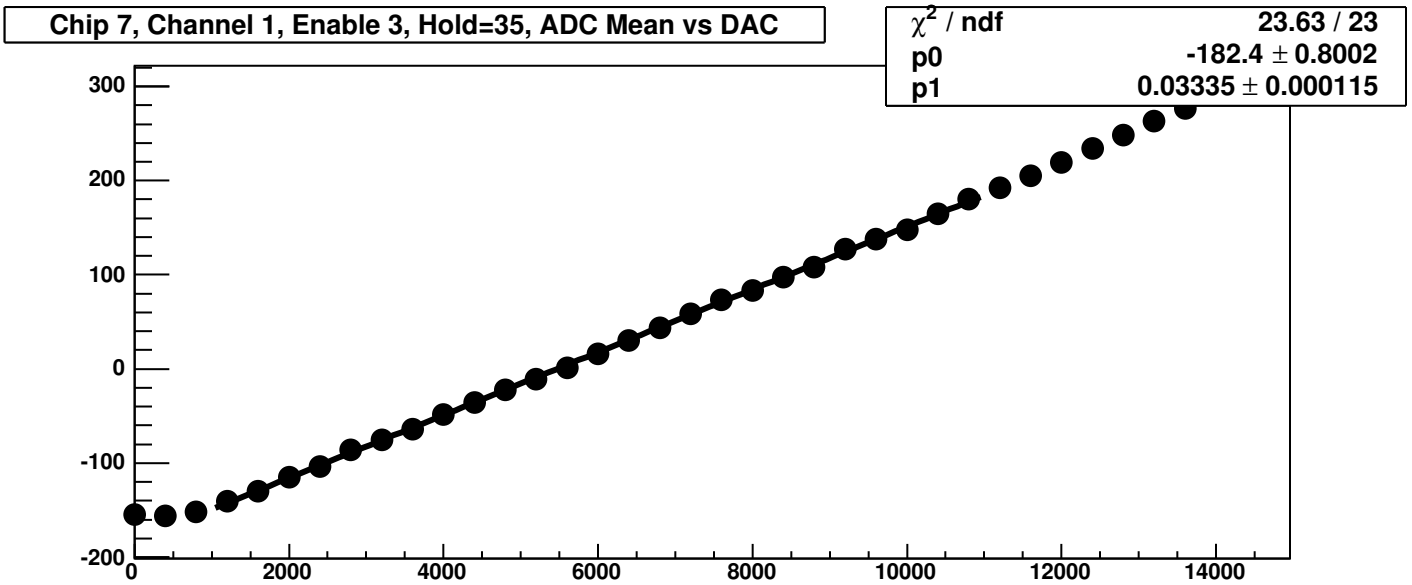


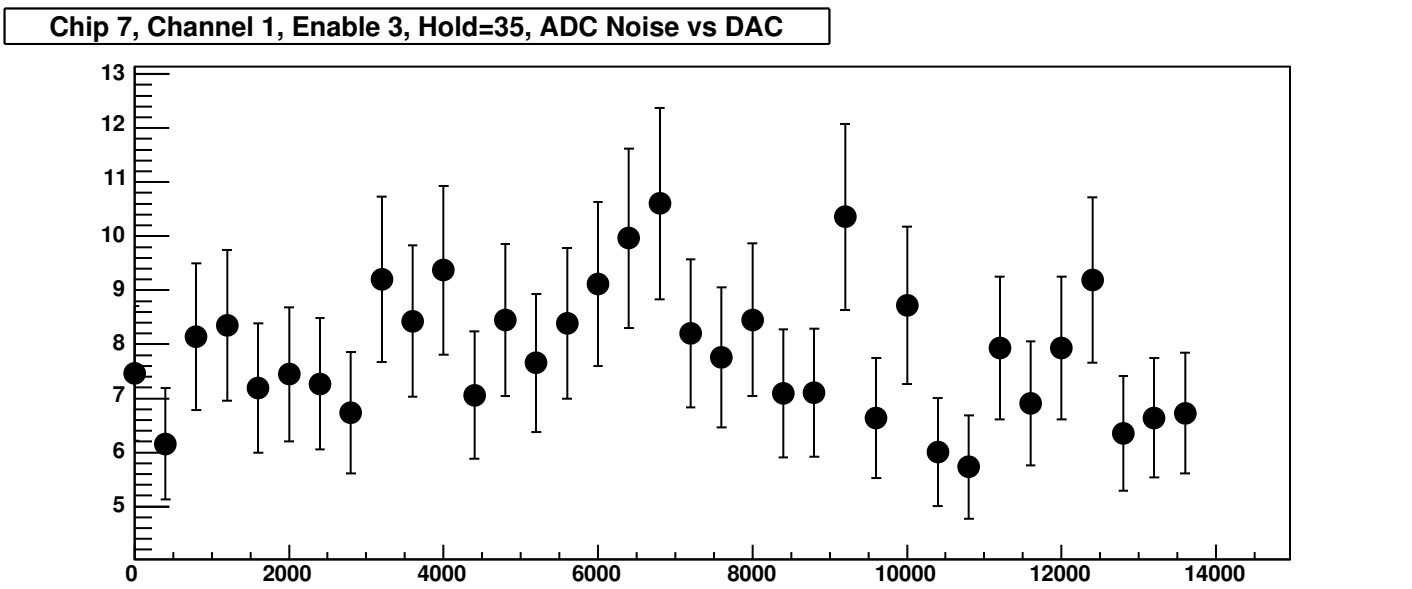
Chip 7, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

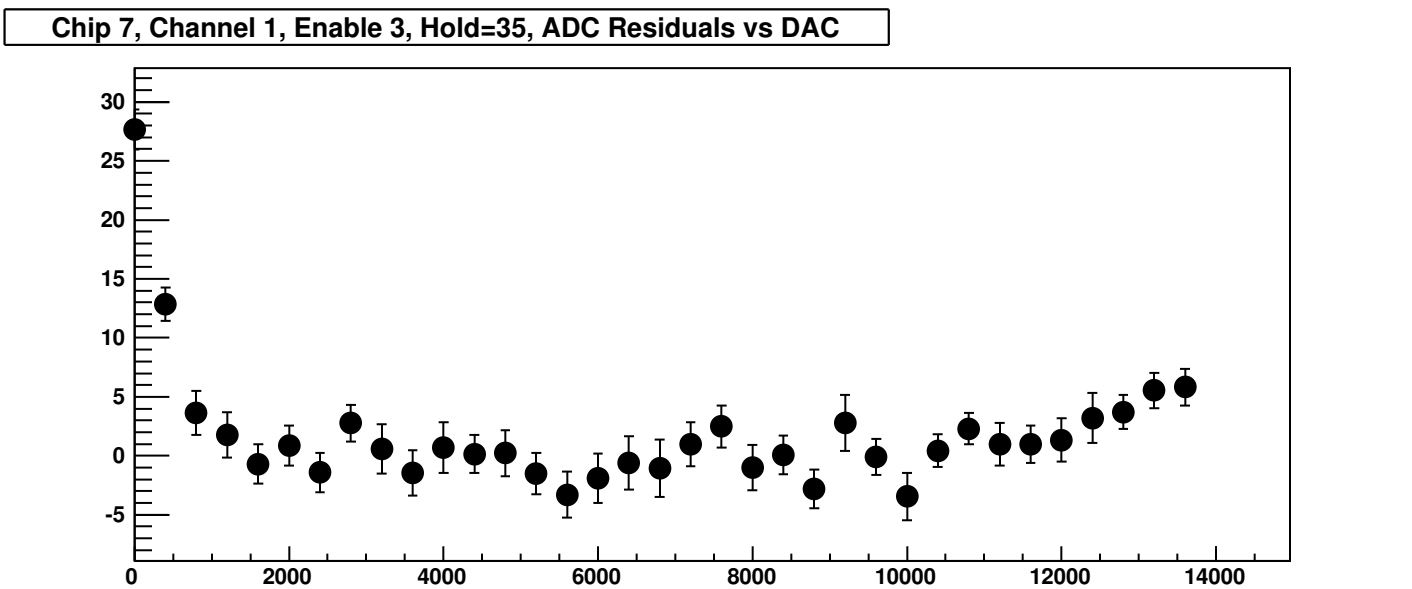


Chip 7, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

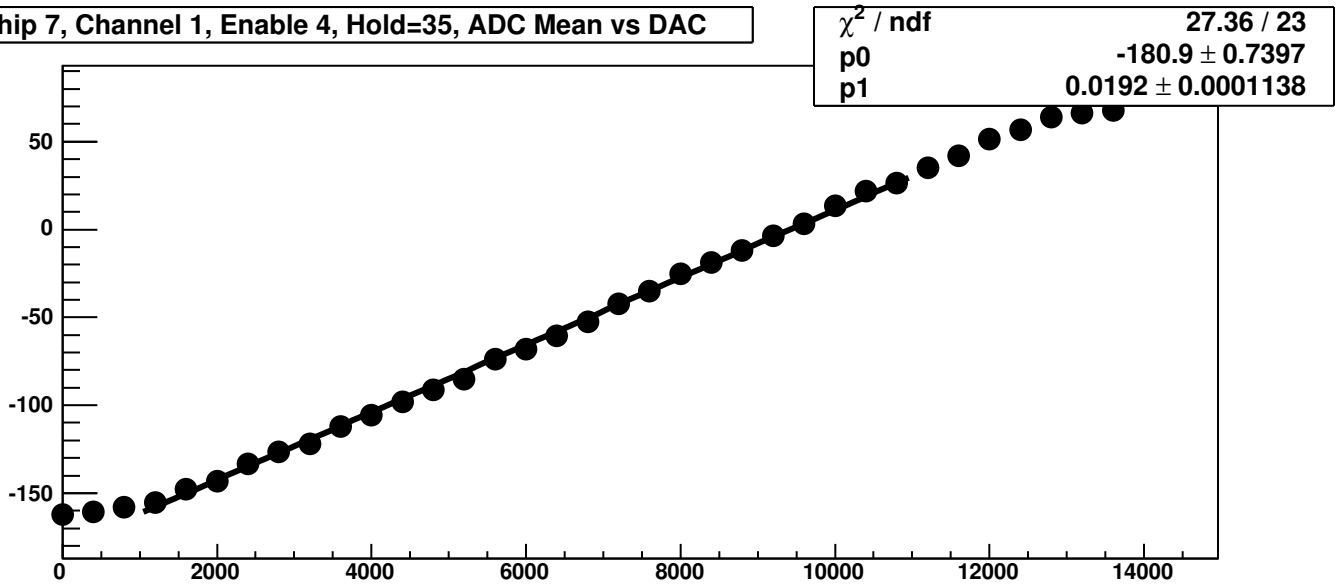



Chip 7, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.63 / 23
p0 -182.4 ± 0.8002
p1 0.03335 ± 0.000115
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

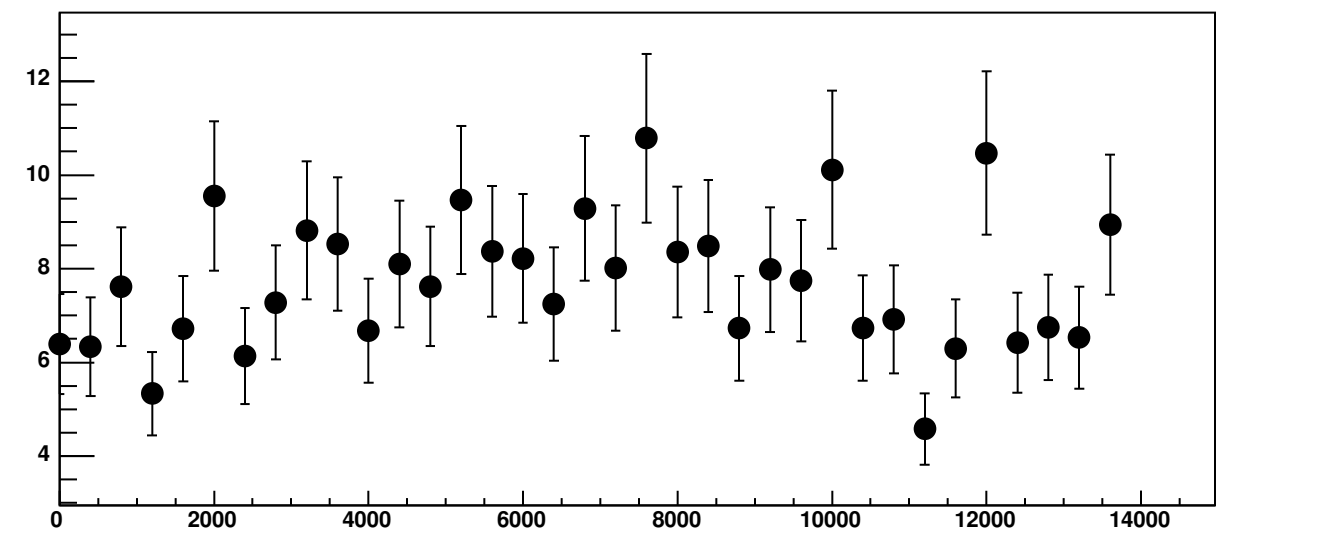

Chip 7, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

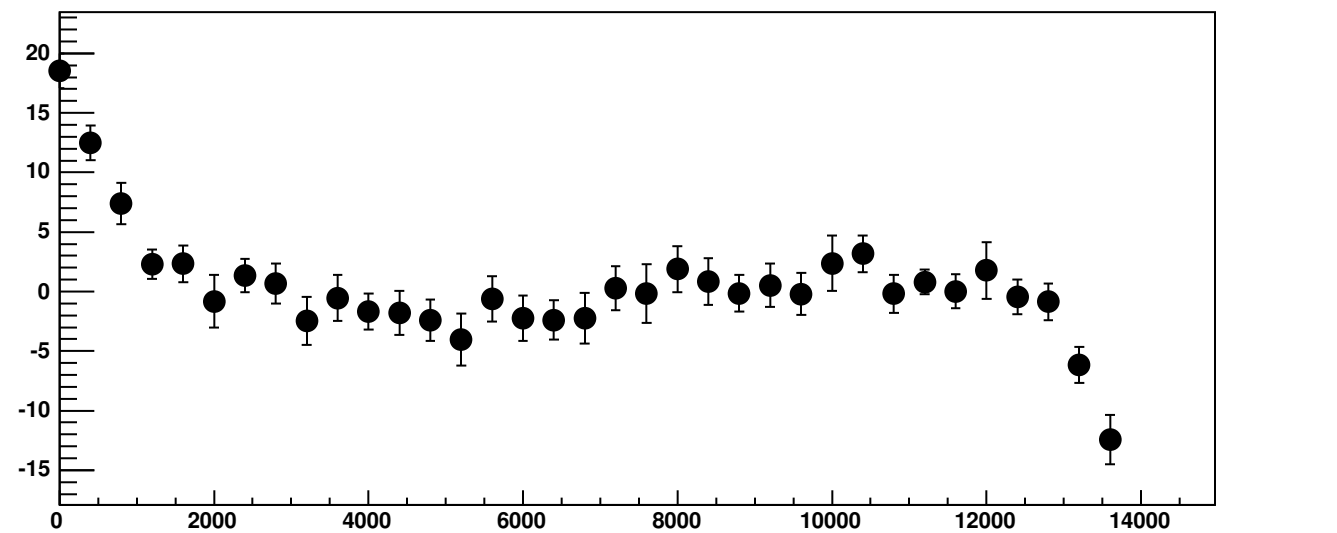
Chip 7, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

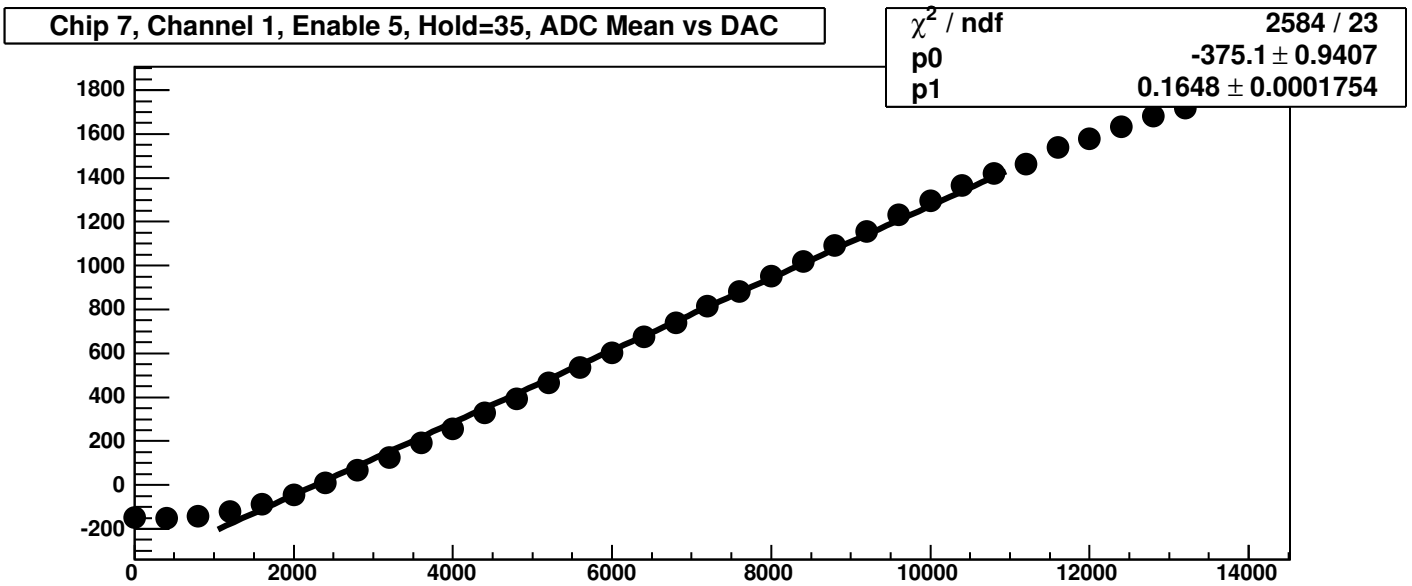


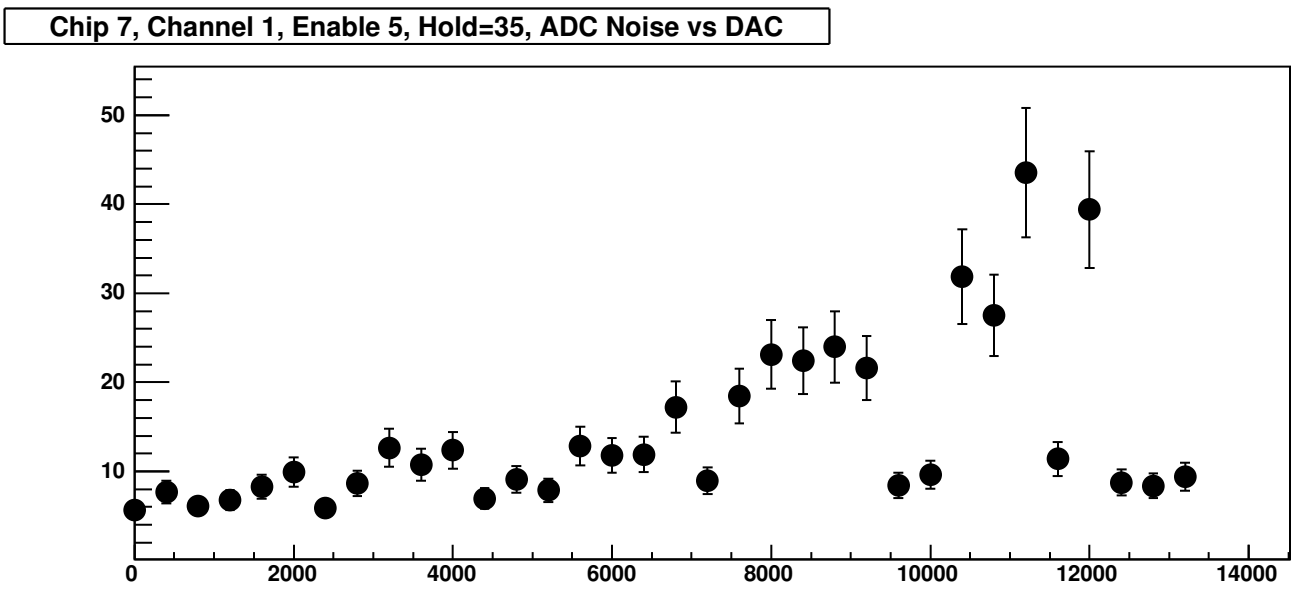
Chip 7, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

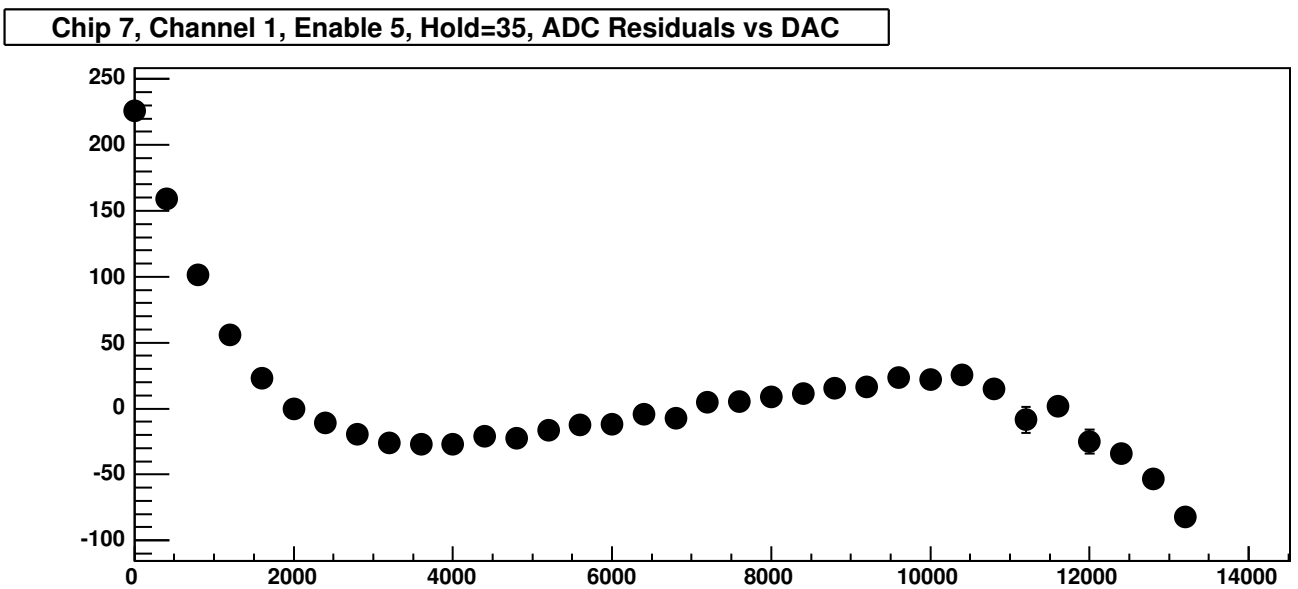


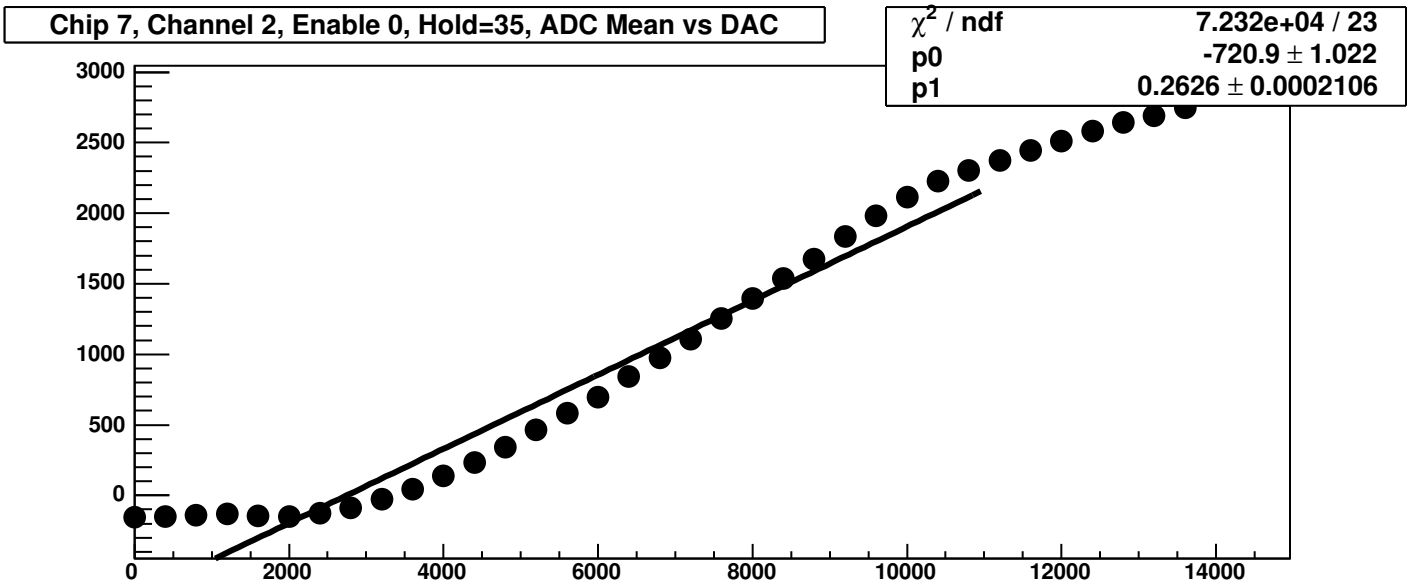
Chip 7, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

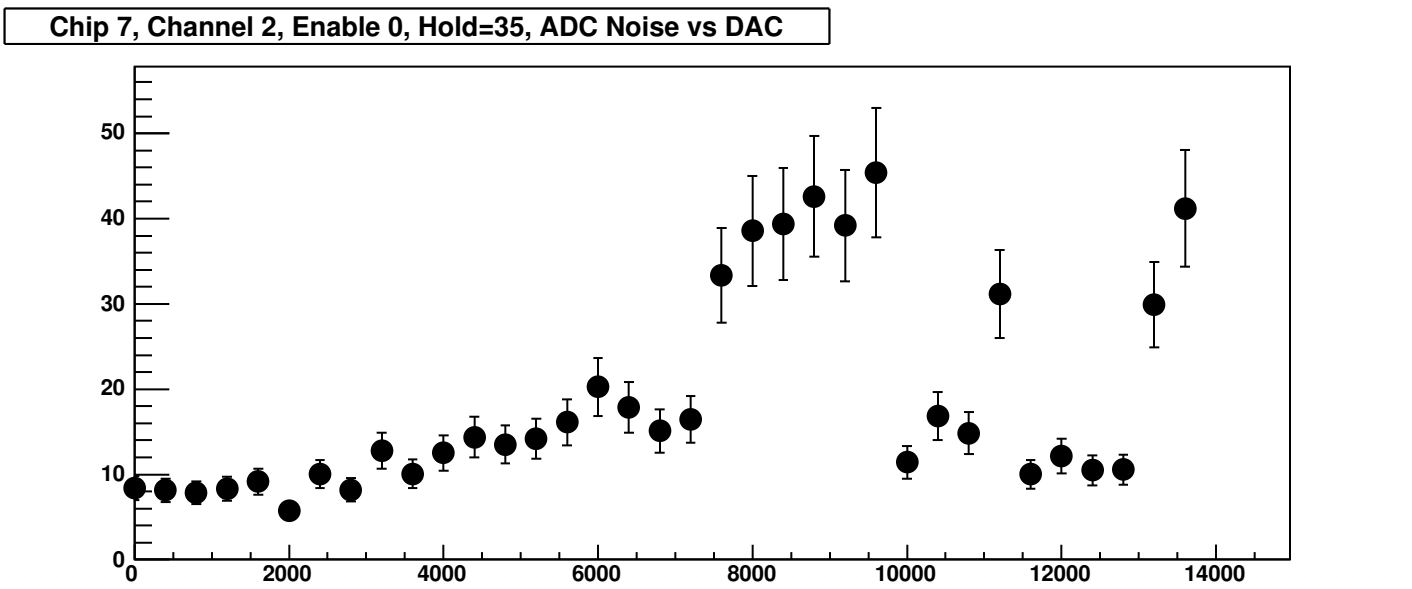


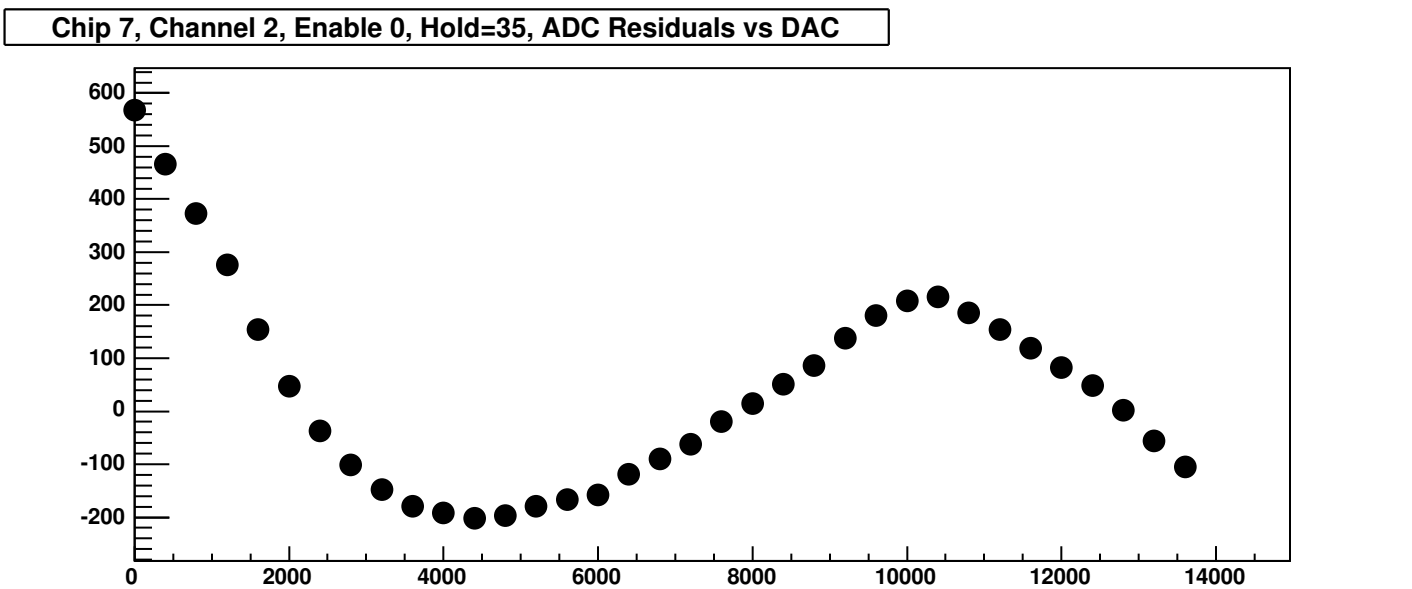

Chip 7, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2584 / 23
p0 -375.1 ± 0.9407
p1 0.1648 ± 0.0001754
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

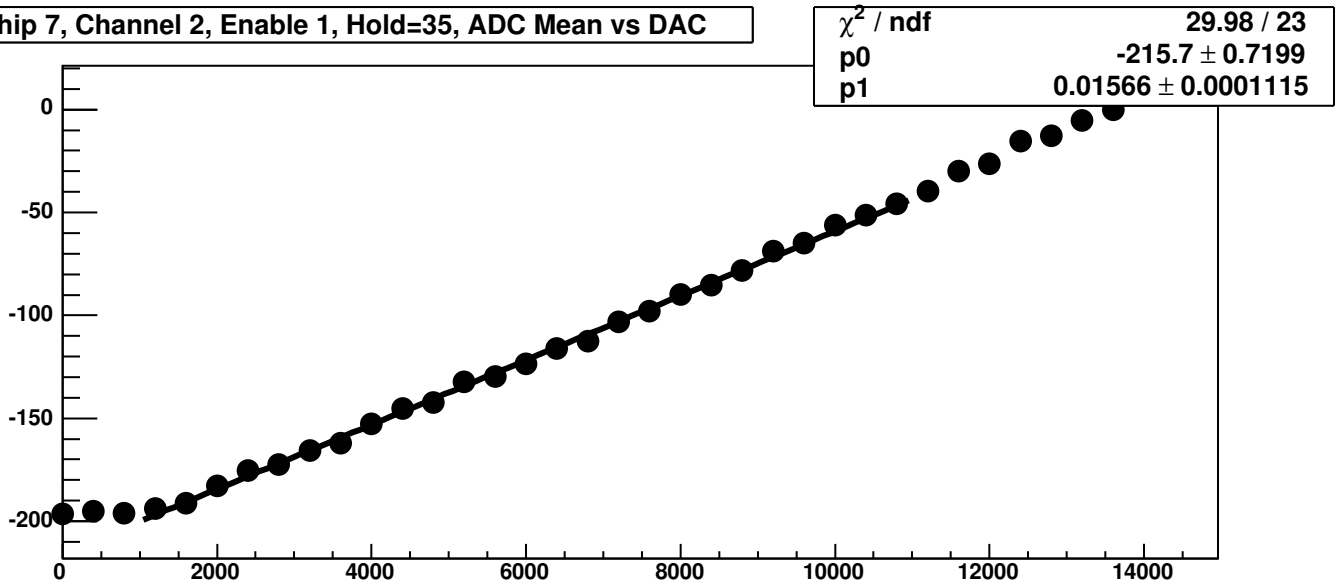

Chip 7, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.232e+04 / 23
p0 -720.9 ± 1.022
p1 0.2626 ± 0.0002106
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

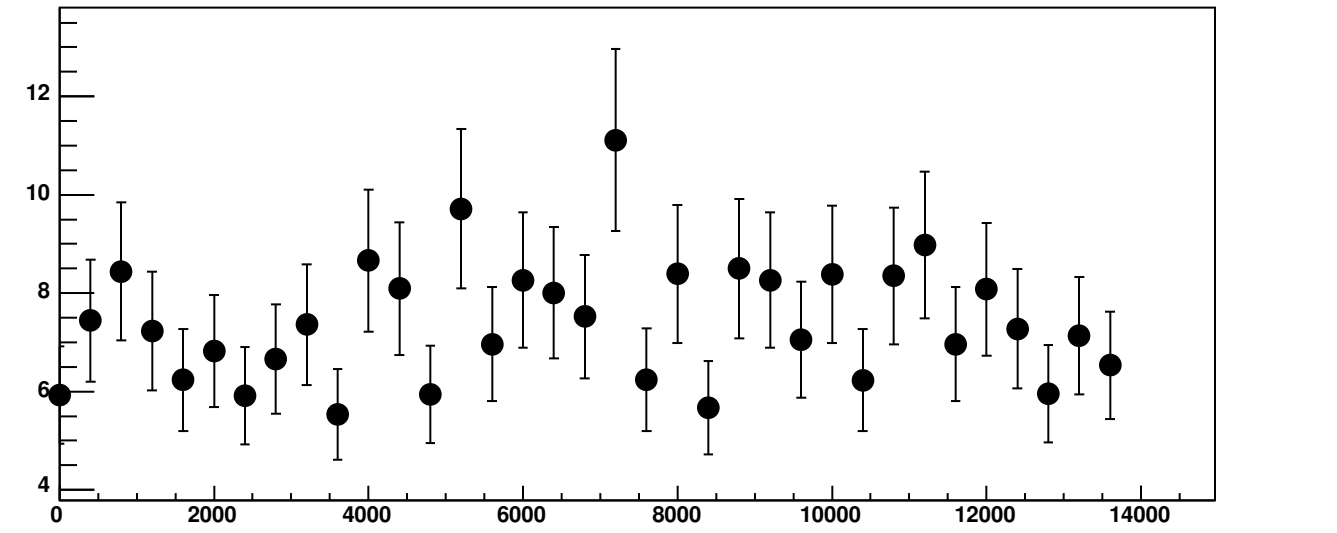

Chip 7, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

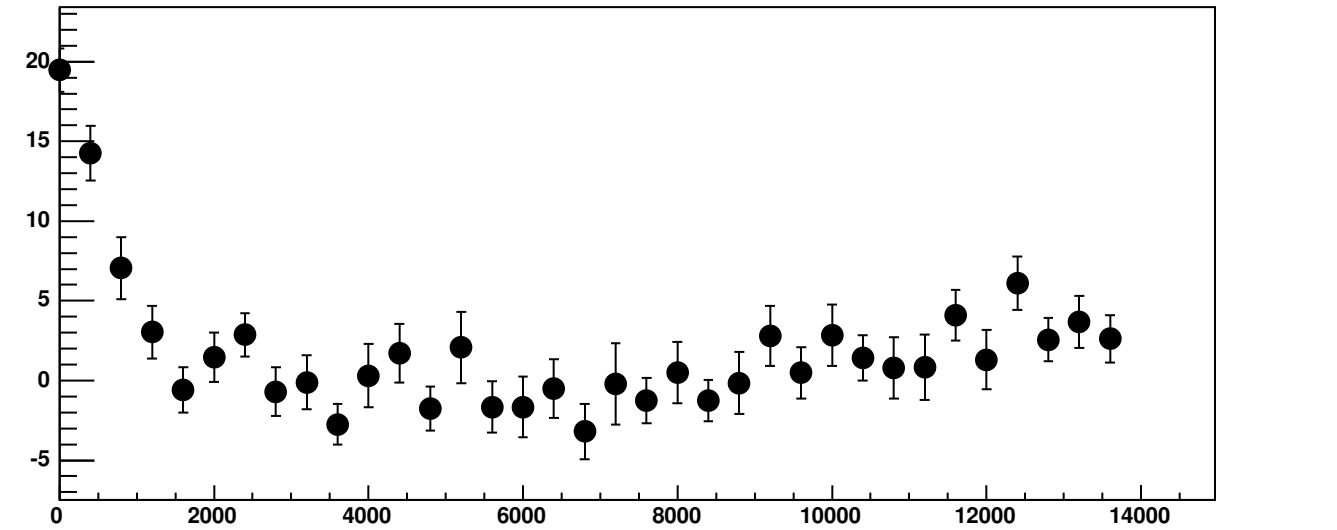
Chip 7, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

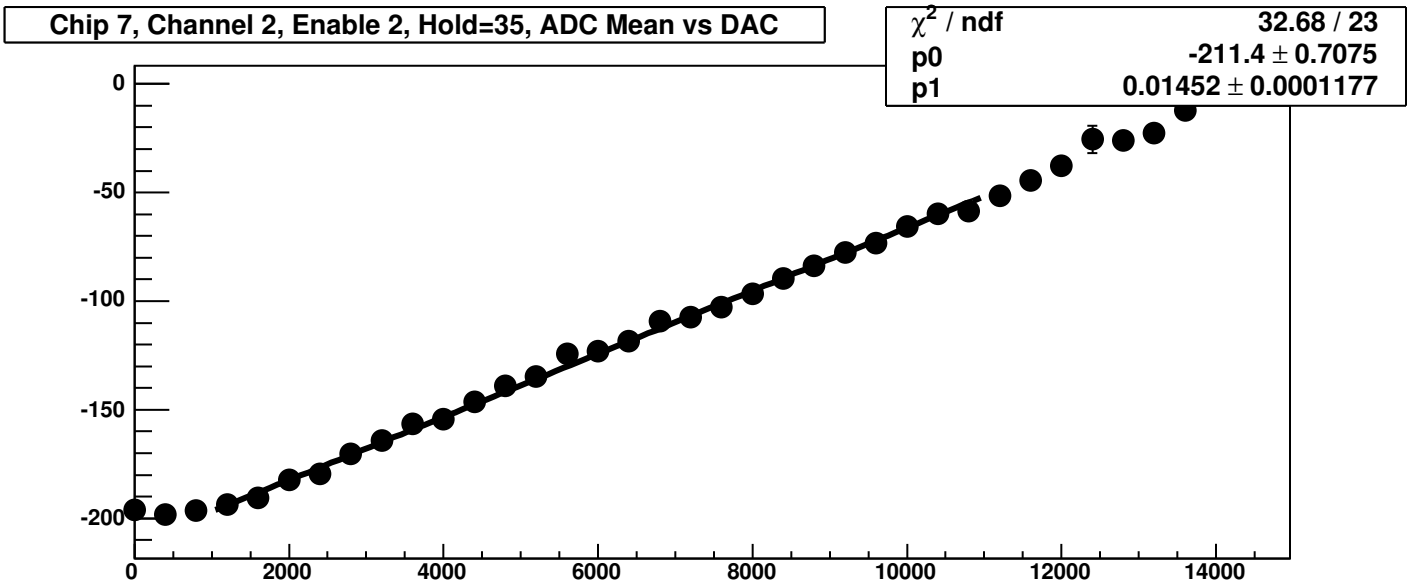


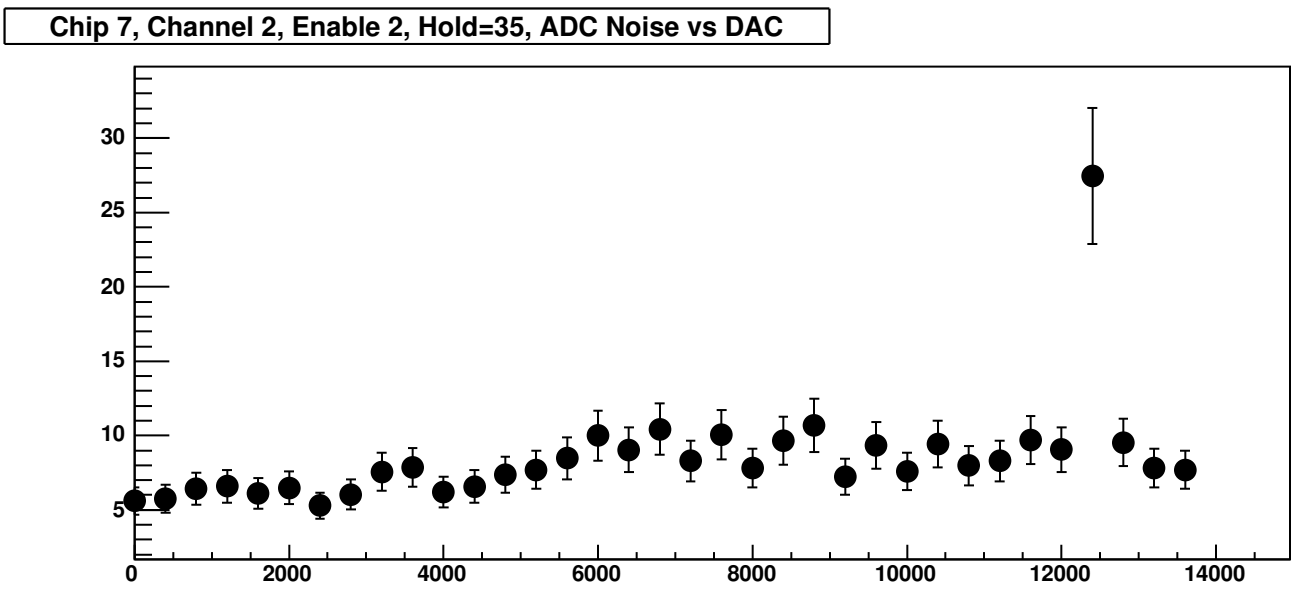
Chip 7, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

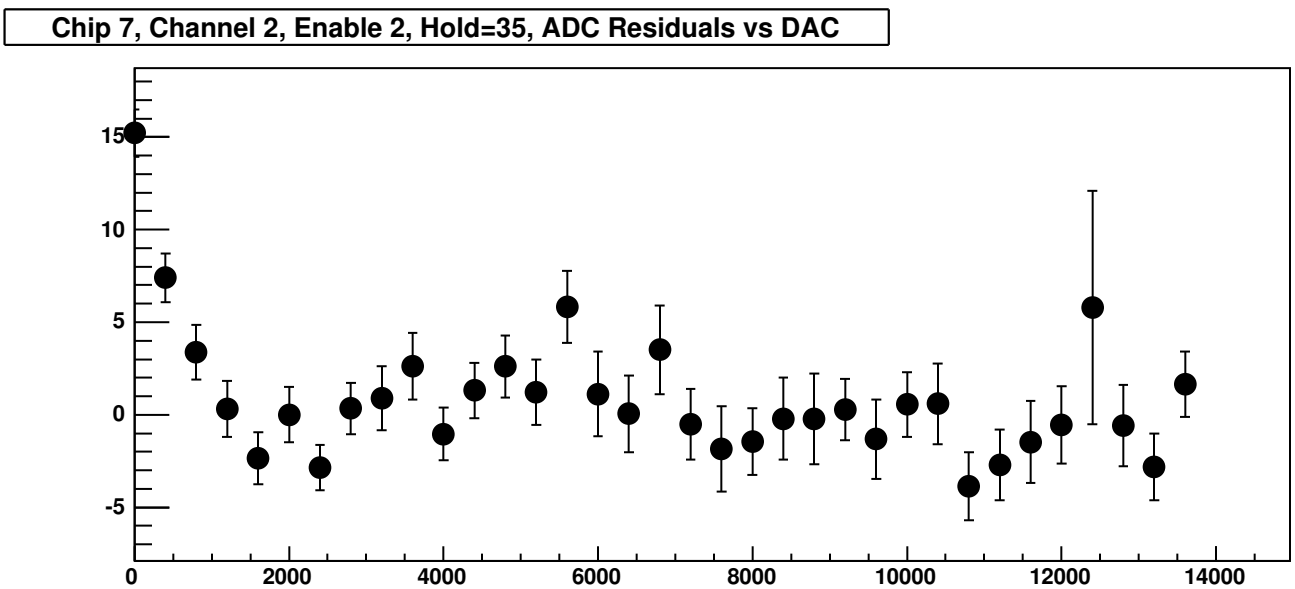


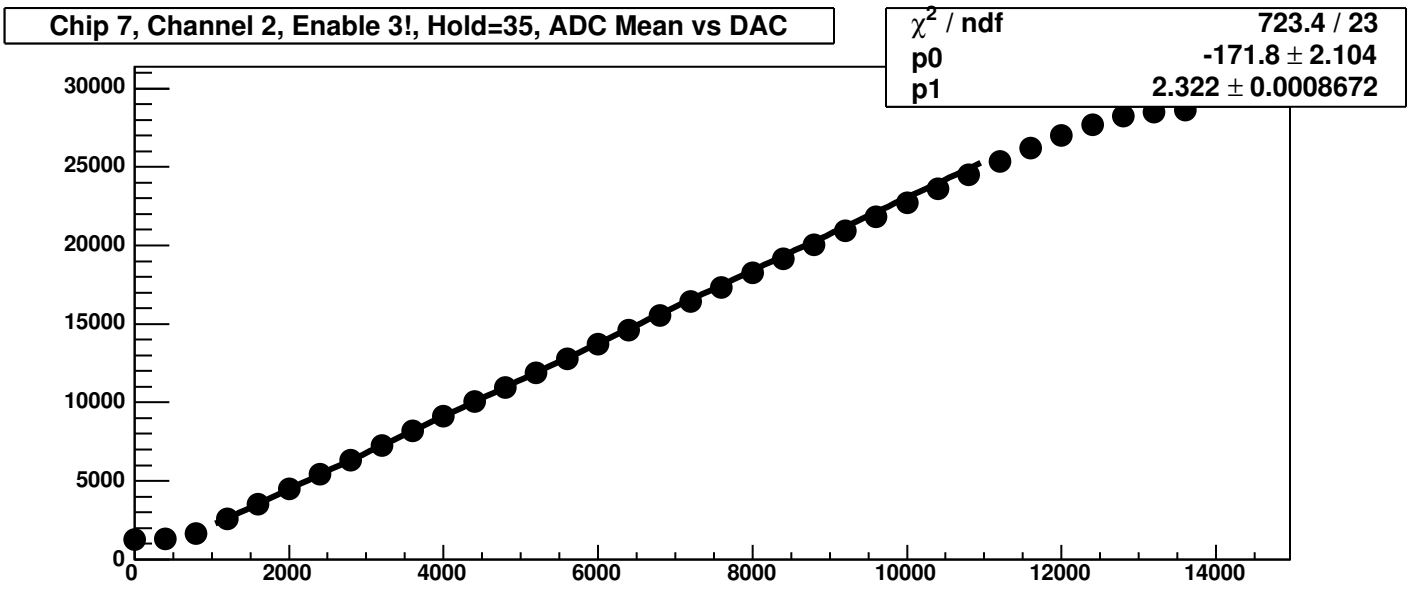
Chip 7, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

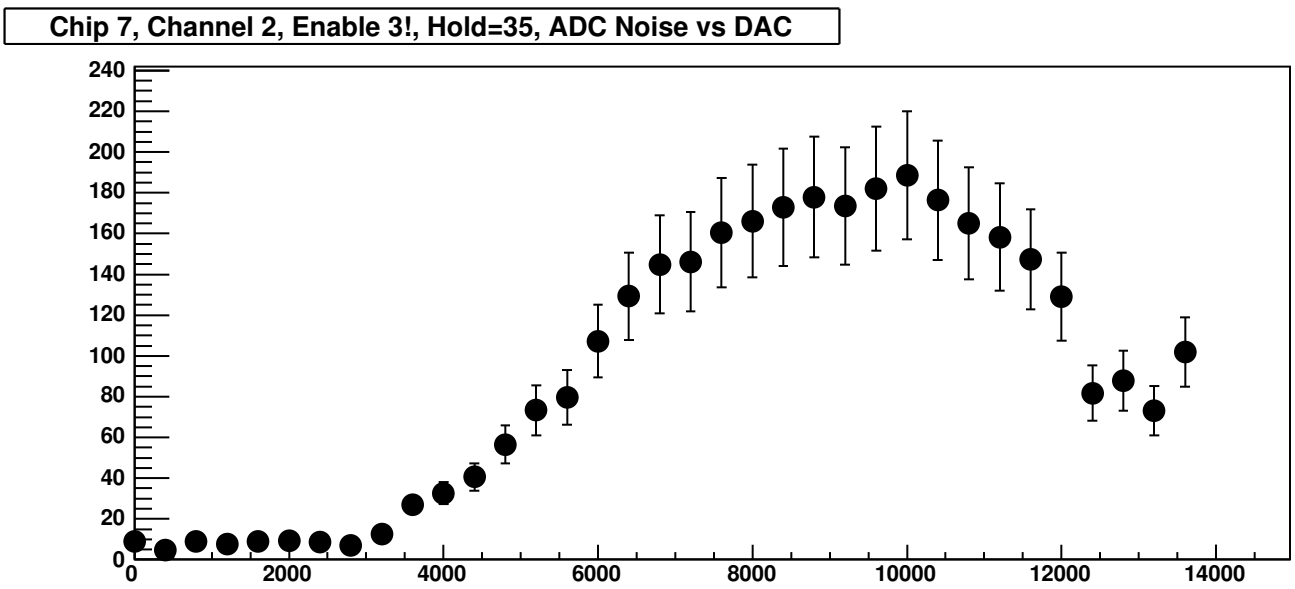


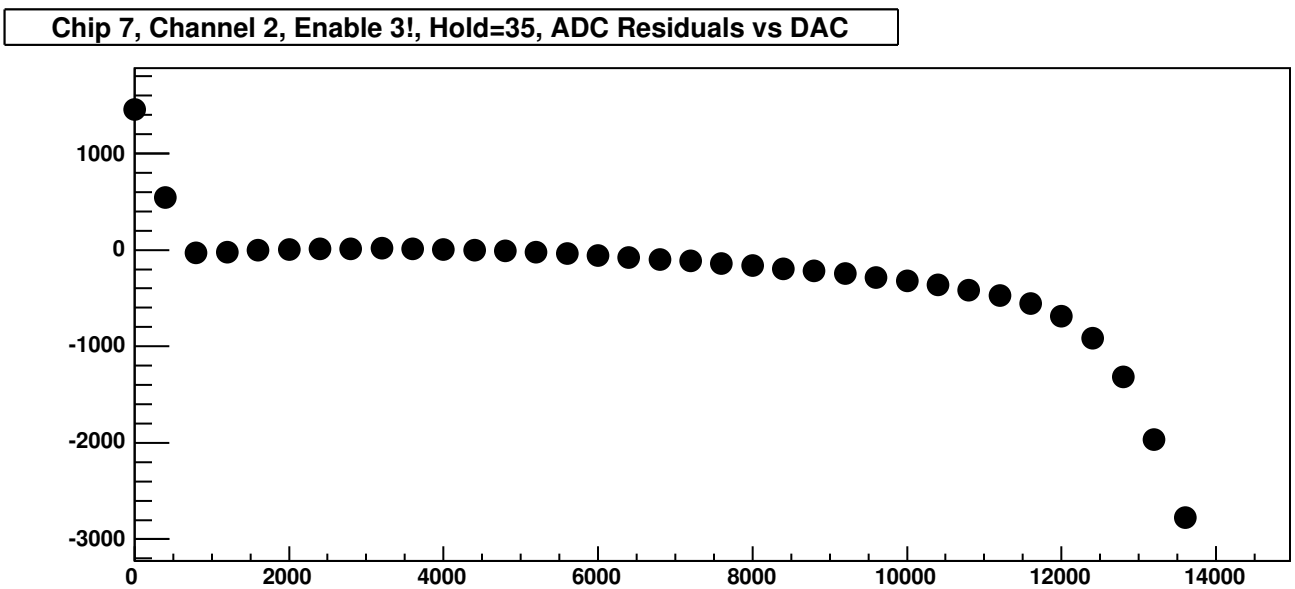

Chip 7, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.68 / 23
p0 -211.4 ± 0.7075
p1 0.01452 ± 0.0001177
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

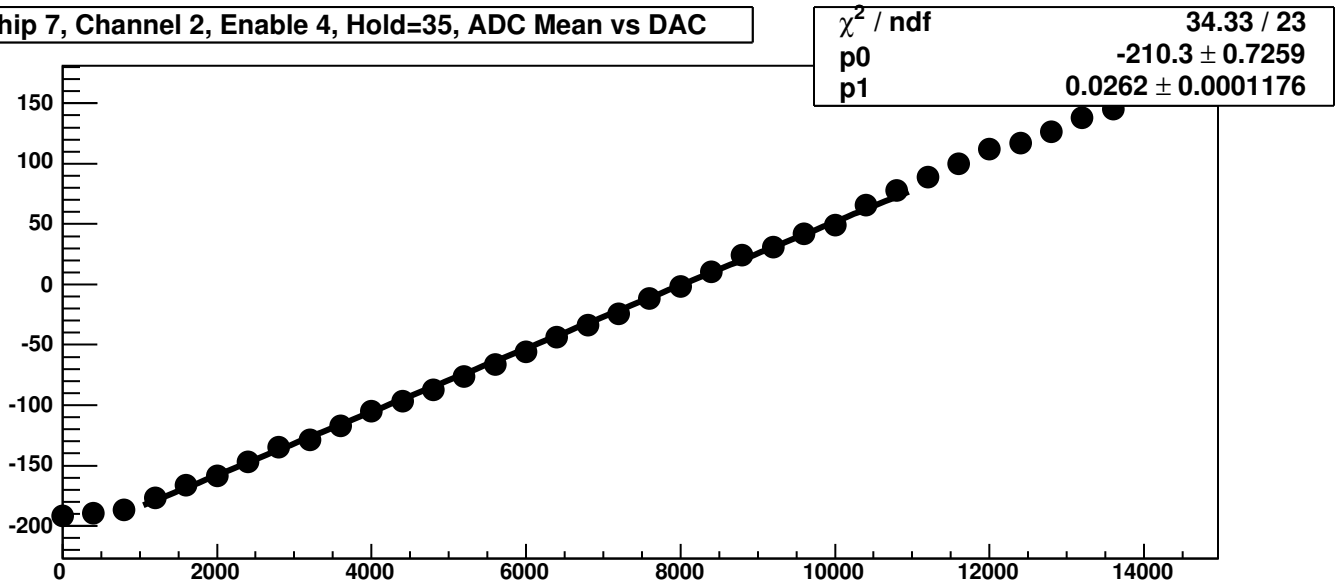

Chip 7, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 723.4 / 23
p0 -171.8 ± 2.104
p1 2.322 ± 0.0008672
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

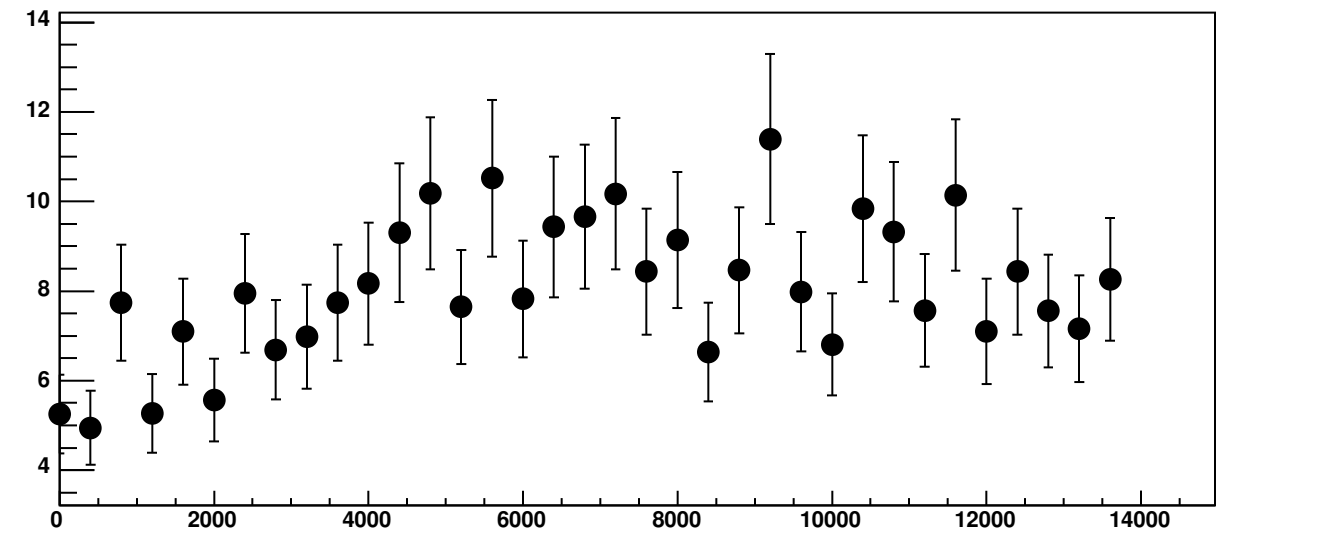

Chip 7, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

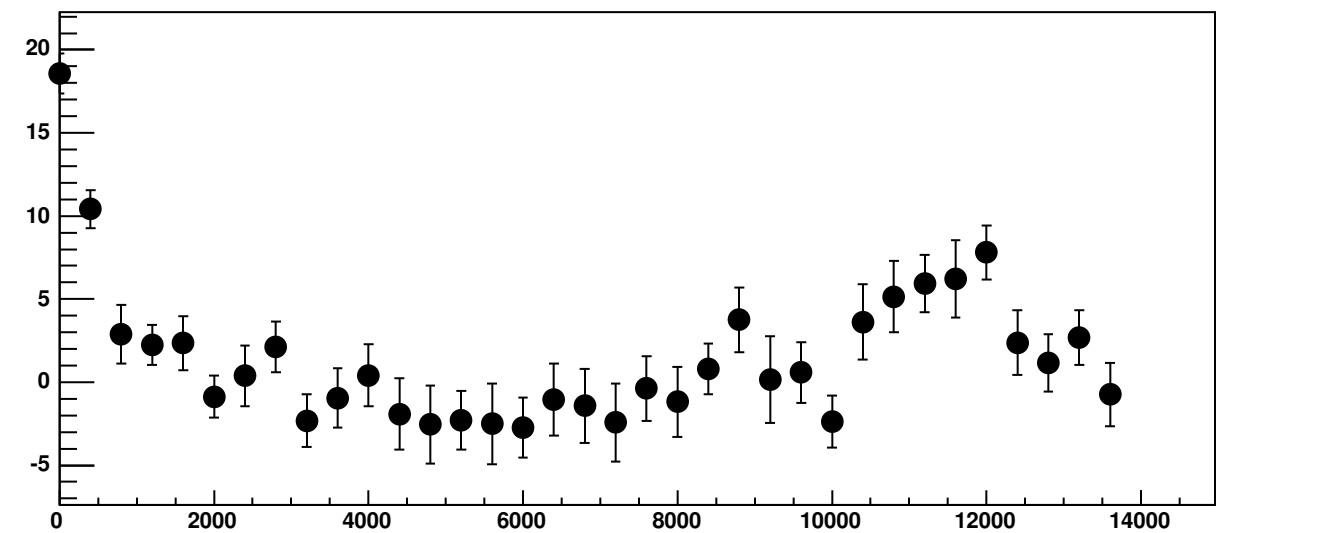
Chip 7, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

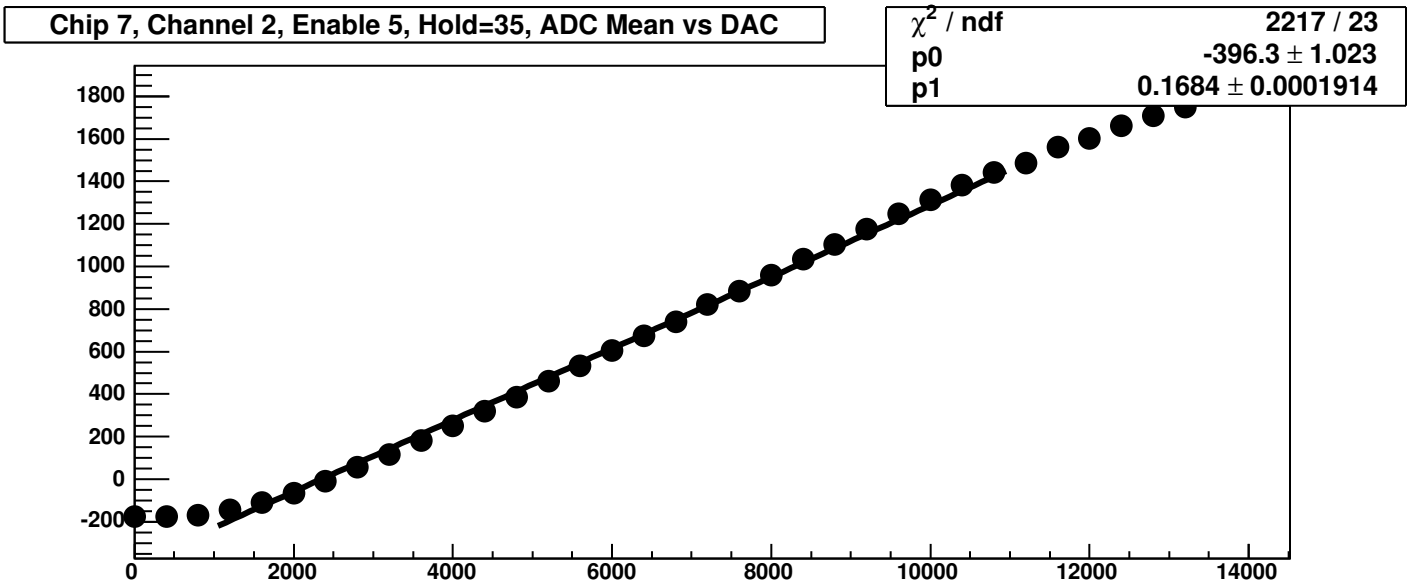


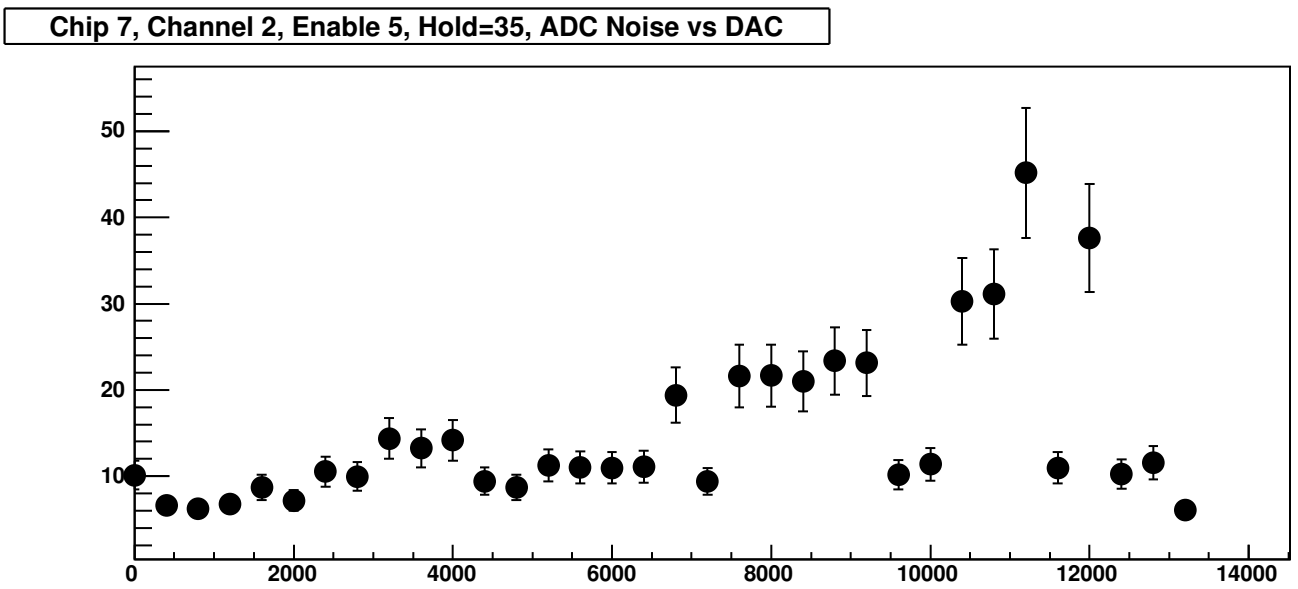
Chip 7, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

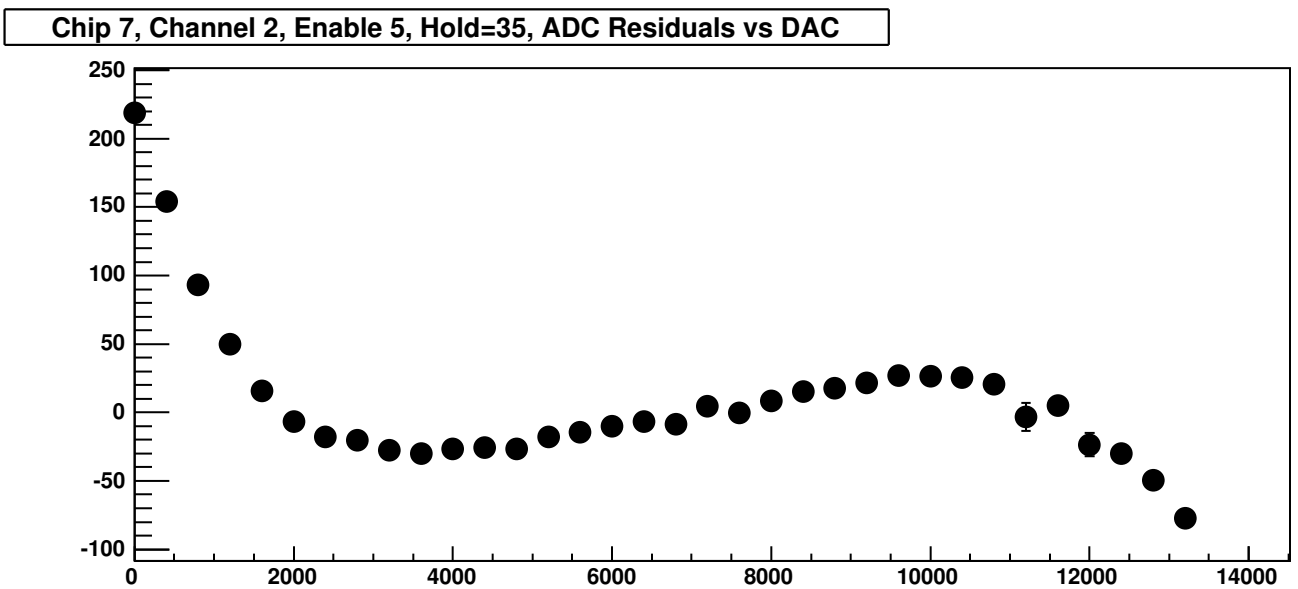


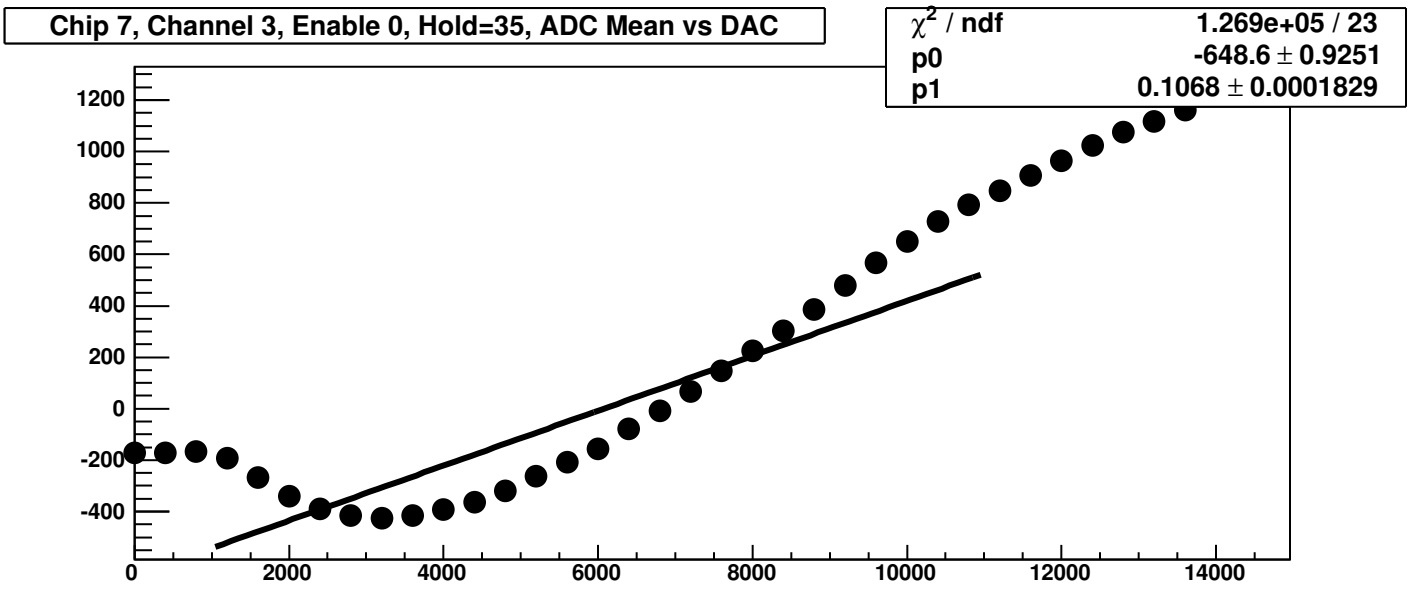
Chip 7, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

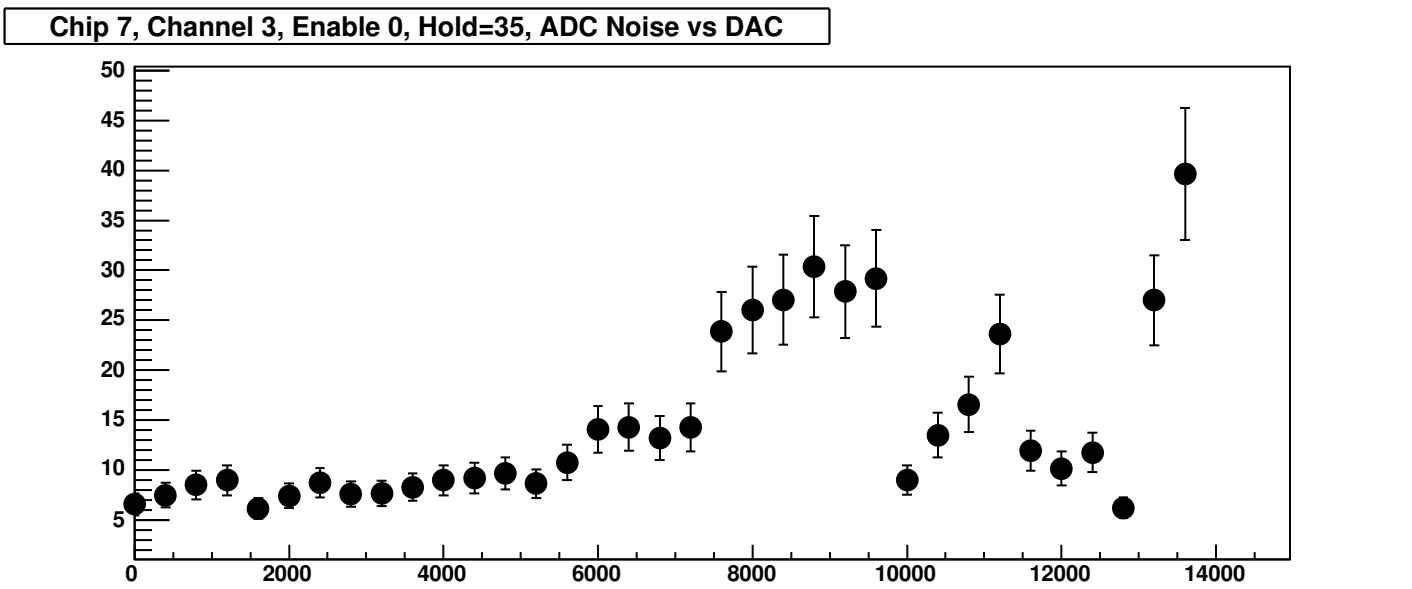


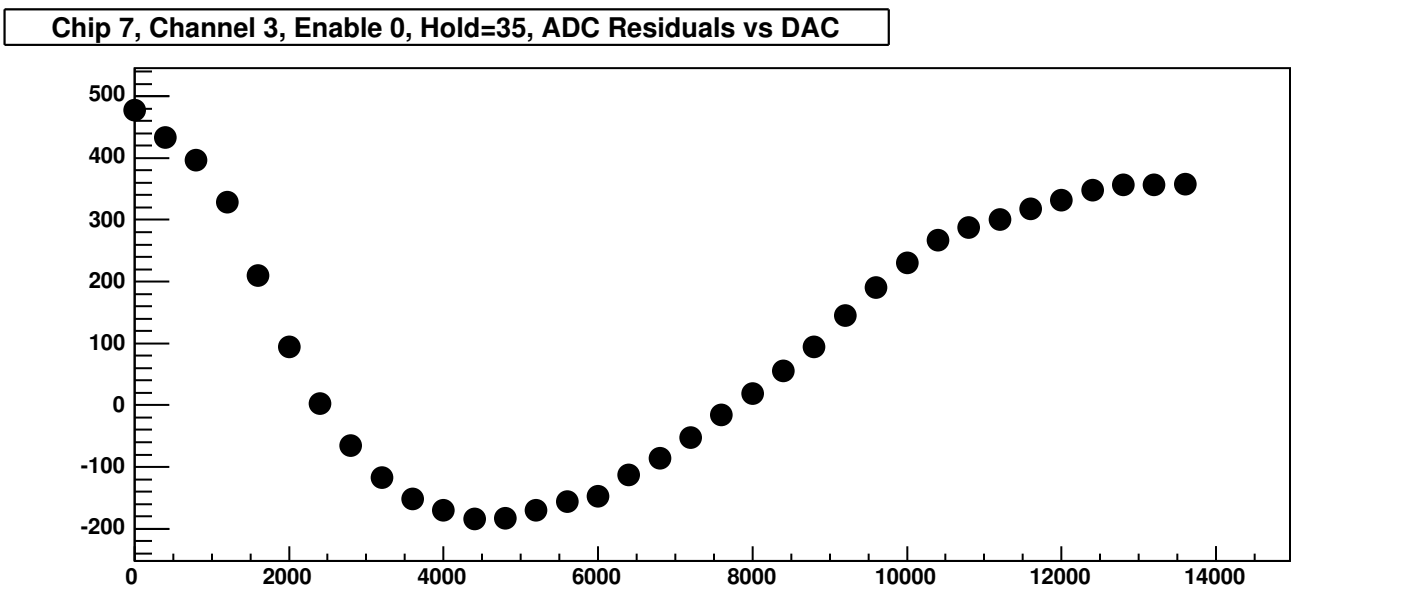

Chip 7, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2217 / 23
p0 -396.3 ± 1.023
p1 0.1684 ± 0.0001914
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

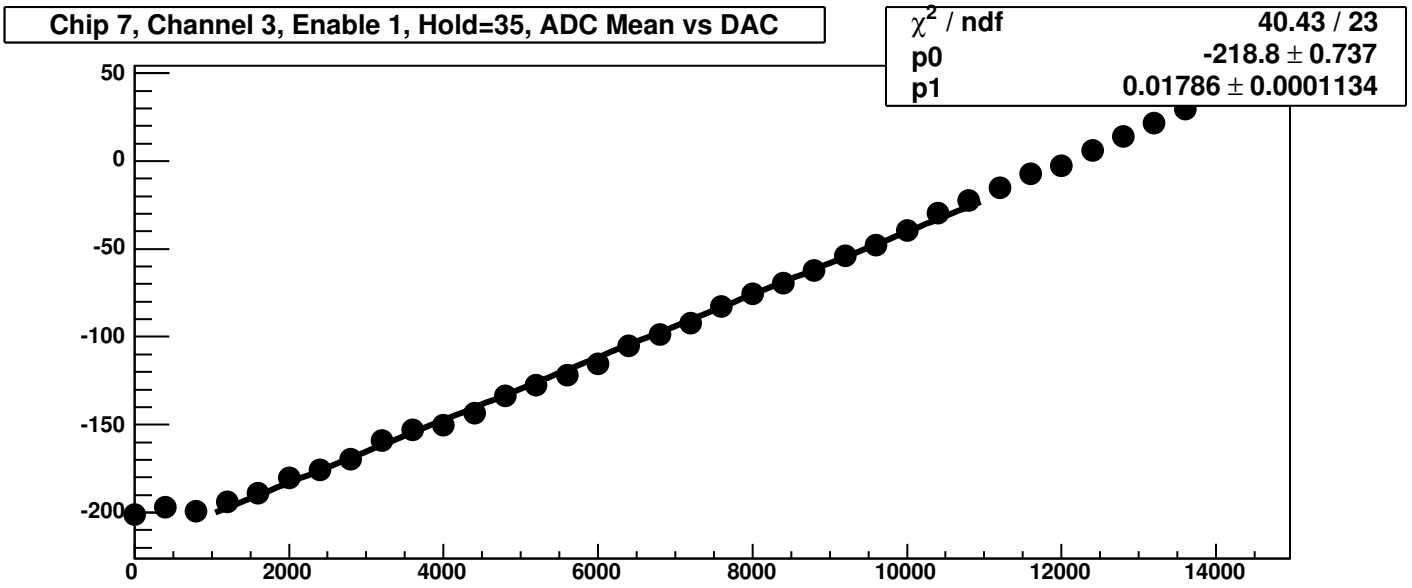

Chip 7, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

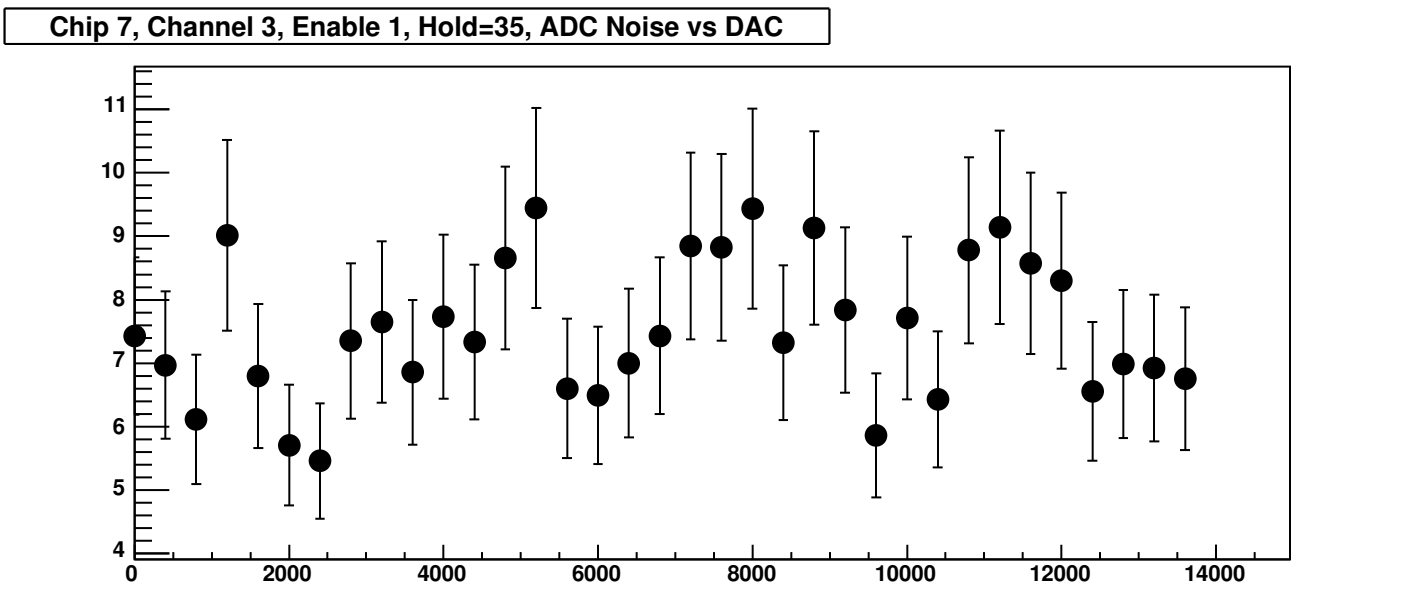

Chip 7, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

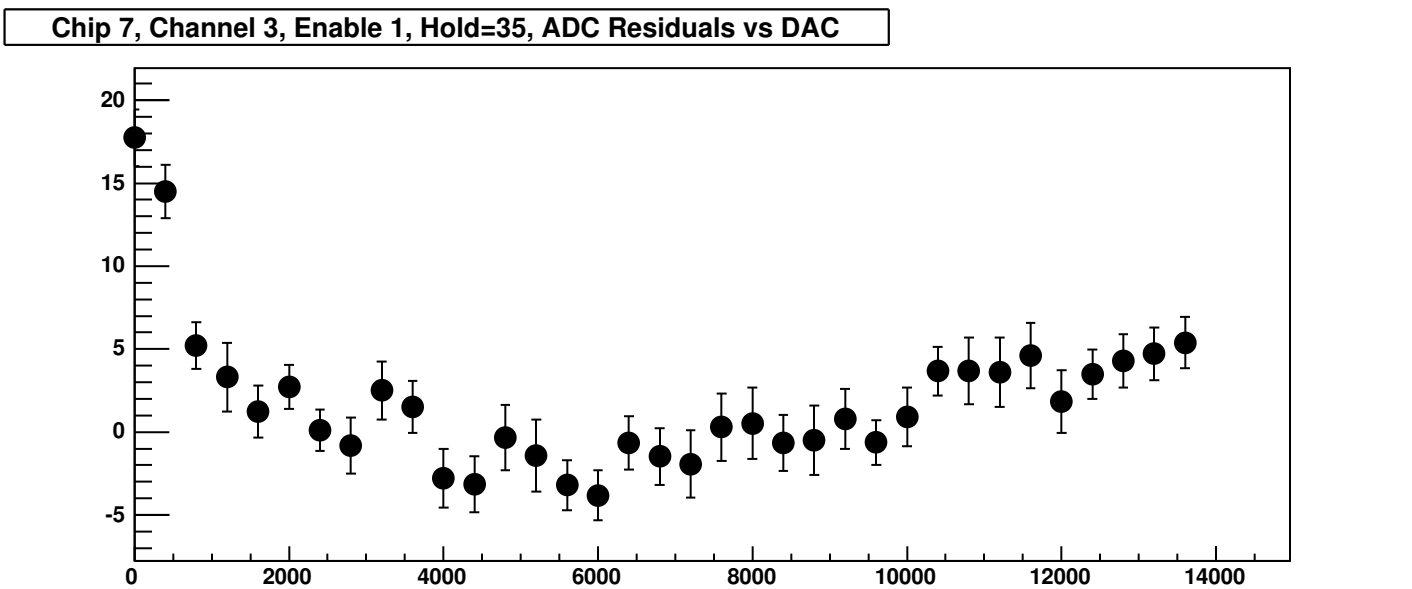

Chip 7, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.269e+05 / 23
p0 -648.6 ± 0.9251
p1 0.1068 ± 0.0001829
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

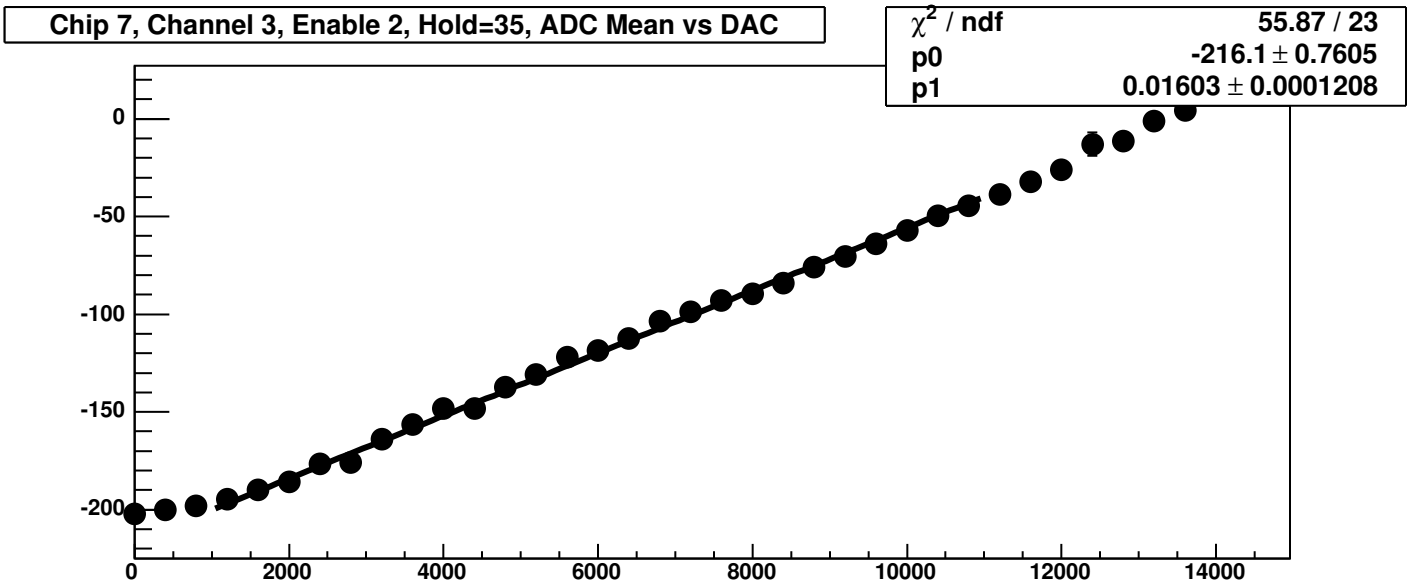

Chip 7, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

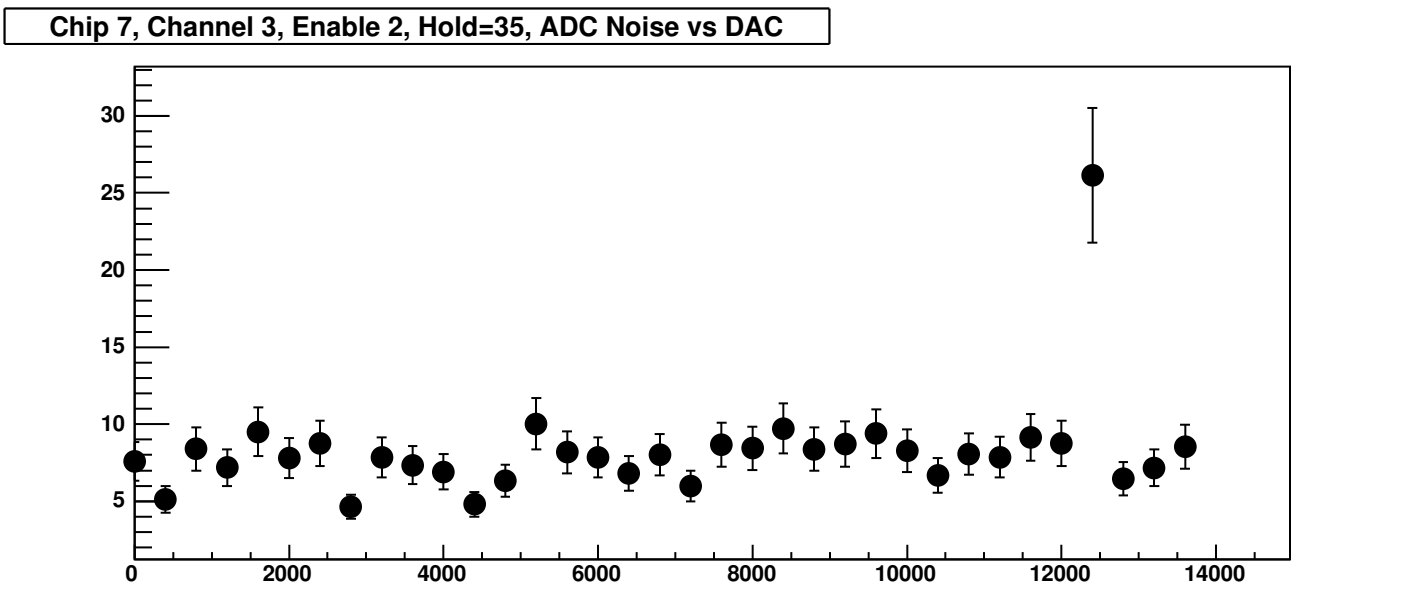

Chip 7, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

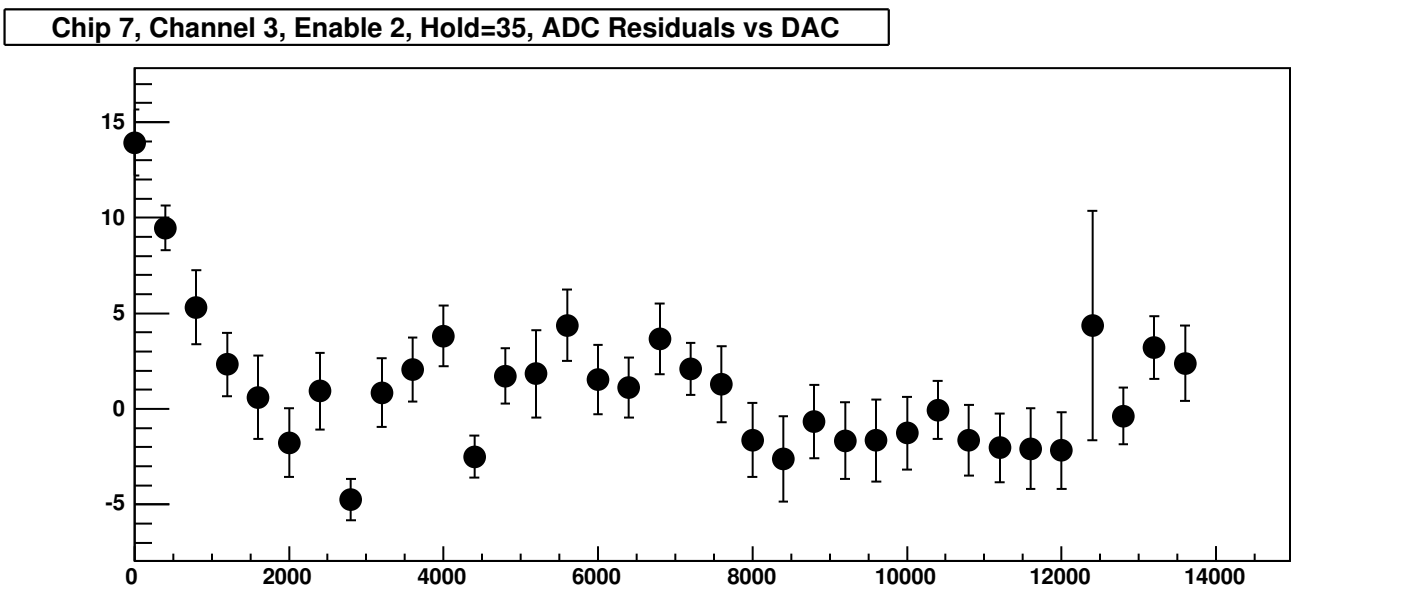

Chip 7, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.43 / 23
p0 -218.8 ± 0.737
p1 0.01786 ± 0.0001134
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

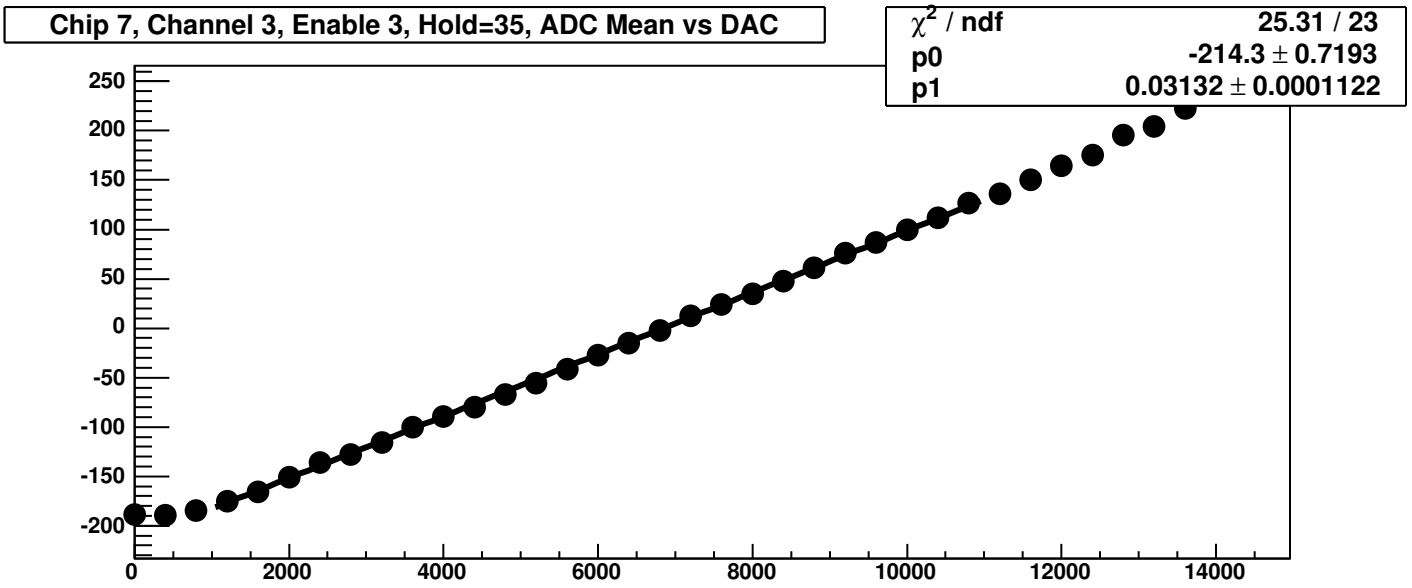

Chip 7, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

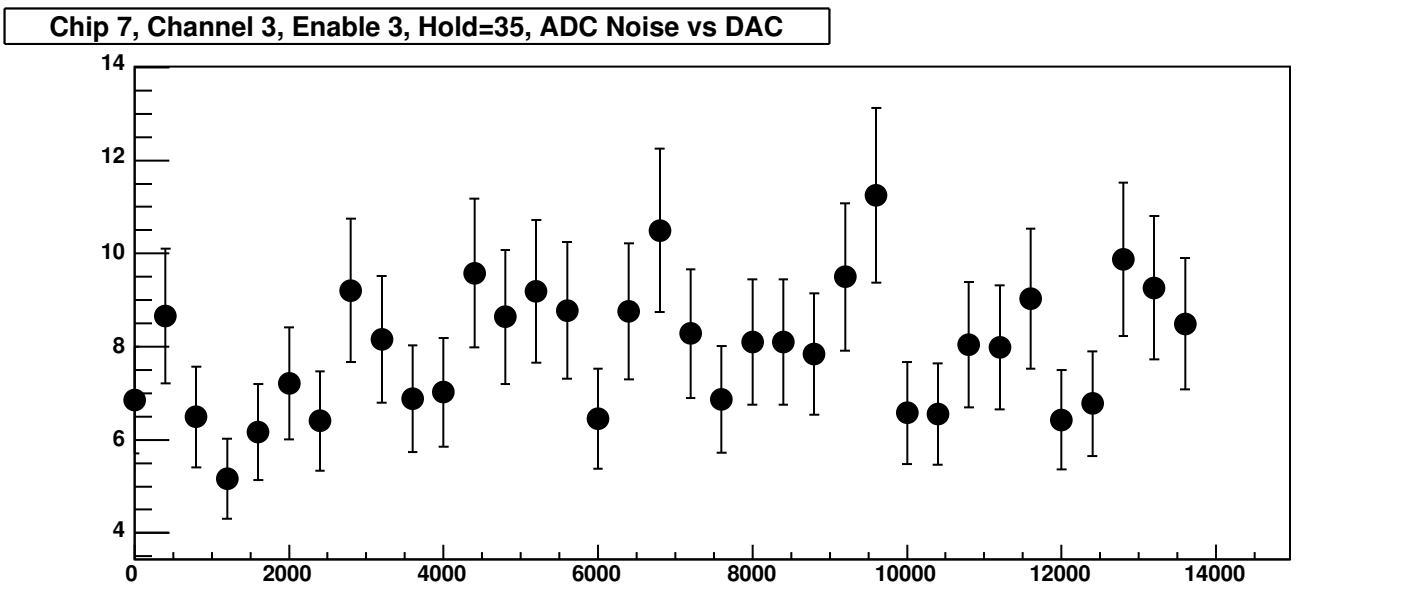

Chip 7, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

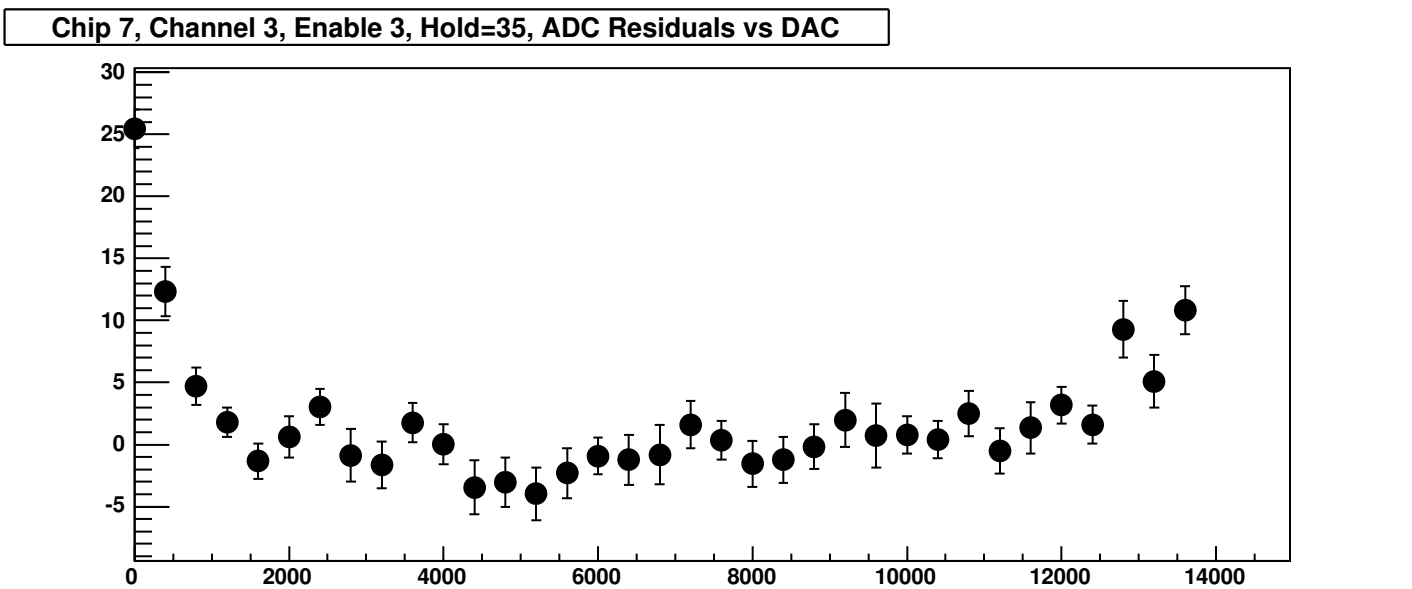

Chip 7, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 55.87 / 23
p0 -216.1 ± 0.7605
p1 0.01603 ± 0.0001208
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

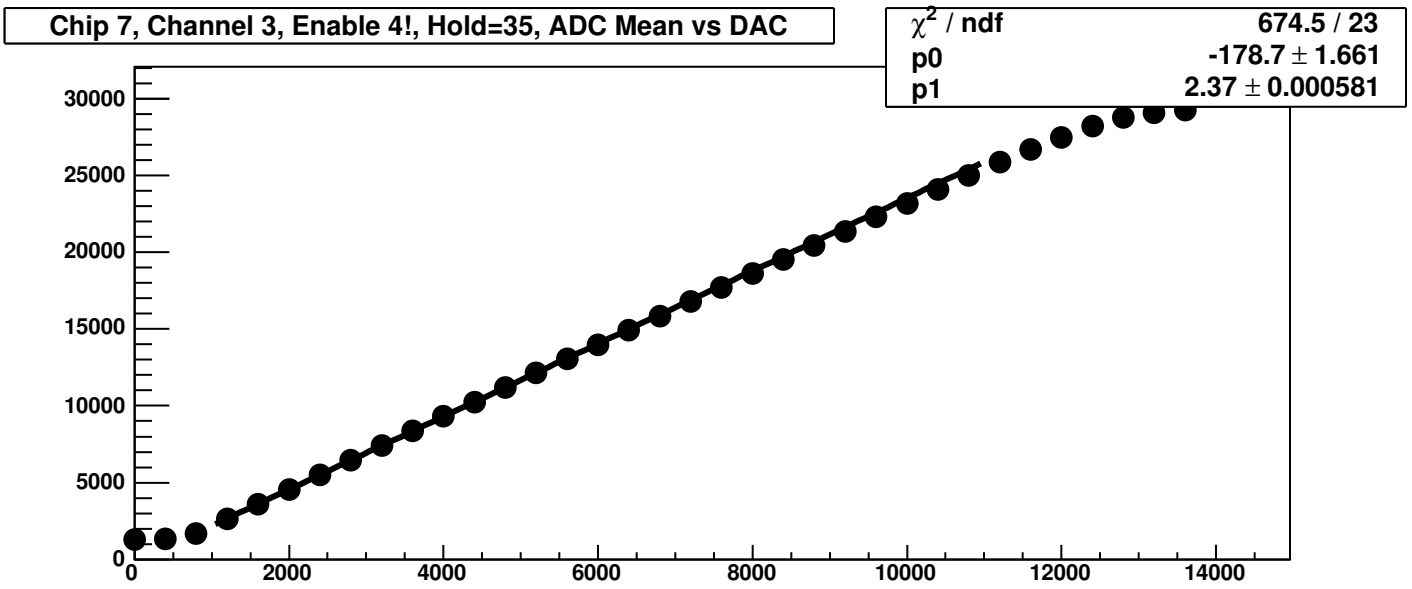

Chip 7, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

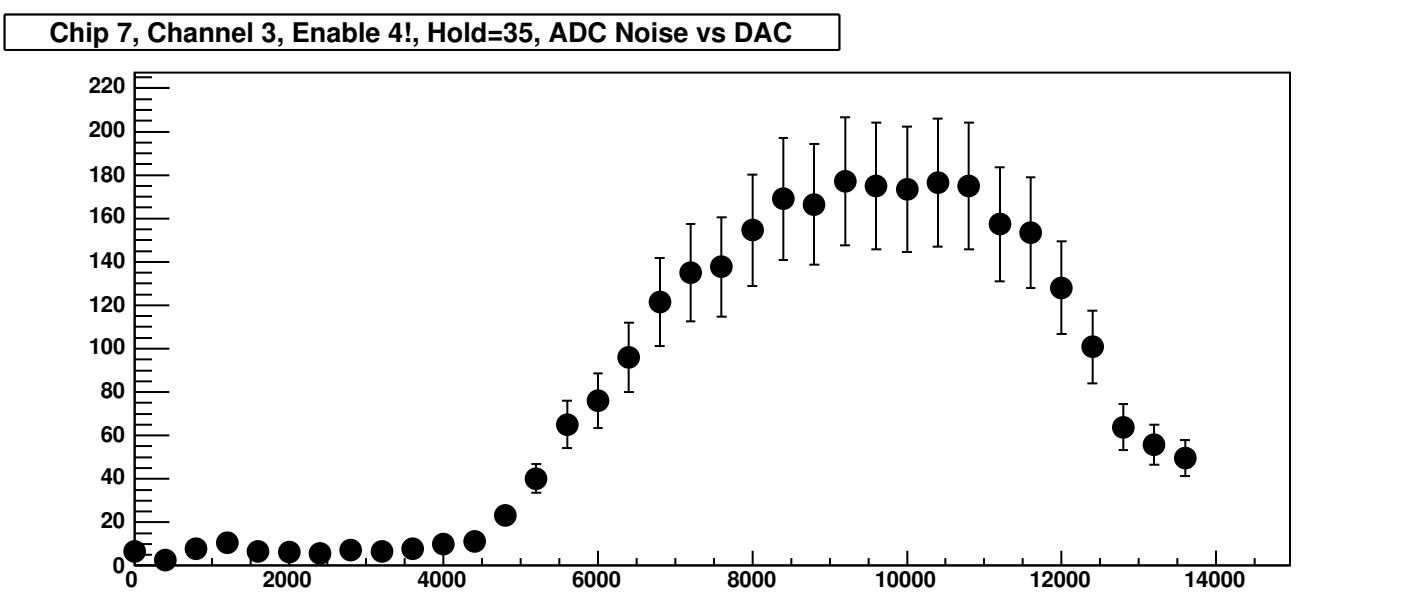

Chip 7, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

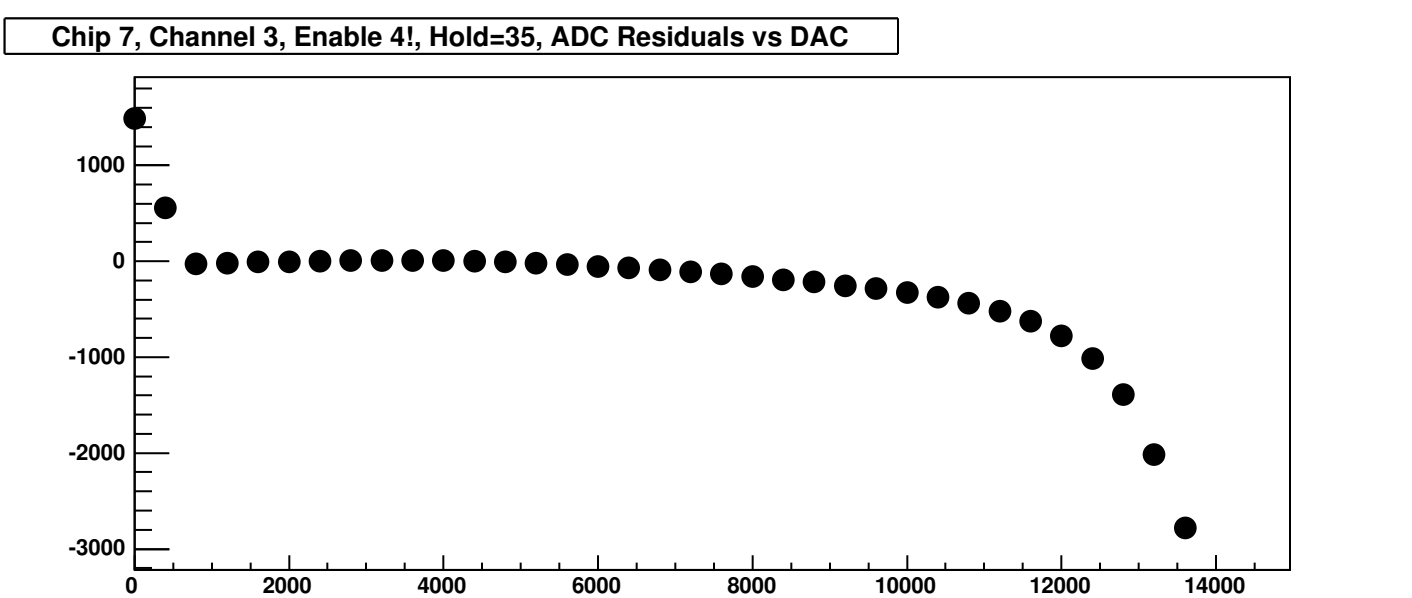

Chip 7, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.31 / 23
p0 -214.3 ± 0.7193
p1 0.03132 ± 0.0001122
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

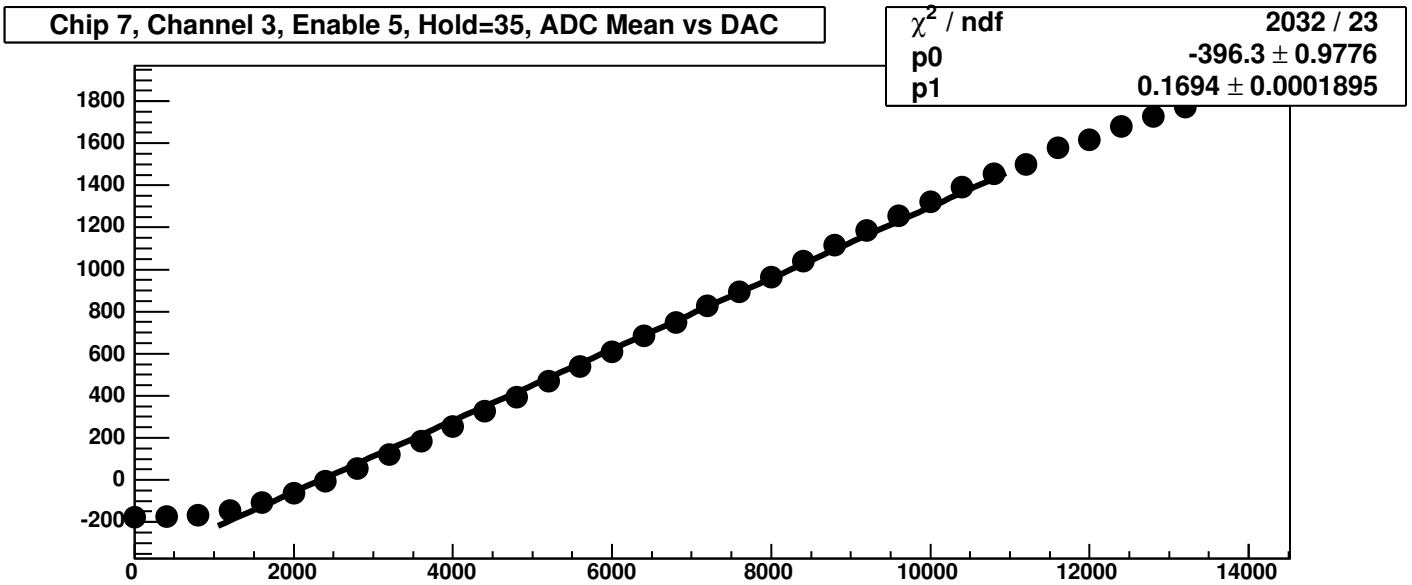

Chip 7, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

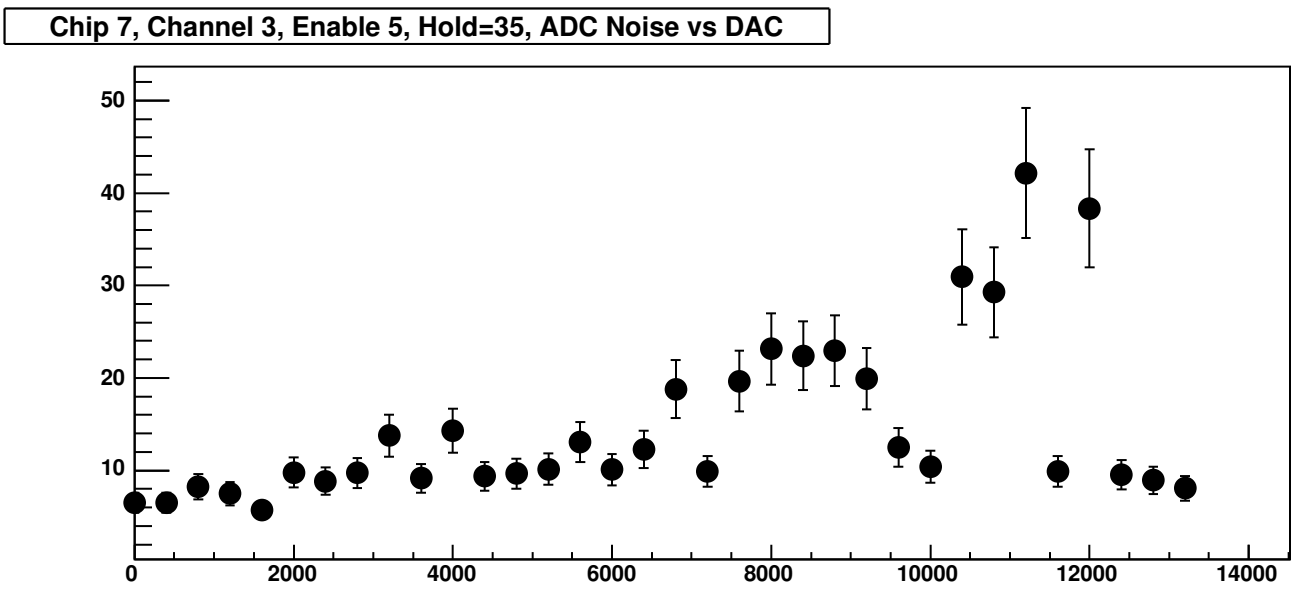

Chip 7, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

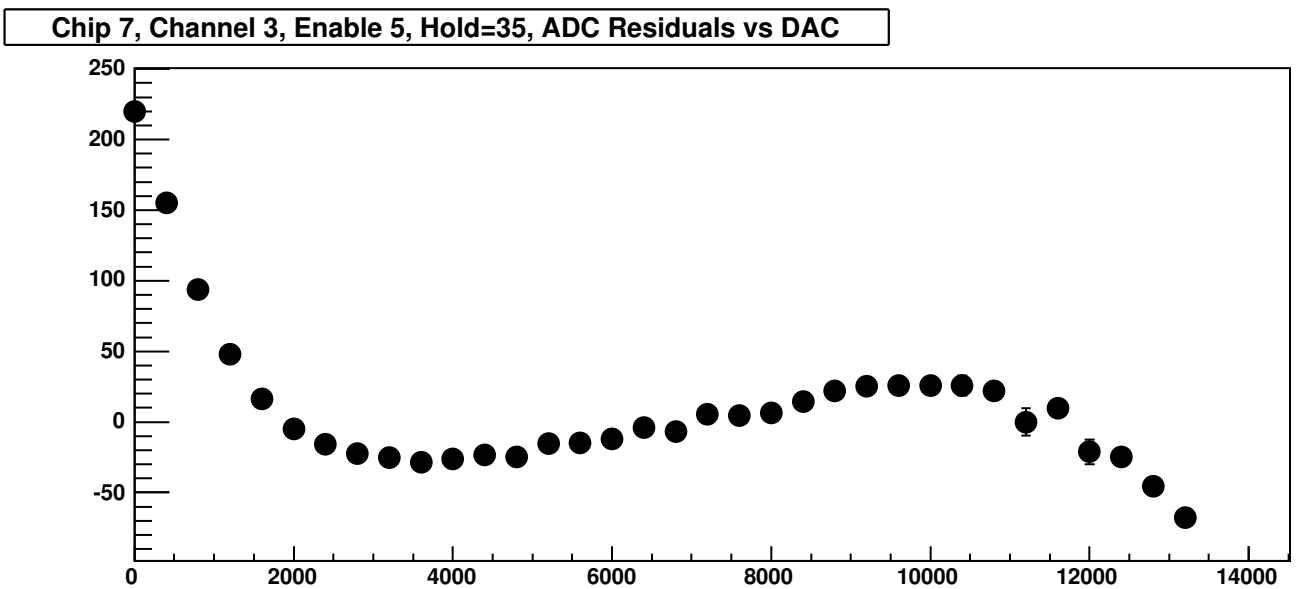

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 674.5 / 23
p0 -178.7 ± 1.661
p1 2.37 ± 0.000581
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

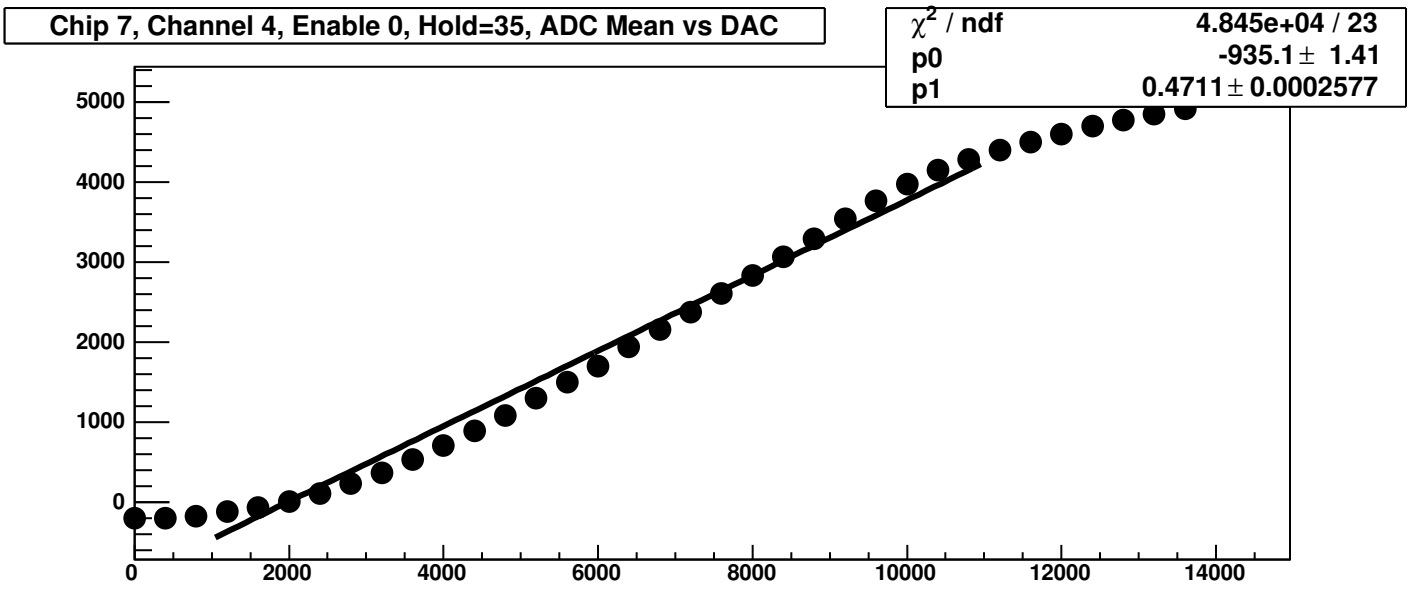

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

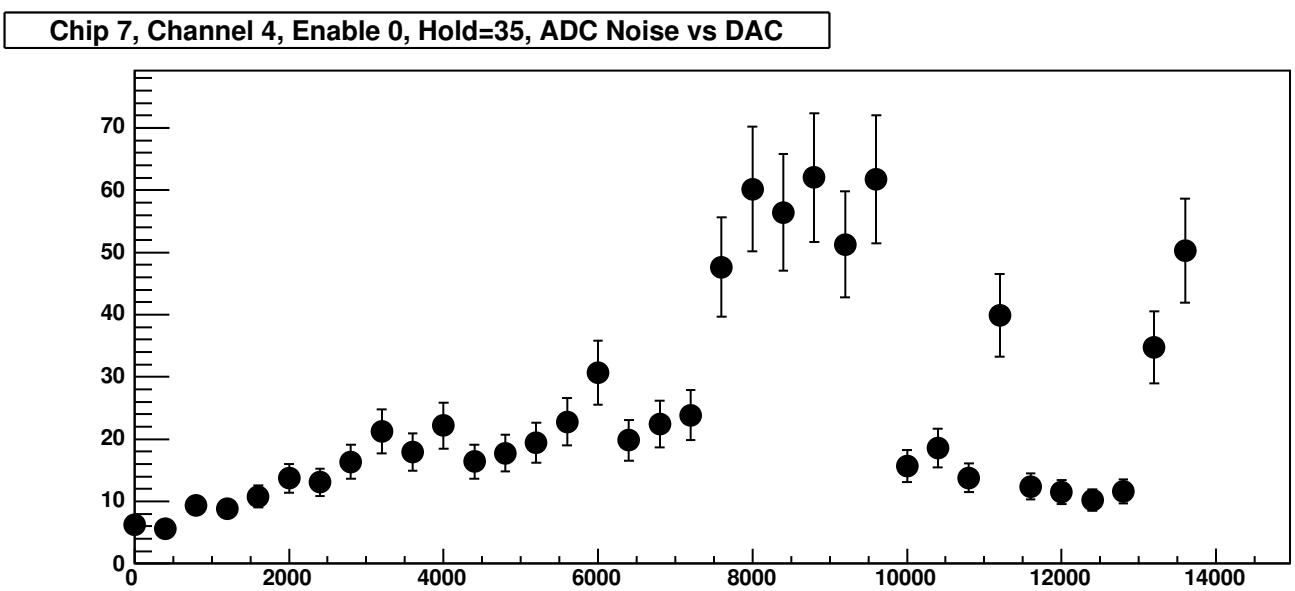

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

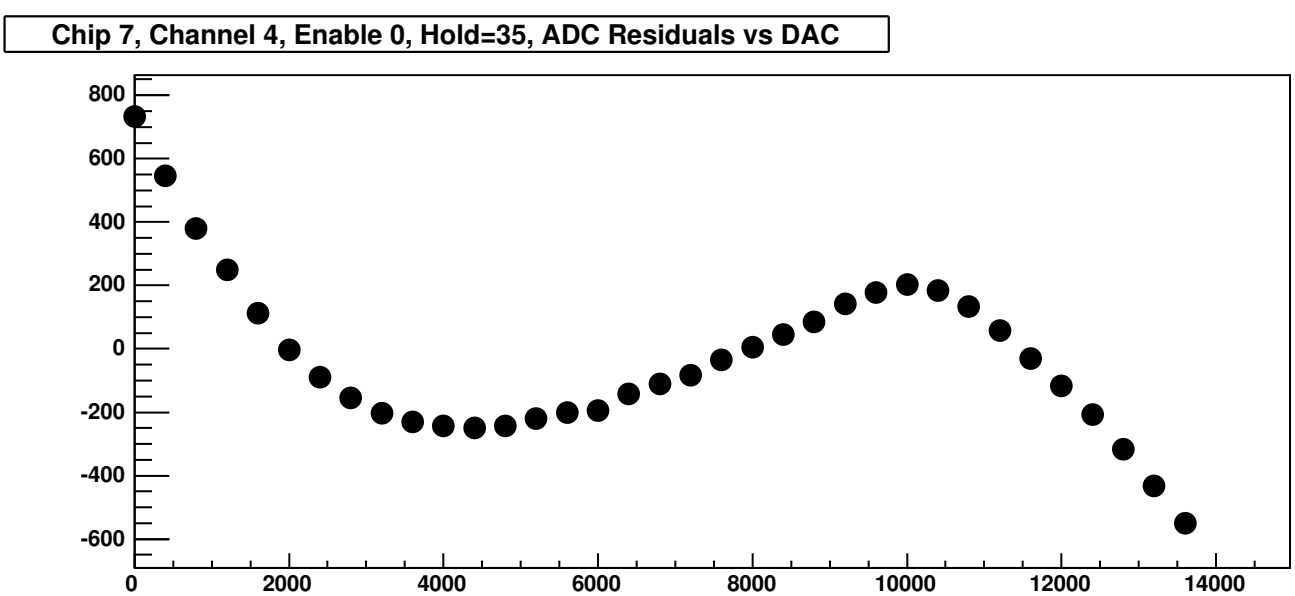

Chip 7, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2032 / 23
p0 -396.3 ± 0.9776
p1 0.1694 ± 0.0001895
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

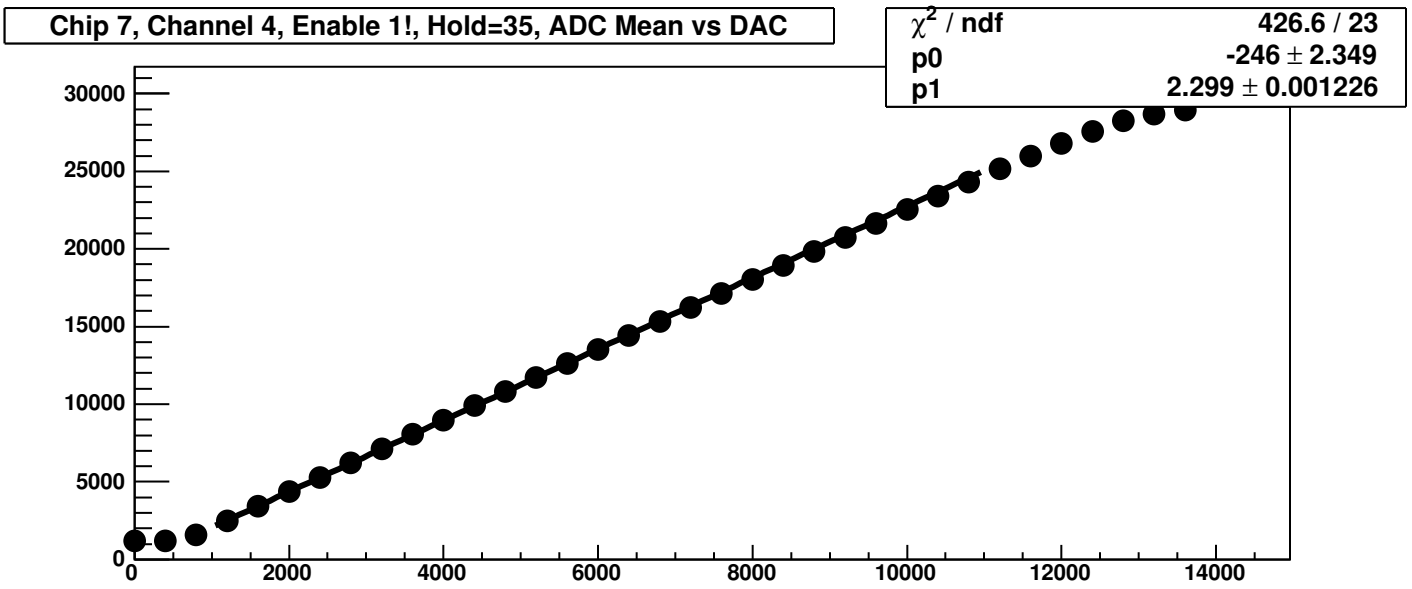

Chip 7, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

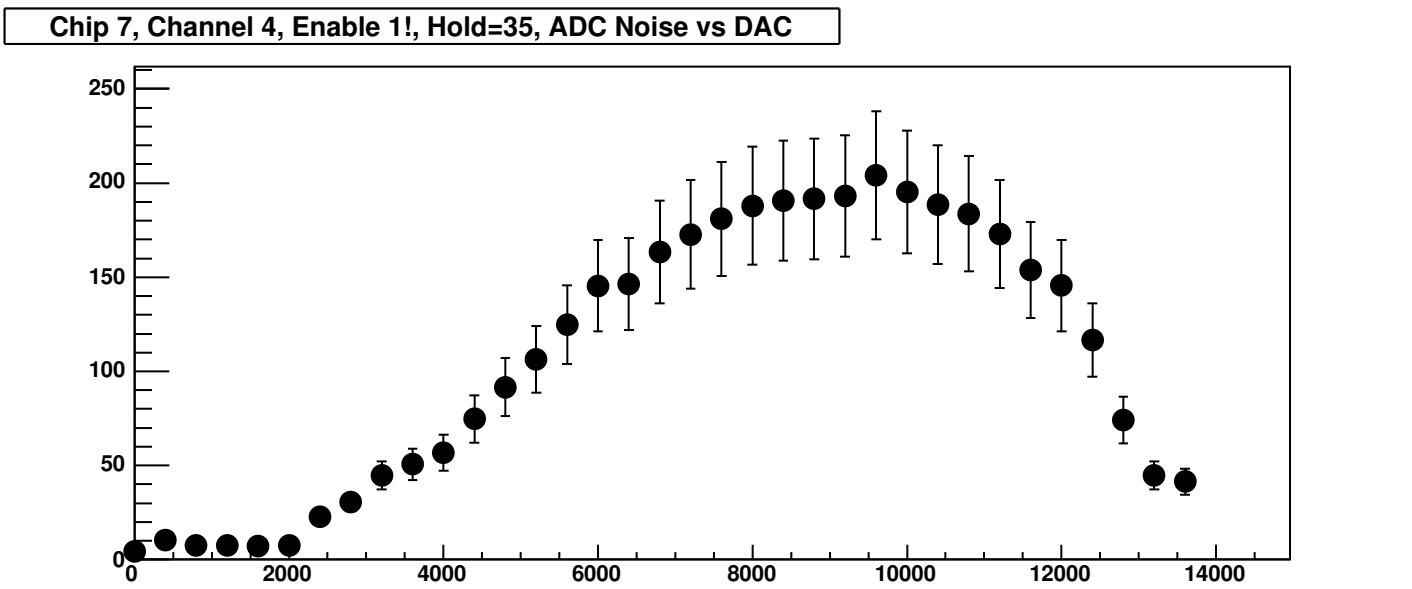

Chip 7, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

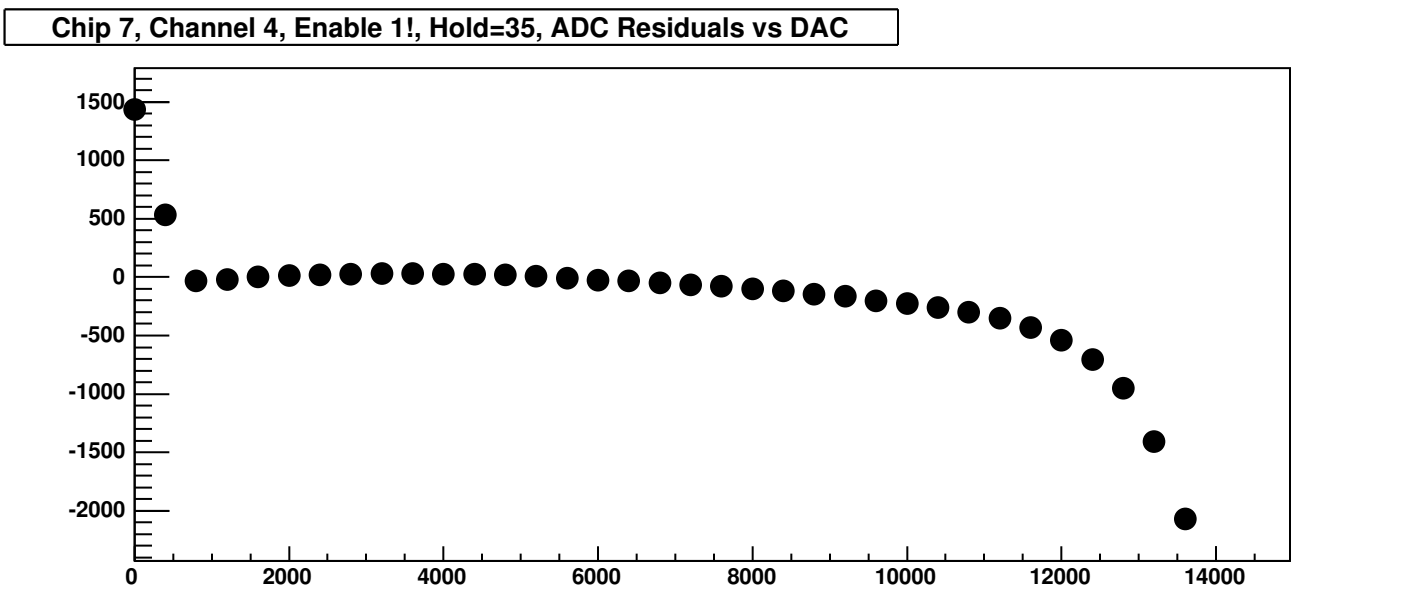

Chip 7, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.845e+04 / 23
p0 -935.1 ± 1.41
p1 0.4711 ± 0.0002577
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

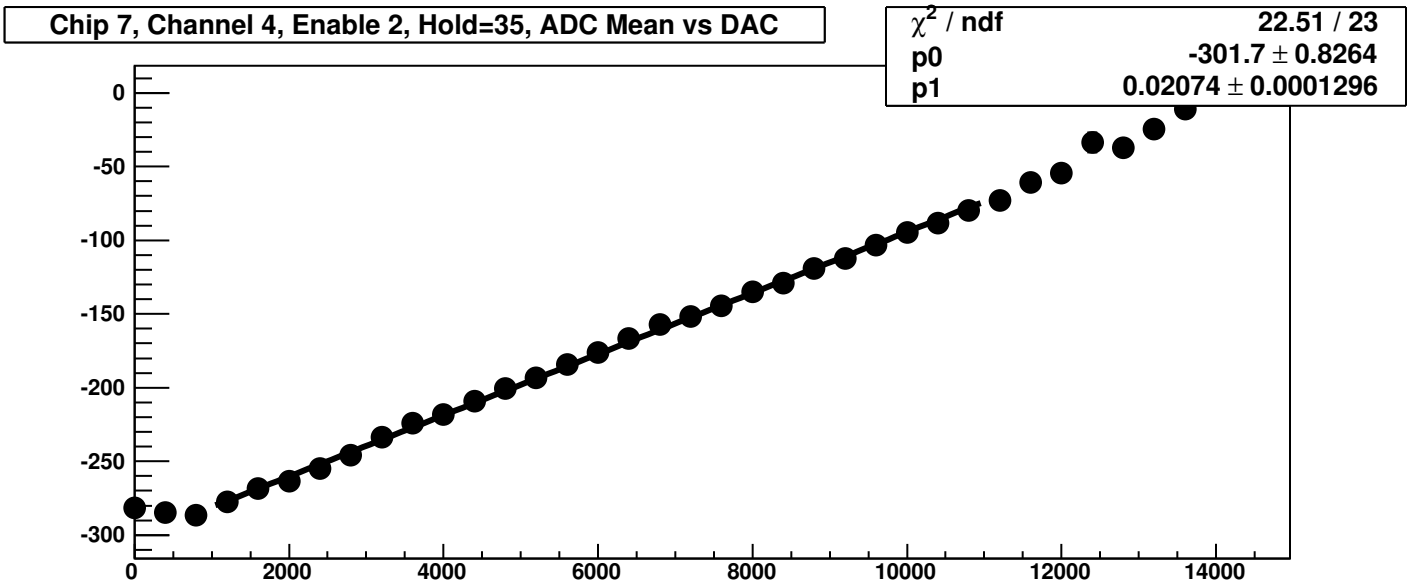

Chip 7, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

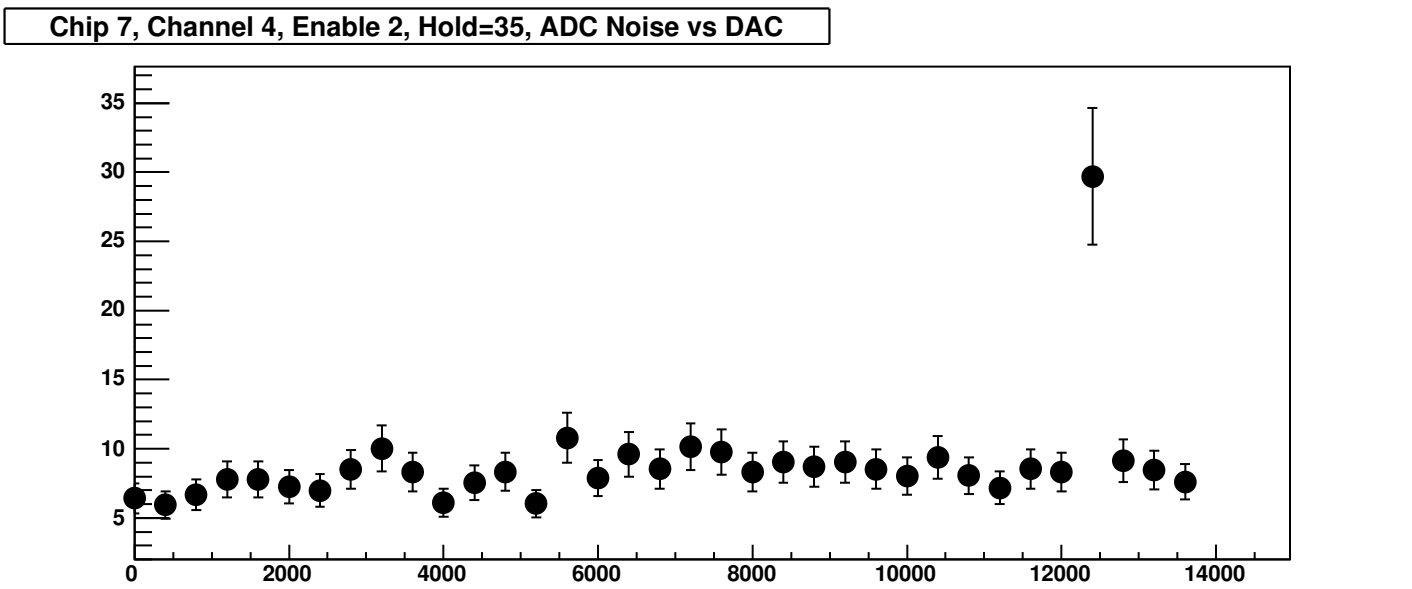

Chip 7, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

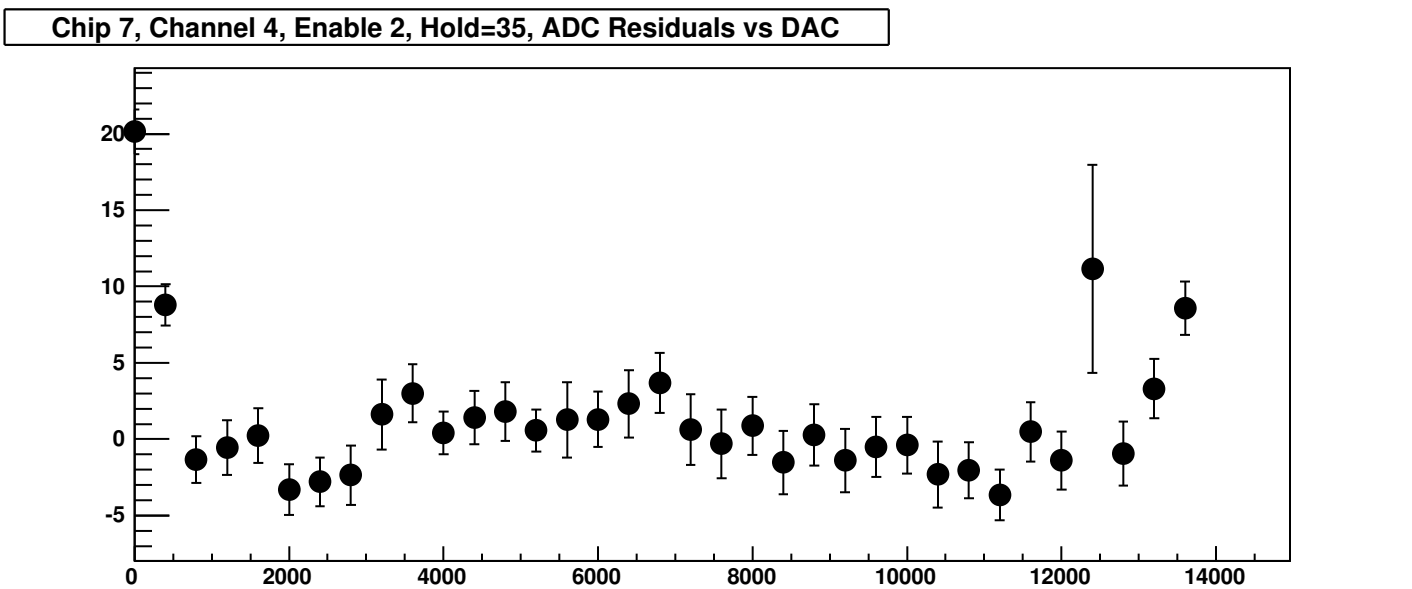

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 426.6 / 23
p0 -246 ± 2.349
p1 2.299 ± 0.001226
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

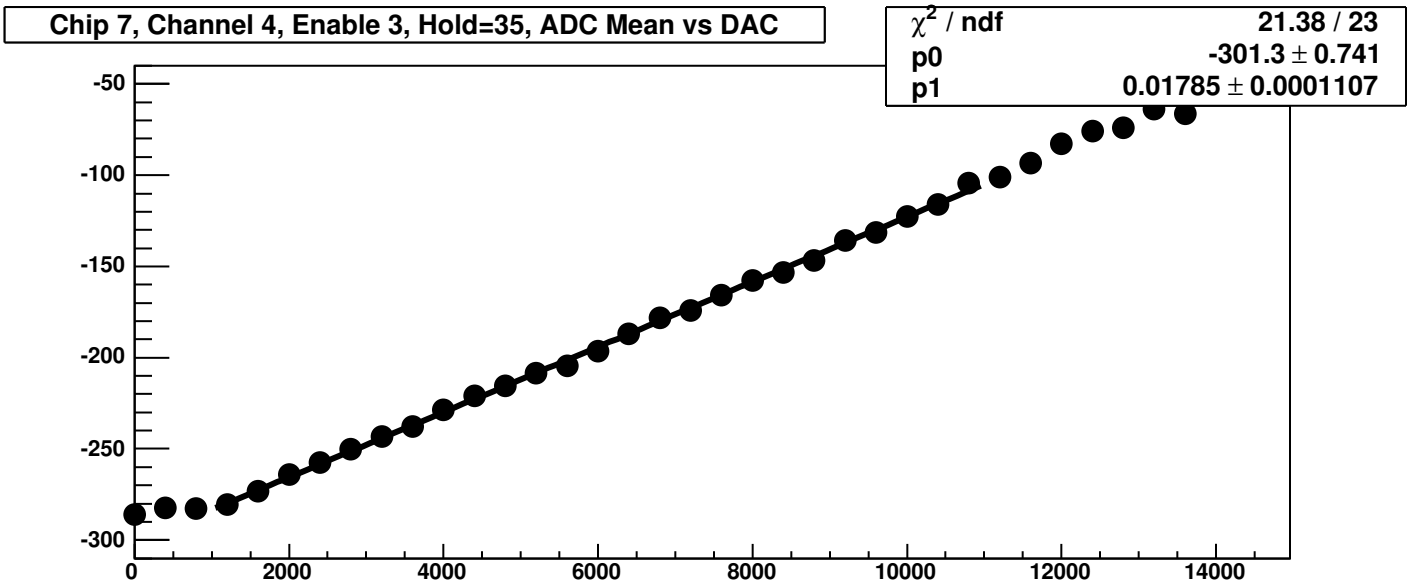

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

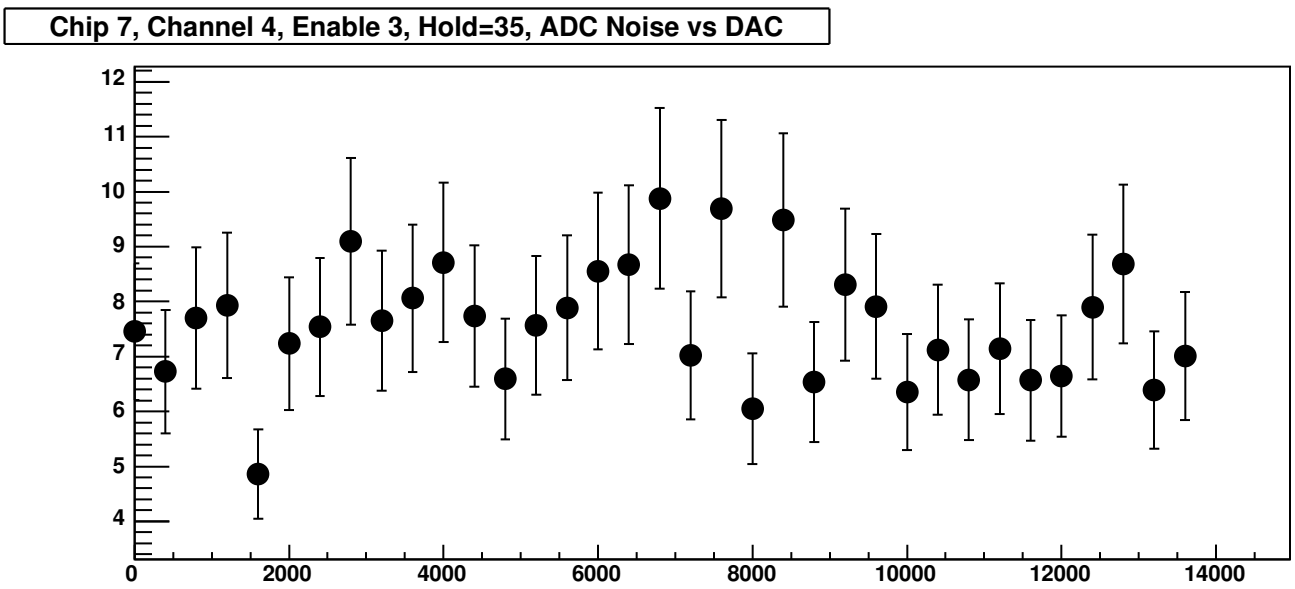

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

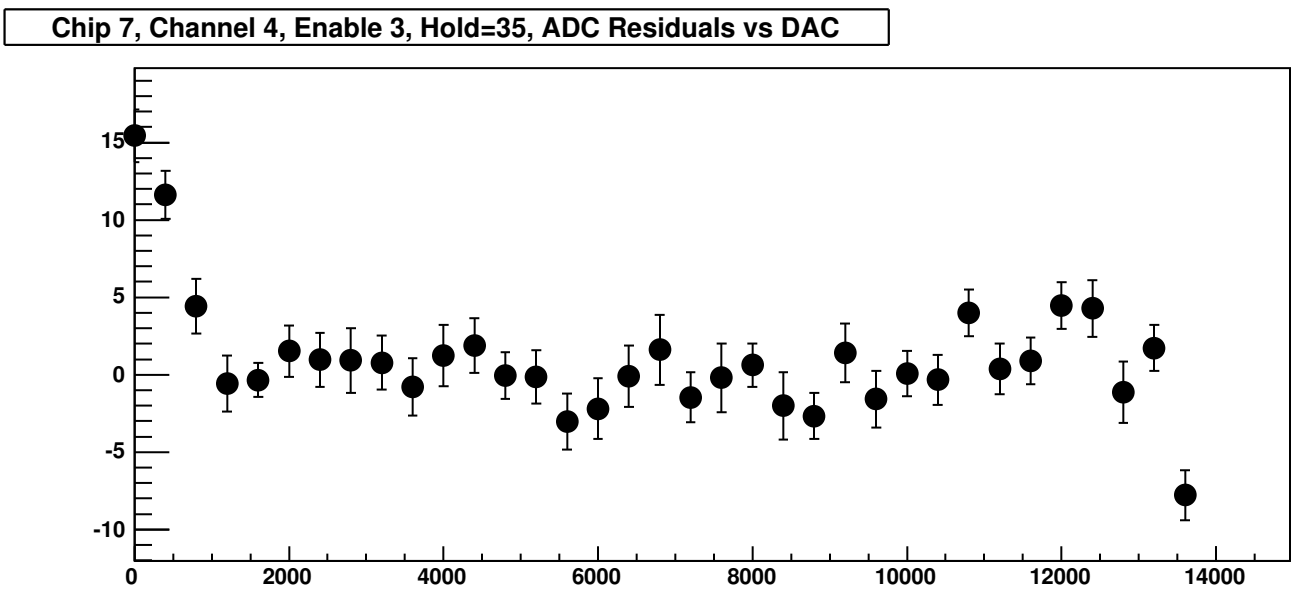

Chip 7, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.51 / 23
p0 -301.7 ± 0.8264
p1 0.02074 ± 0.0001296
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

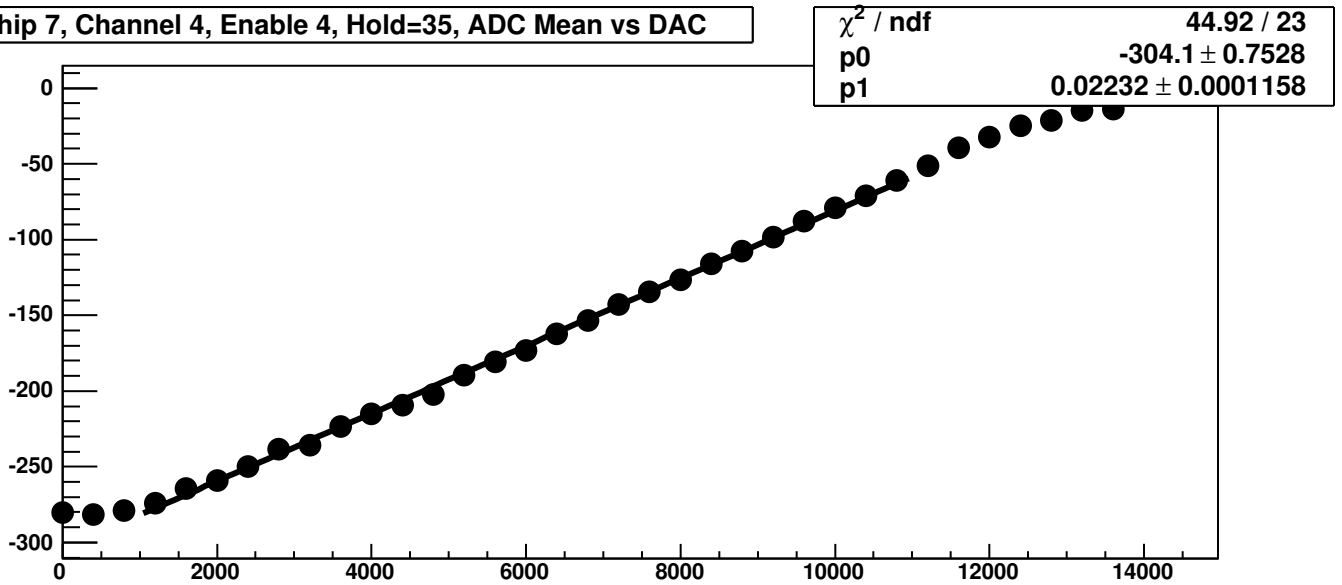

Chip 7, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.38 / 23
p0 -301.3 ± 0.741
p1 0.01785 ± 0.0001107
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

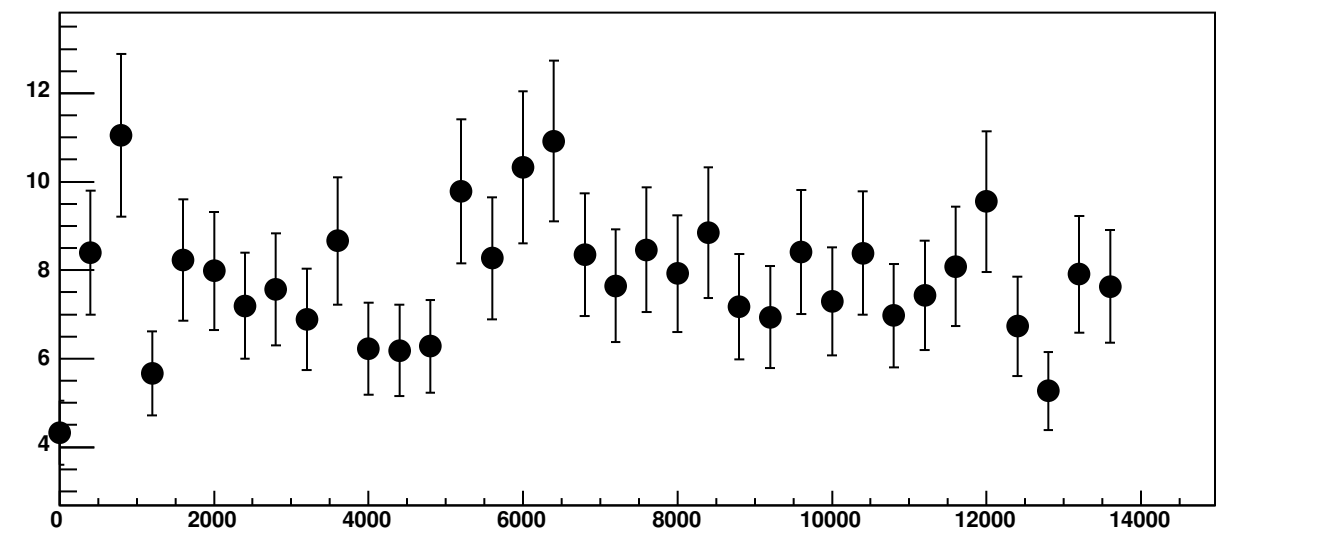

Chip 7, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

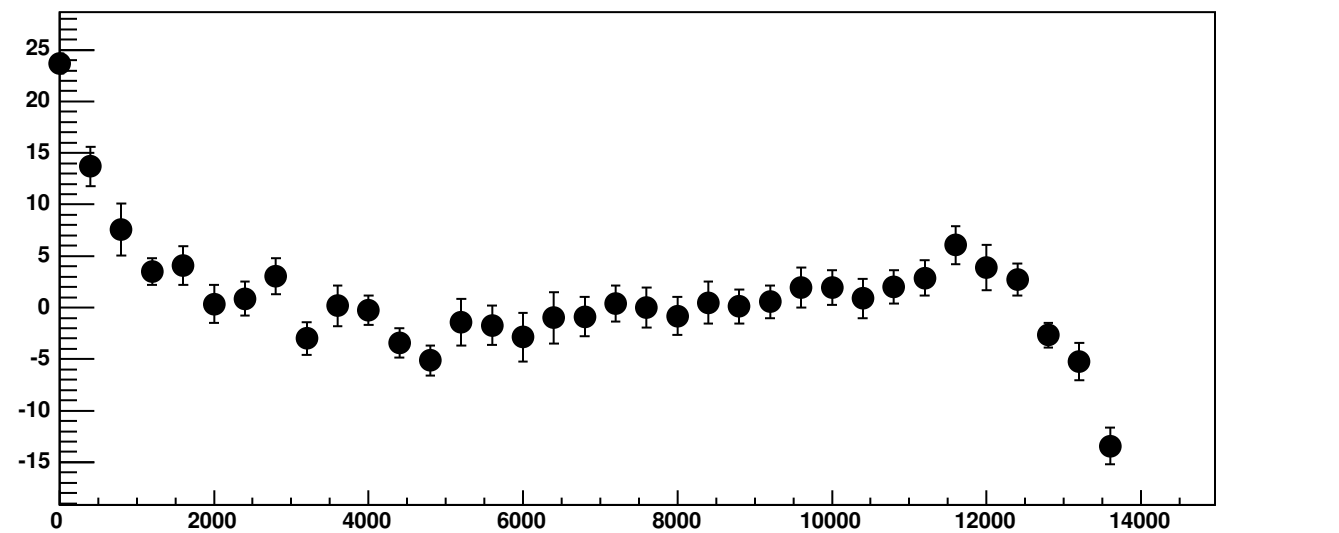
Chip 7, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

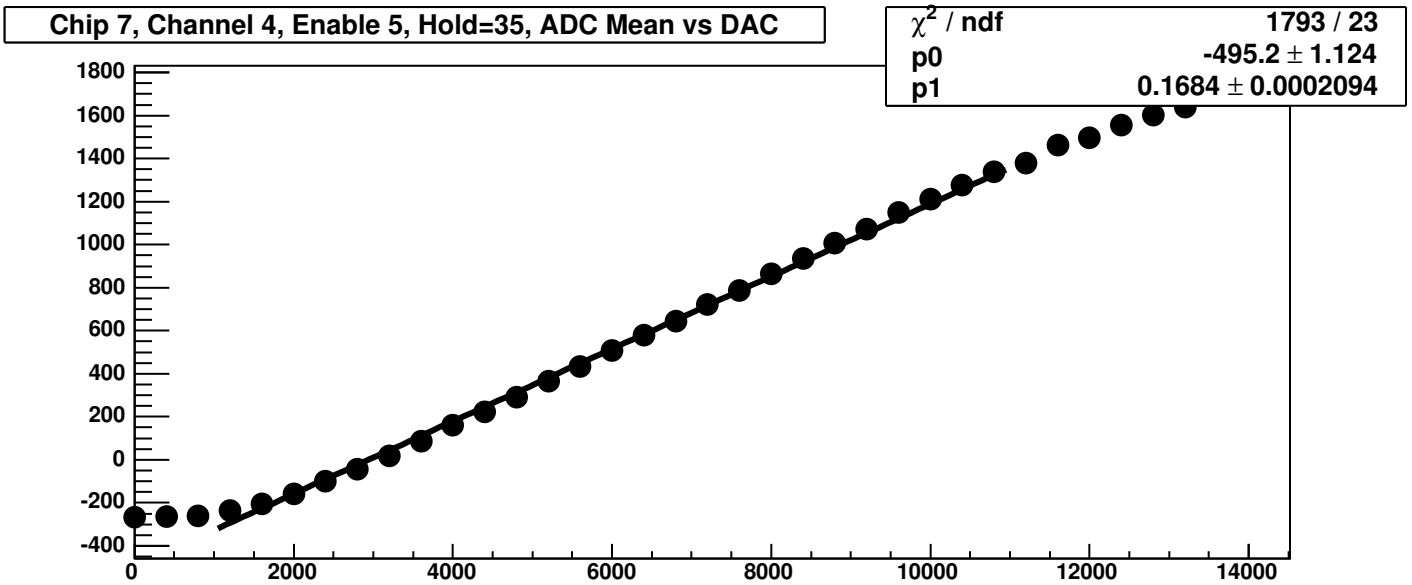


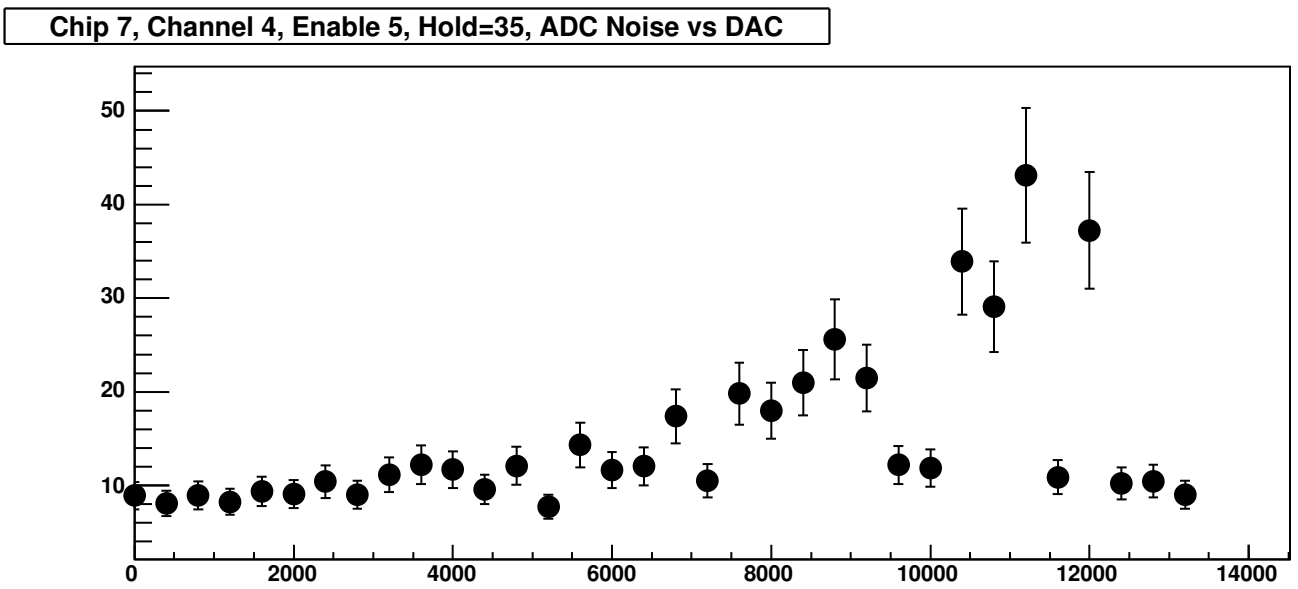
Chip 7, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

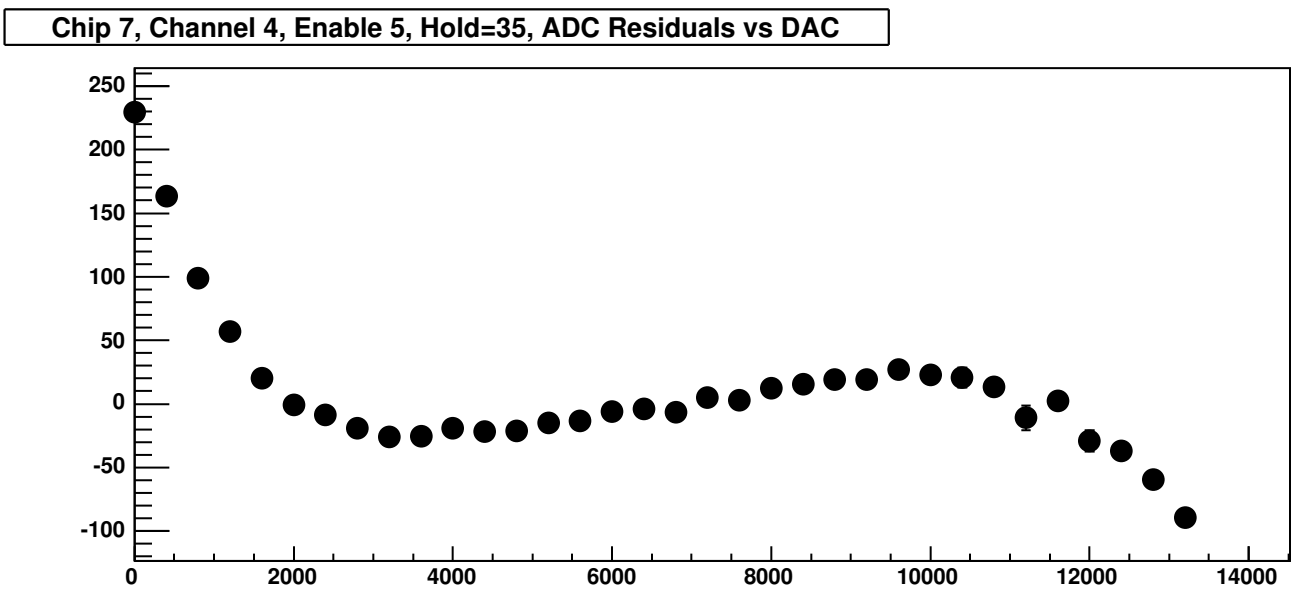


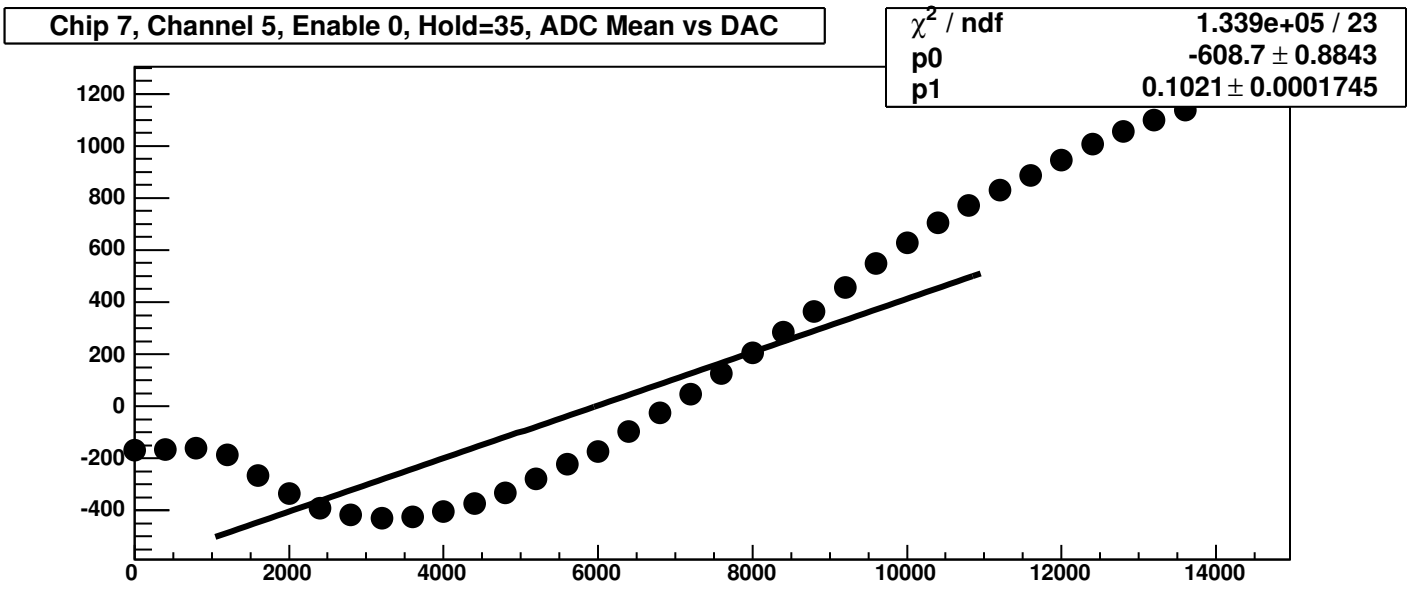
Chip 7, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

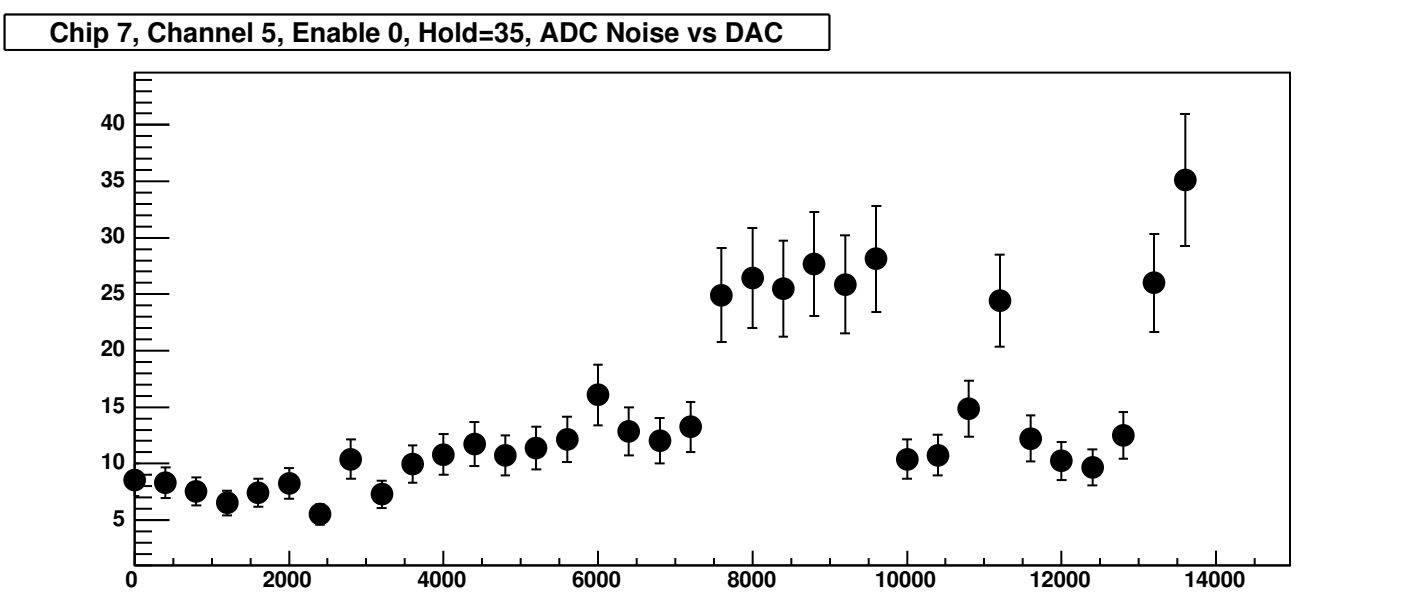


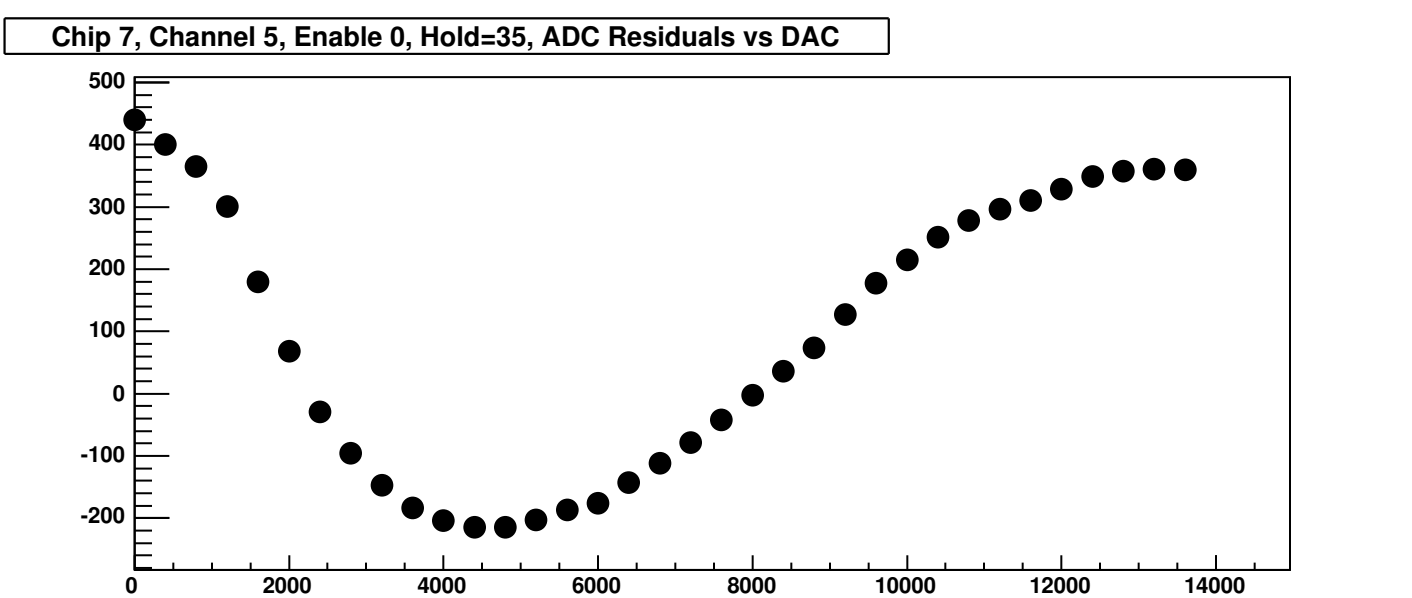

Chip 7, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1793 / 23
p0 -495.2 ± 1.124
p1 0.1684 ± 0.0002094
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

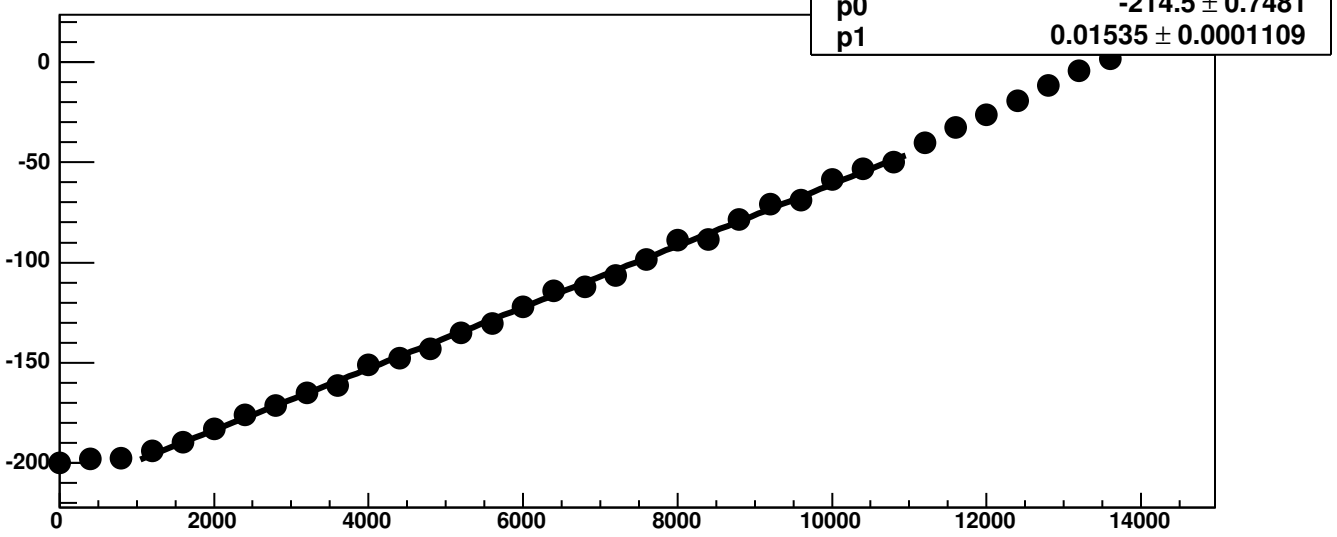

Chip 7, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.339e+05 / 23
p0 -608.7 ± 0.8843
p1 0.1021 ± 0.0001745
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

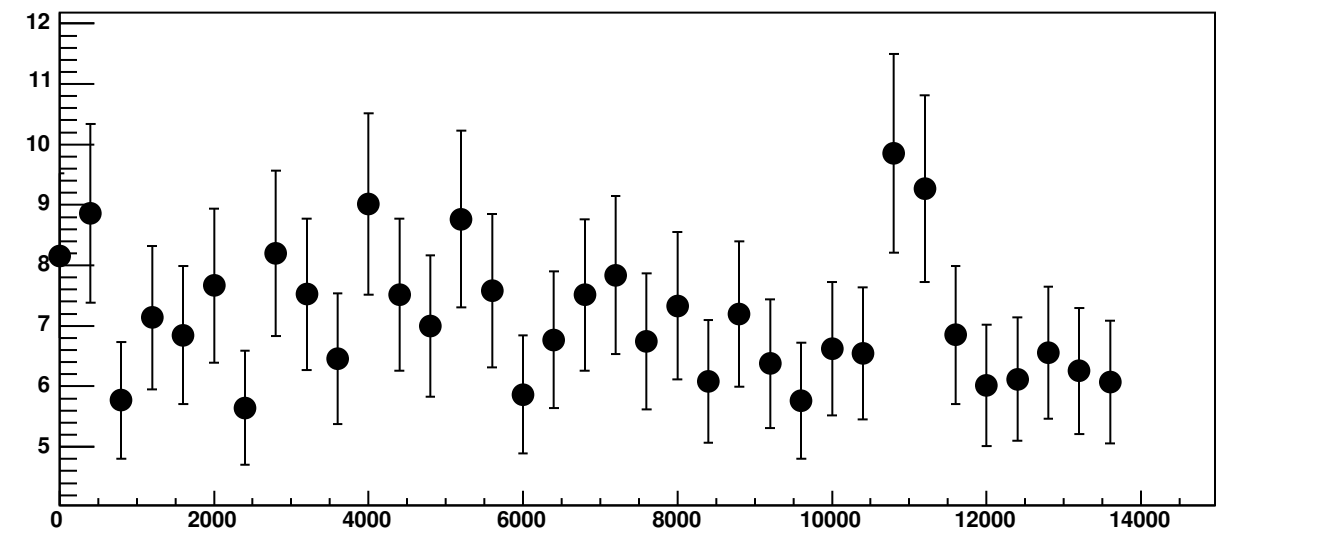

Chip 7, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

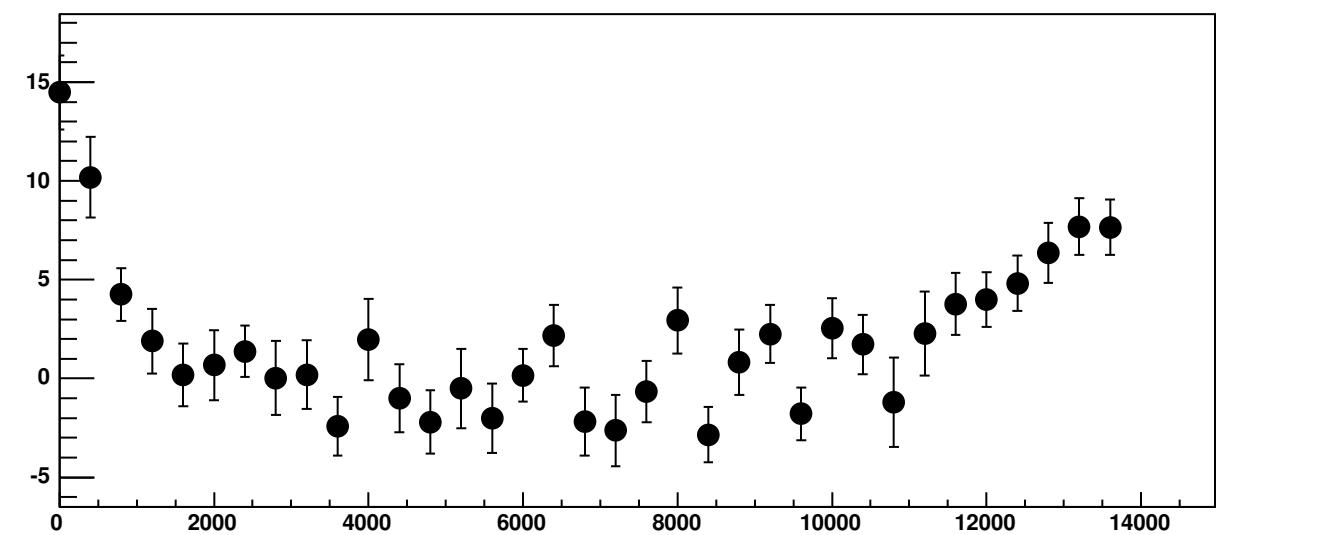
Chip 7, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

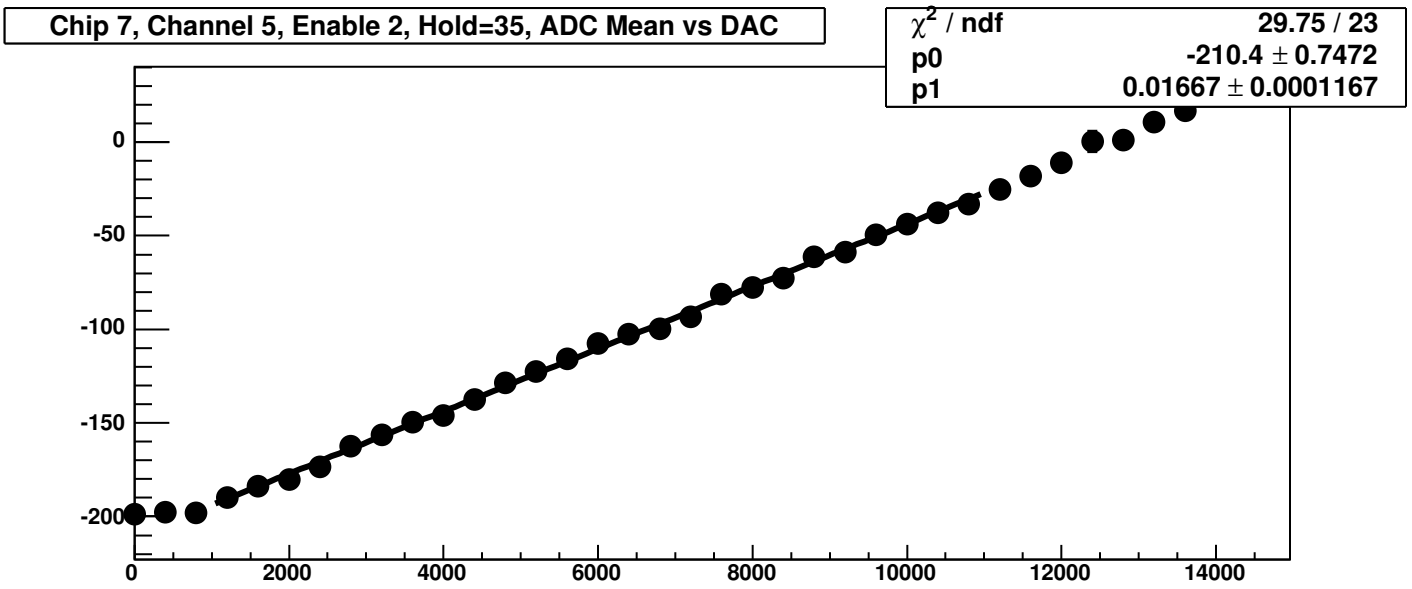


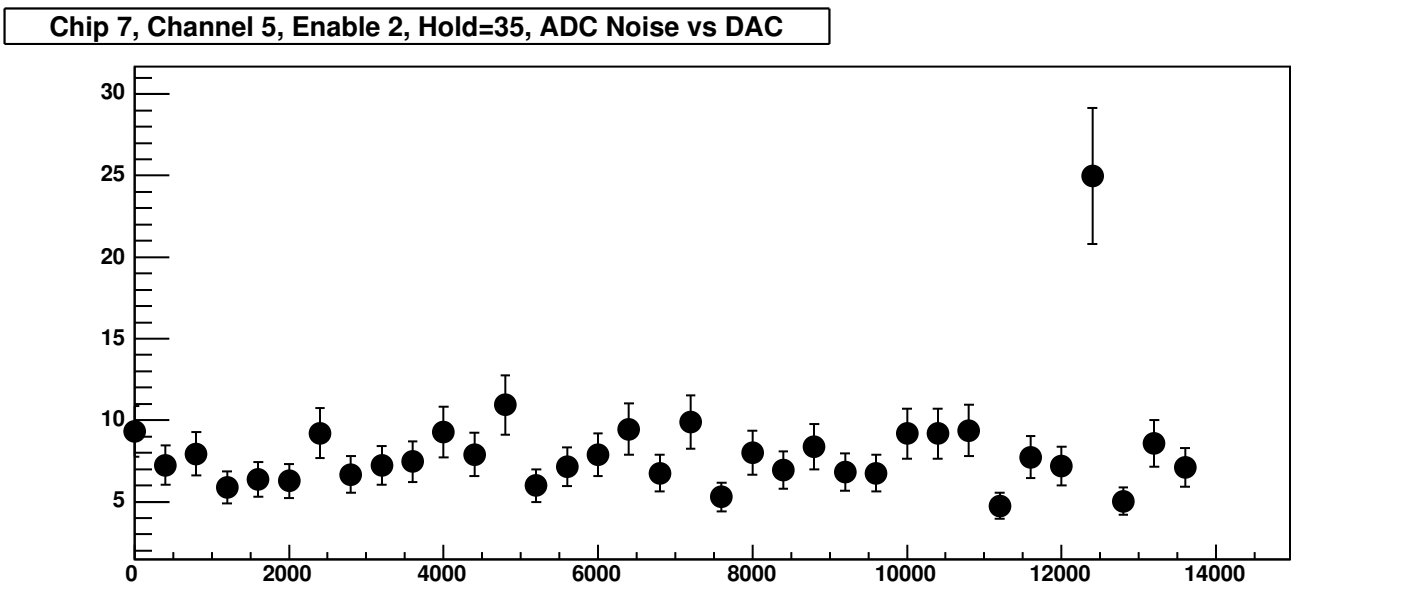
Chip 7, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC

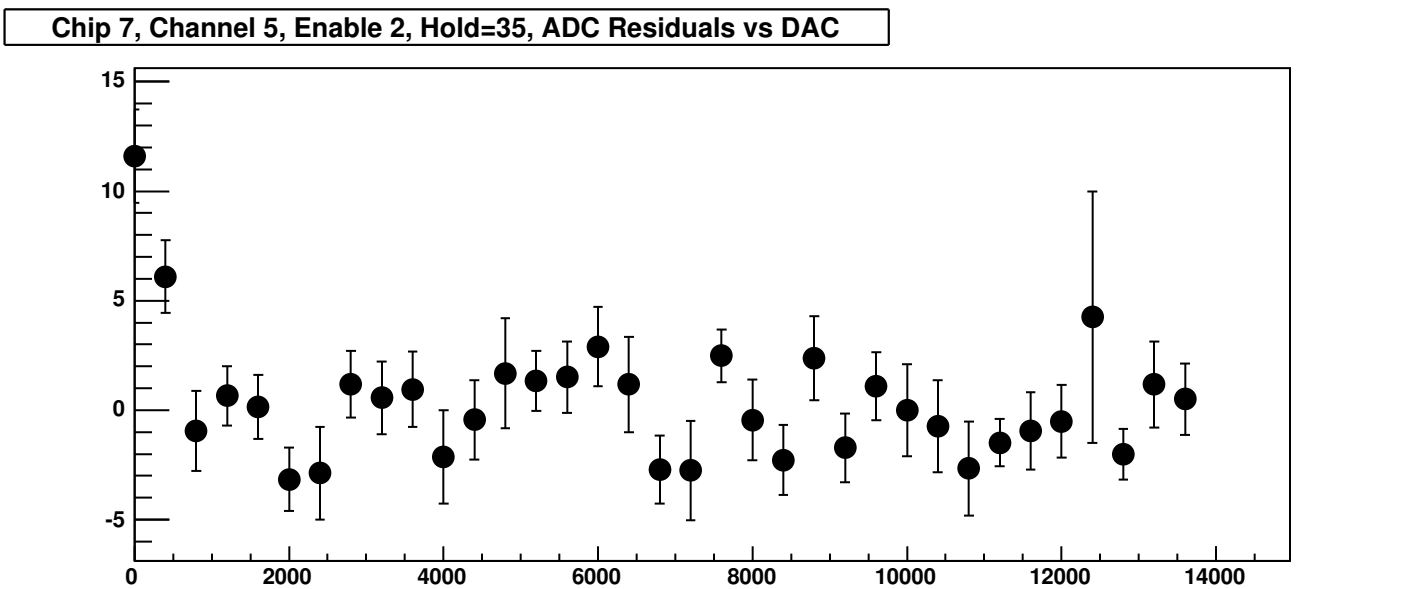


Chip 7, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

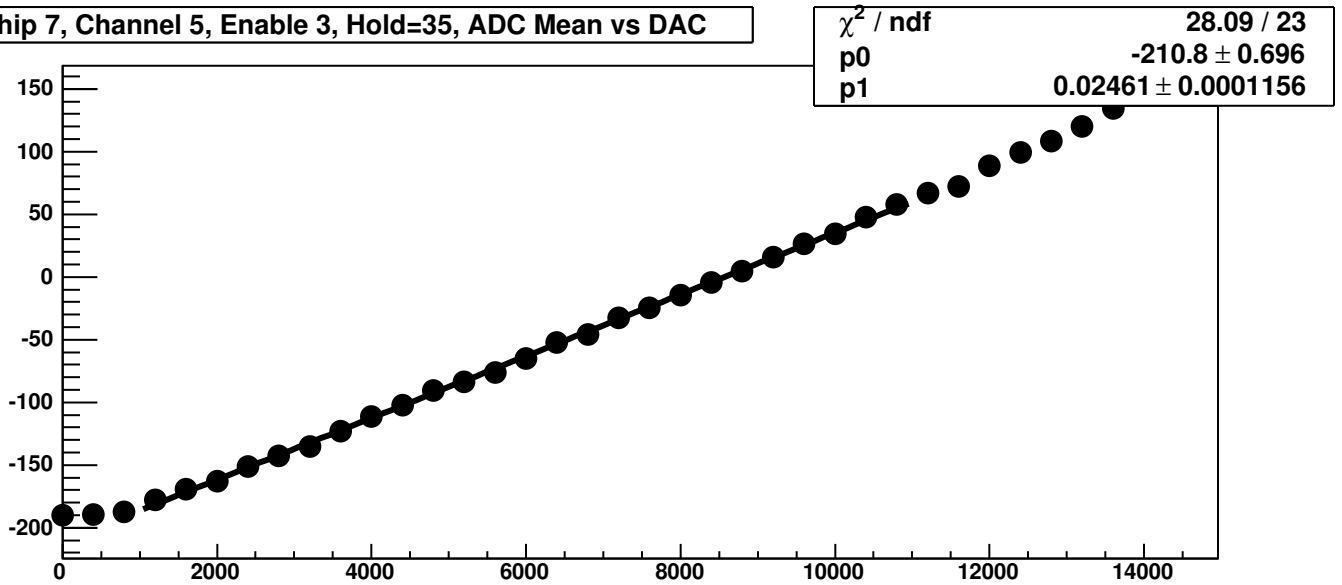



Chip 7, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.75 / 23
p0 -210.4 ± 0.7472
p1 0.01667 ± 0.0001167
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

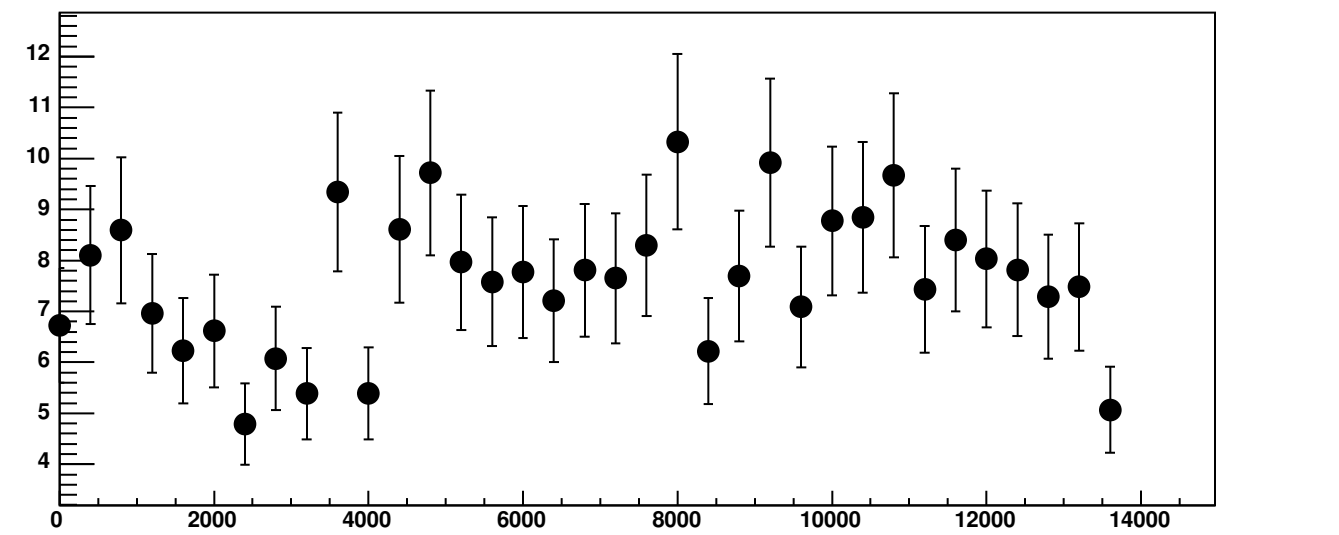

Chip 7, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

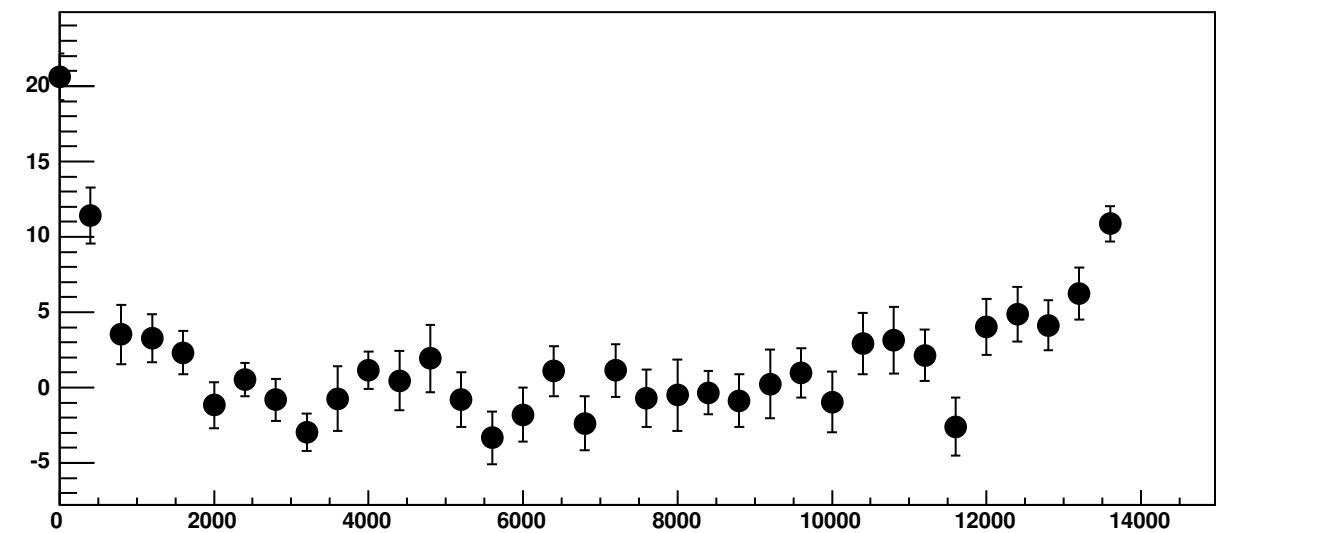
Chip 7, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

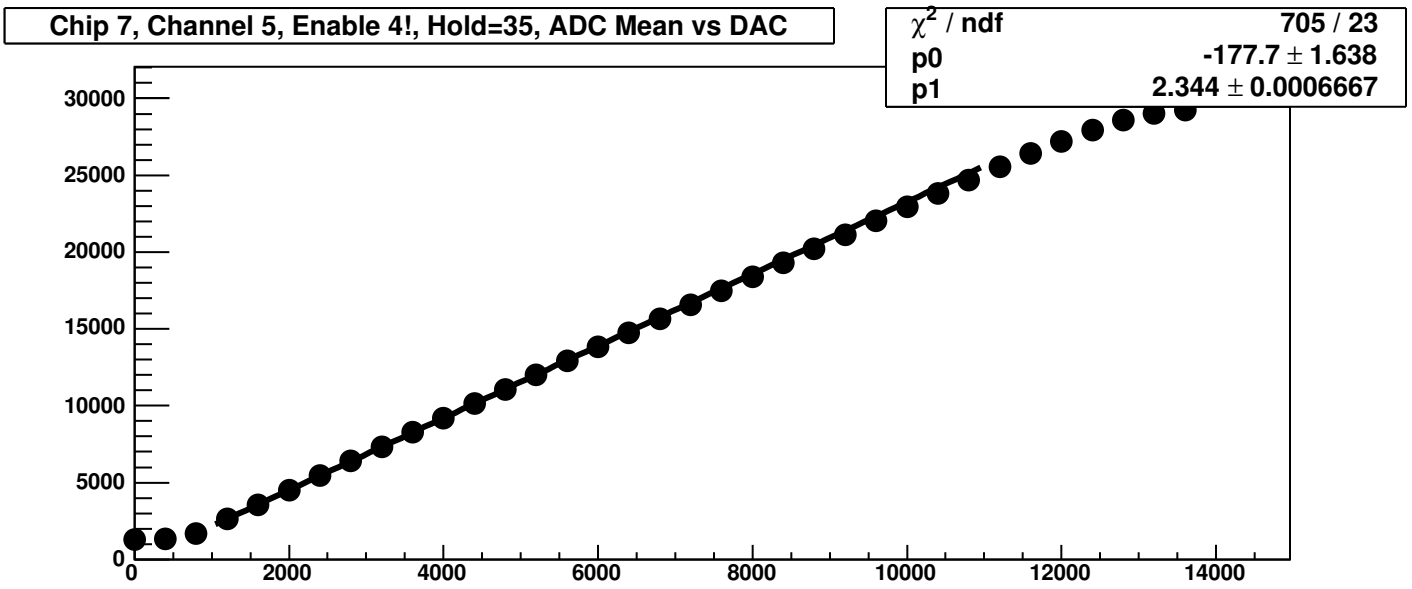


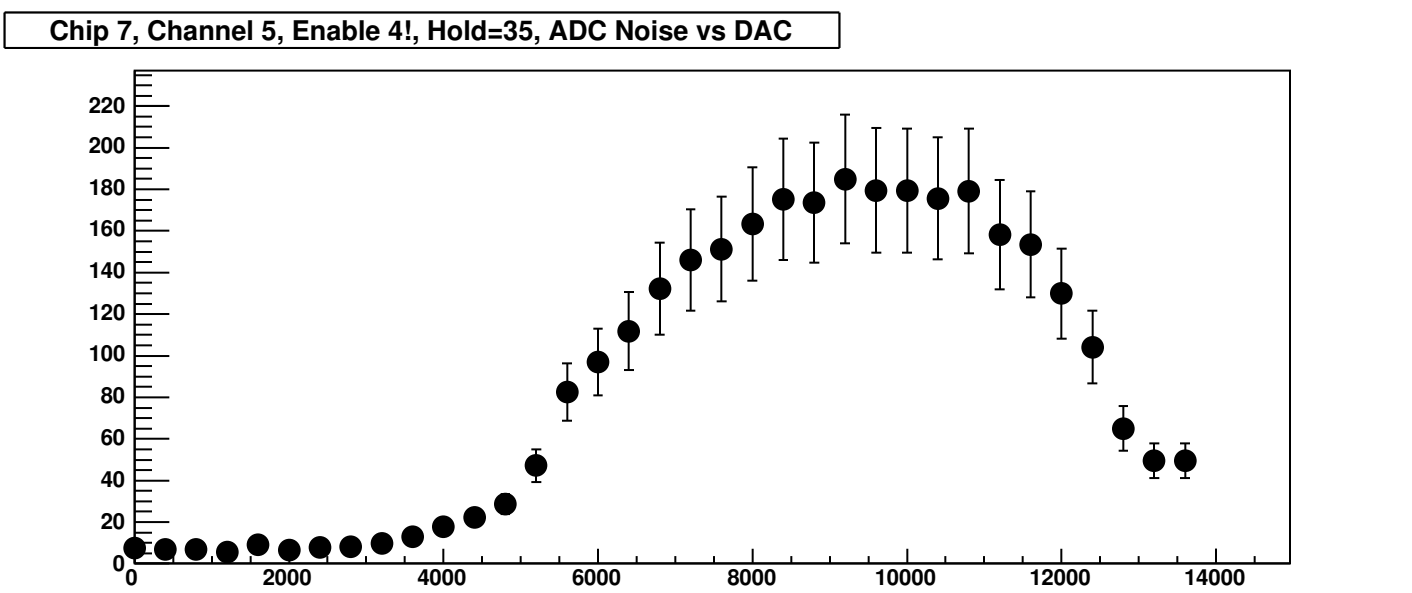
Chip 7, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

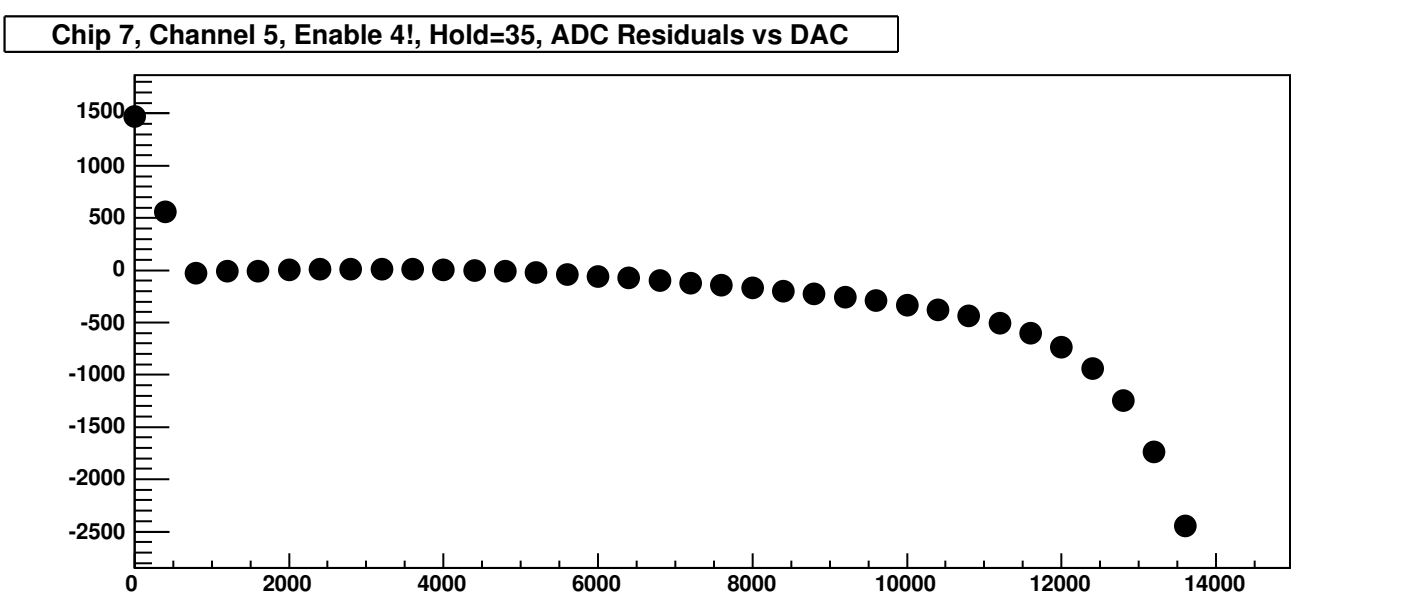


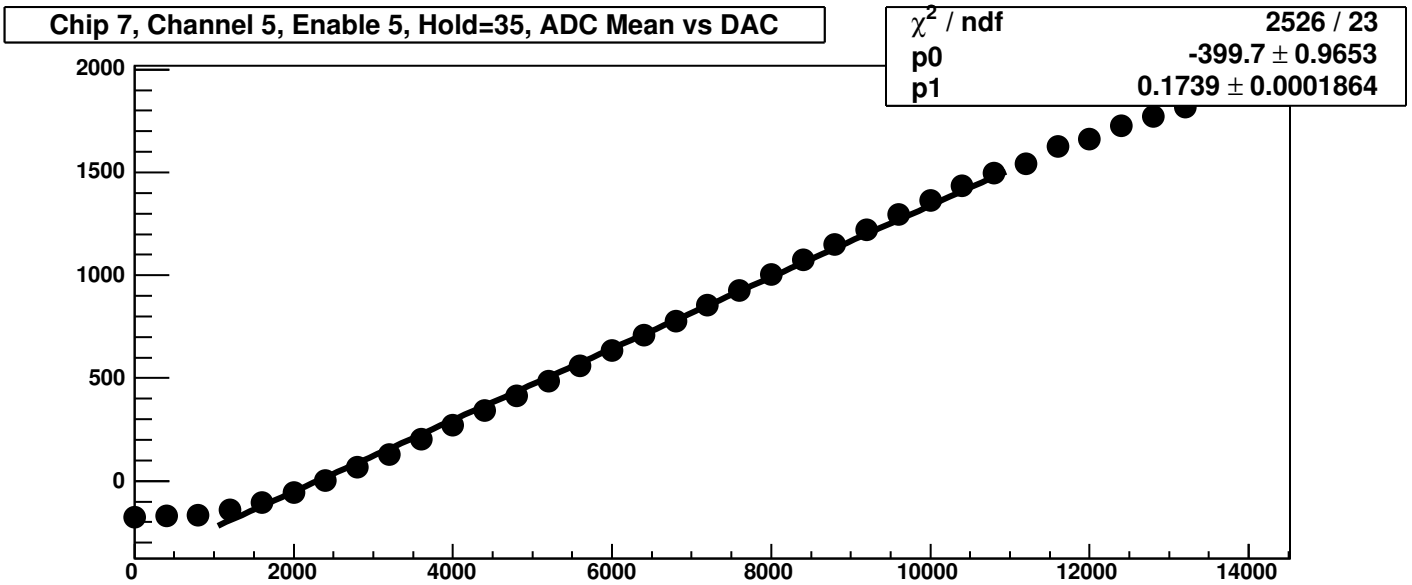
Chip 7, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

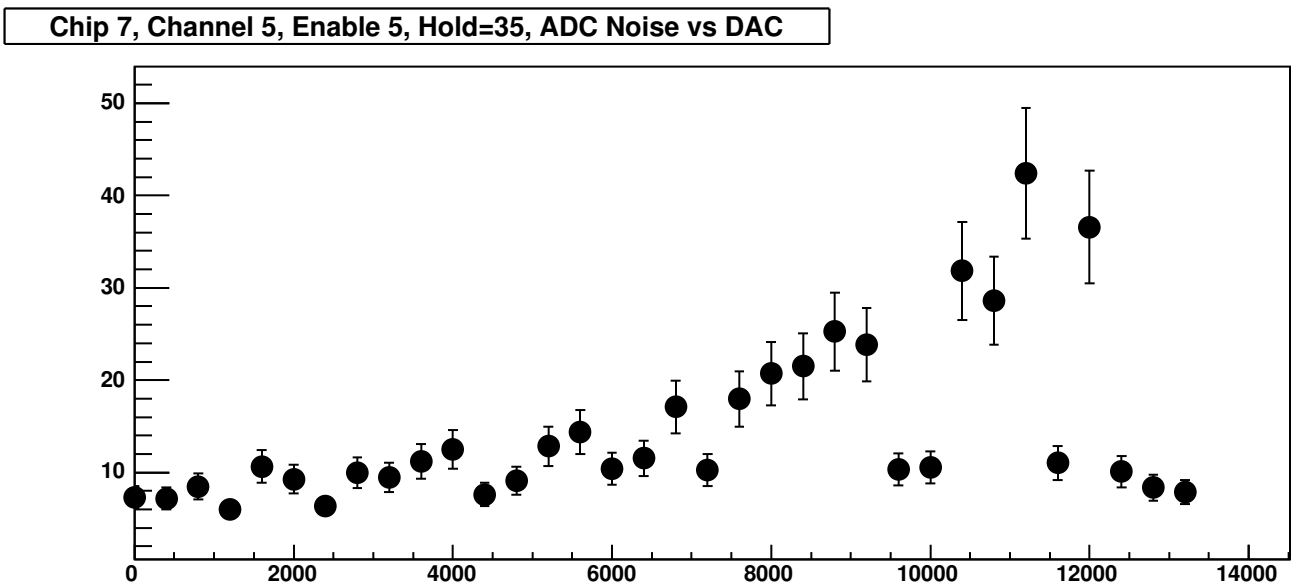


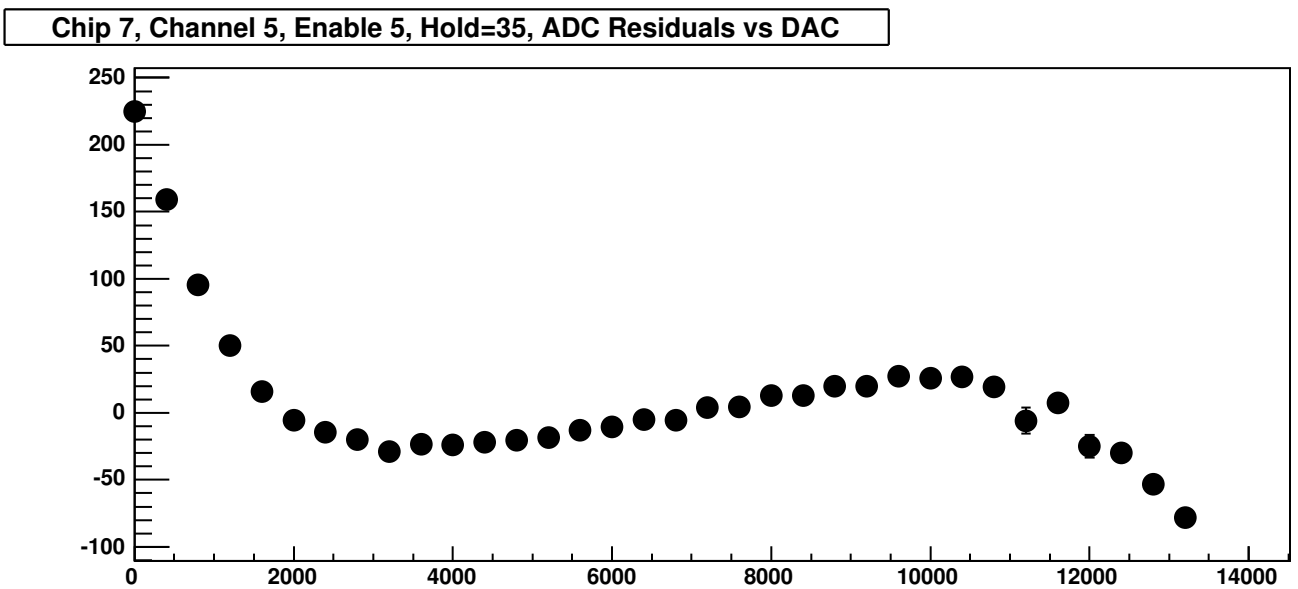

Chip 7, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 705 / 23
p0 -177.7 ± 1.638
p1 2.344 ± 0.0006667
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

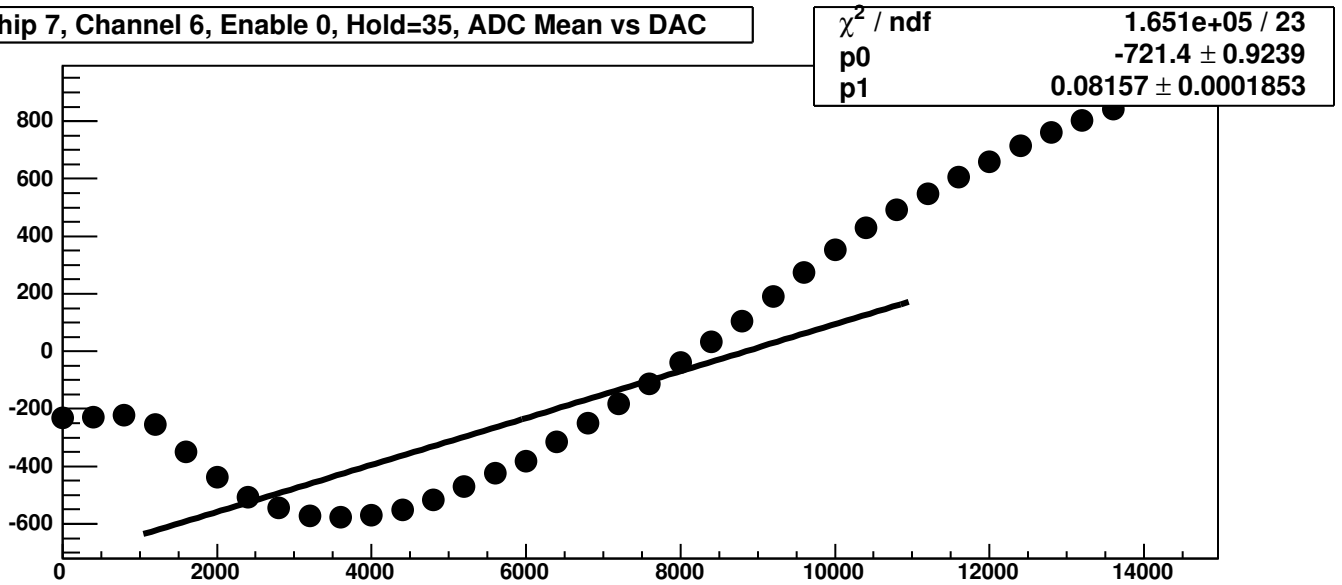

Chip 7, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2526 / 23
p0 -399.7 ± 0.9653
p1 0.1739 ± 0.0001864
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

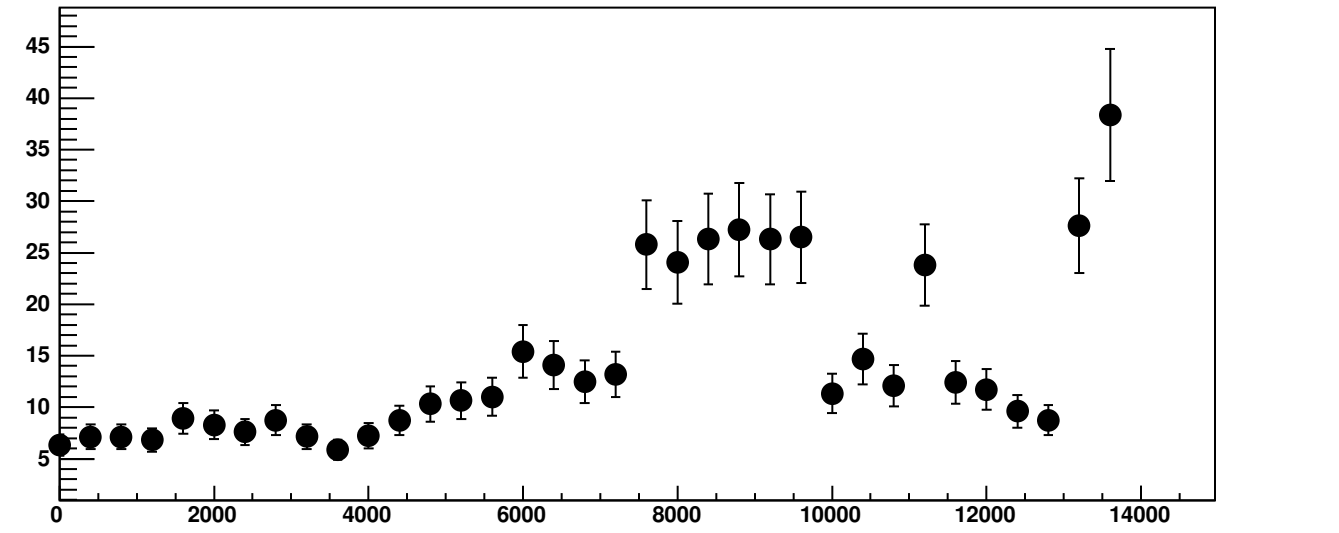

Chip 7, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

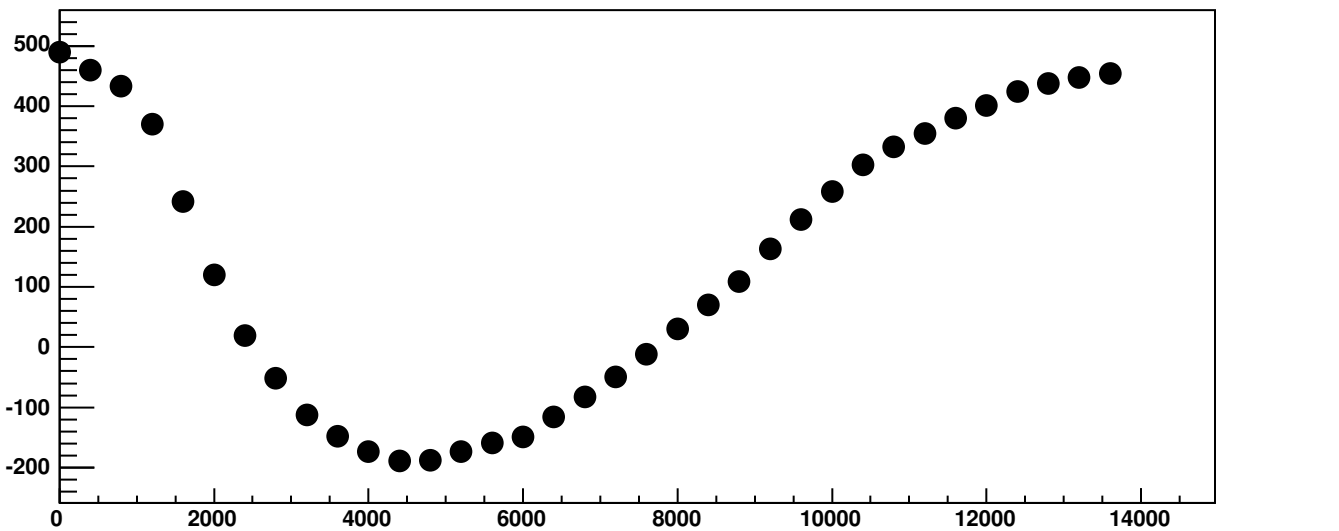
Chip 7, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

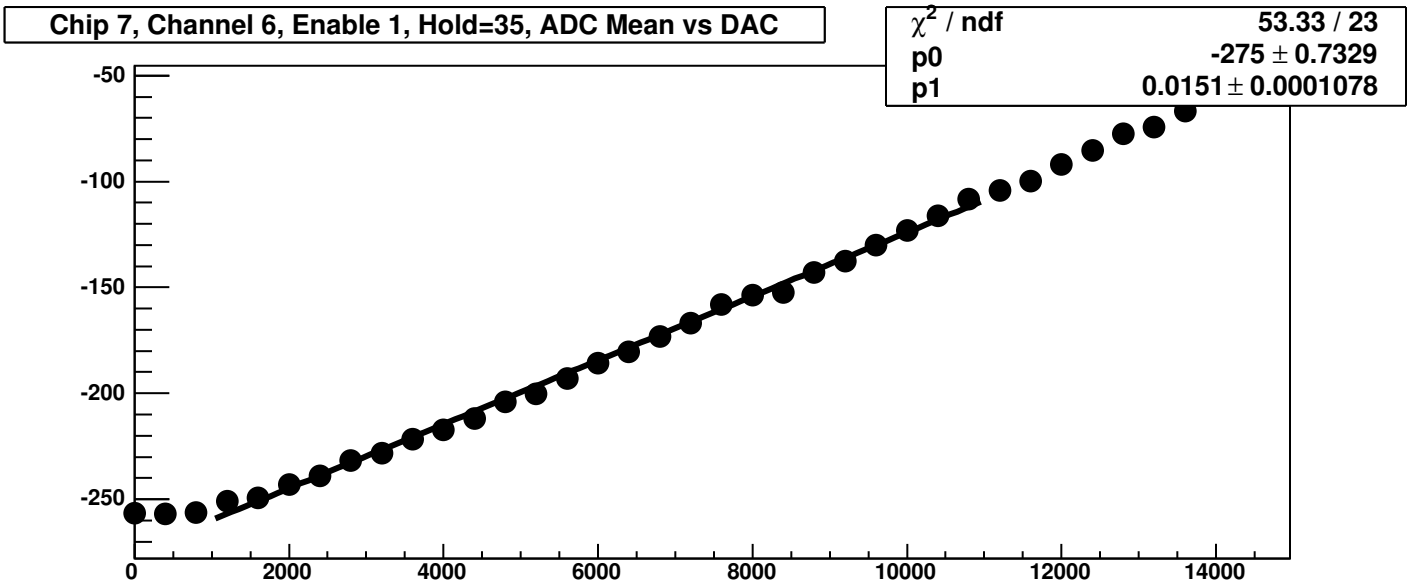


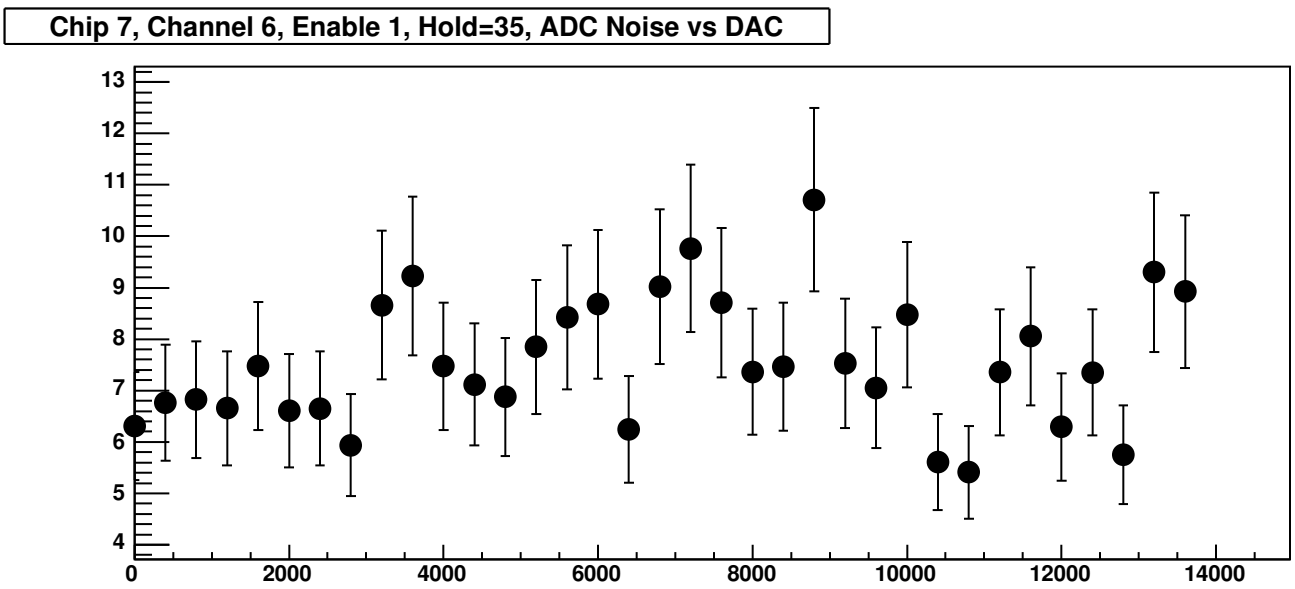
Chip 7, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

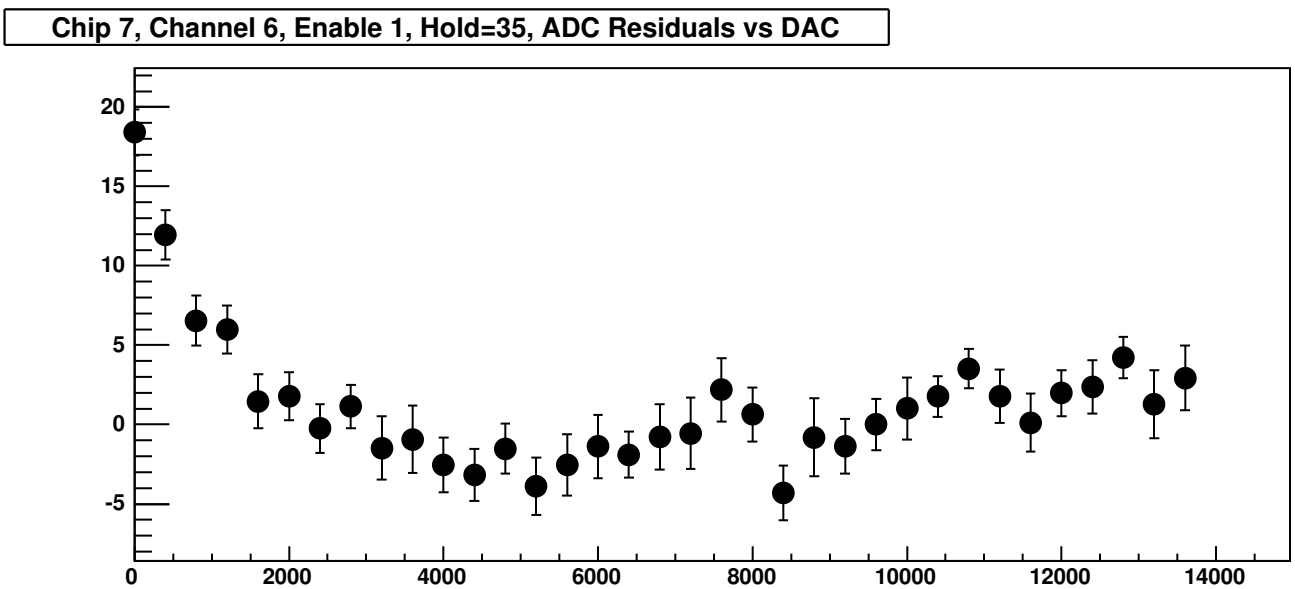


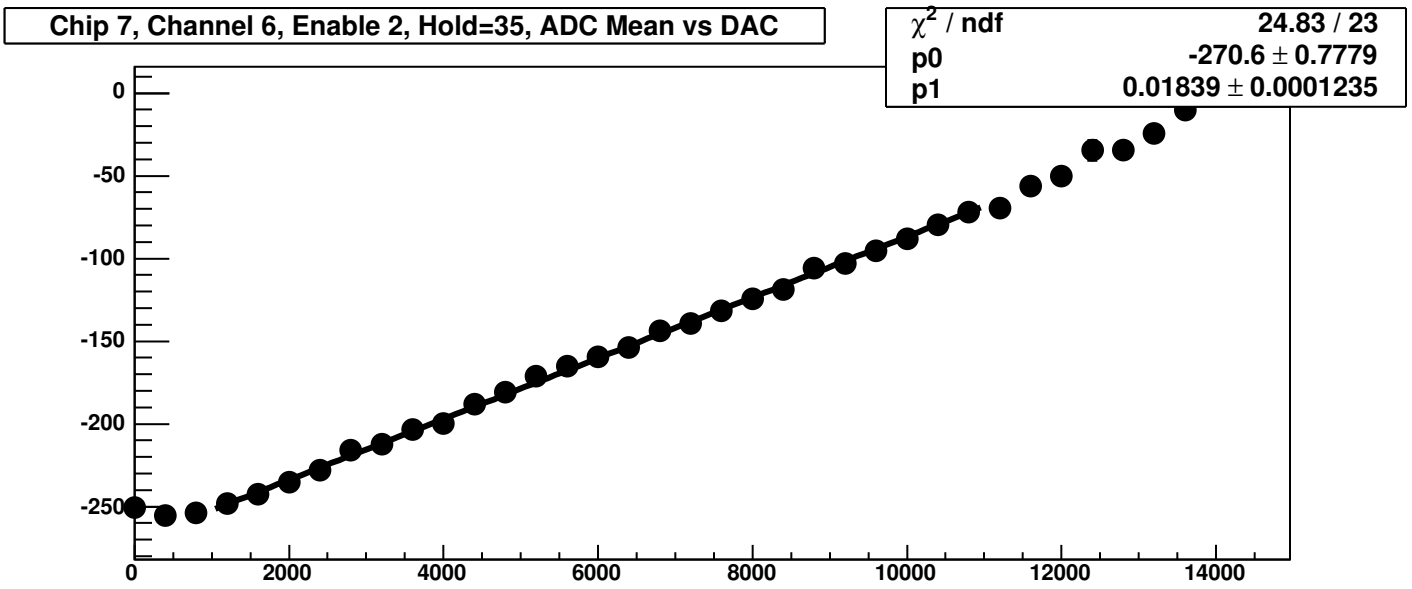
Chip 7, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

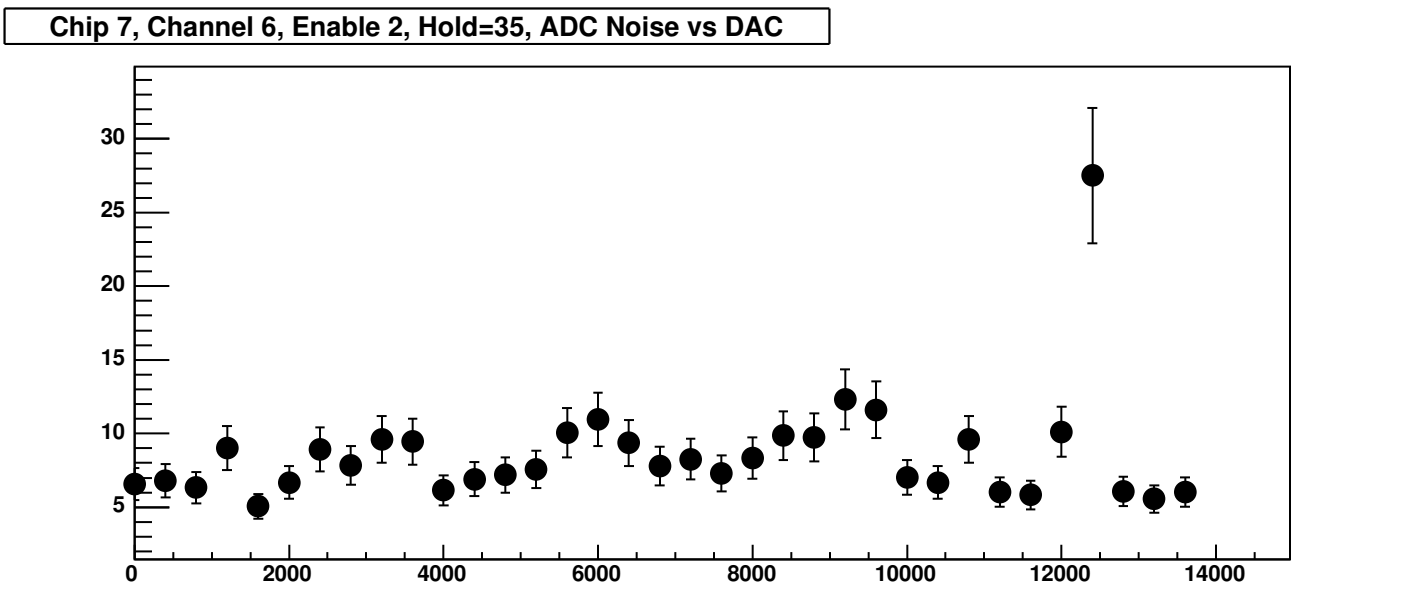


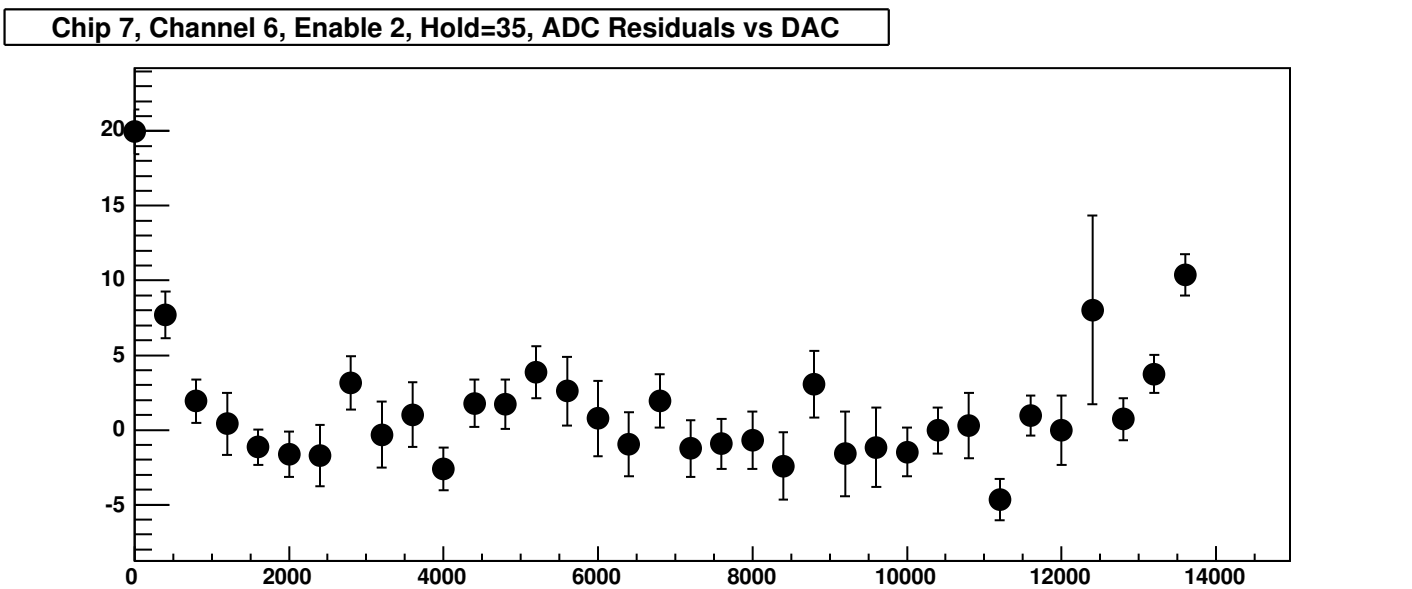

Chip 7, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 53.33 / 23
p0 -275 ± 0.7329
p1 0.0151 ± 0.0001078
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

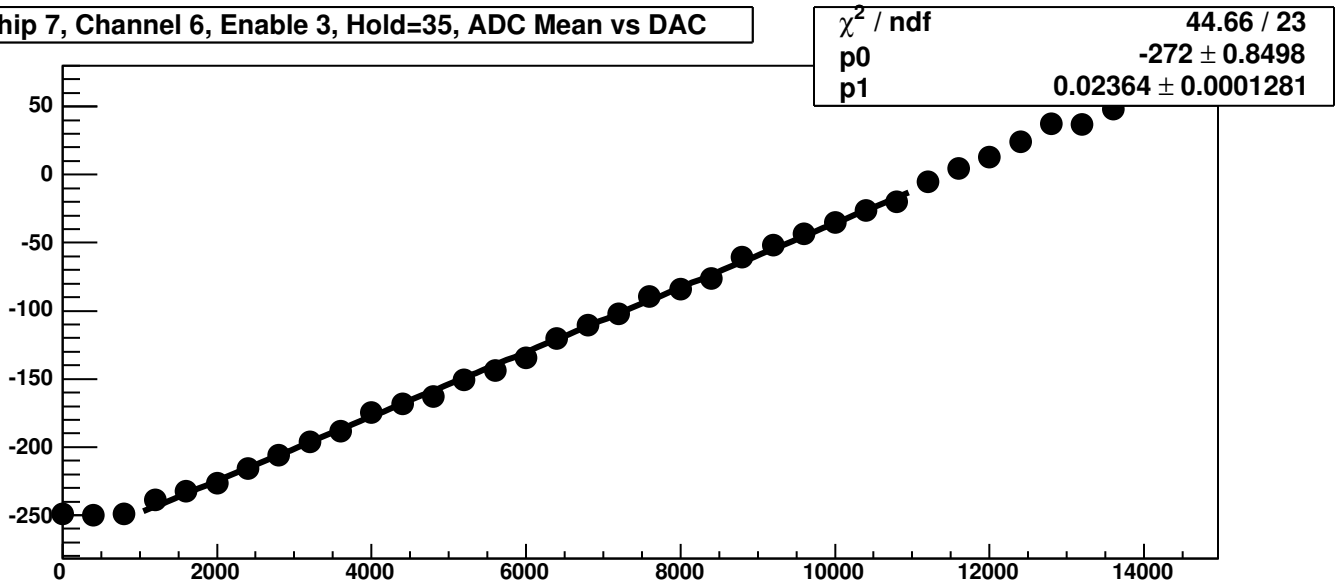

Chip 7, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.83 / 23
p0 -270.6 ± 0.7779
p1 0.01839 ± 0.0001235
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

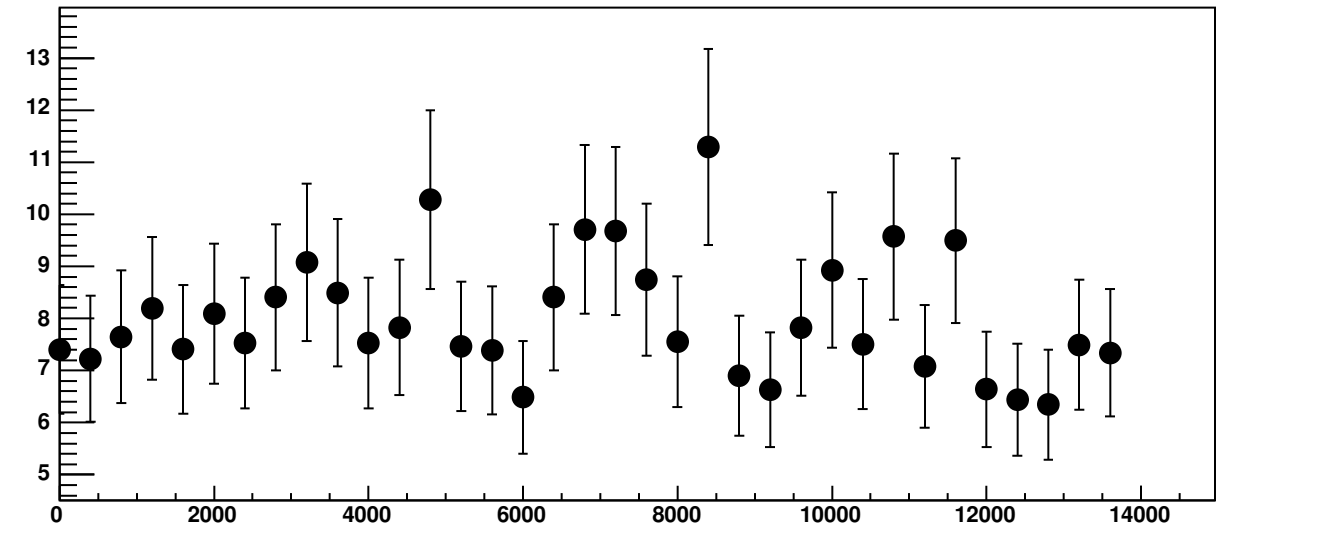

Chip 7, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

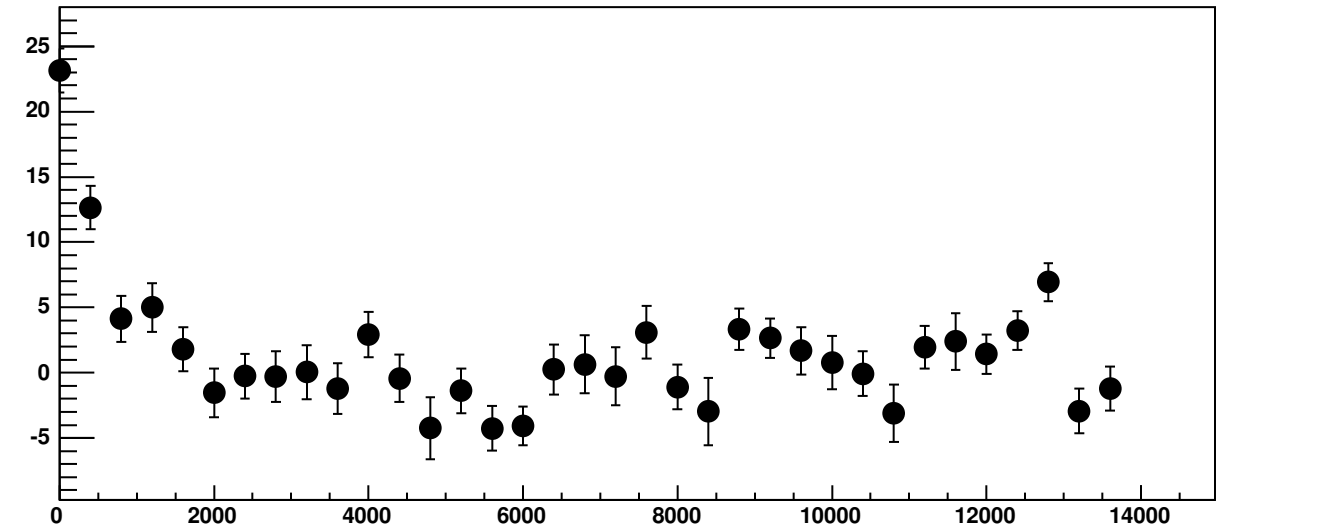
Chip 7, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



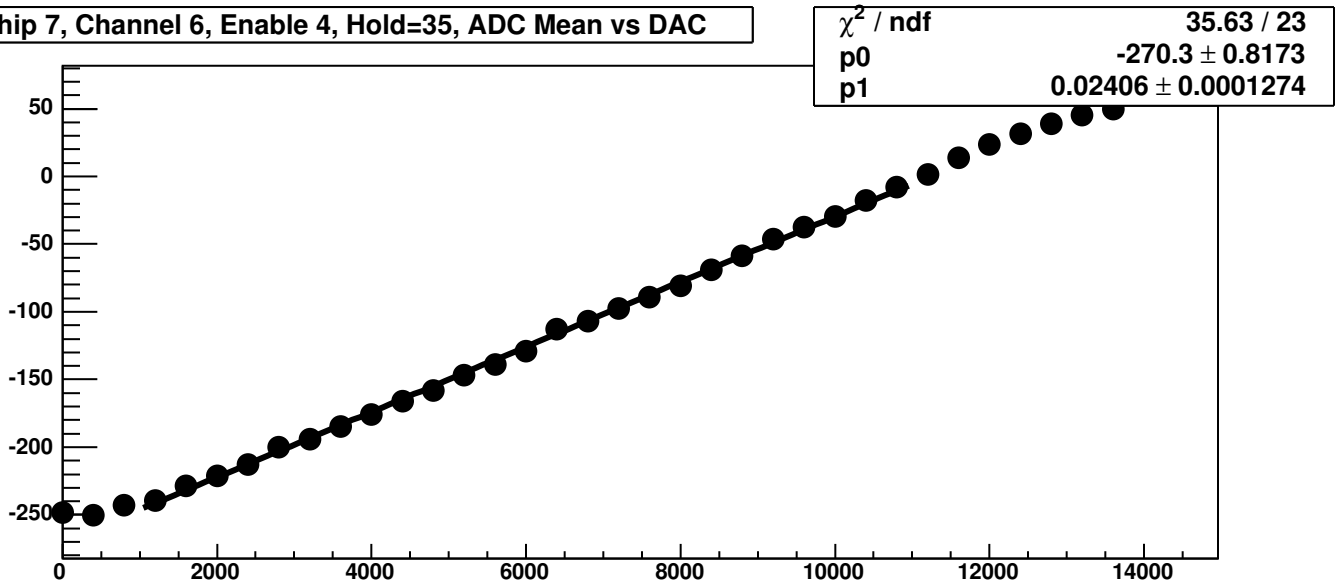
Chip 7, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



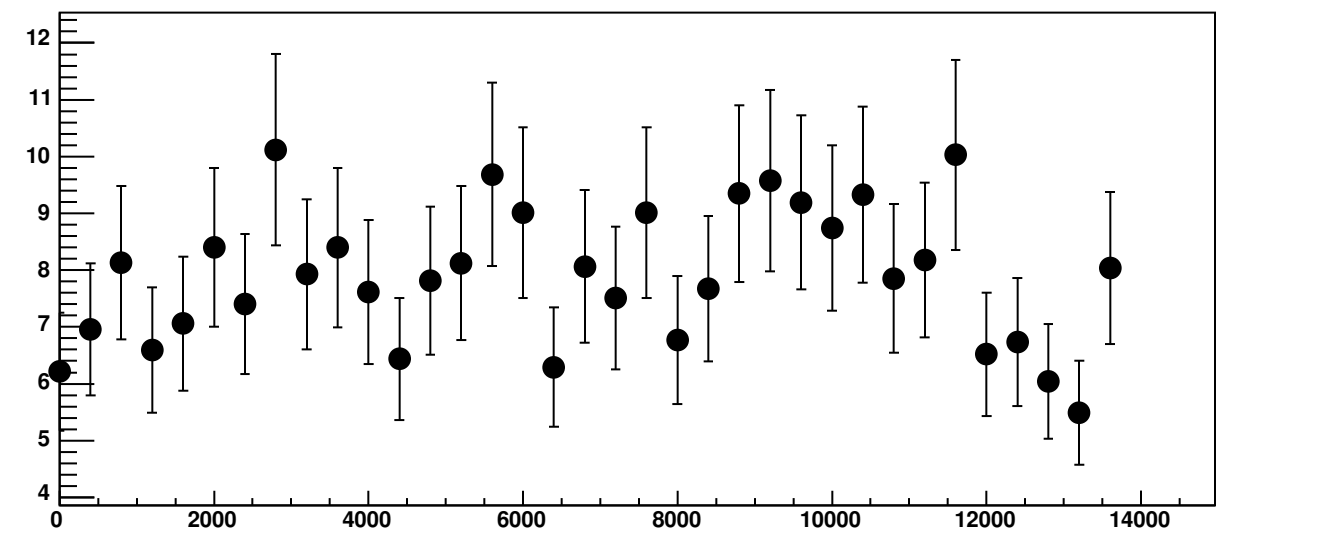
Chip 7, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



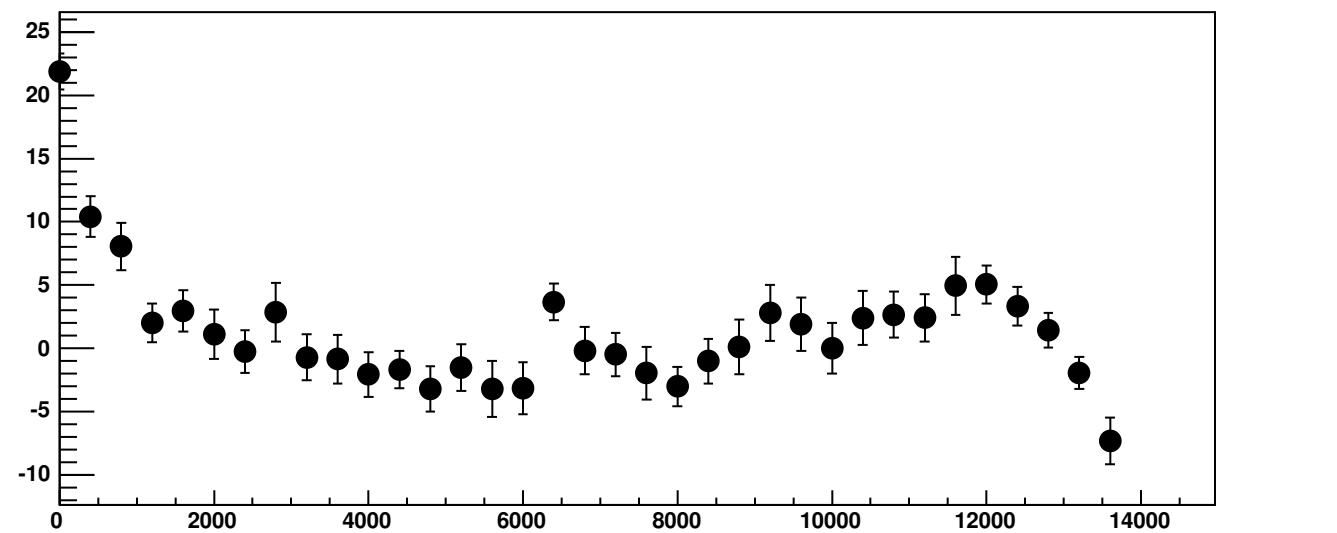
Chip 7, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

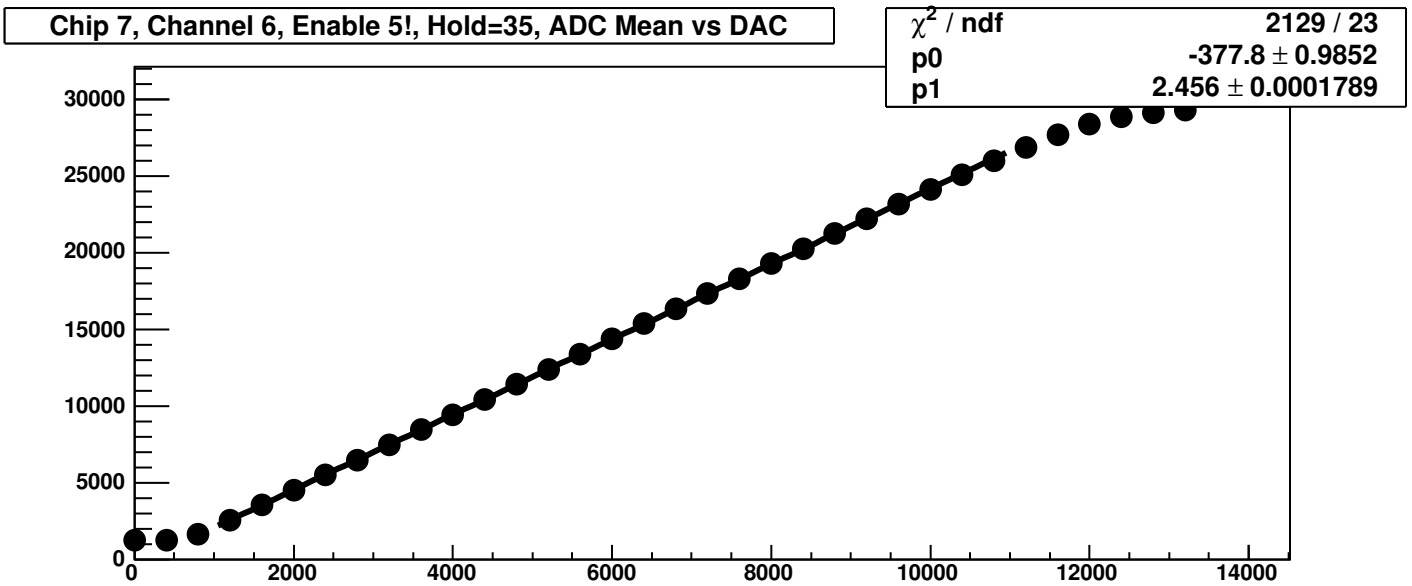


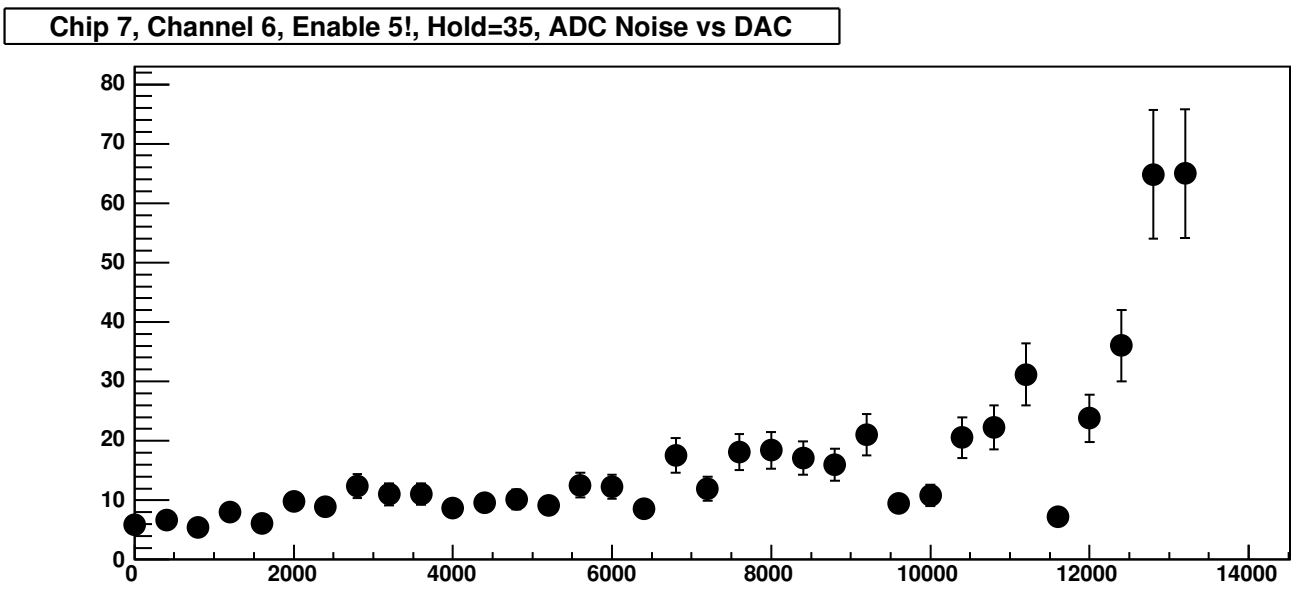
Chip 7, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

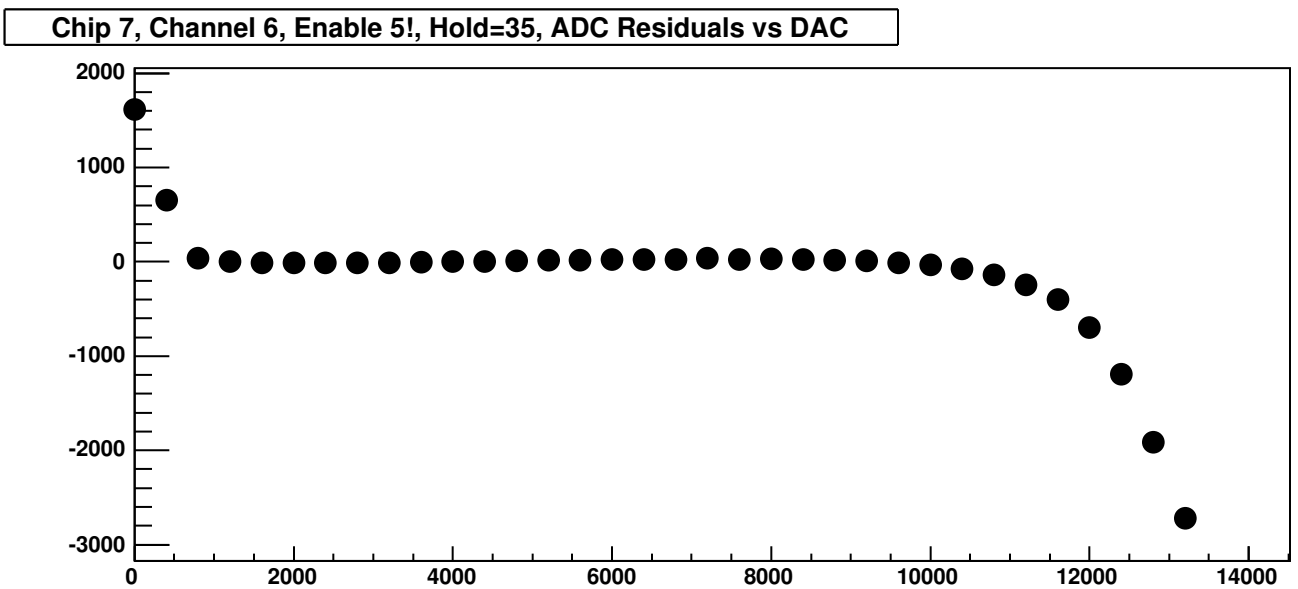


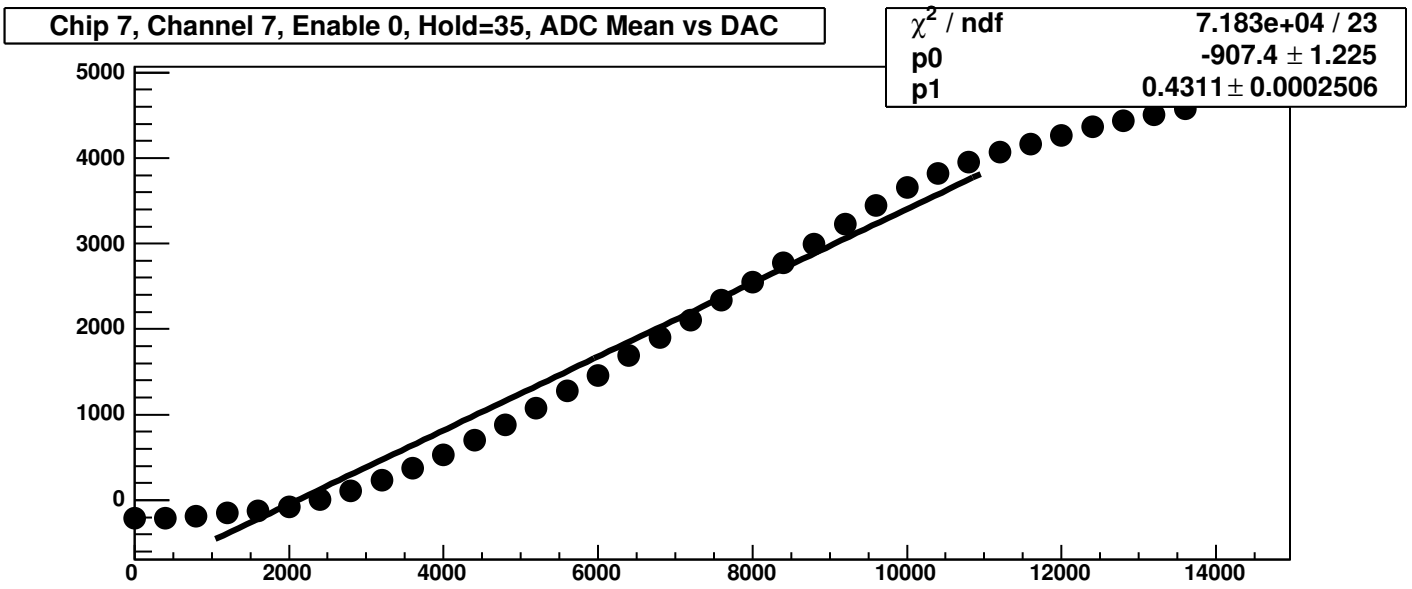
Chip 7, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

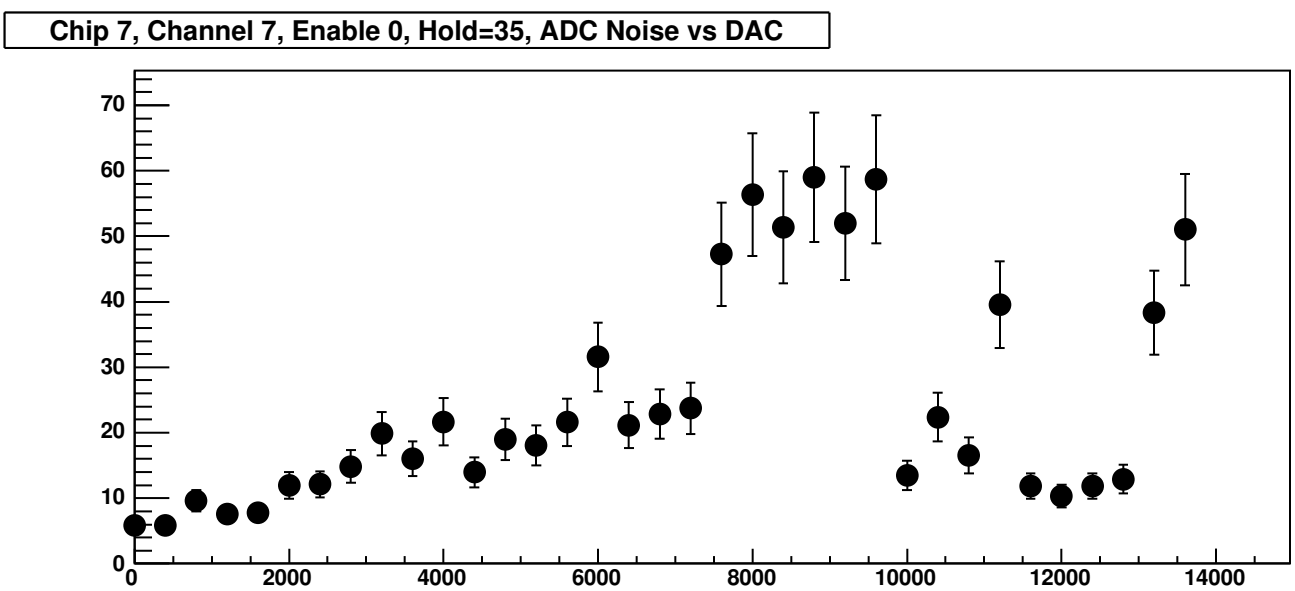


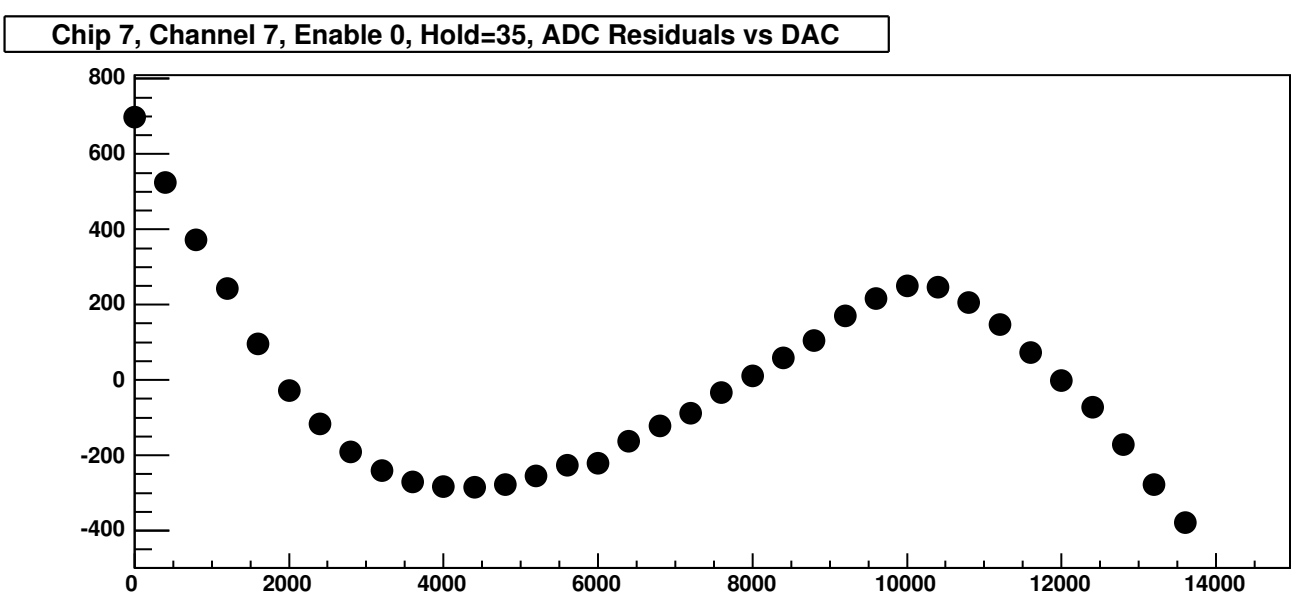

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2129 / 23
p0 -377.8 ± 0.9852
p1 2.456 ± 0.0001789
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

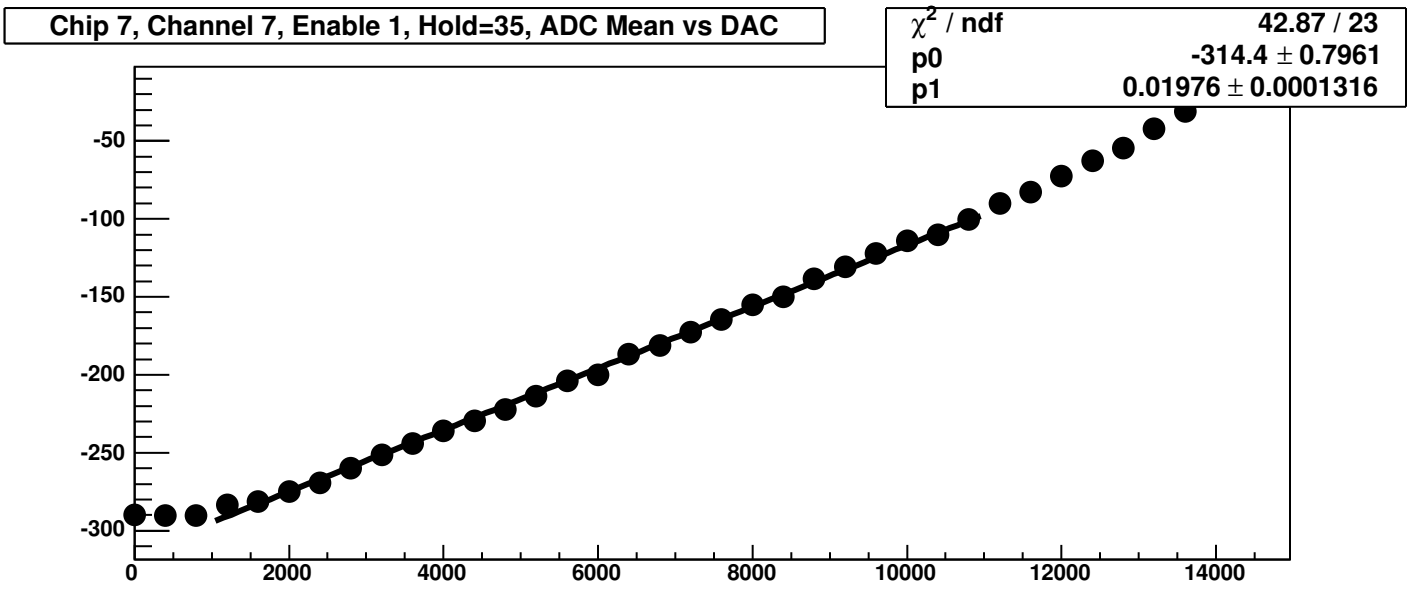

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

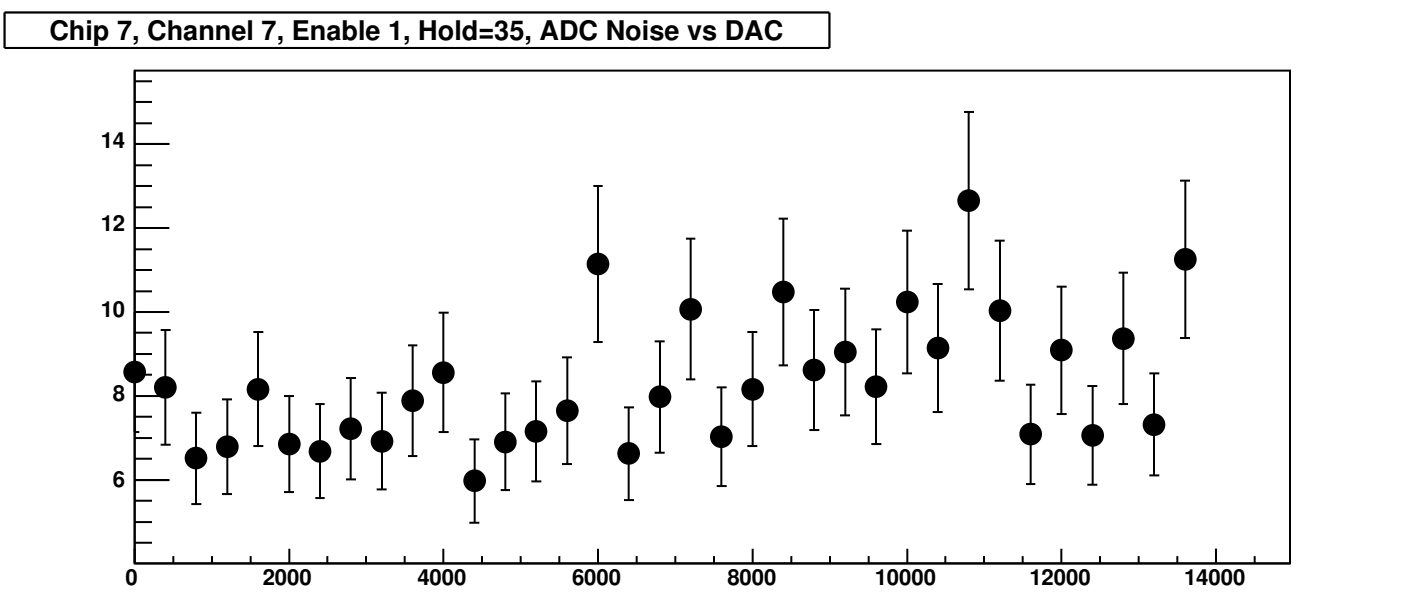

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

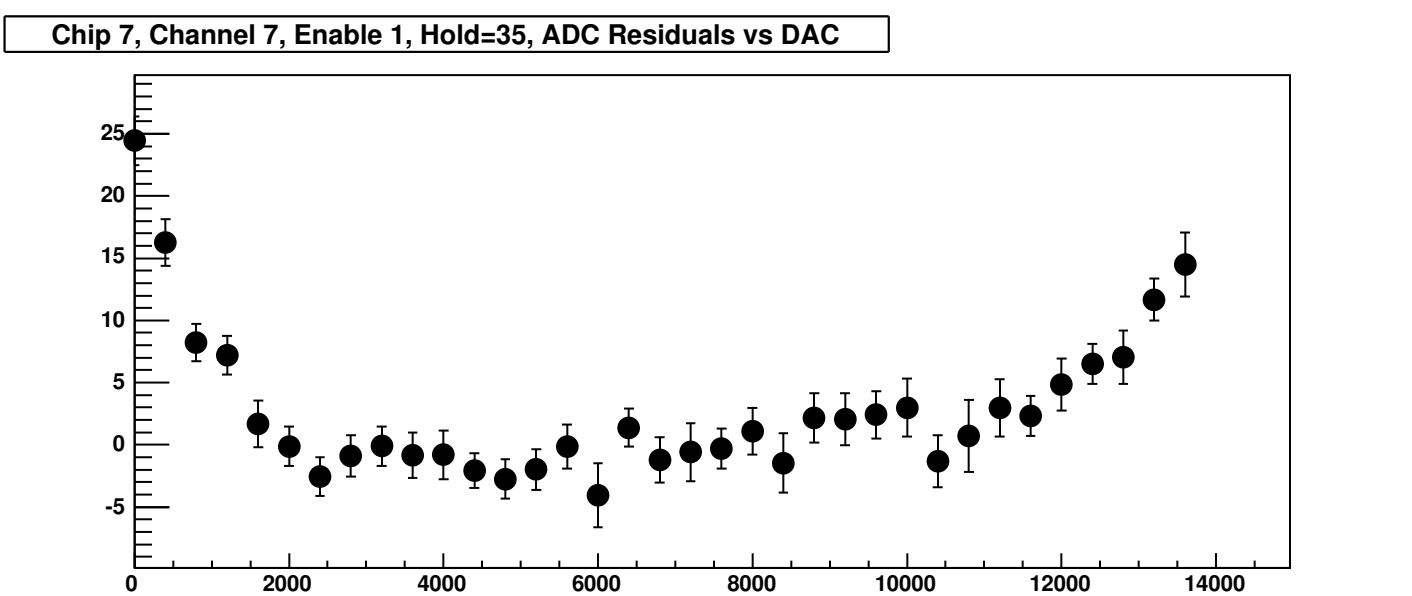

Chip 7, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.183e+04 / 23
p0 -907.4 ± 1.225
p1 0.4311 ± 0.0002506
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

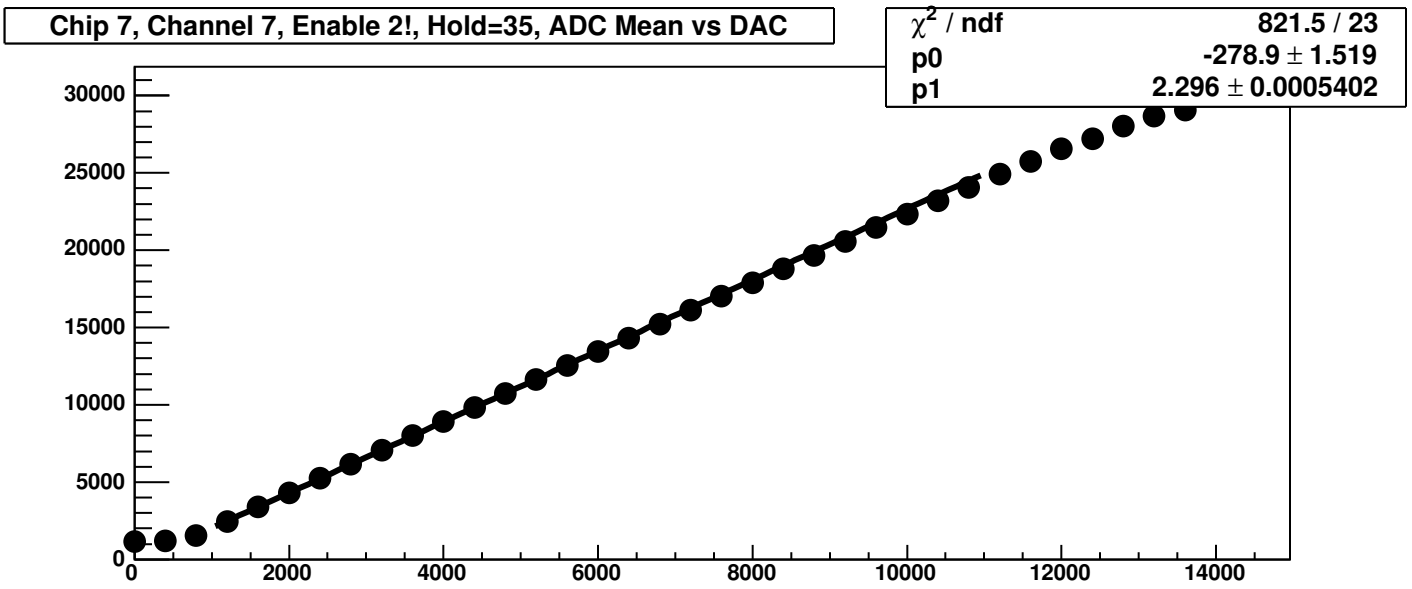

Chip 7, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

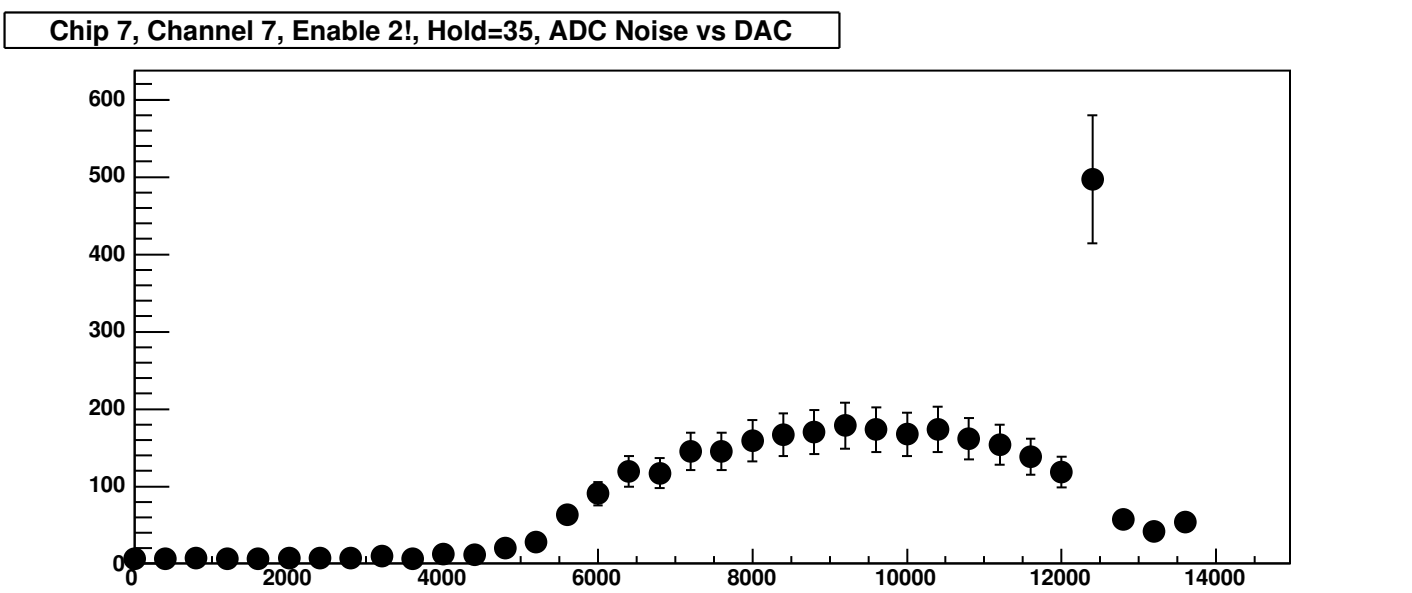

Chip 7, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

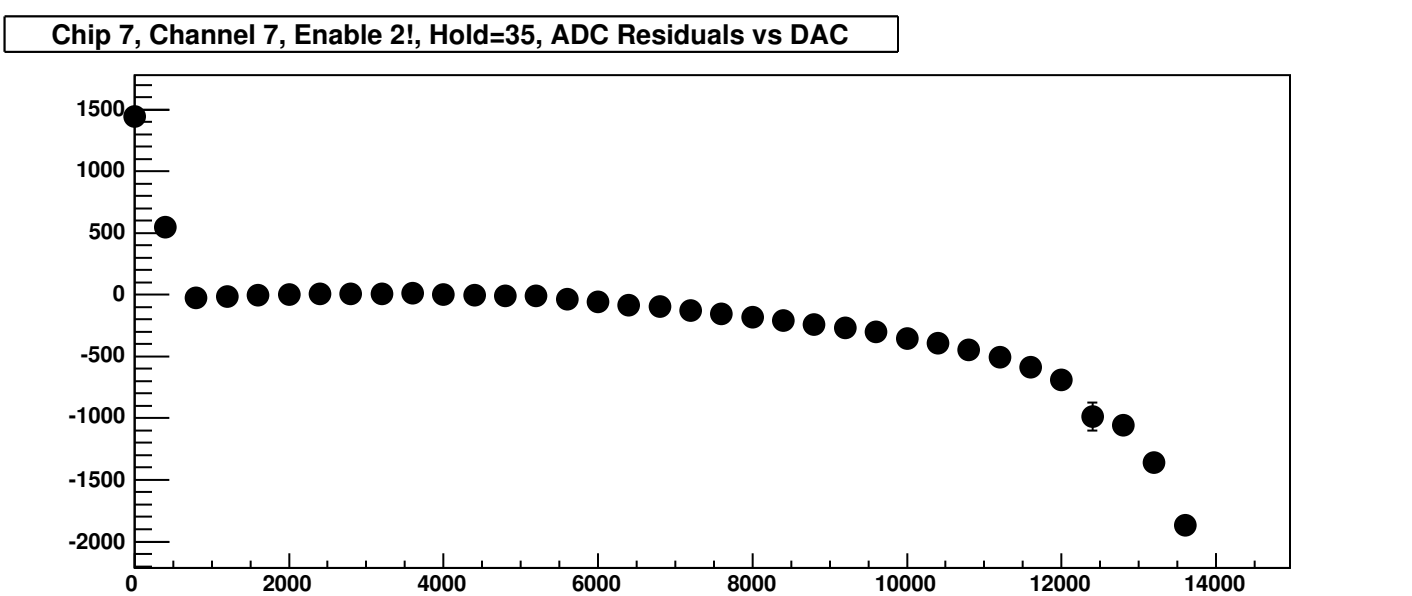

Chip 7, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.87 / 23
p0 -314.4 ± 0.7961
p1 0.01976 ± 0.0001316
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

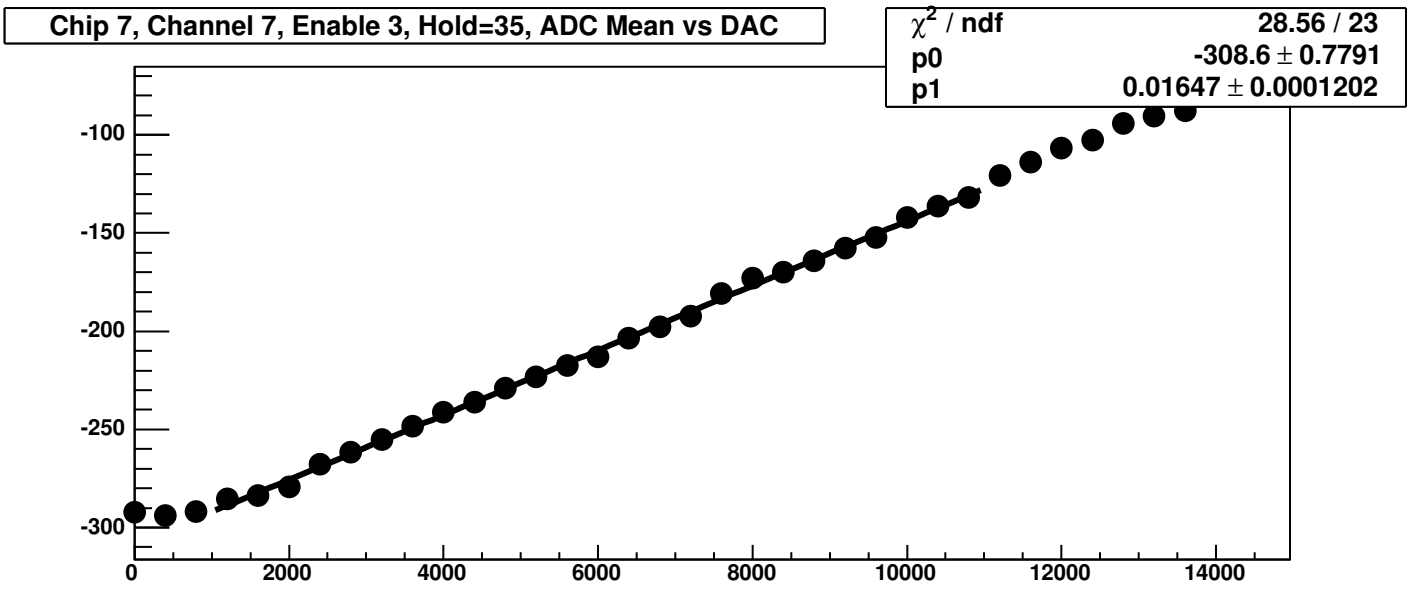

Chip 7, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

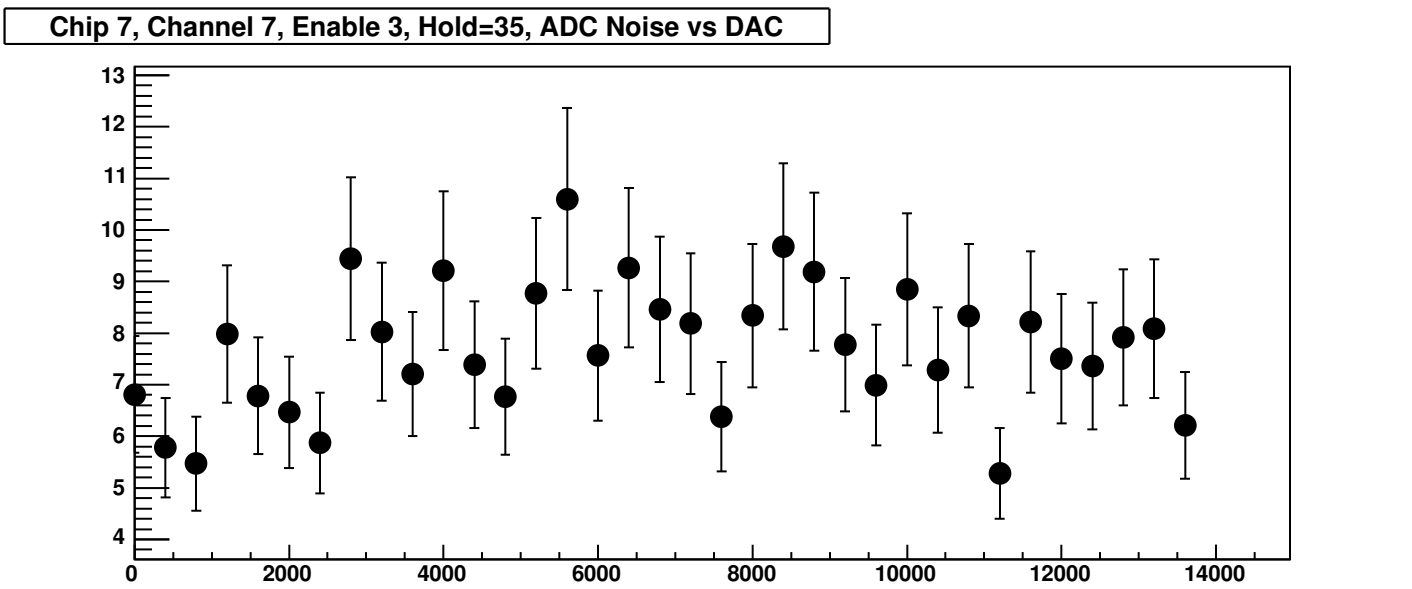

Chip 7, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

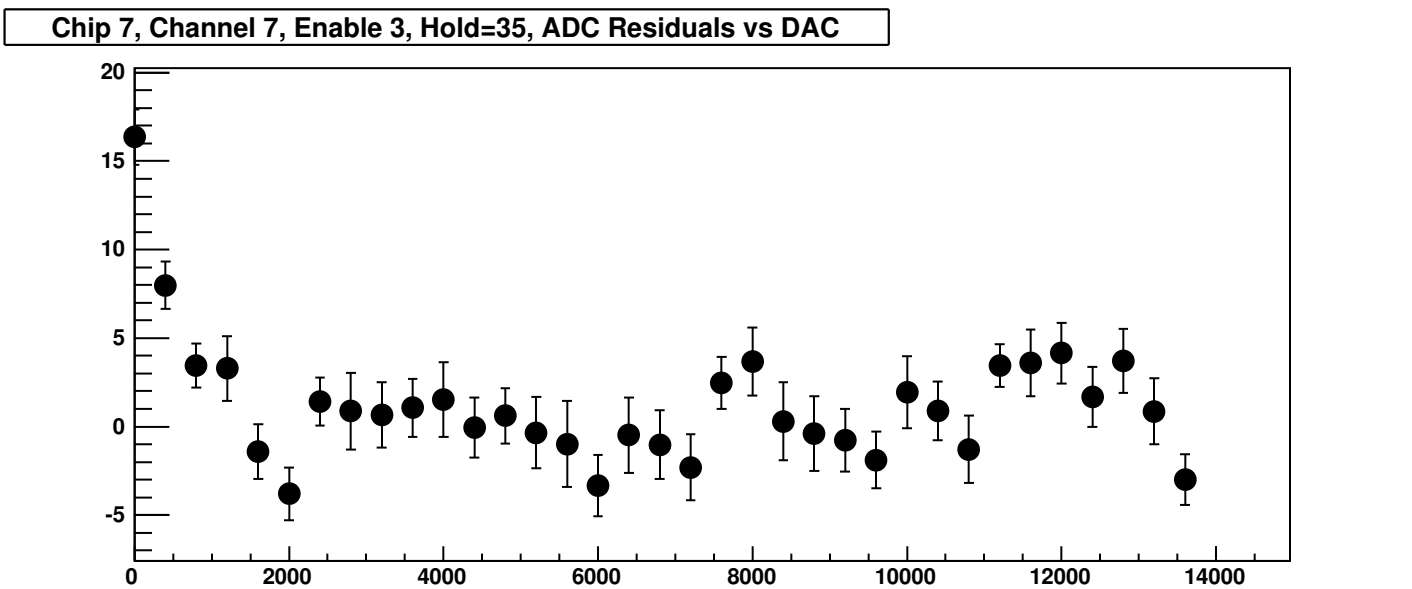

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 821.5 / 23
p0 -278.9 ± 1.519
p1 2.296 ± 0.0005402
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

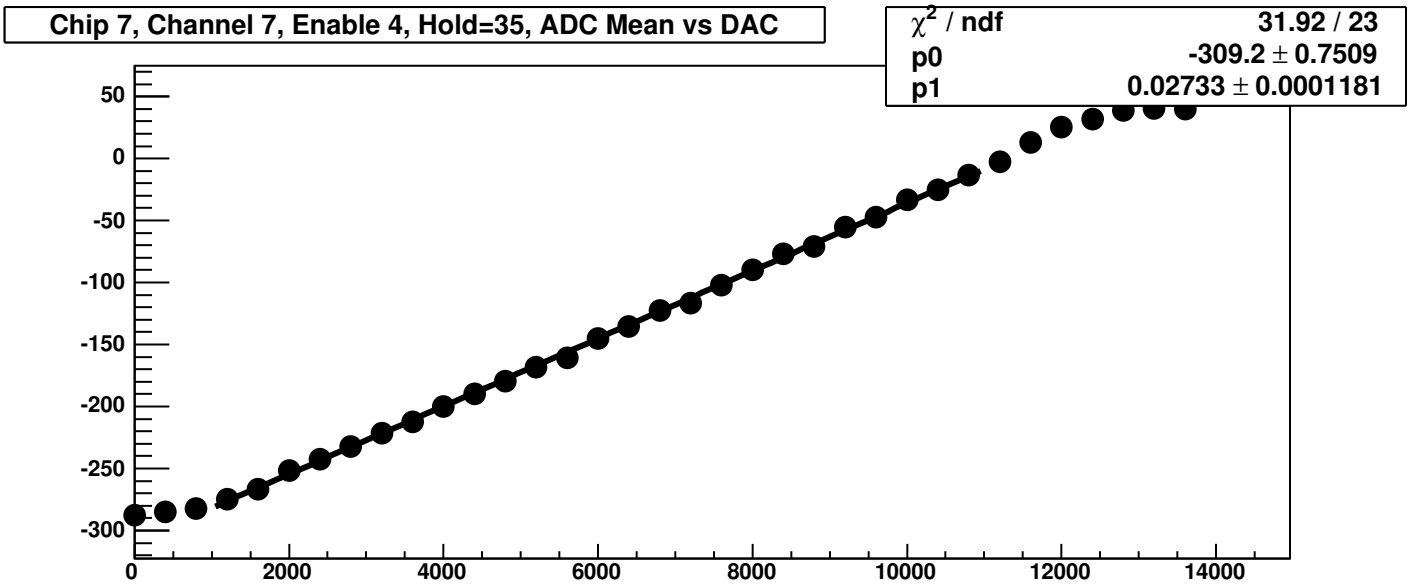

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

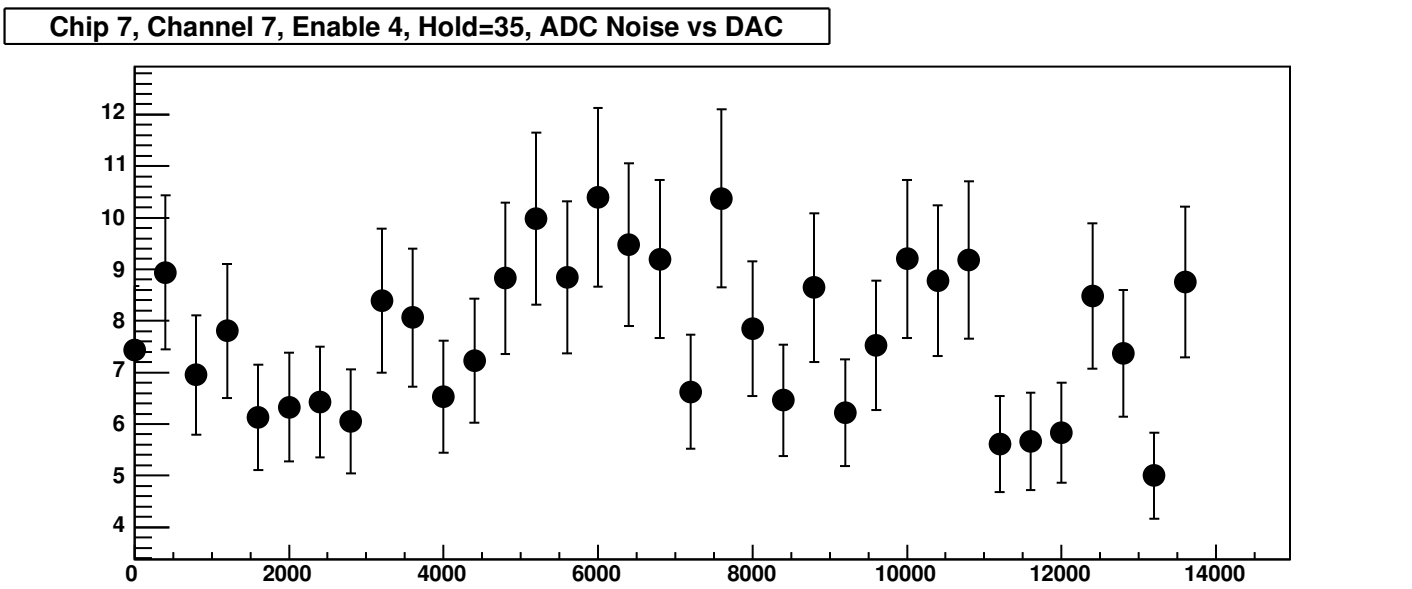

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

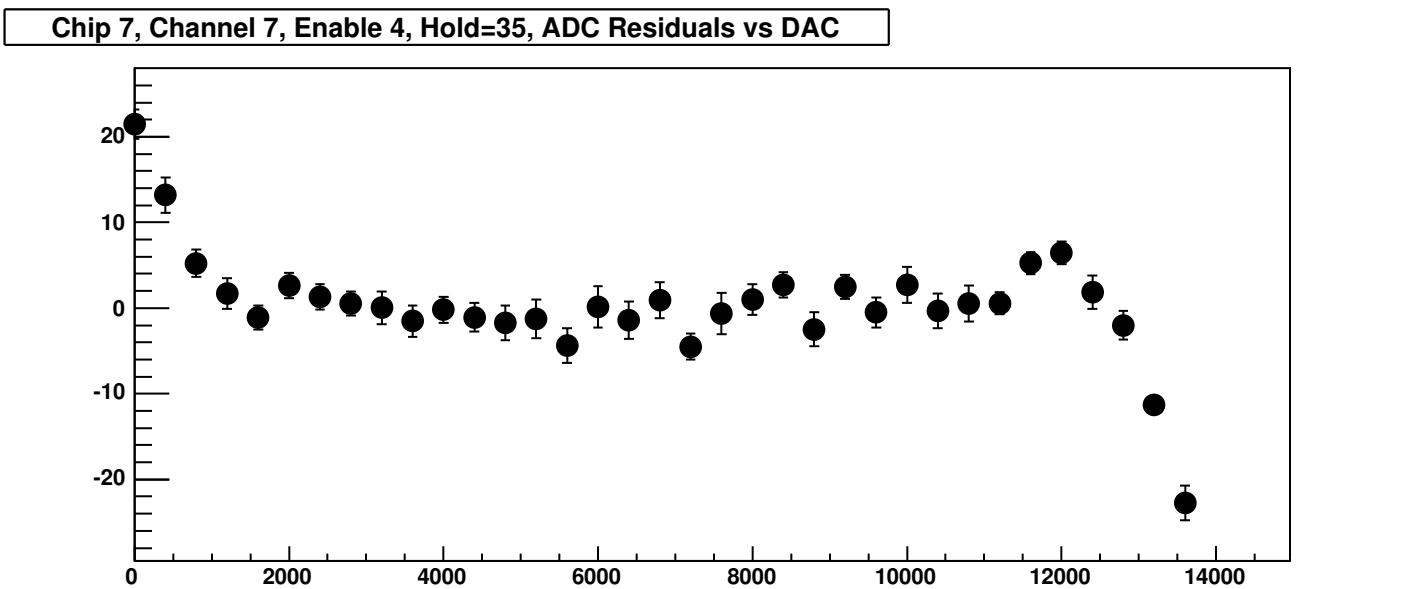

Chip 7, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.56 / 23
p0 -308.6 ± 0.7791
p1 0.01647 ± 0.0001202
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

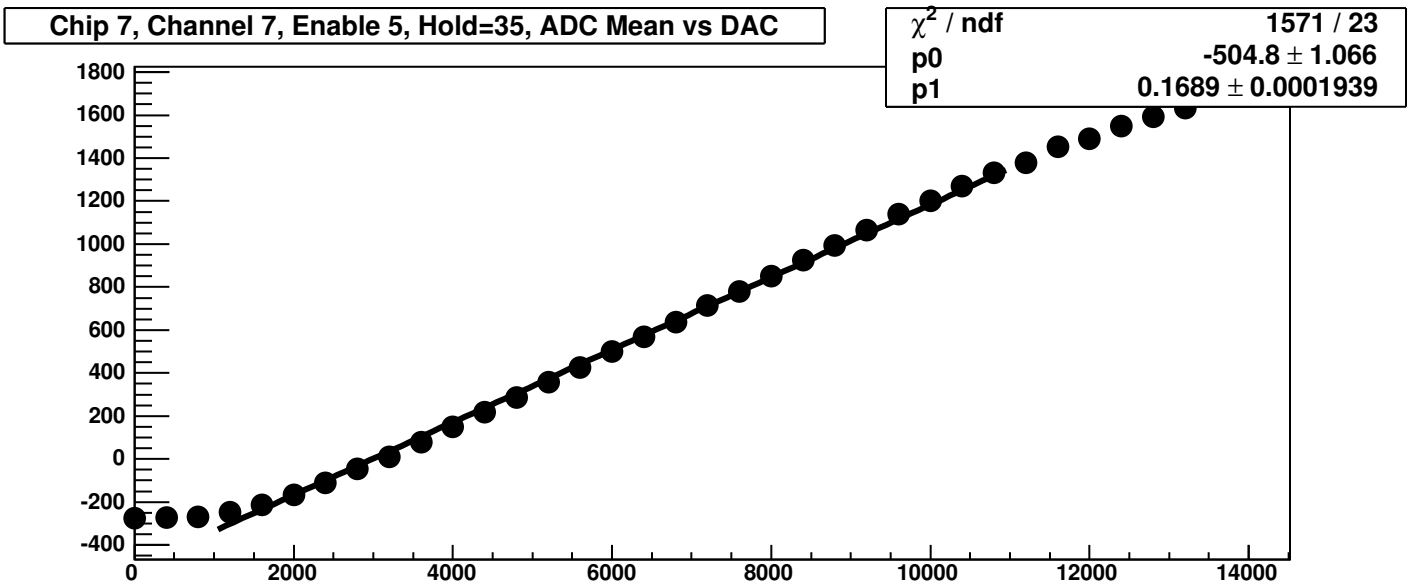

Chip 7, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

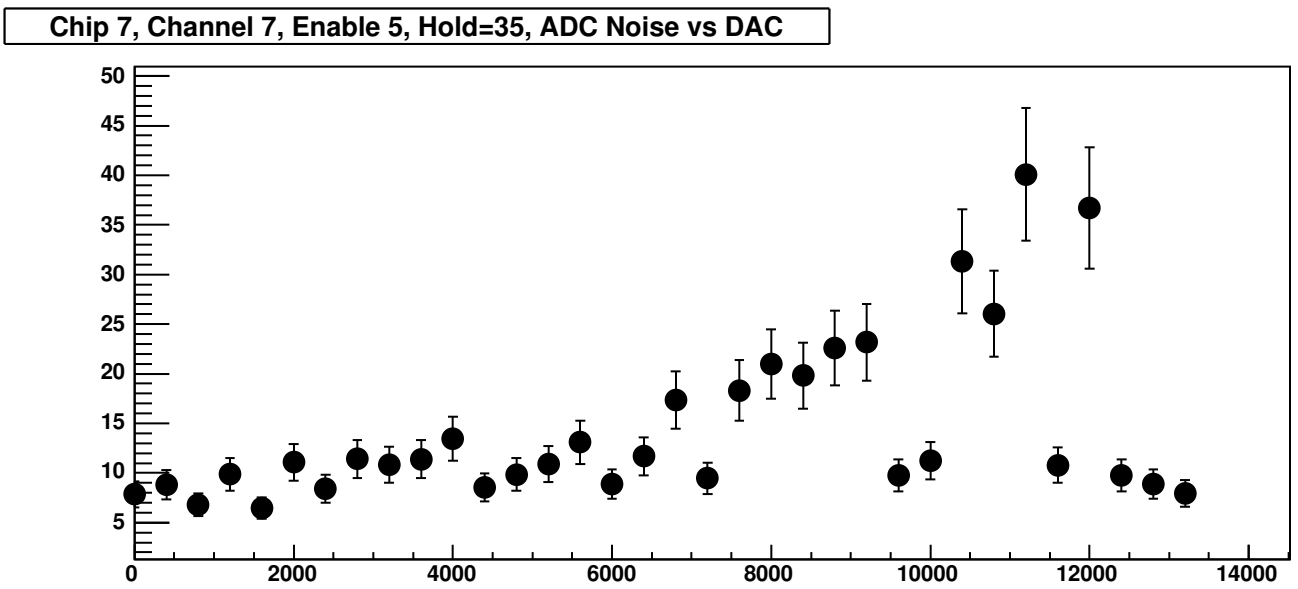

Chip 7, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

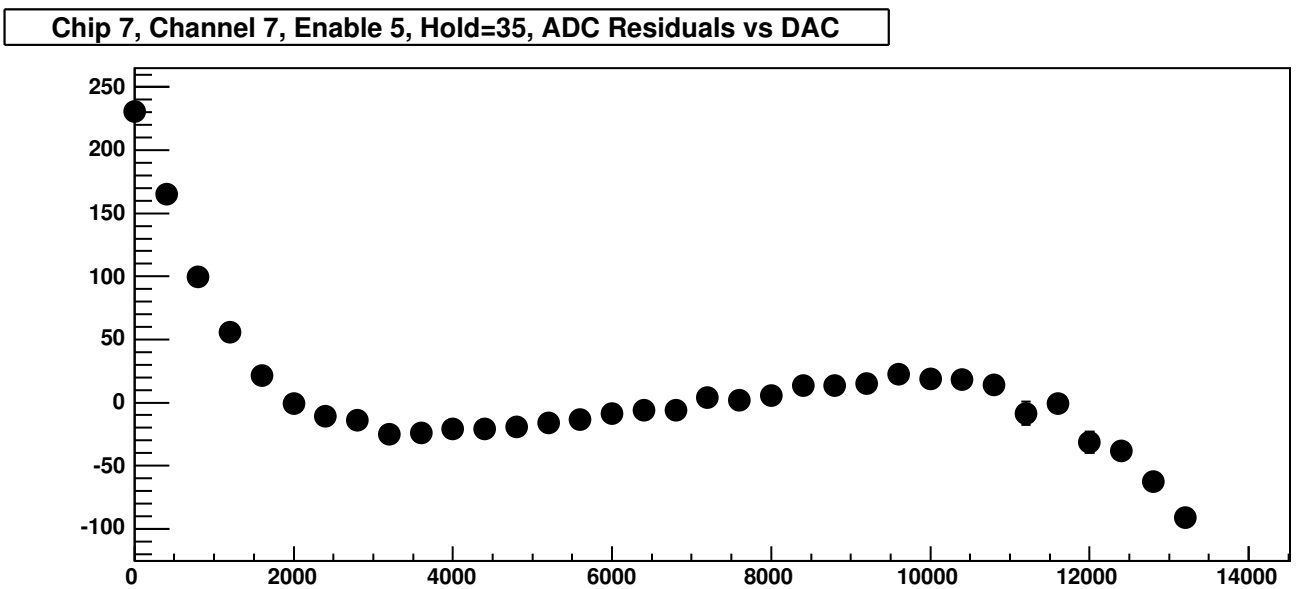

Chip 7, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.92 / 23
p0 -309.2 ± 0.7509
p1 0.02733 ± 0.0001181
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

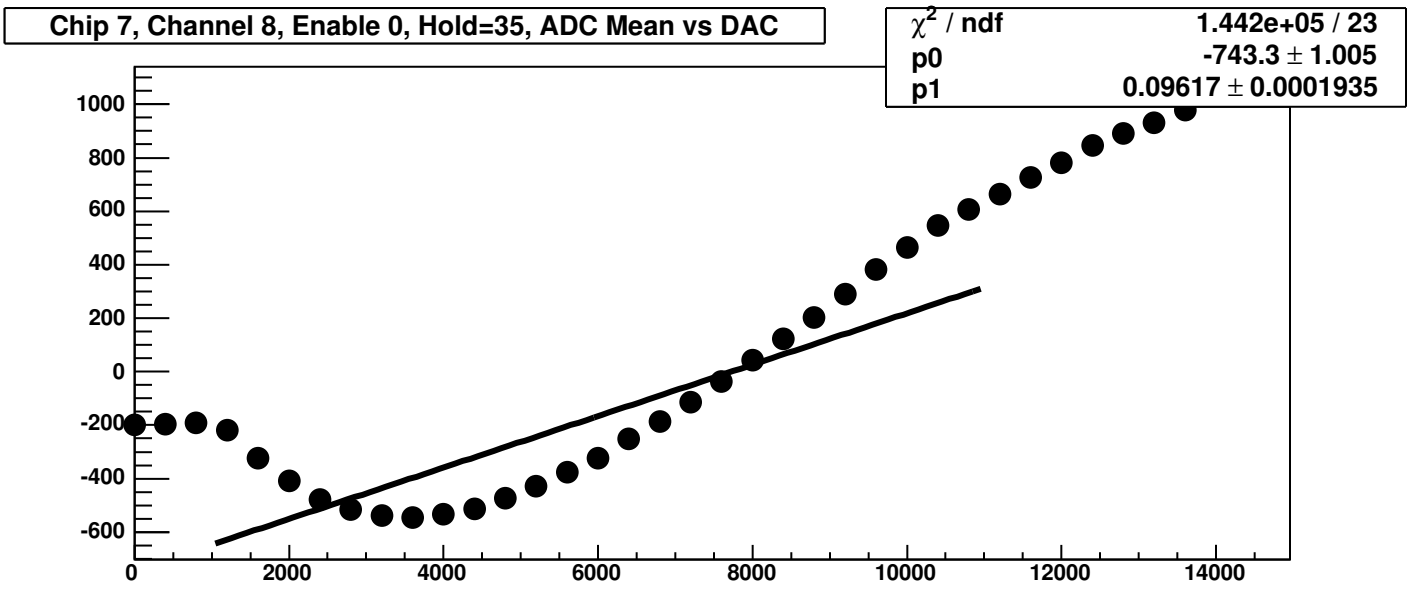

Chip 7, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

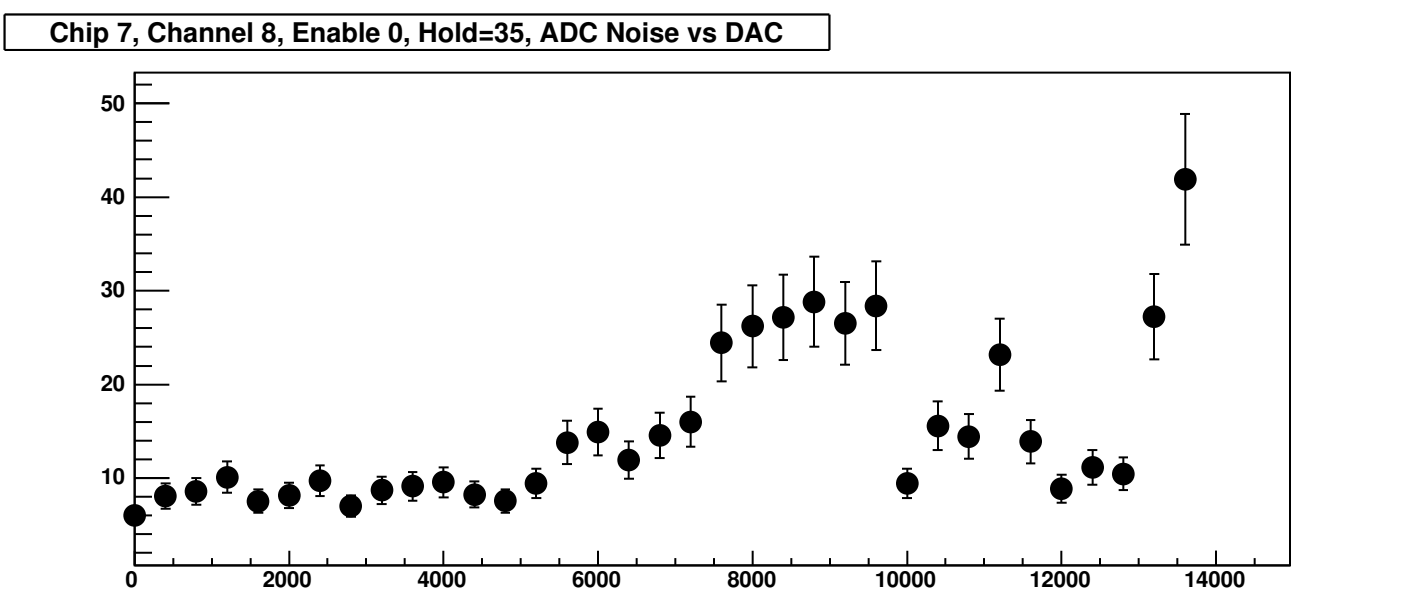

Chip 7, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

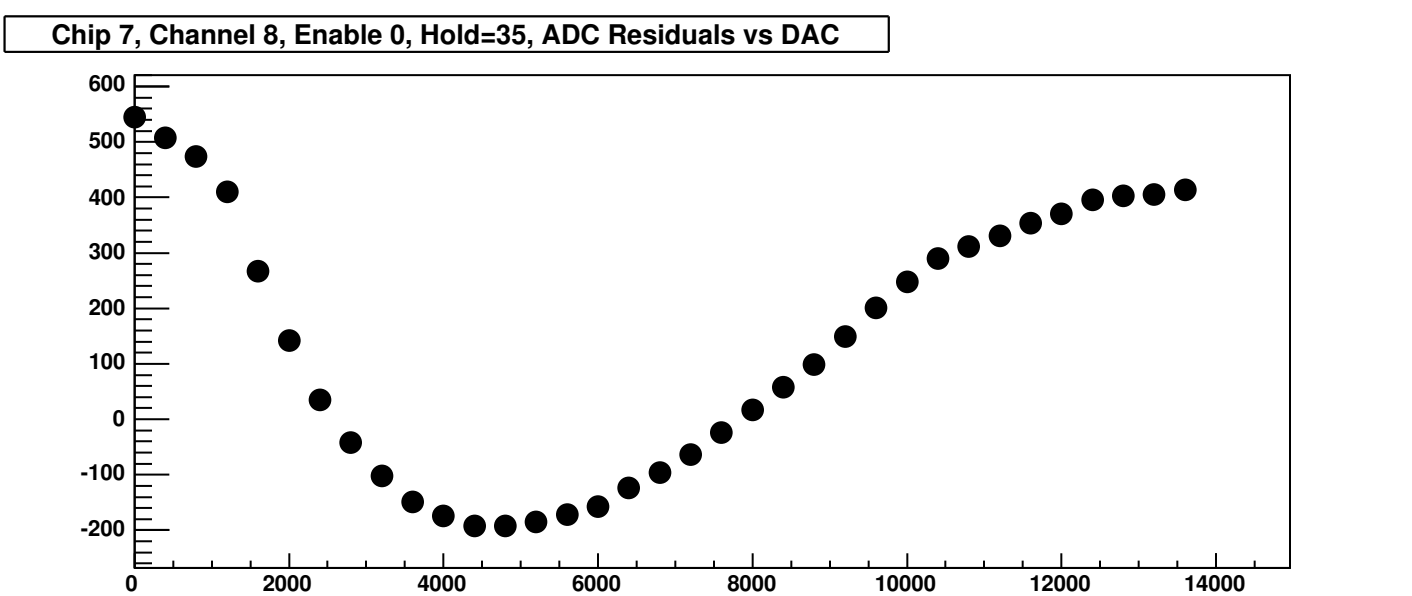

Chip 7, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1571 / 23
p0 -504.8 ± 1.066
p1 0.1689 ± 0.0001939
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

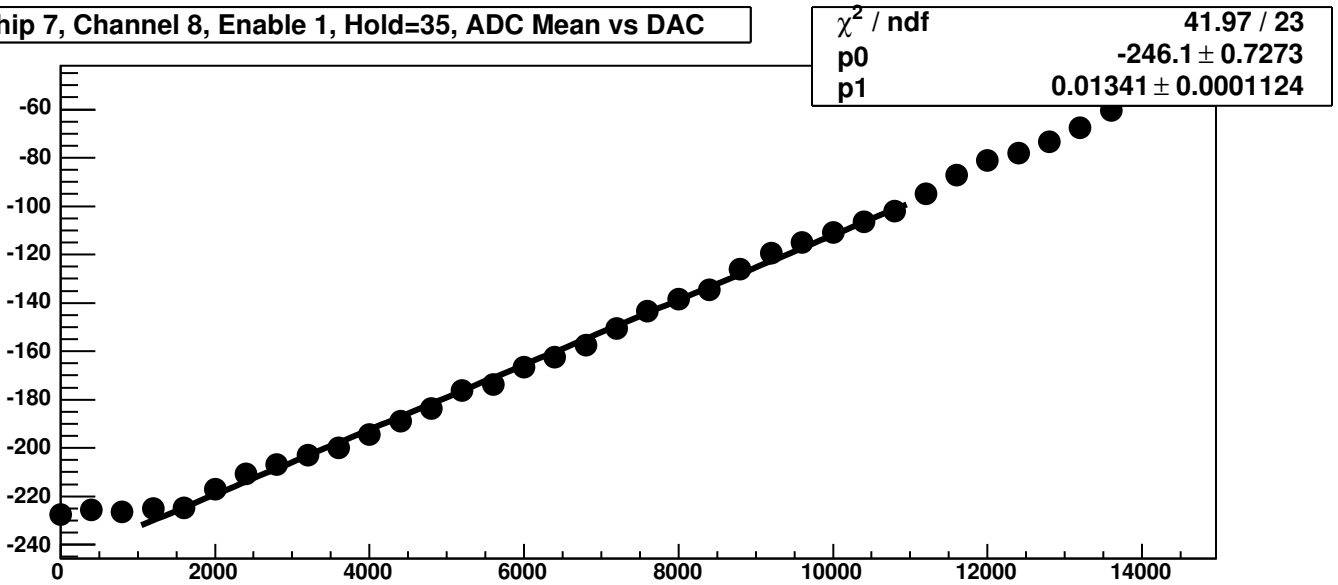

Chip 7, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.442e+05 / 23
p0 -743.3 ± 1.005
p1 0.09617 ± 0.0001935
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

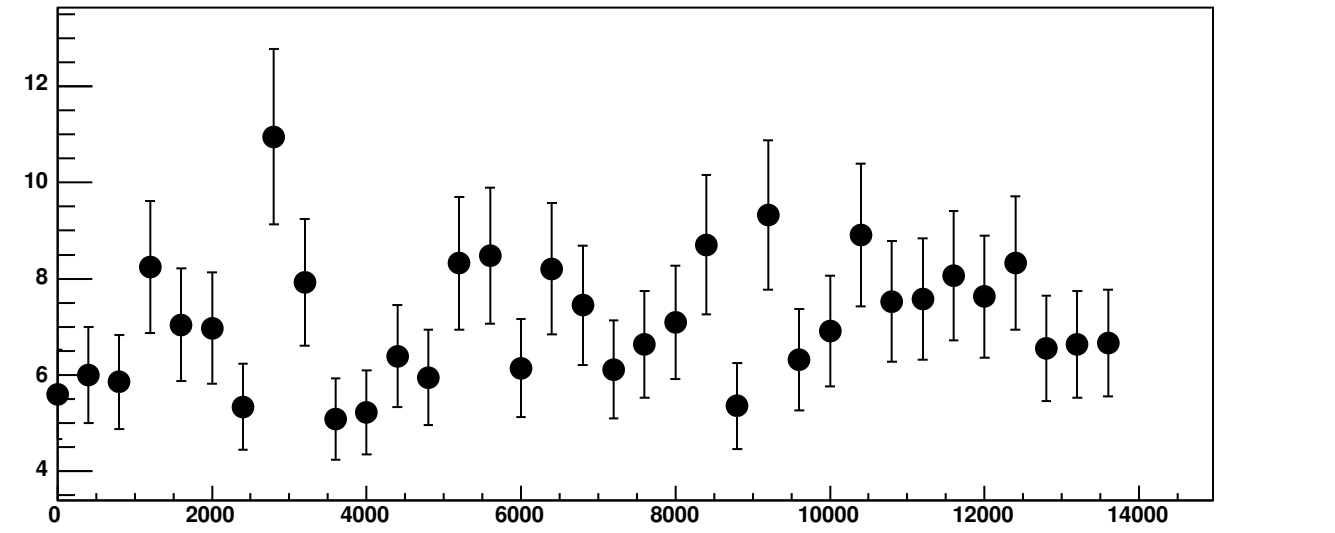

Chip 7, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

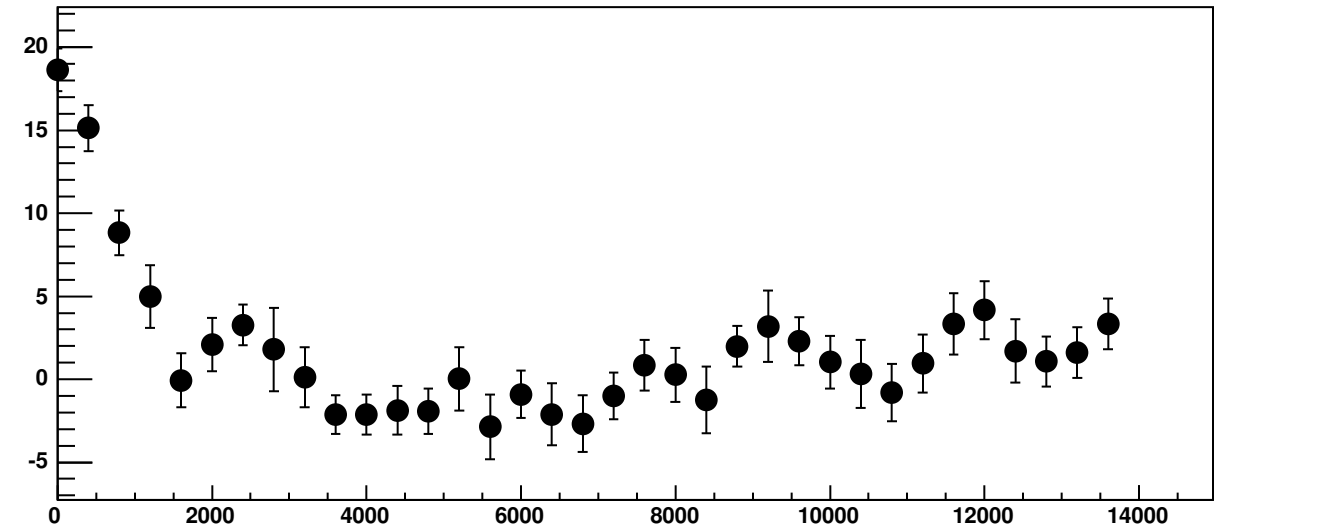
Chip 7, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



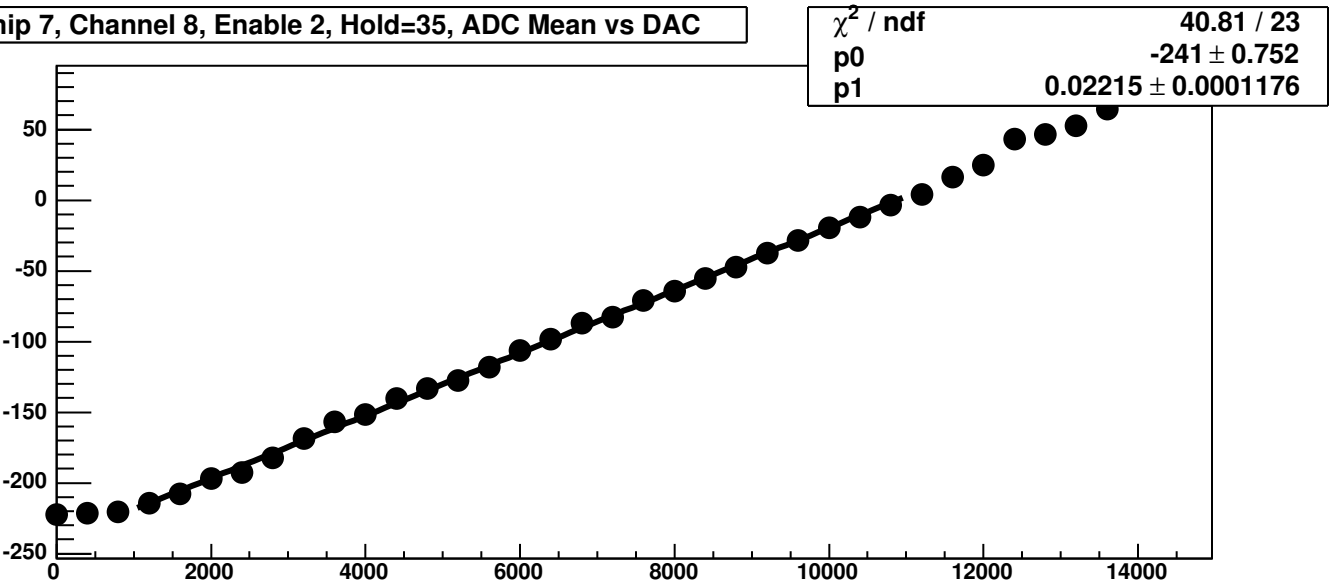
Chip 7, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



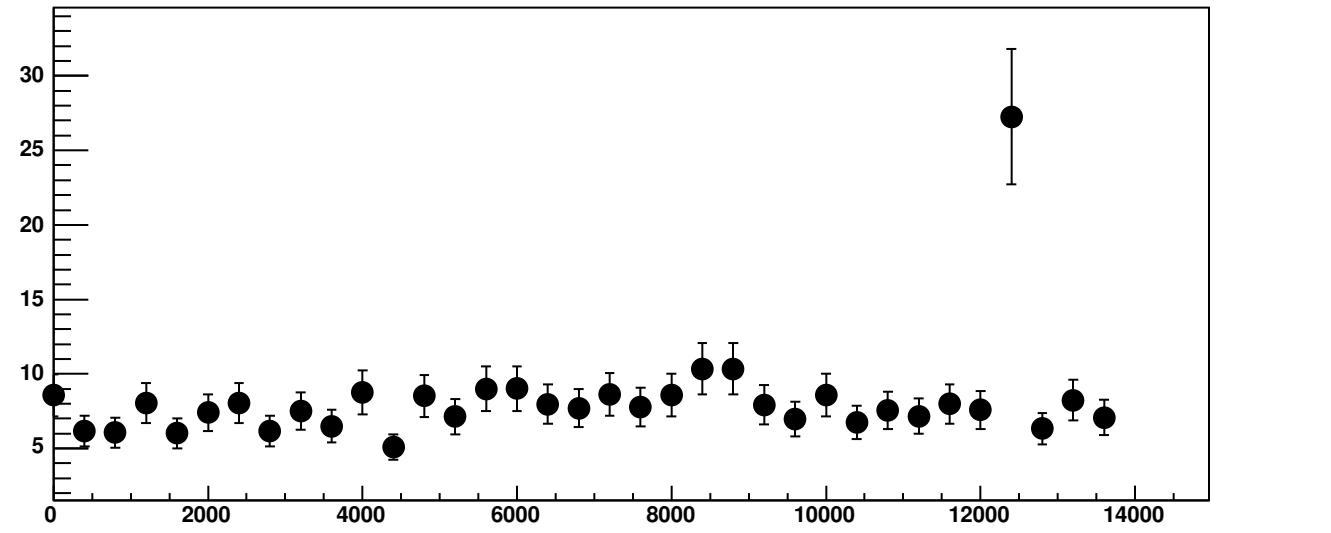
Chip 7, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



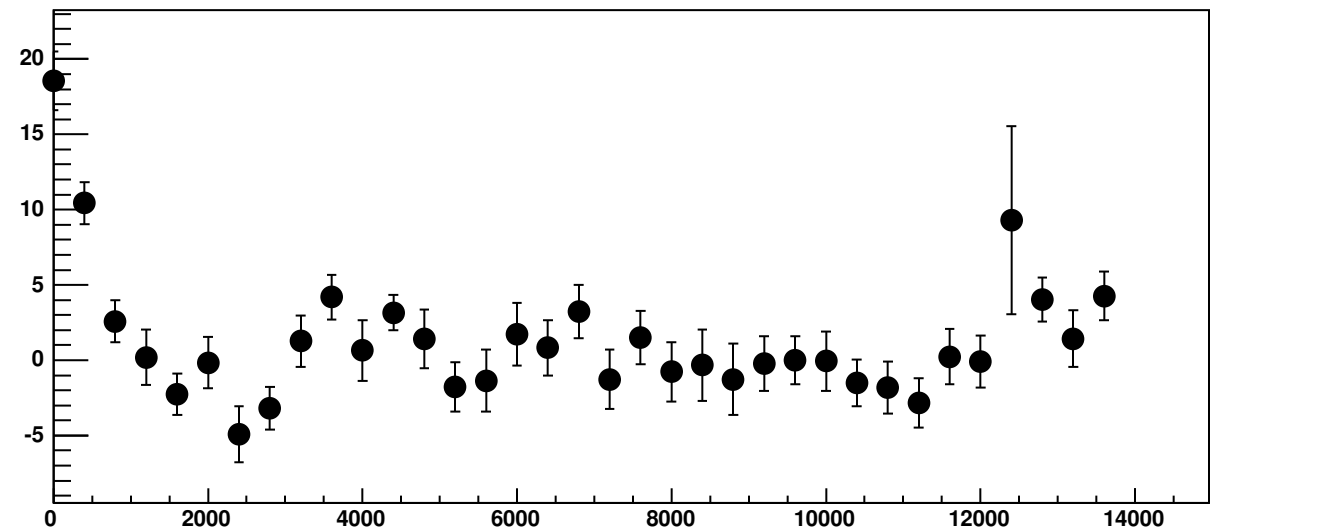
Chip 7, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC



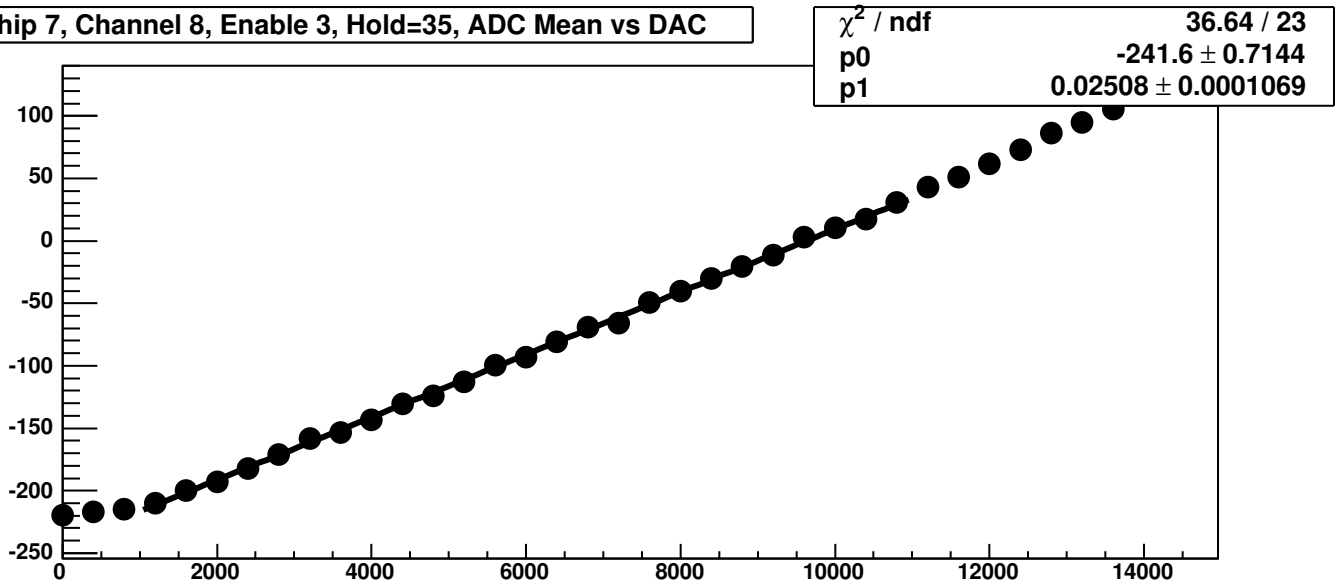
Chip 7, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC



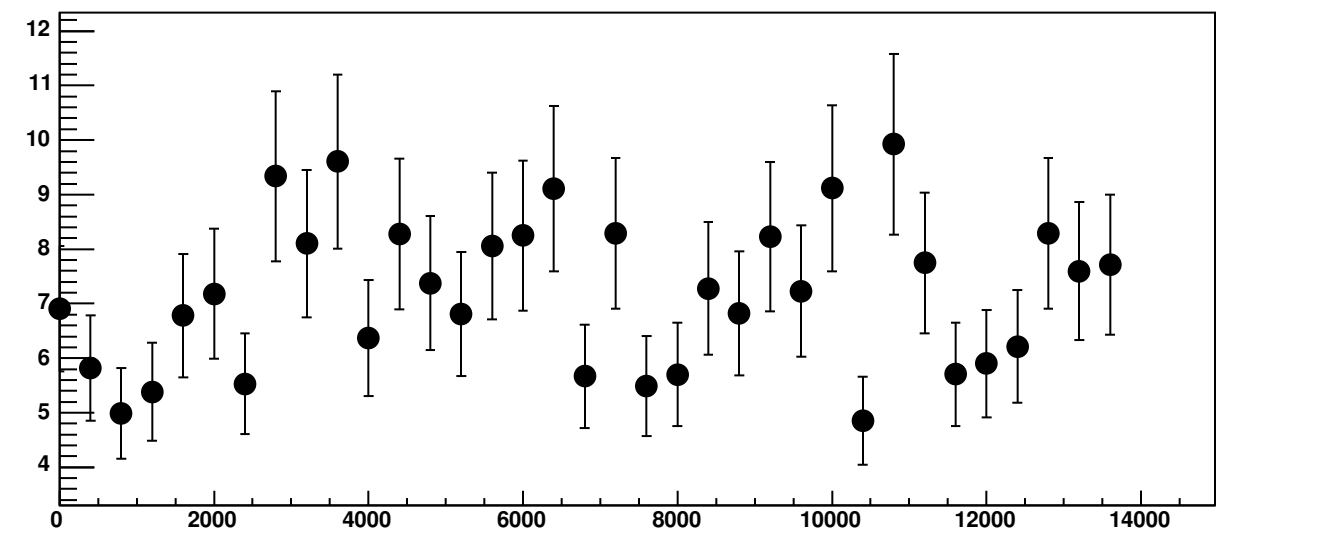
Chip 7, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC



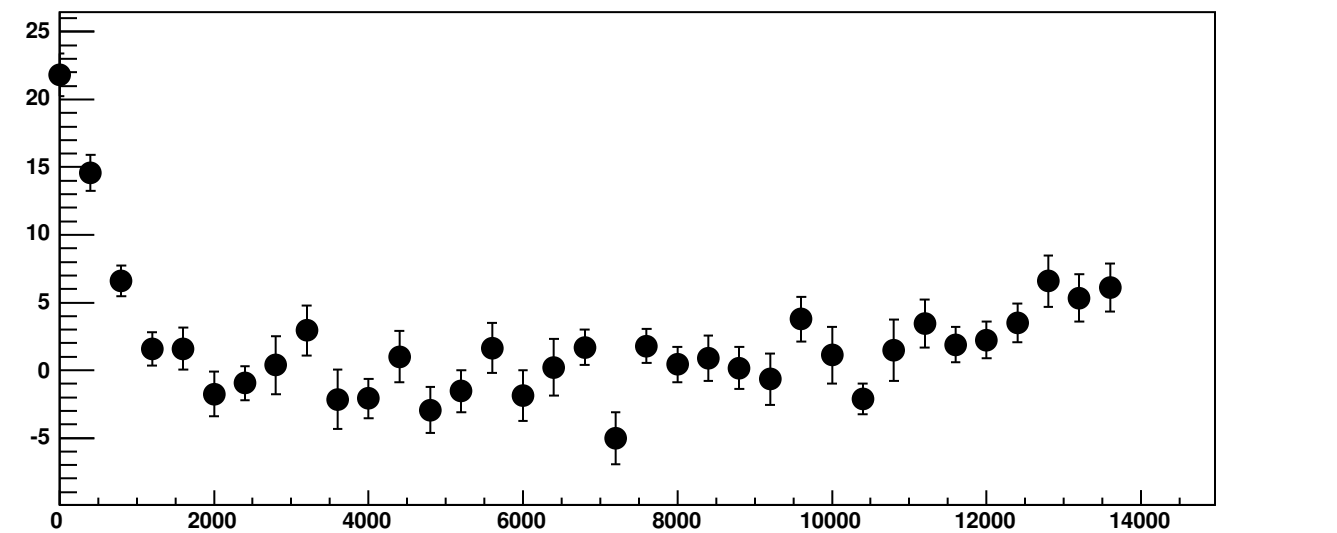
Chip 7, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



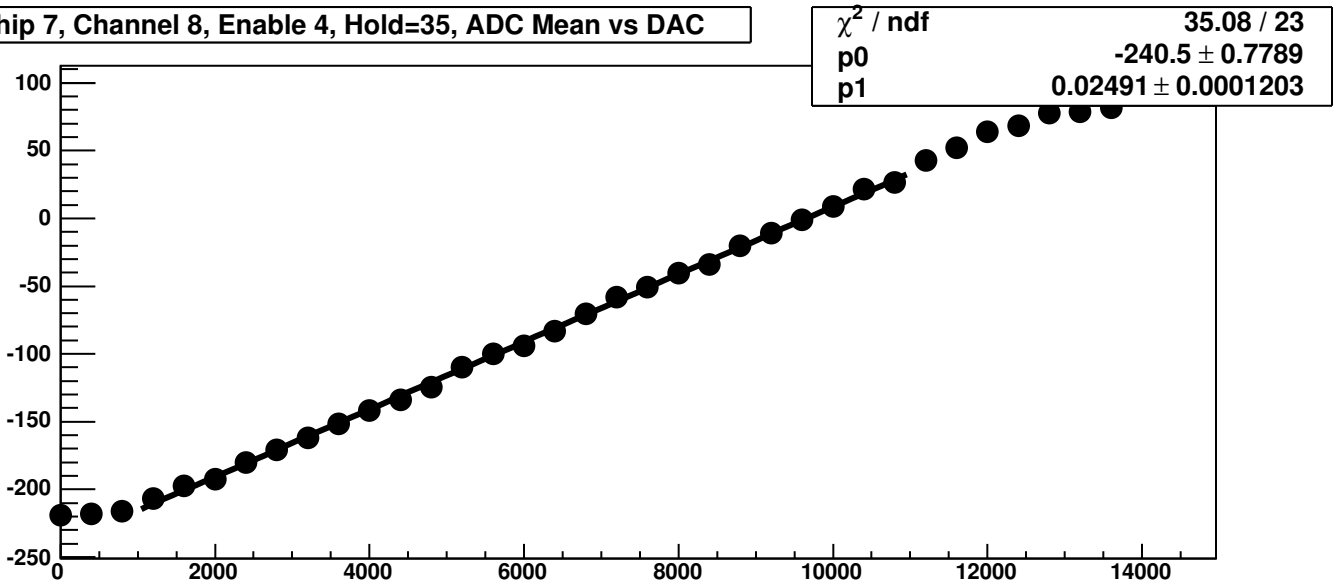
Chip 7, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



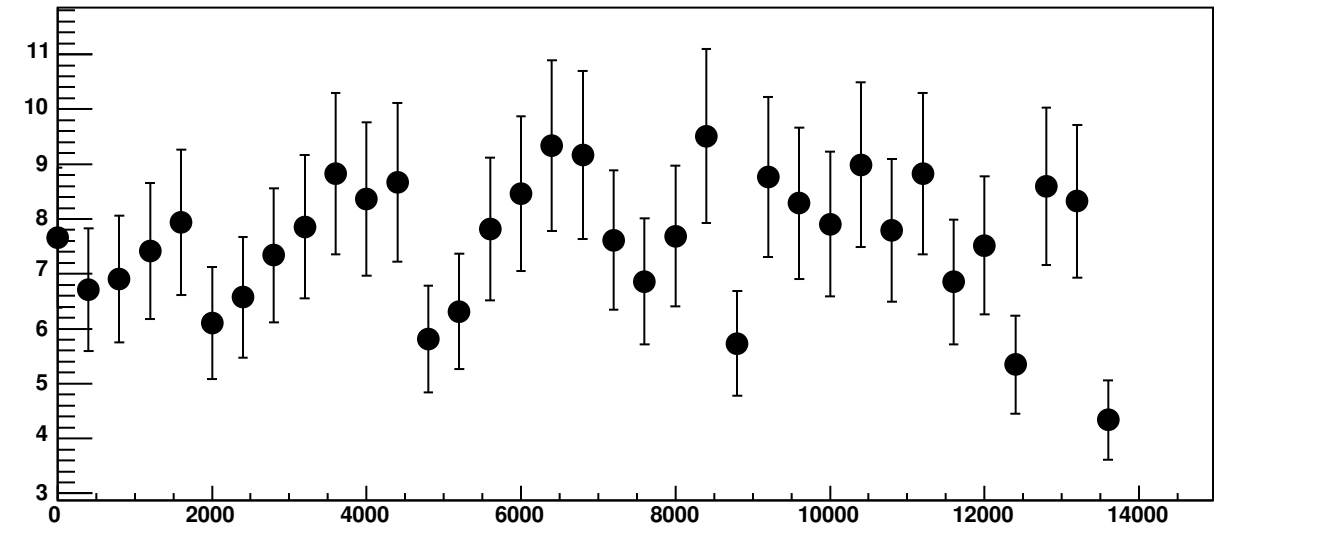
Chip 7, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



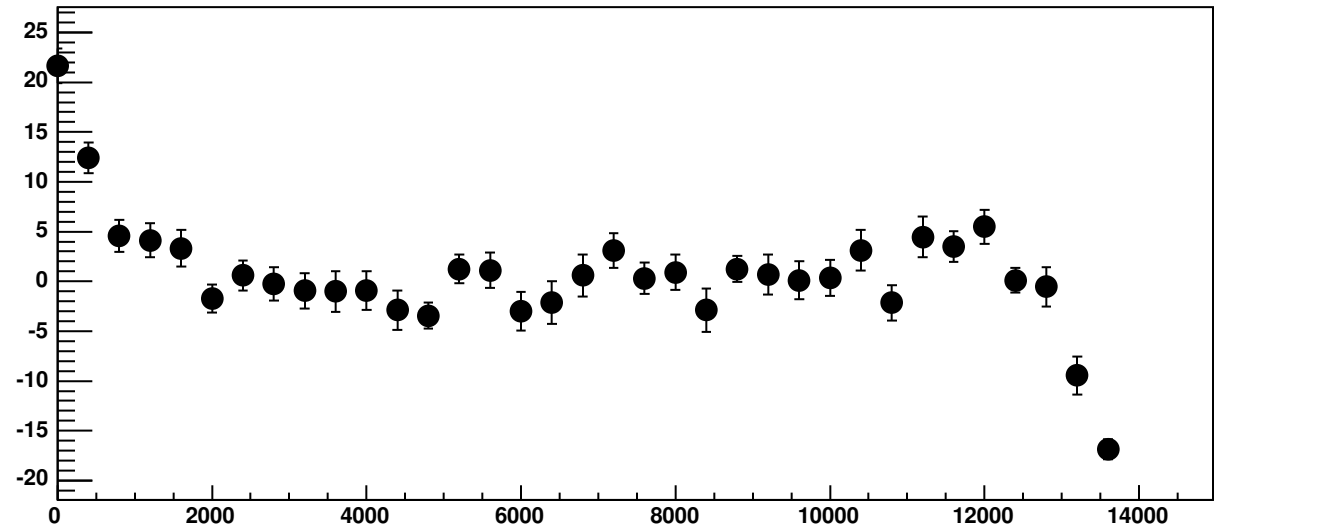
Chip 7, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

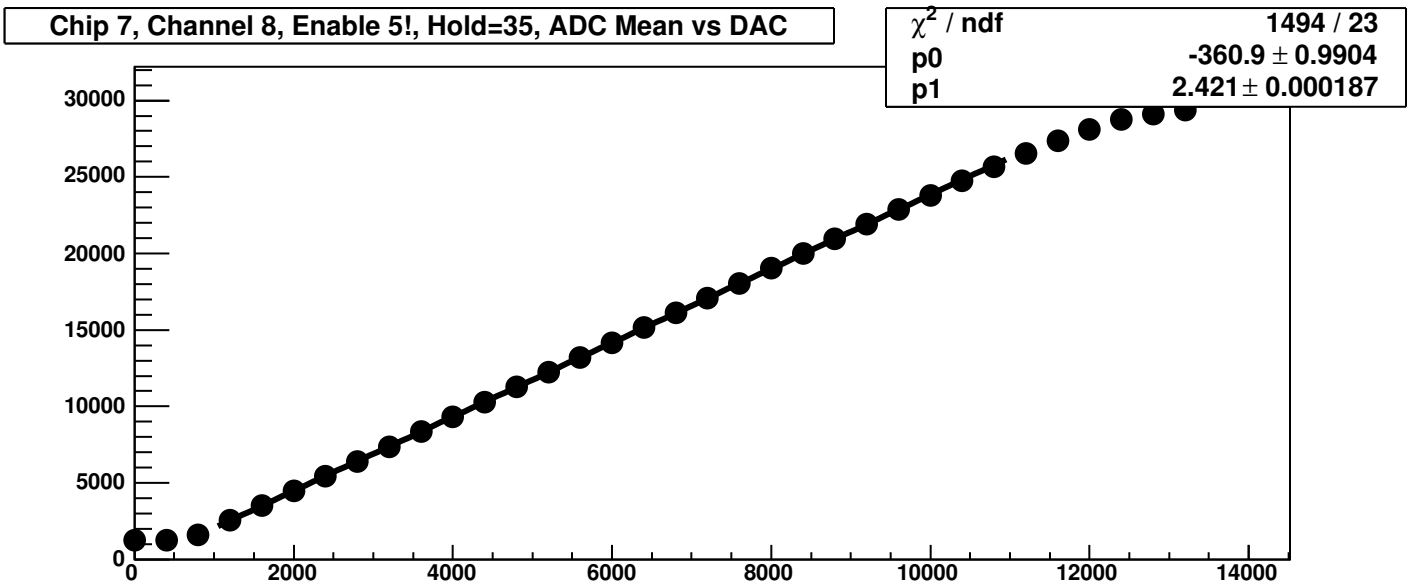


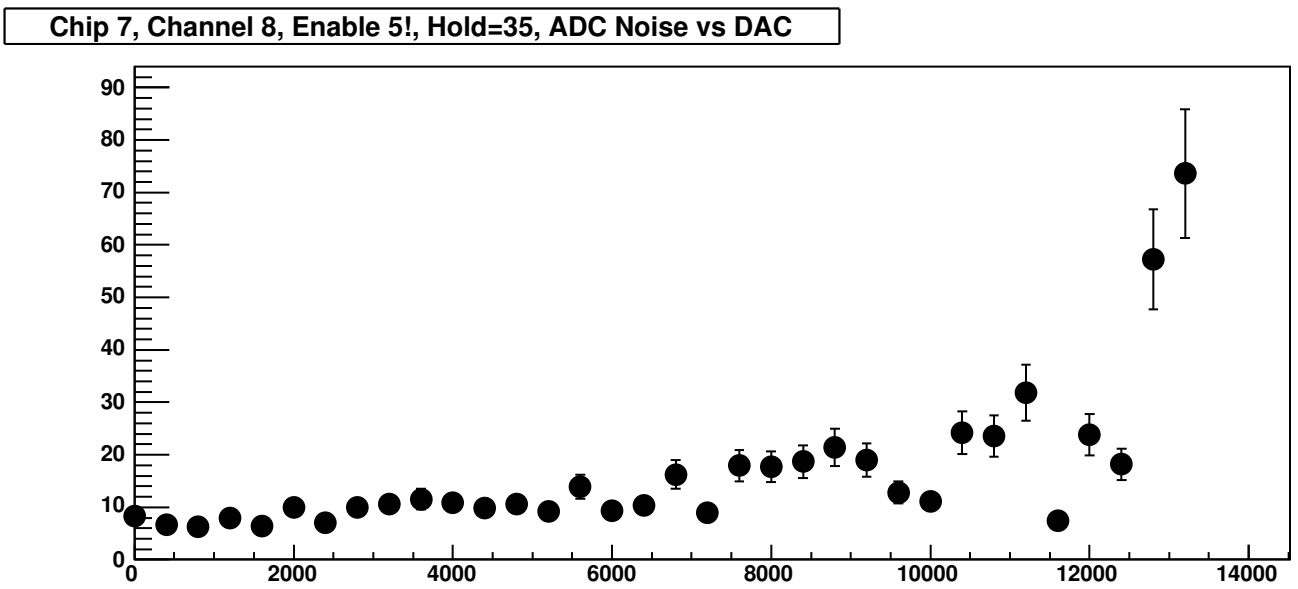
Chip 7, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

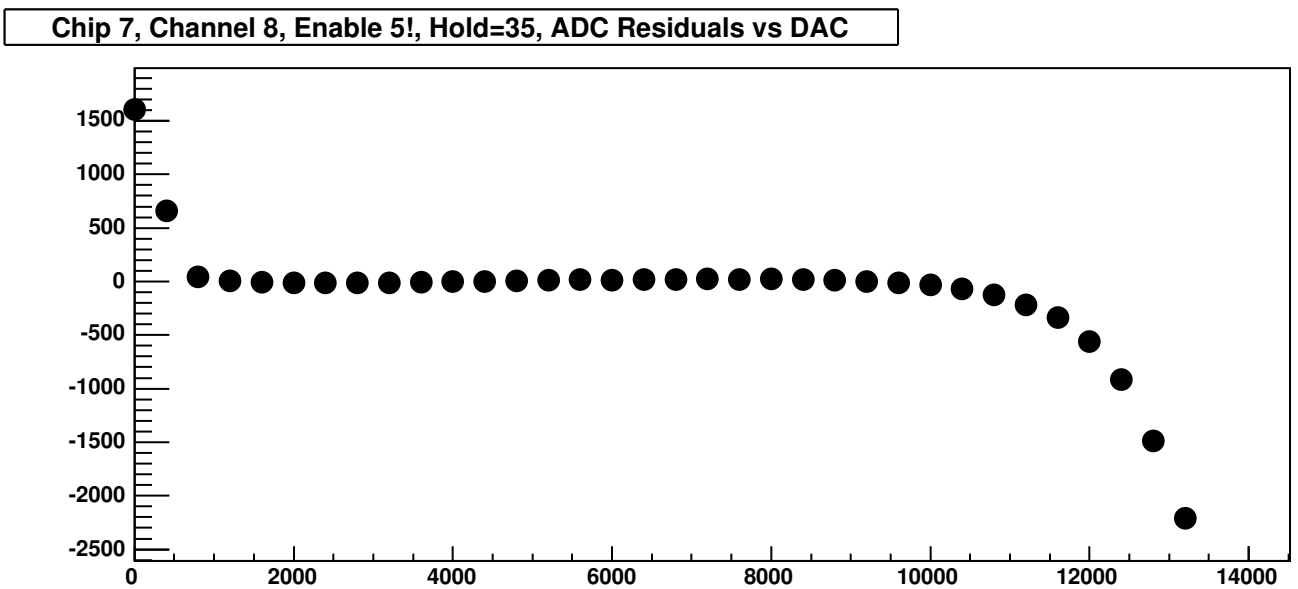


Chip 7, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

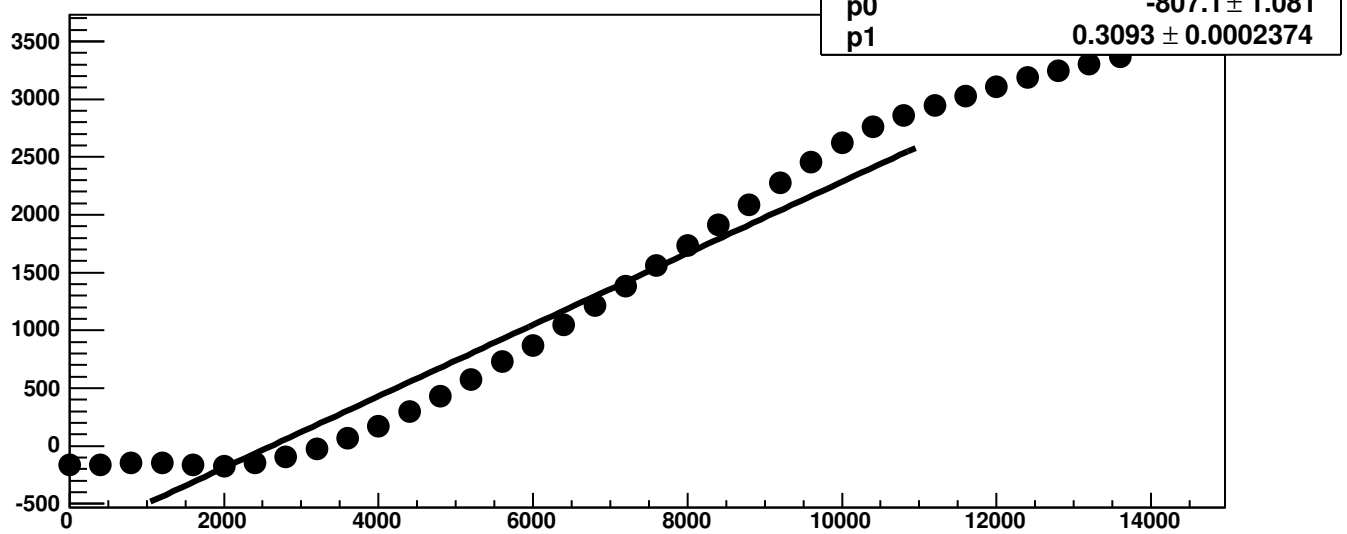



Chip 7, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1494 / 23
p0 -360.9 ± 0.9904
p1 2.421 ± 0.000187
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

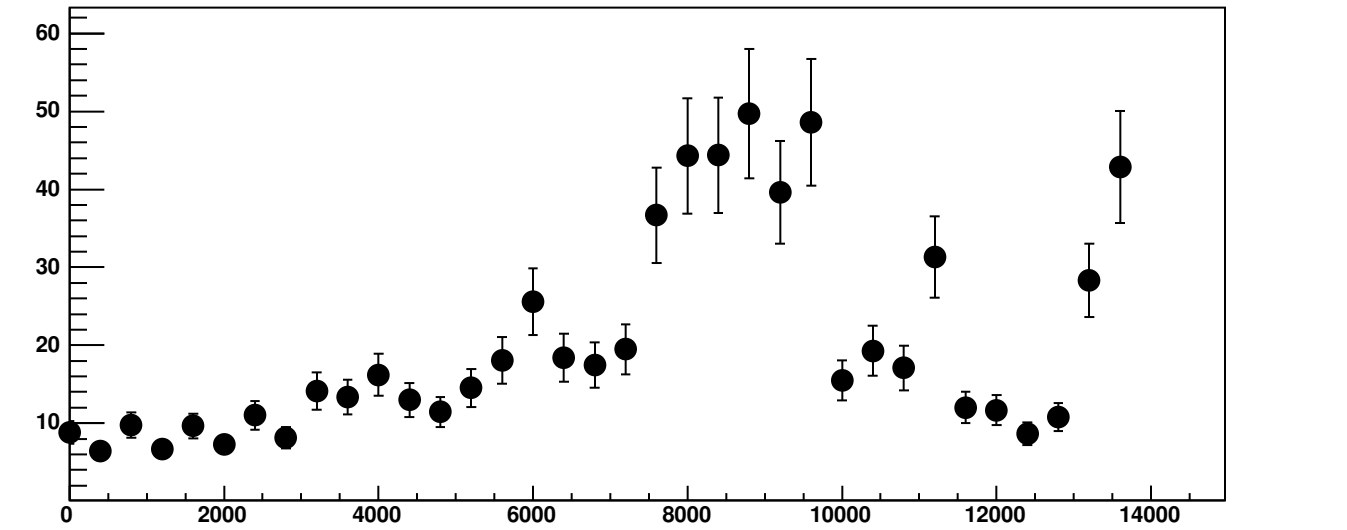

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

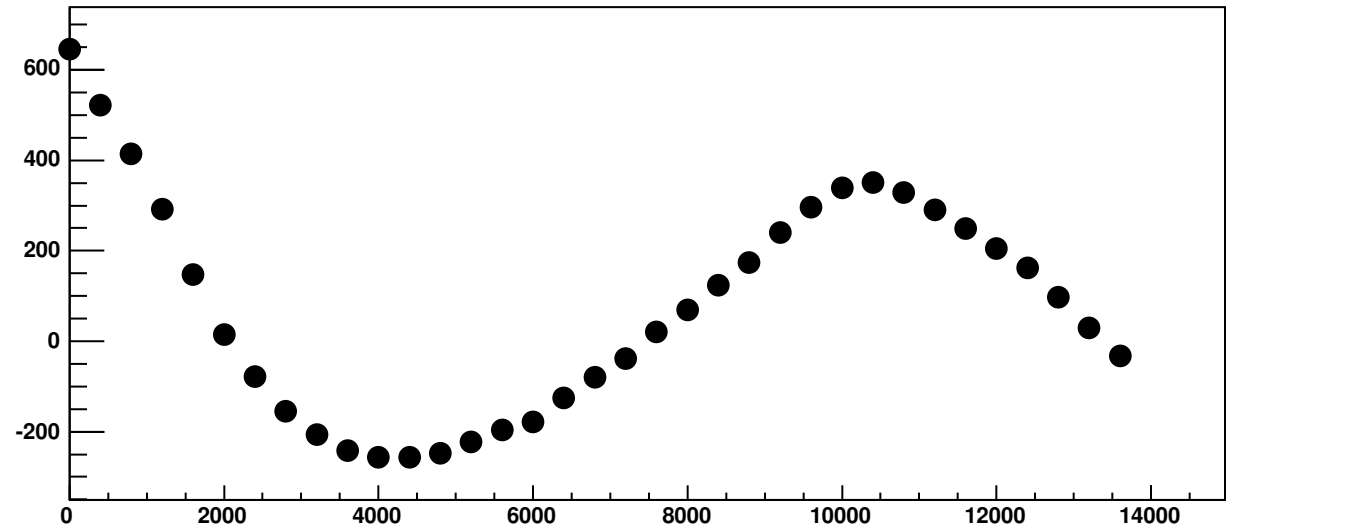
Chip 7, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

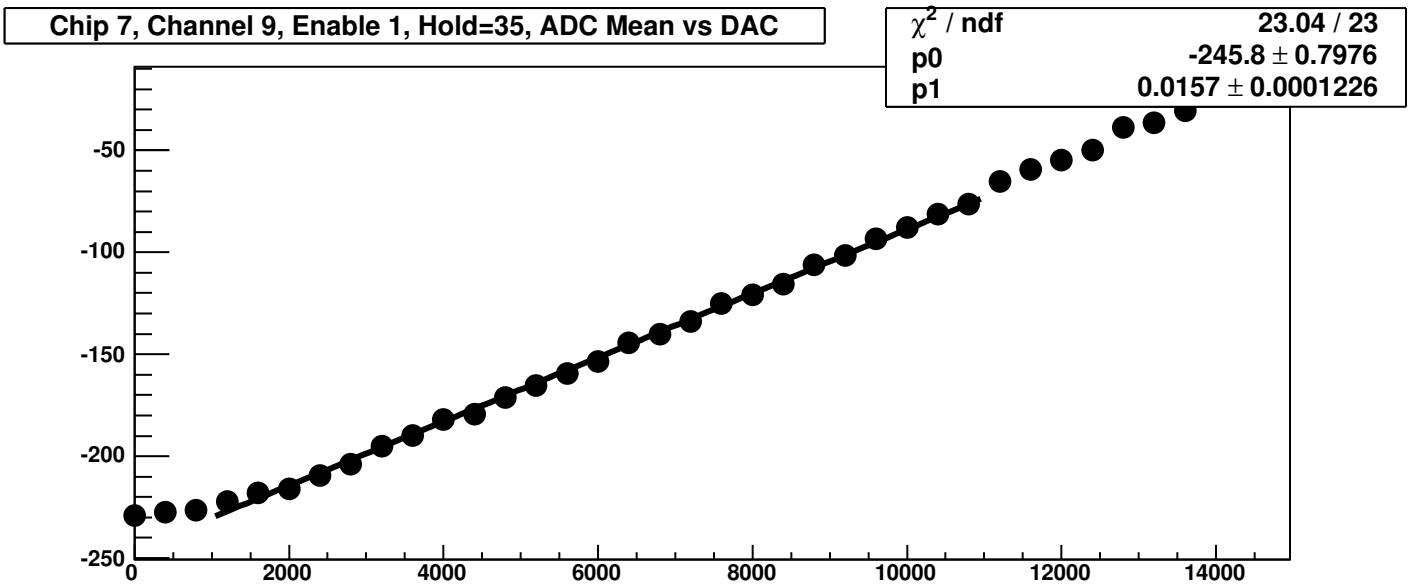


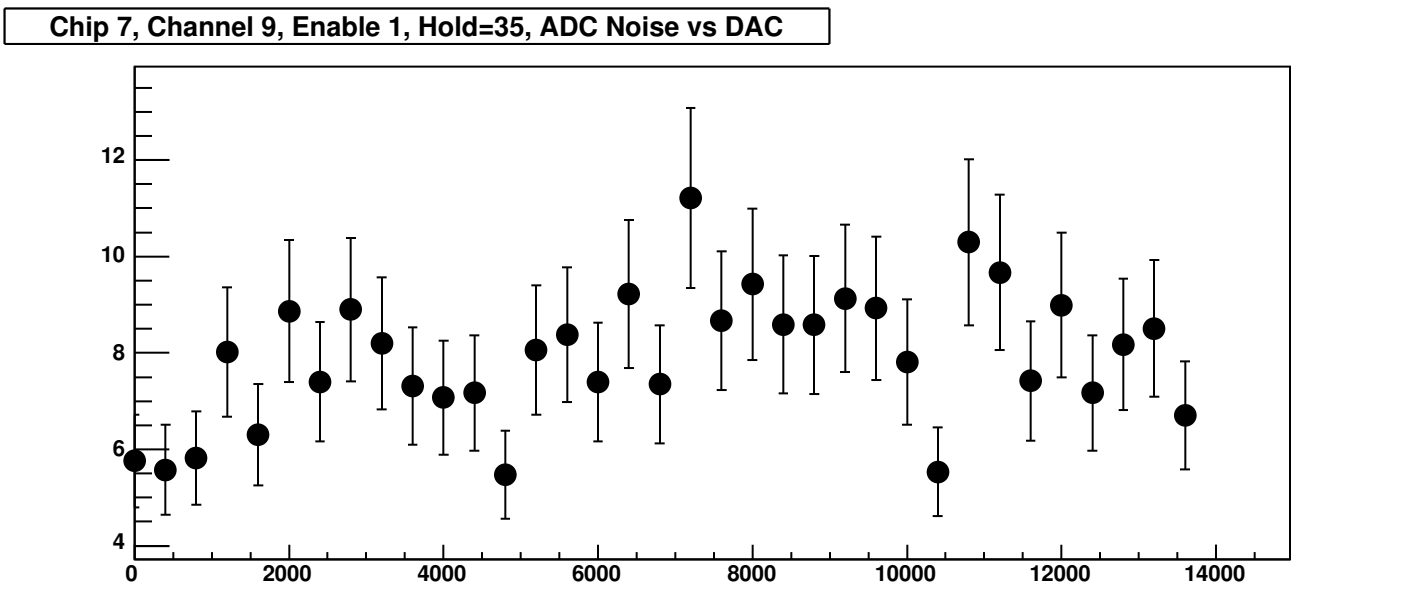
Chip 7, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC

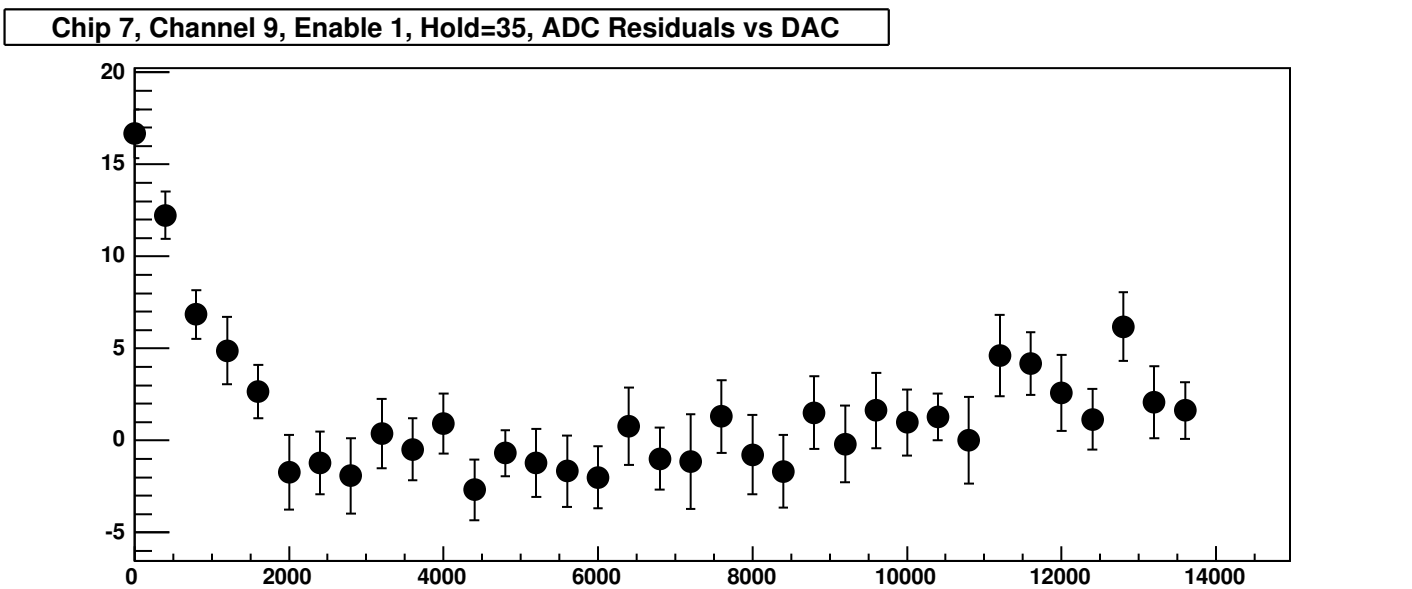


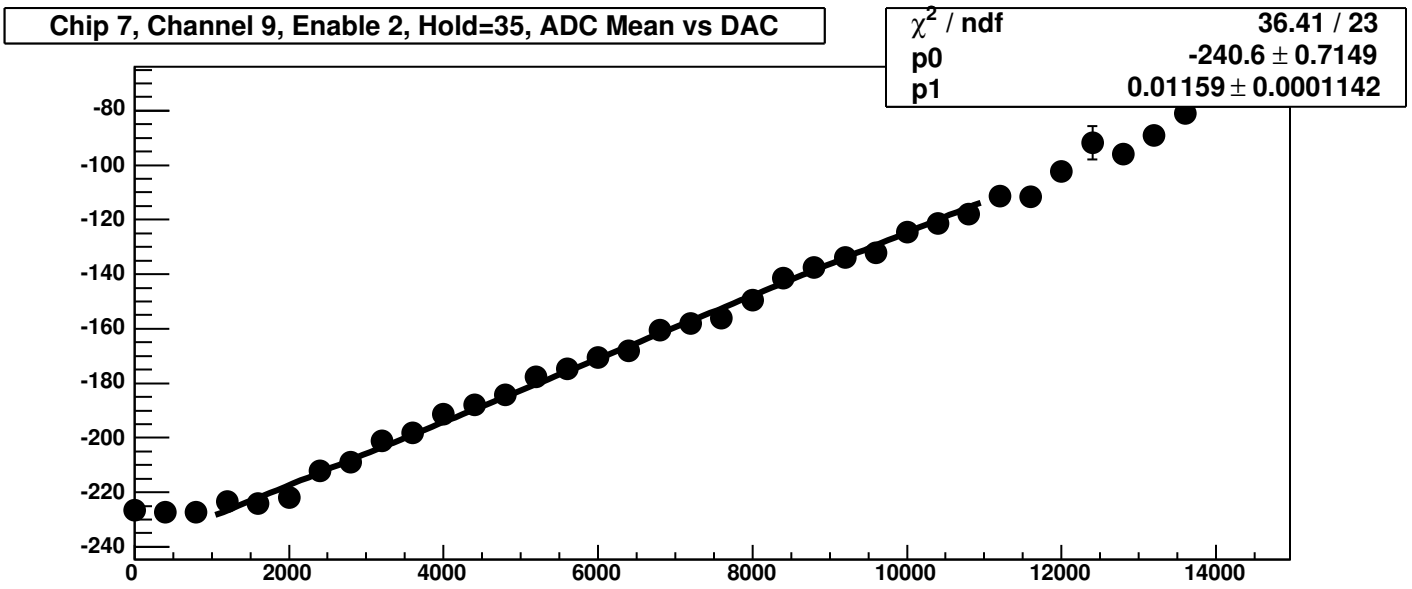
Chip 7, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

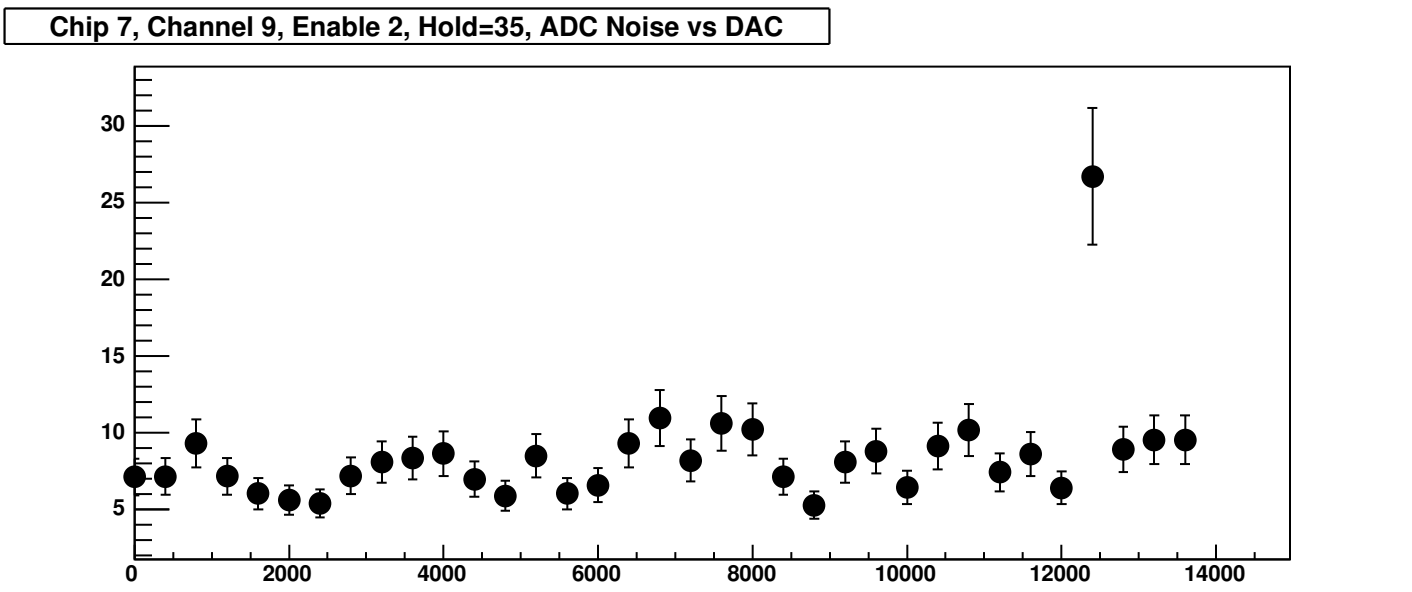


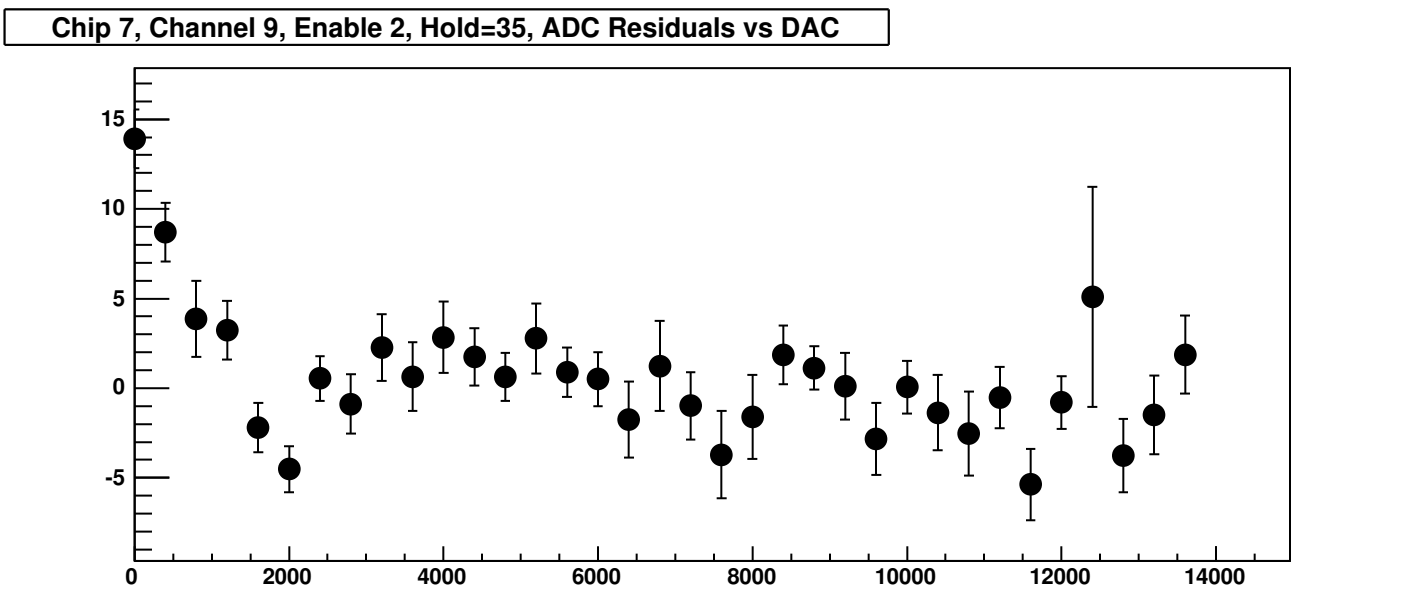

Chip 7, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.04 / 23
p0 -245.8 ± 0.7976
p1 0.0157 ± 0.0001226
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

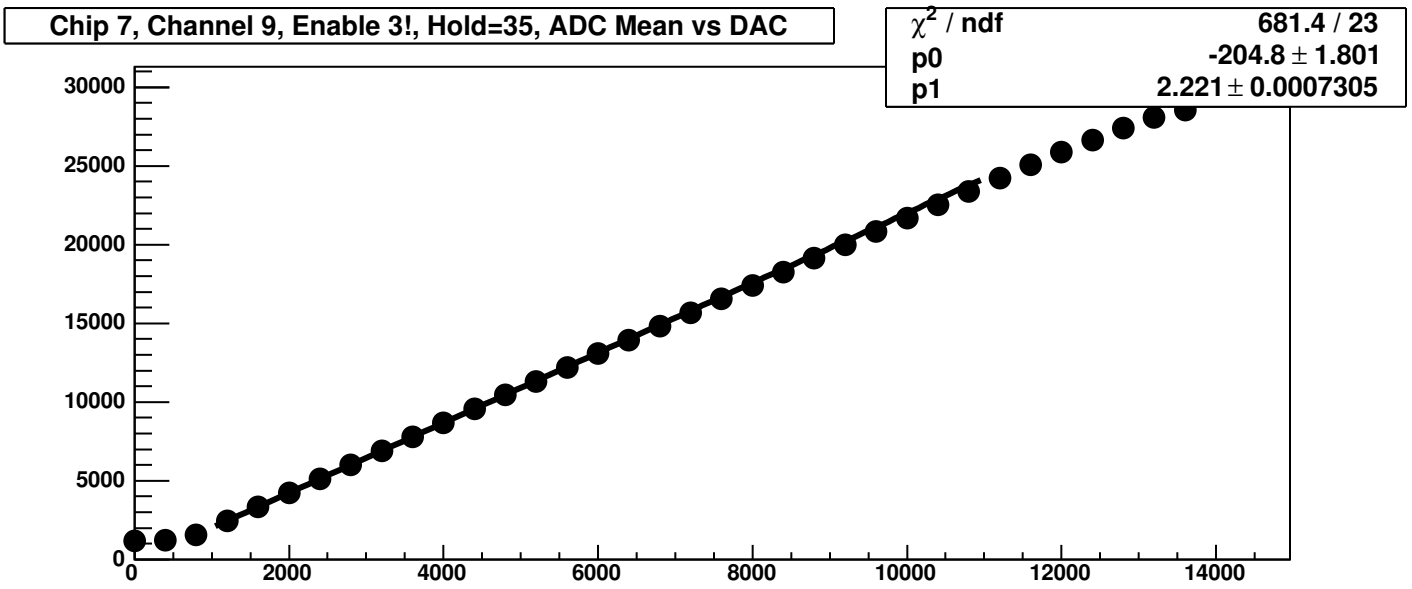

Chip 7, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

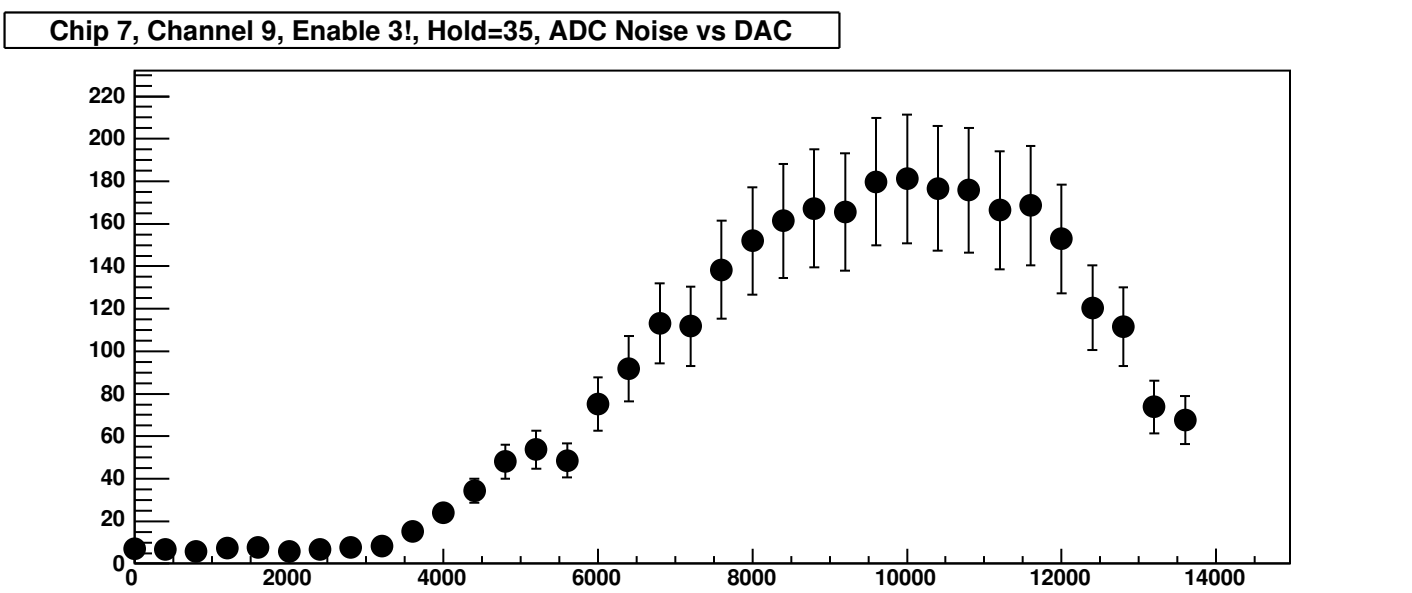

Chip 7, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

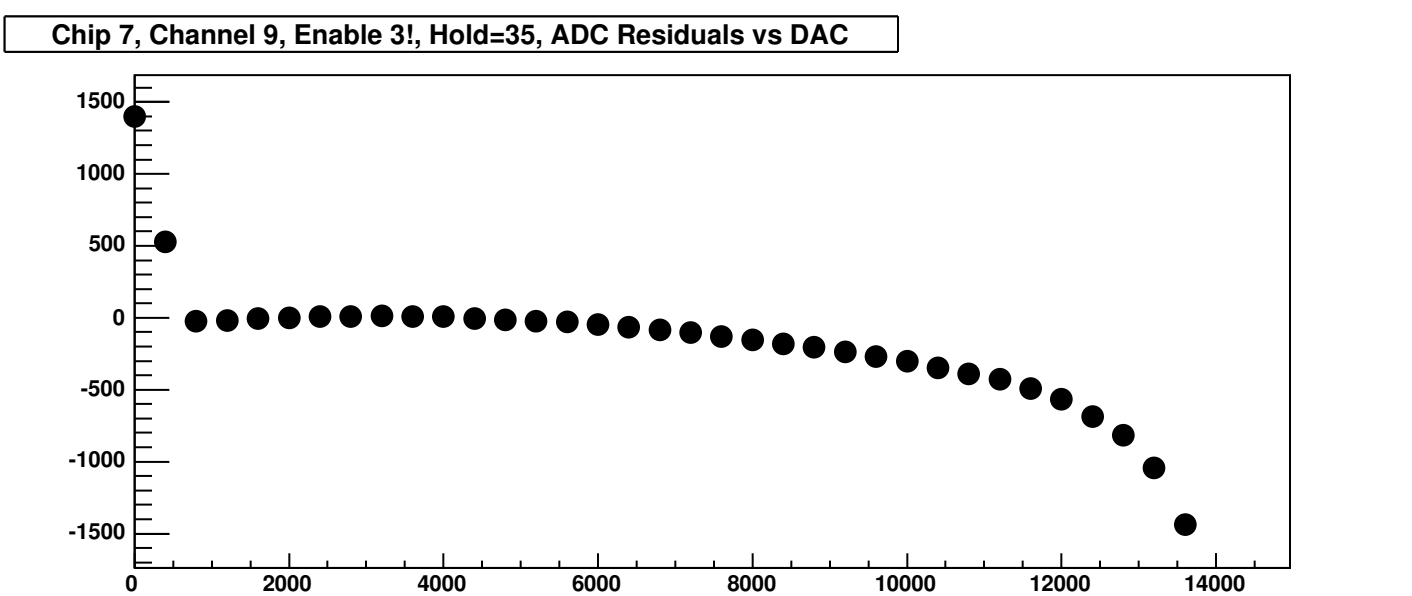

Chip 7, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.41 / 23
p0 -240.6 ± 0.7149
p1 0.01159 ± 0.0001142
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
2000
4000
6000
8000
10000
12000
14000

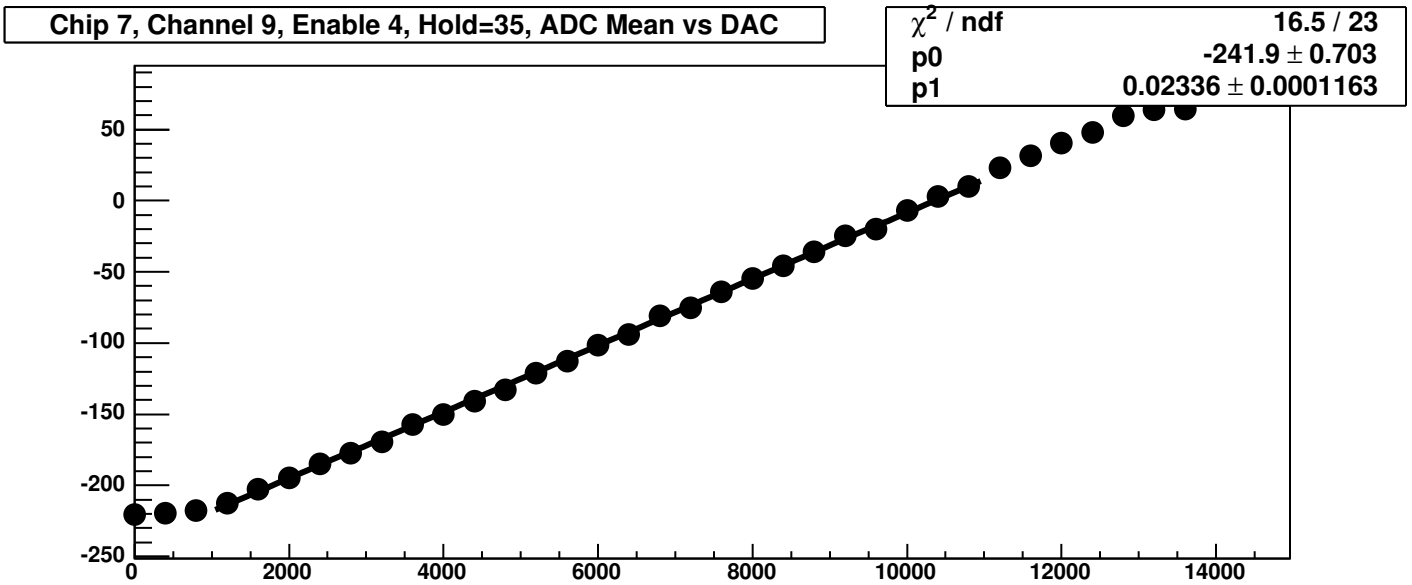

Chip 7, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

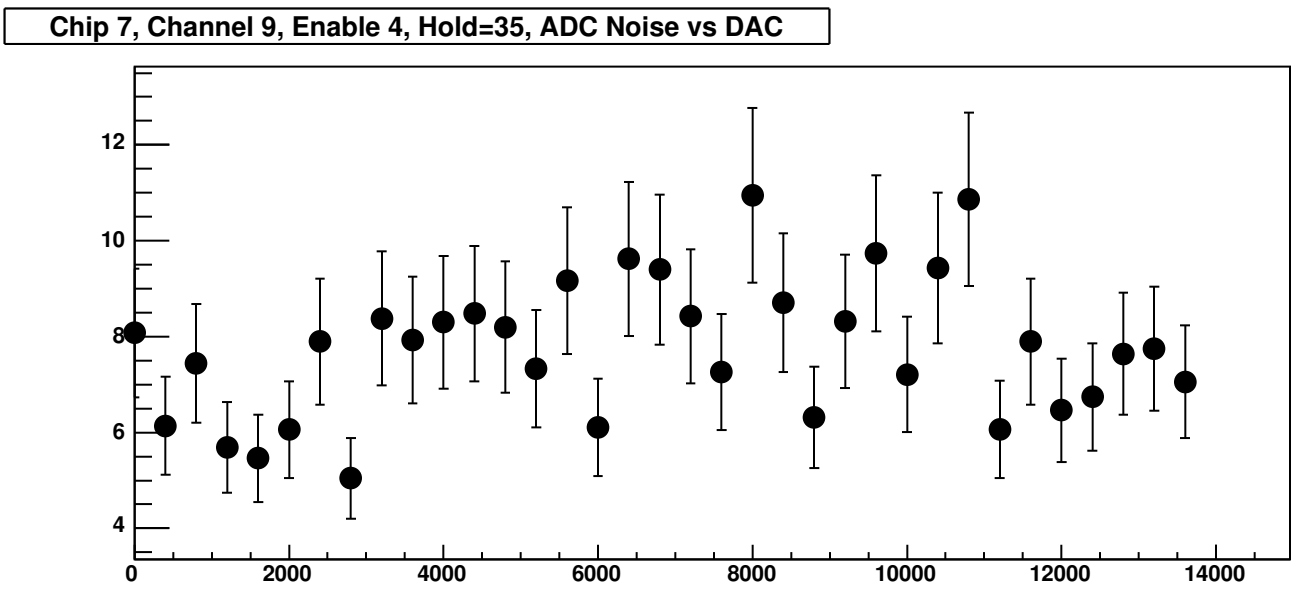

Chip 7, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

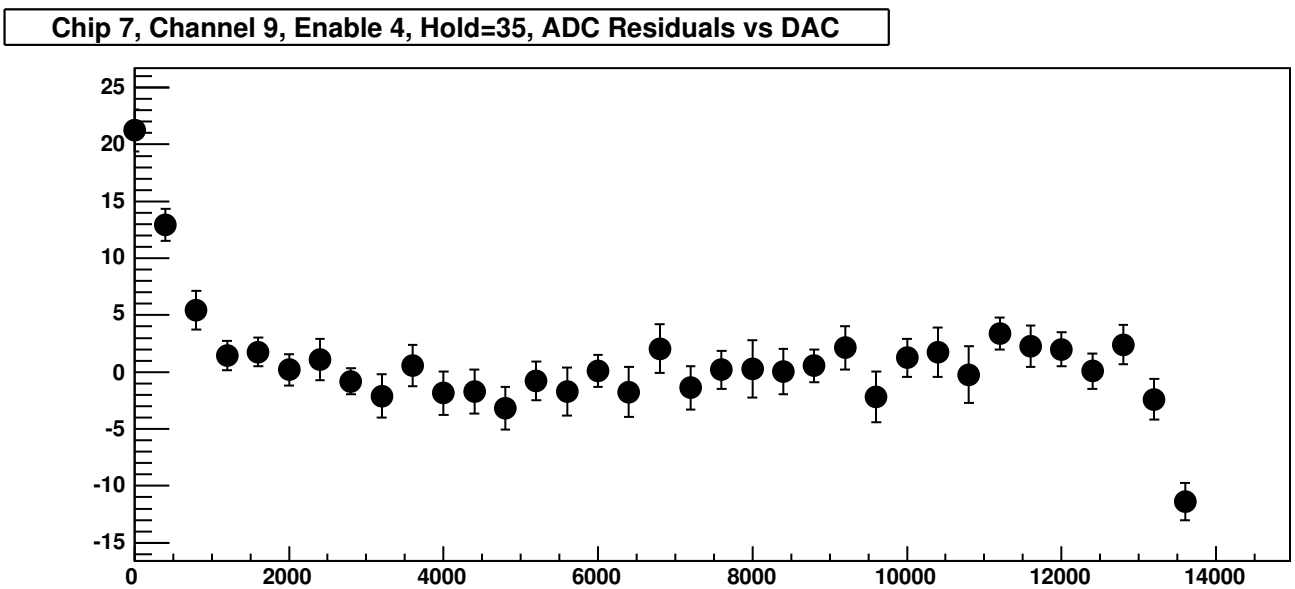

Chip 7, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 681.4 / 23
p0 -204.8 ± 1.801
p1 2.221 ± 0.0007305
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

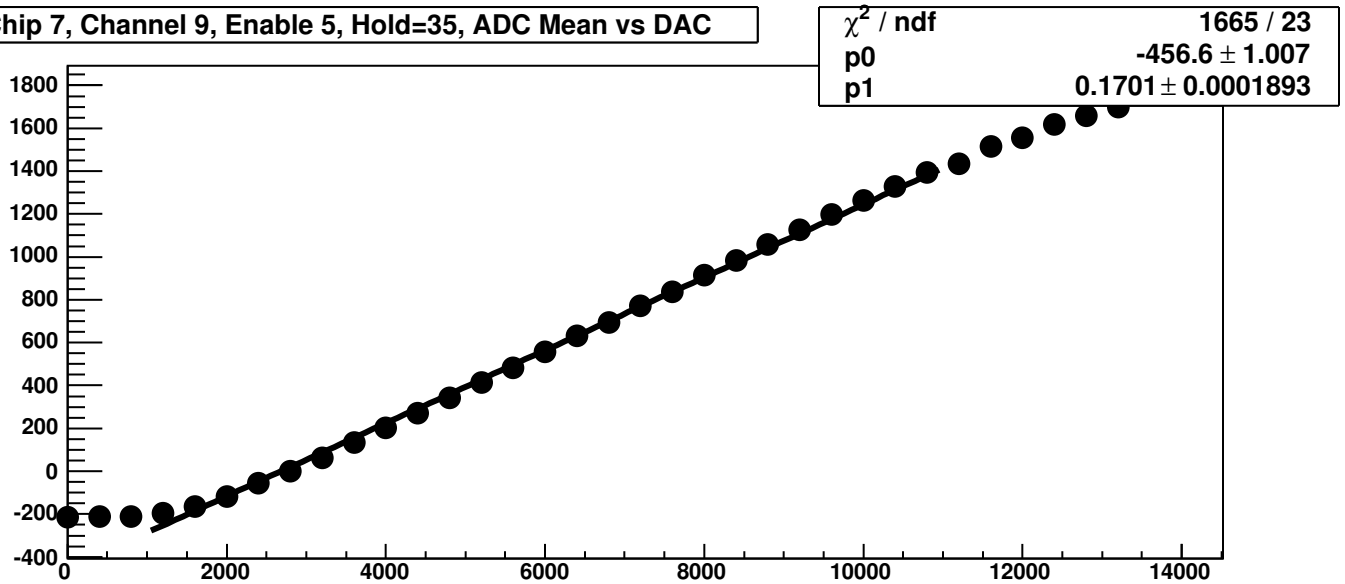

Chip 7, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.5 / 23
p0 -241.9 ± 0.703
p1 0.02336 ± 0.0001163
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

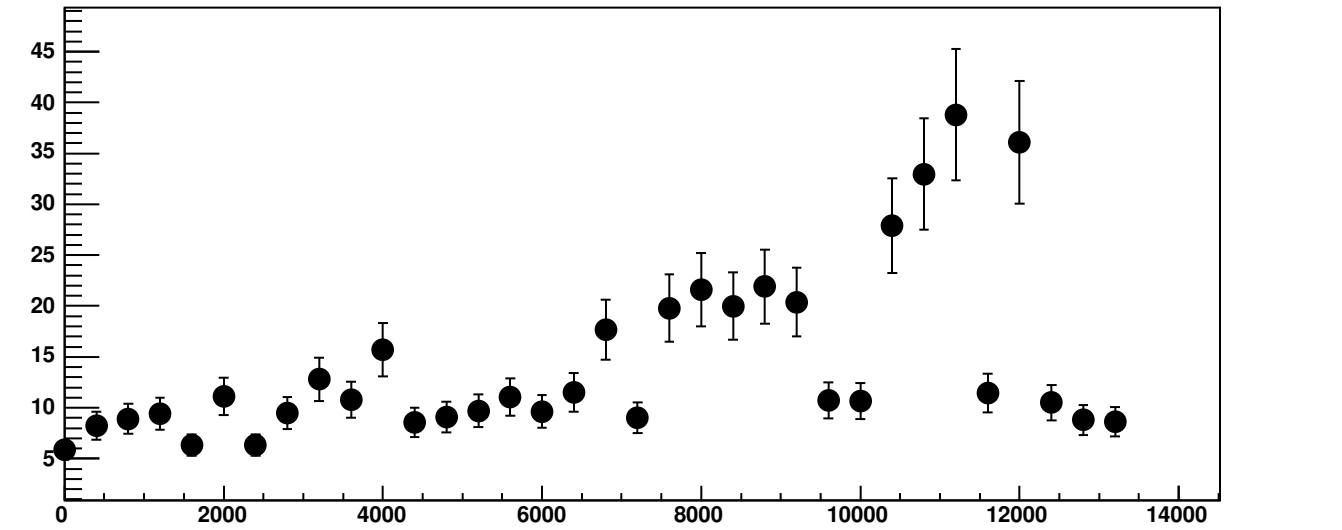

Chip 7, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

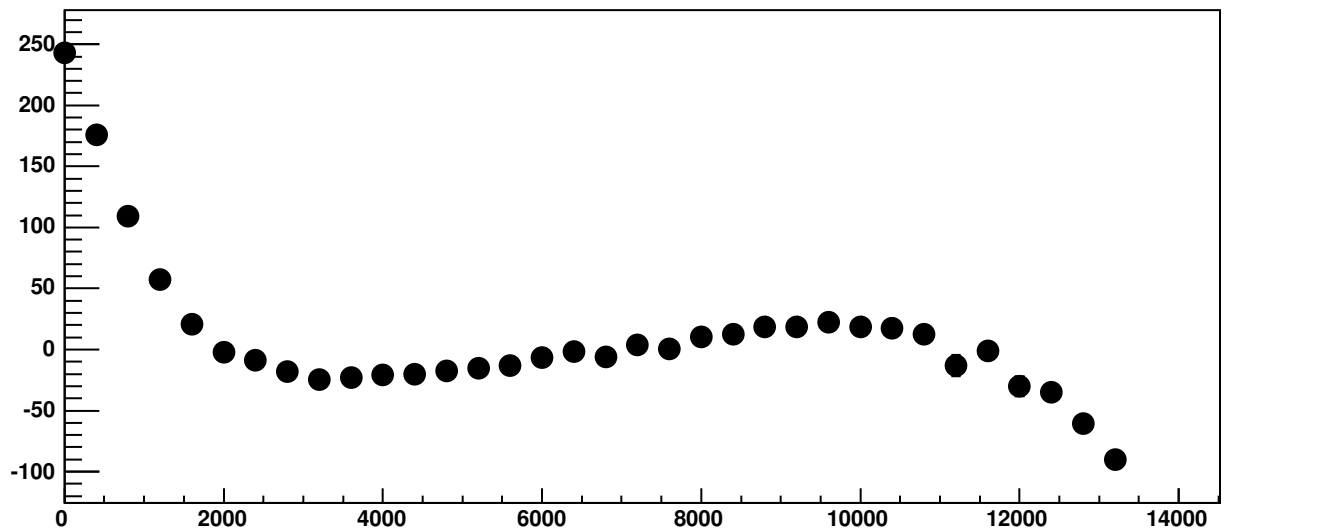
Chip 7, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC



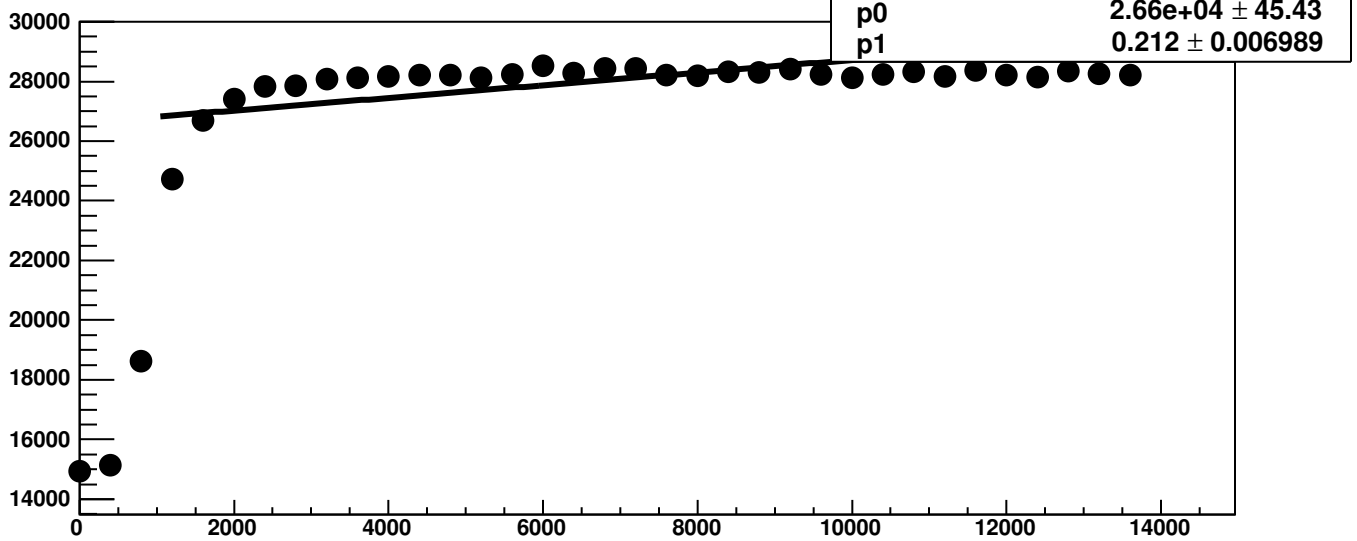
Chip 7, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC



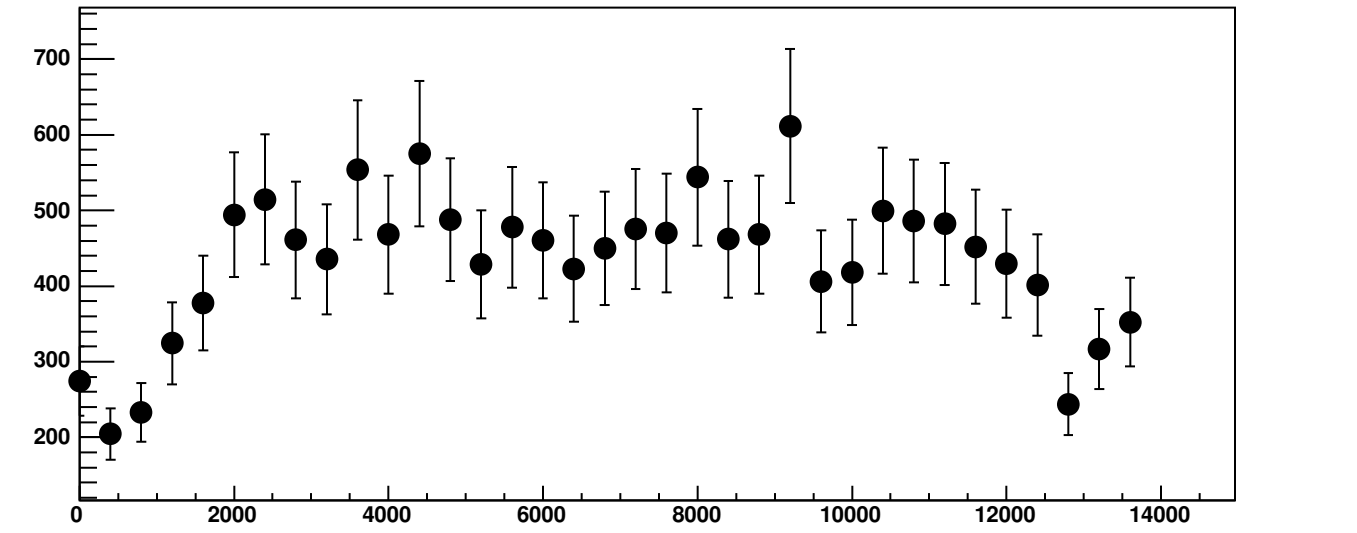
Chip 7, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC



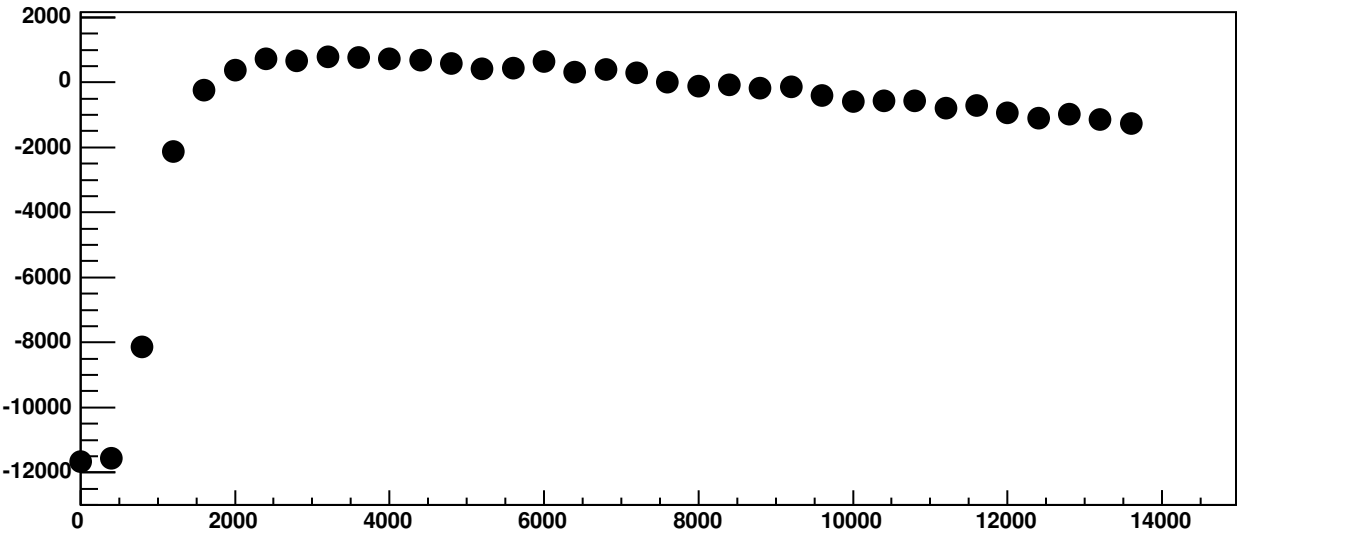
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC

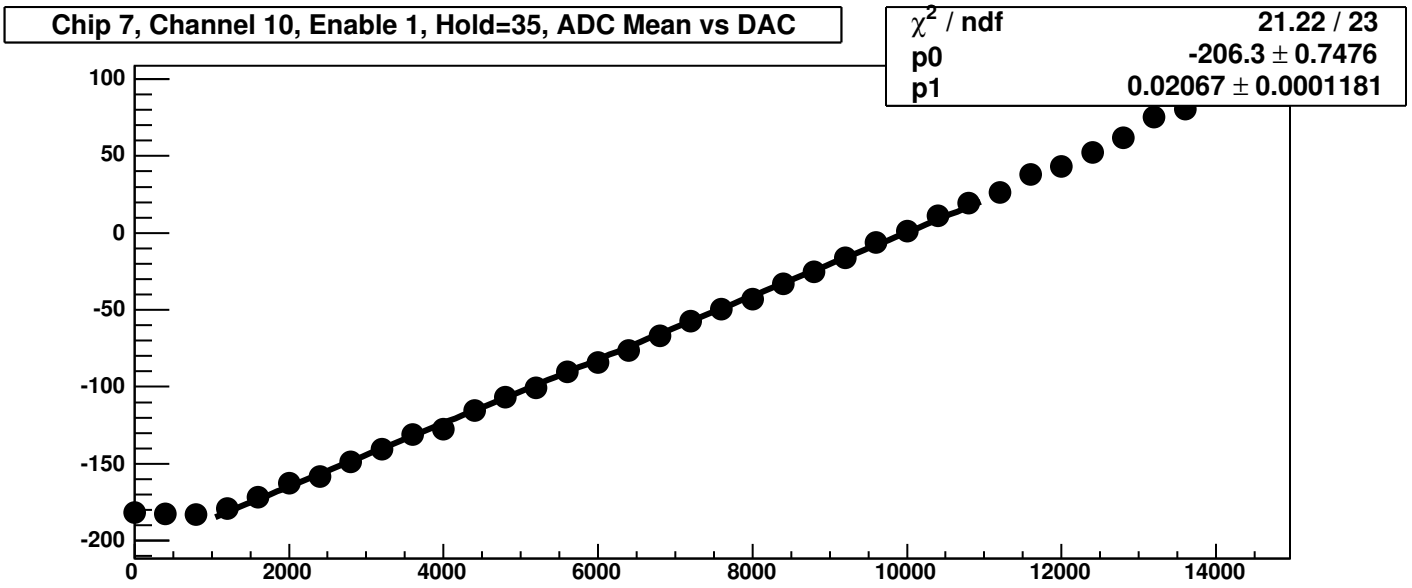


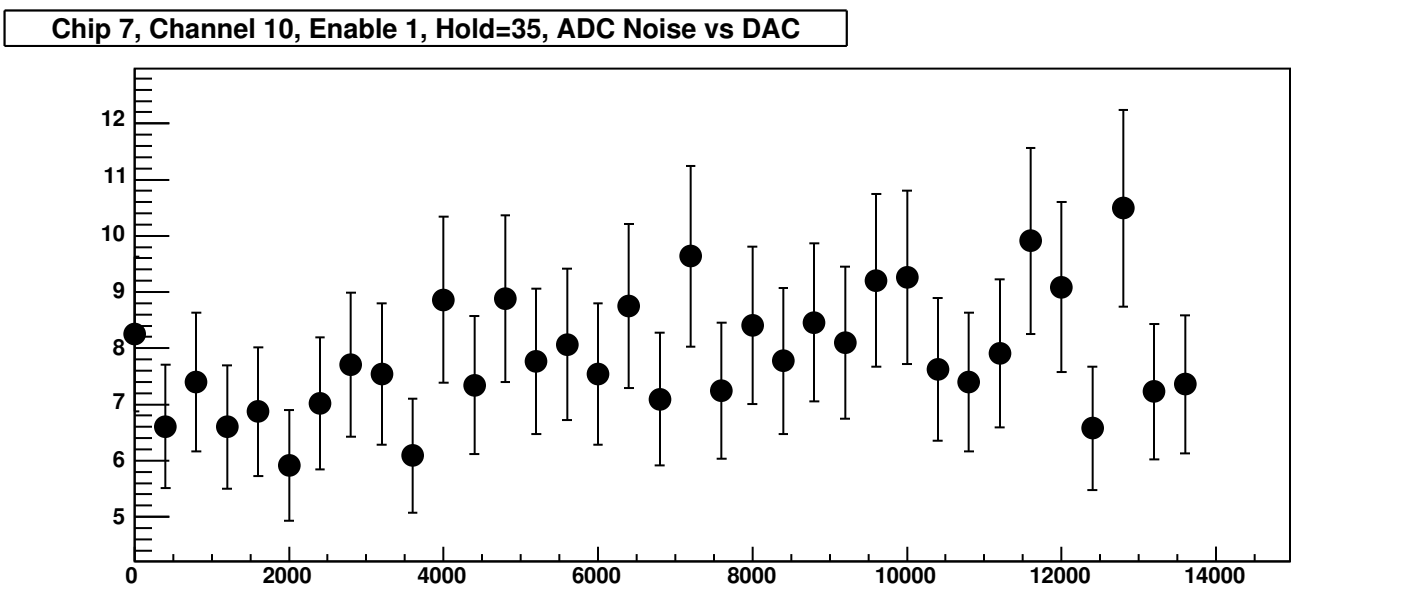
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC

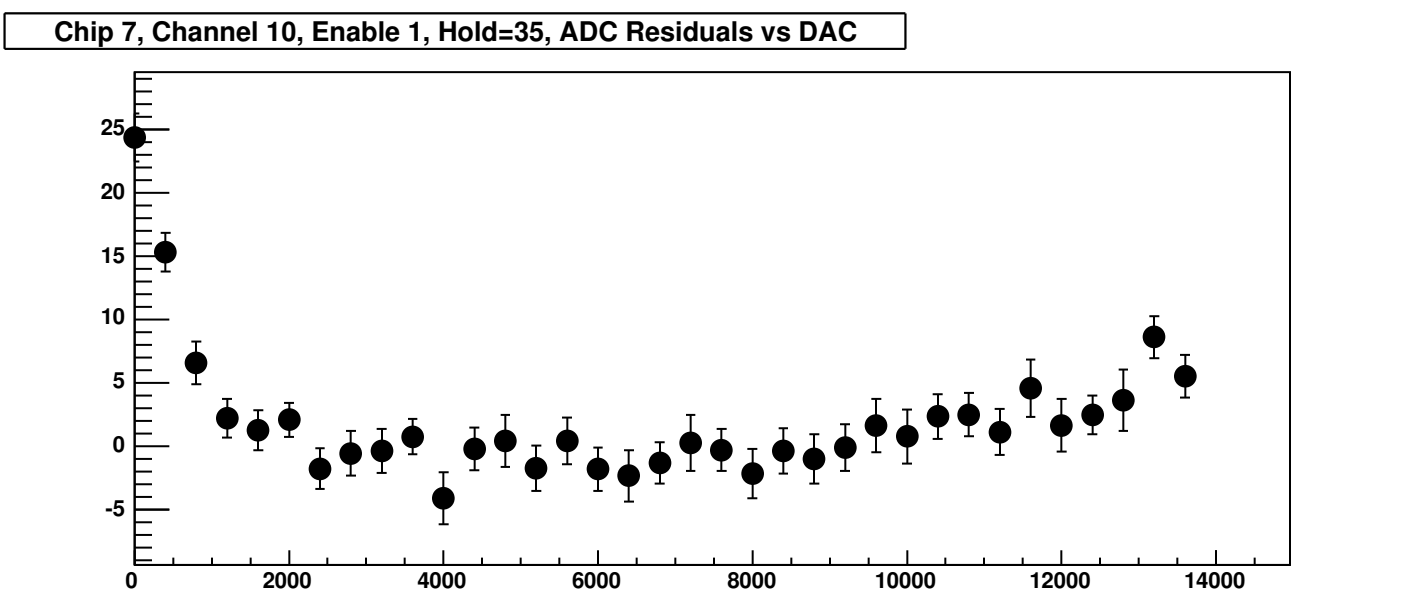


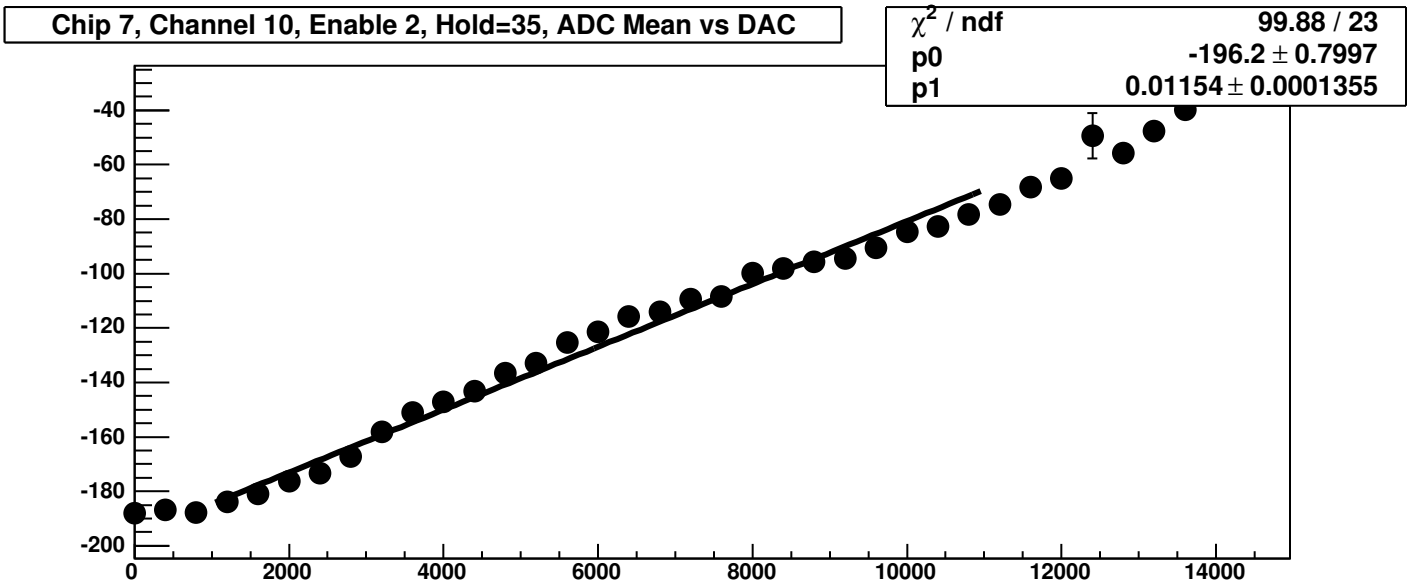
Chip 7, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC

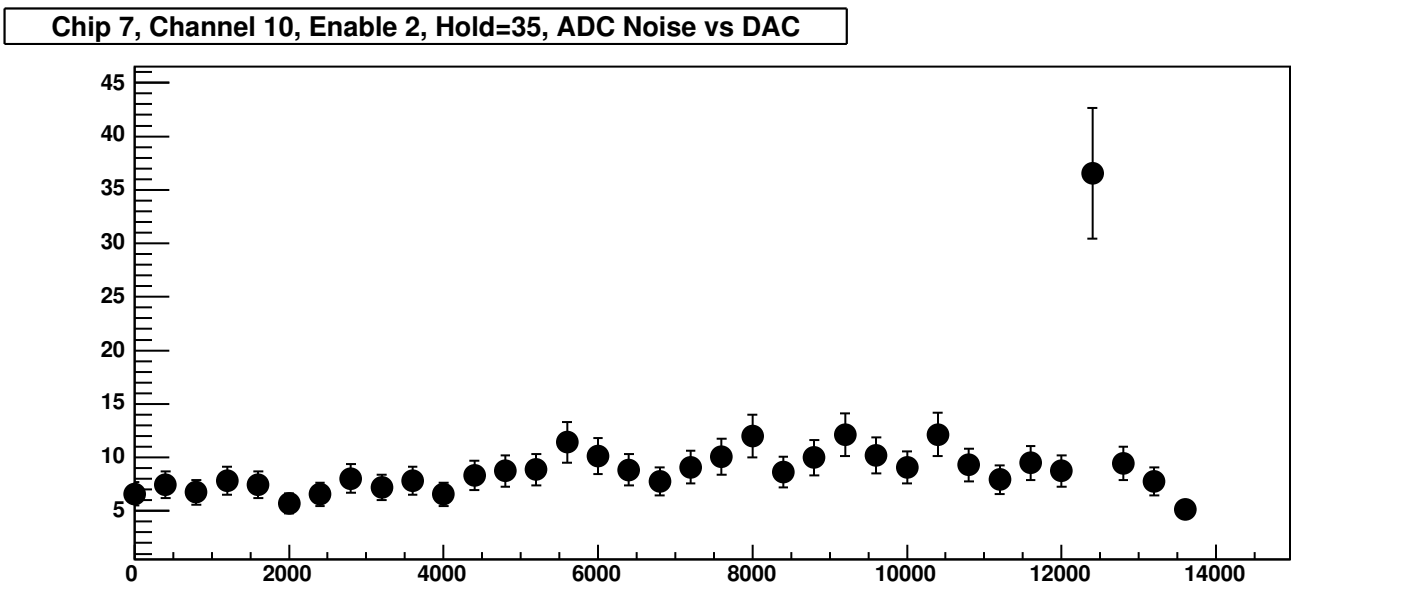


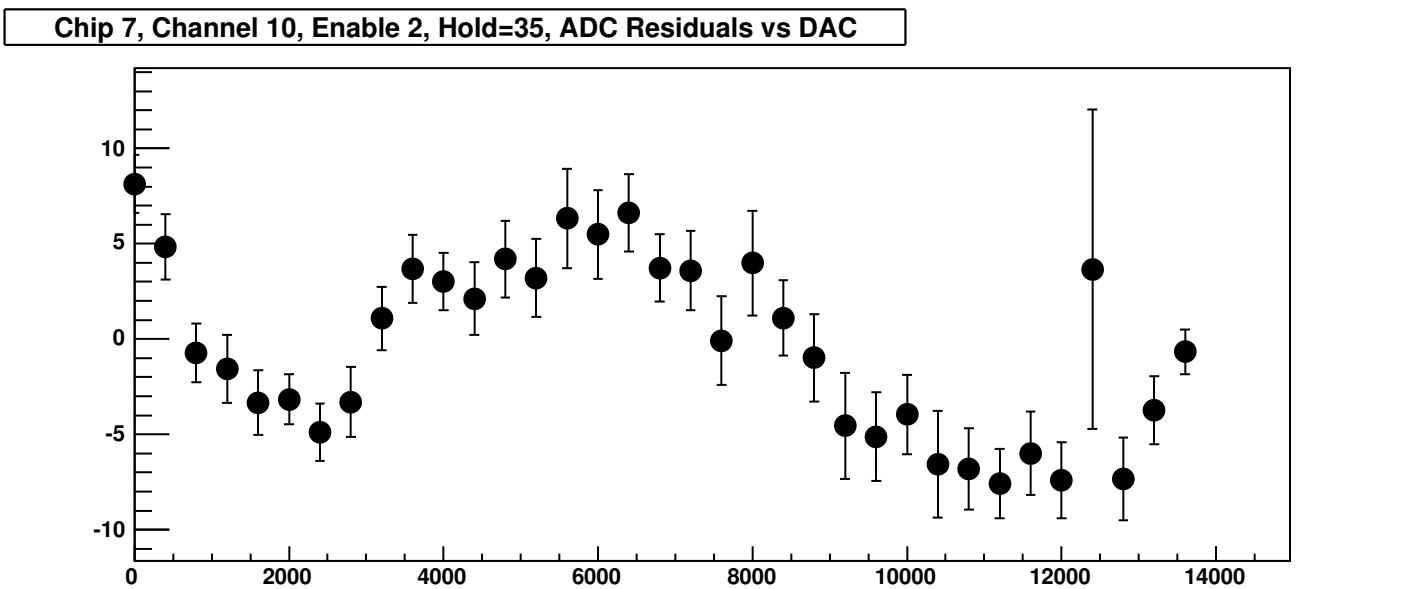

Chip 7, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.22 / 23
p0 -206.3 ± 0.7476
p1 0.02067 ± 0.0001181
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

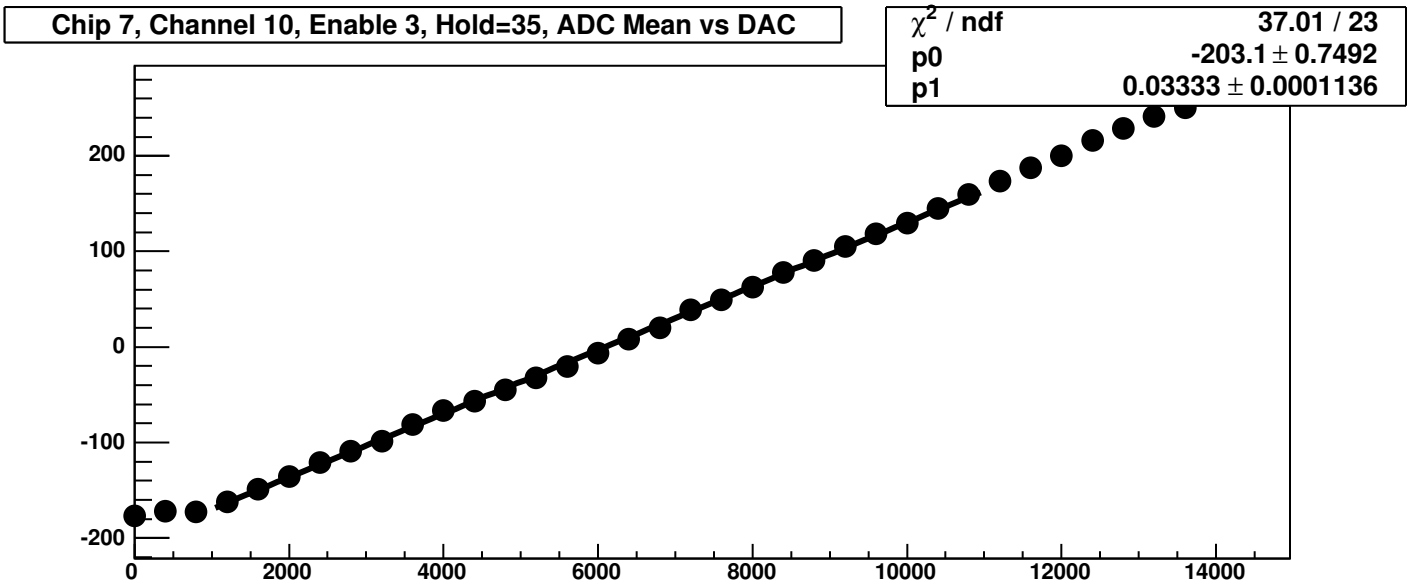

Chip 7, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

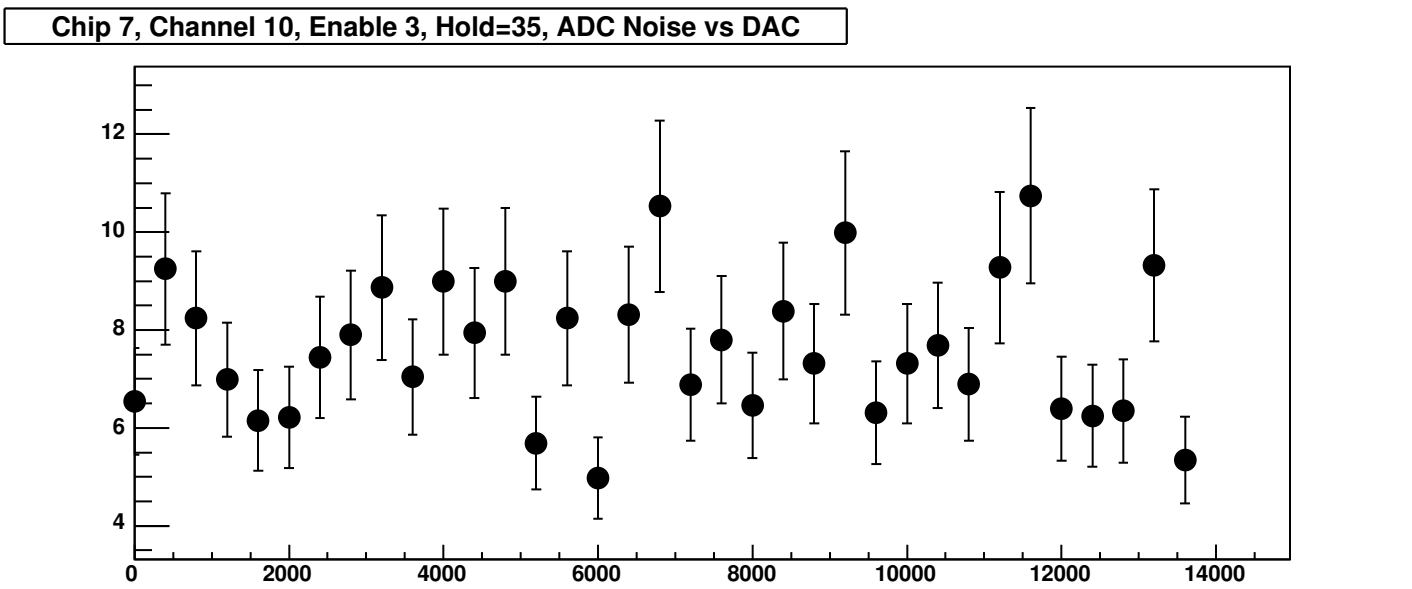

Chip 7, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

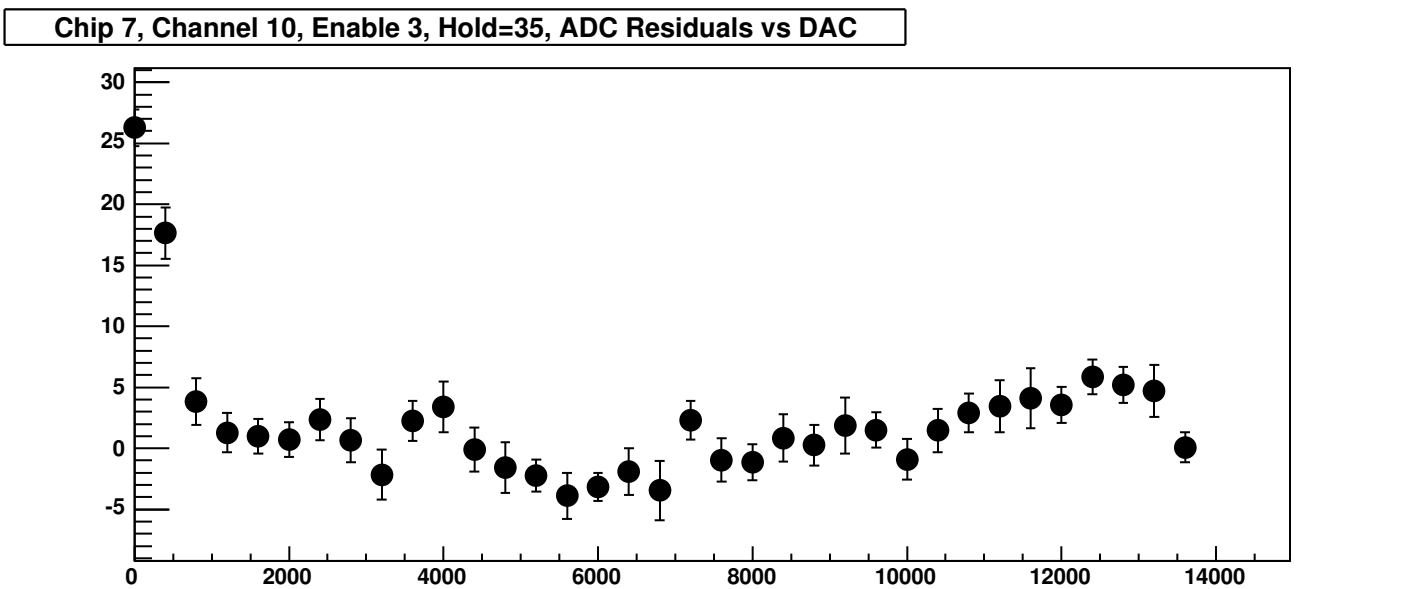

Chip 7, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 99.88 / 23
p0 -196.2 ± 0.7997
p1 0.01154 ± 0.0001355
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
2000
4000
6000
8000
10000
12000
14000

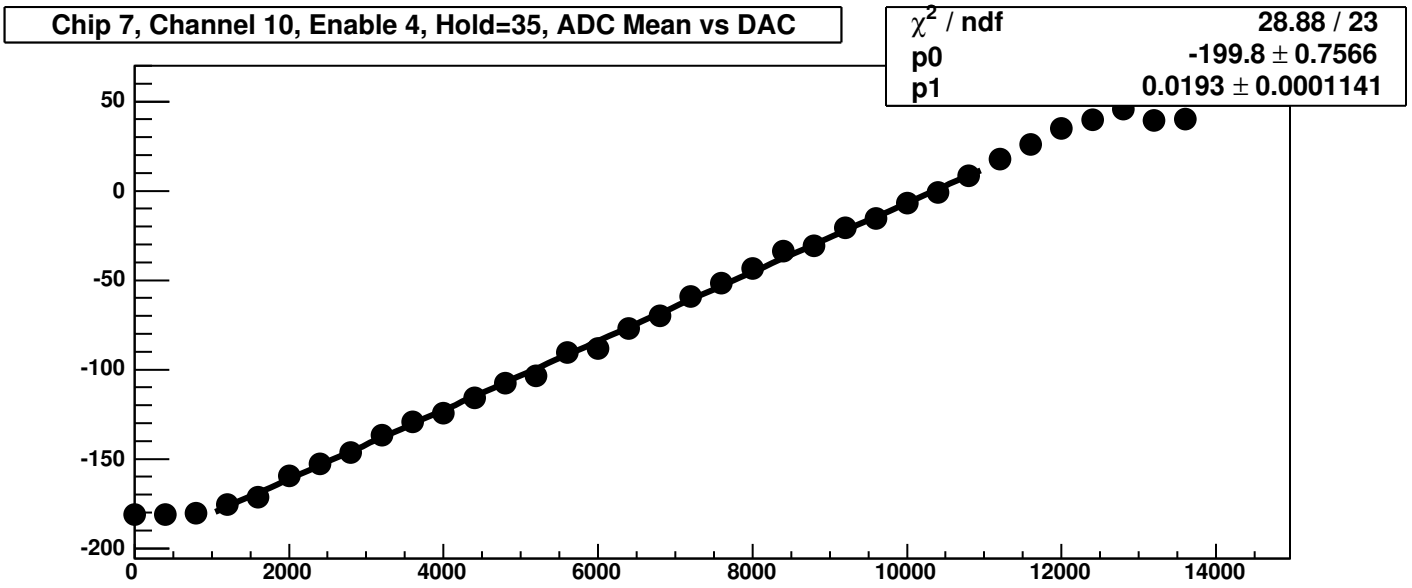

Chip 7, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

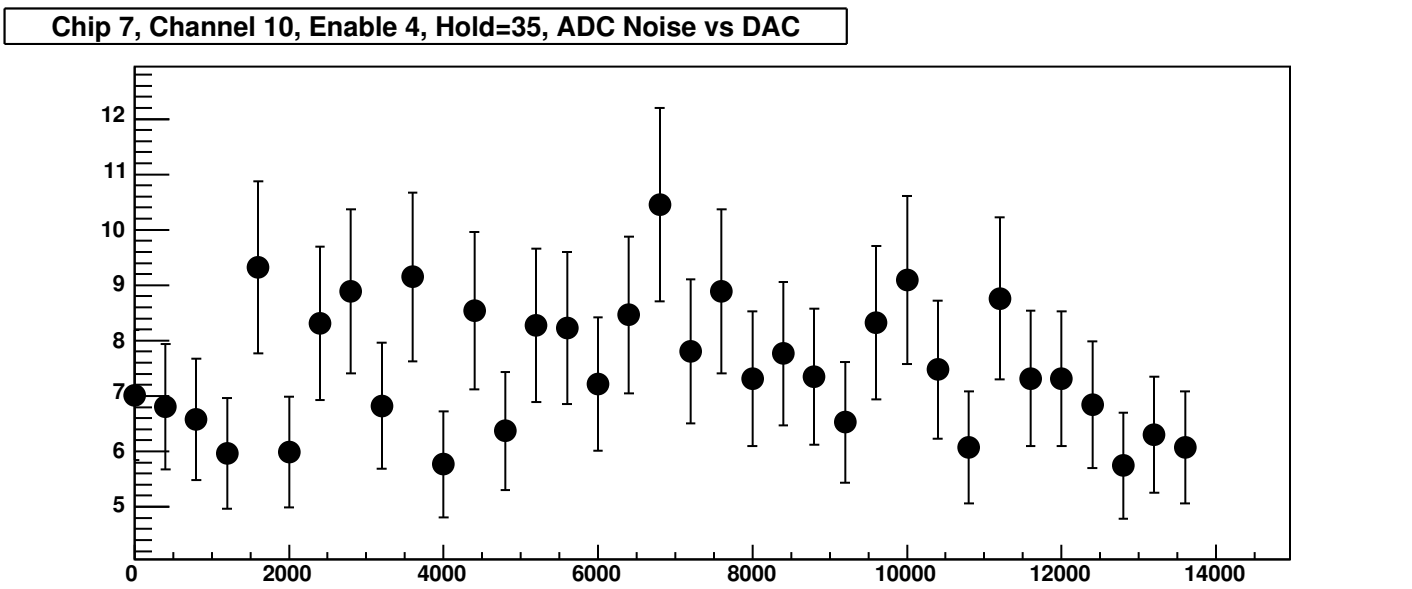

Chip 7, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

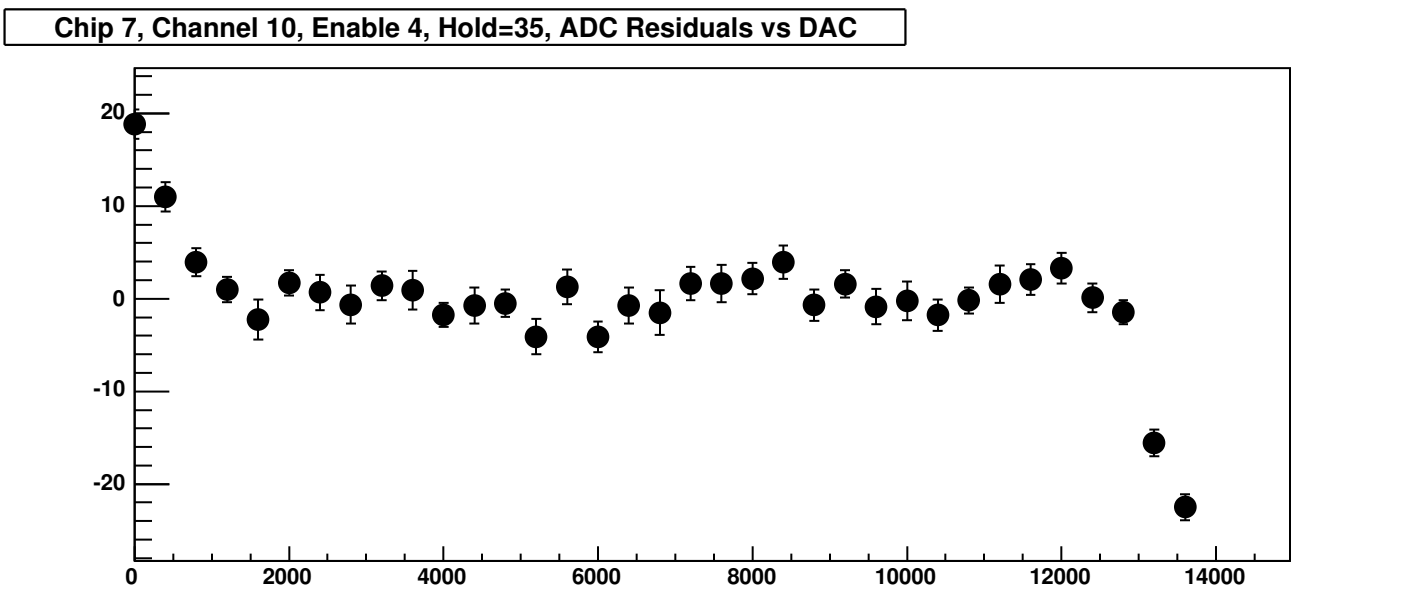

Chip 7, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.01 / 23
p0 -203.1 ± 0.7492
p1 0.03333 ± 0.0001136
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

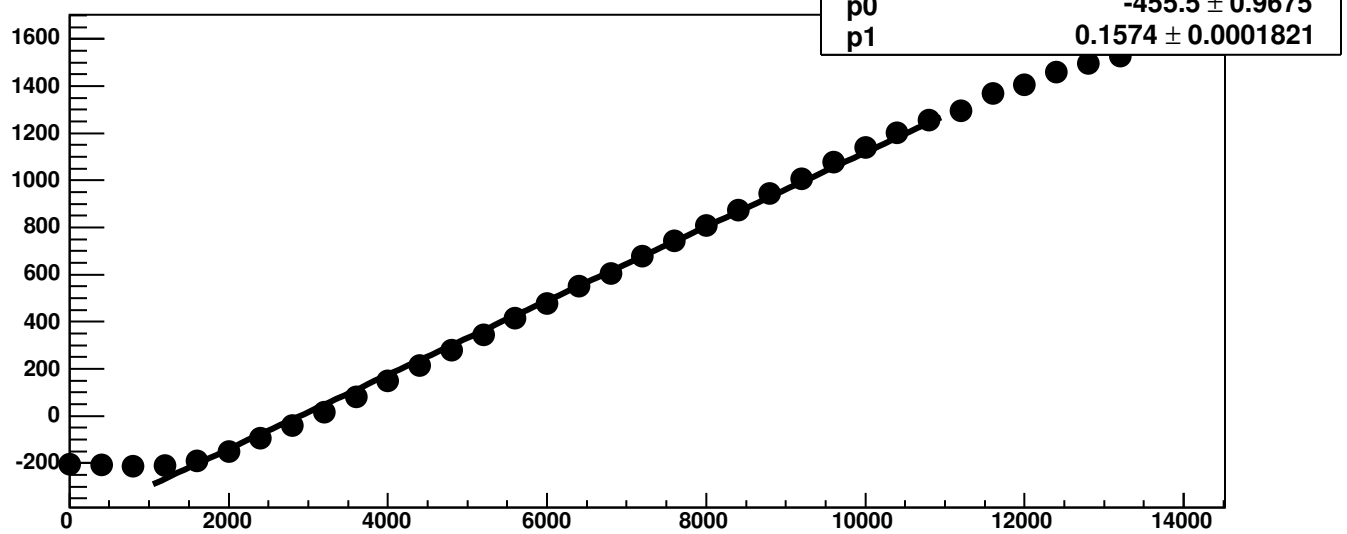

Chip 7, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.88 / 23
p0 -199.8 ± 0.7566
p1 0.0193 ± 0.0001141
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

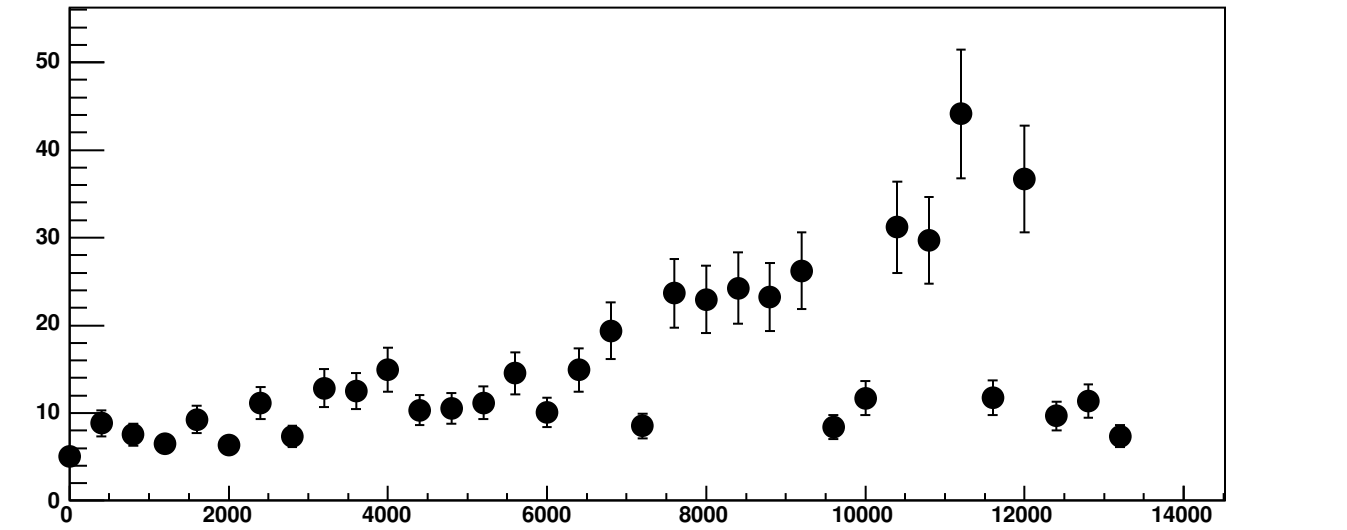

Chip 7, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

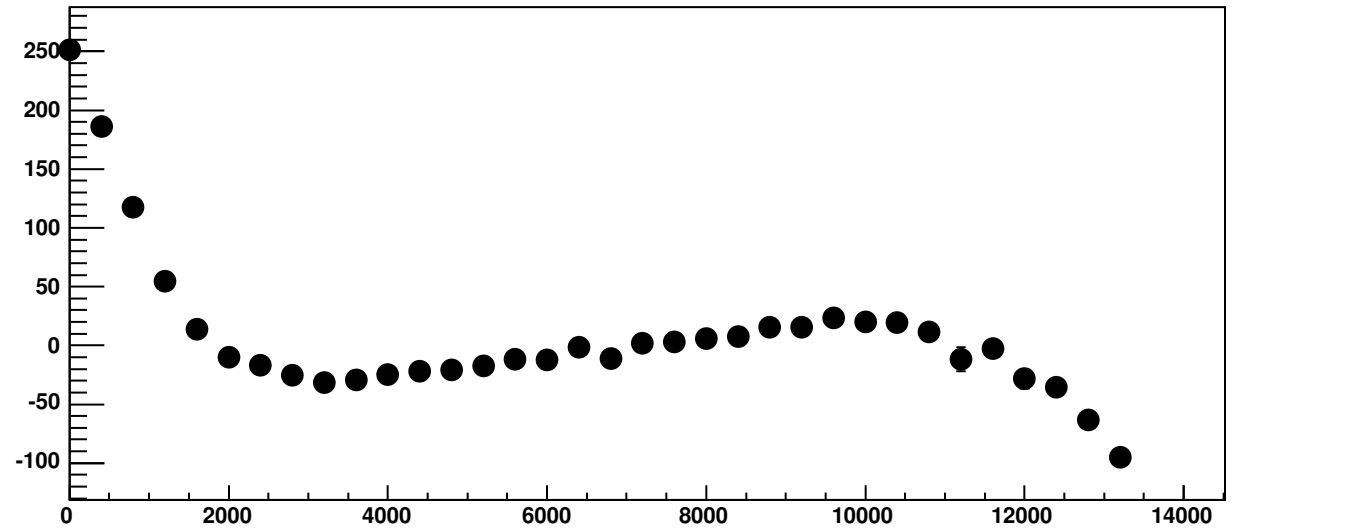
Chip 7, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

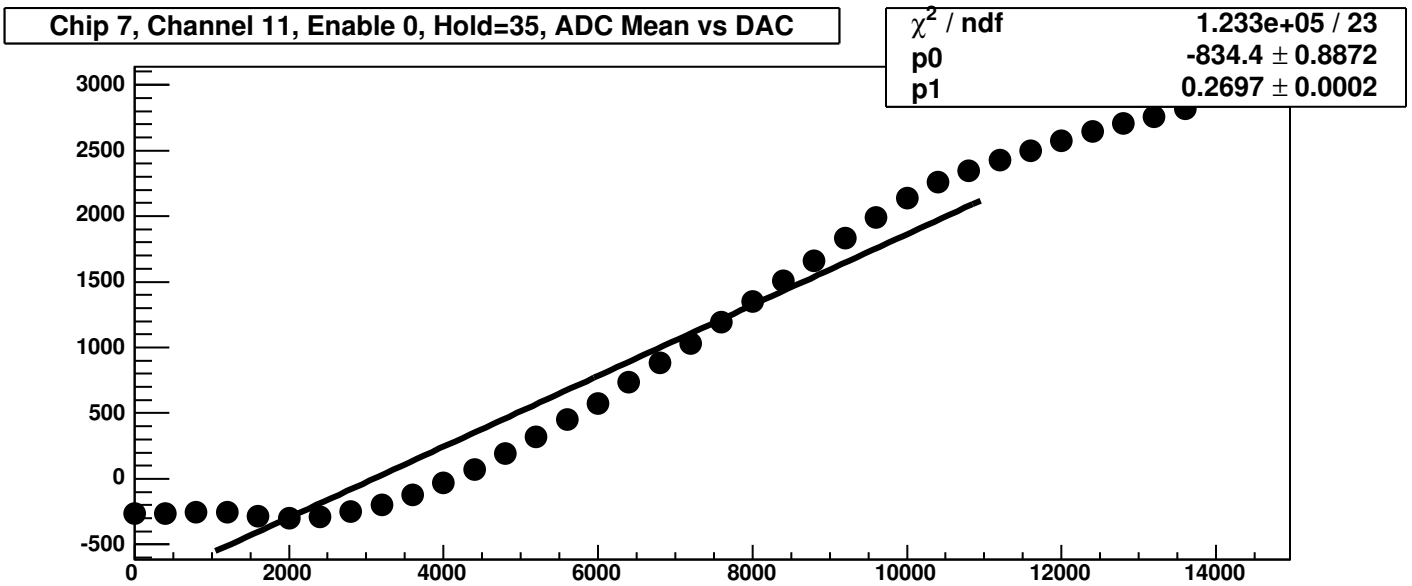


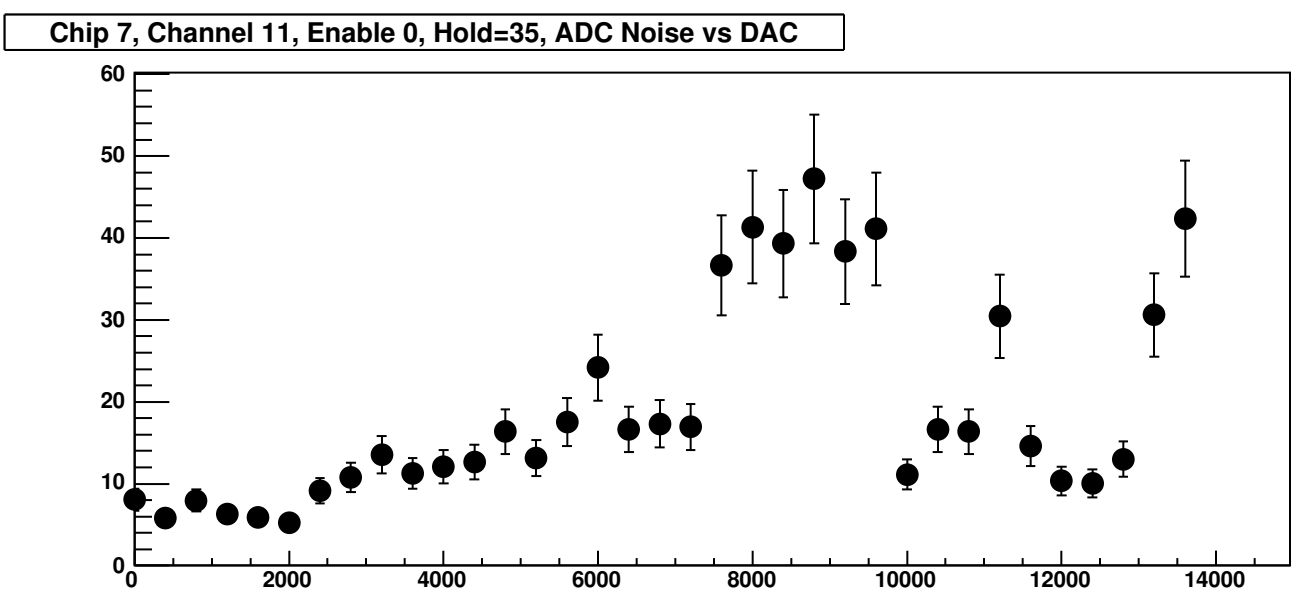
Chip 7, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

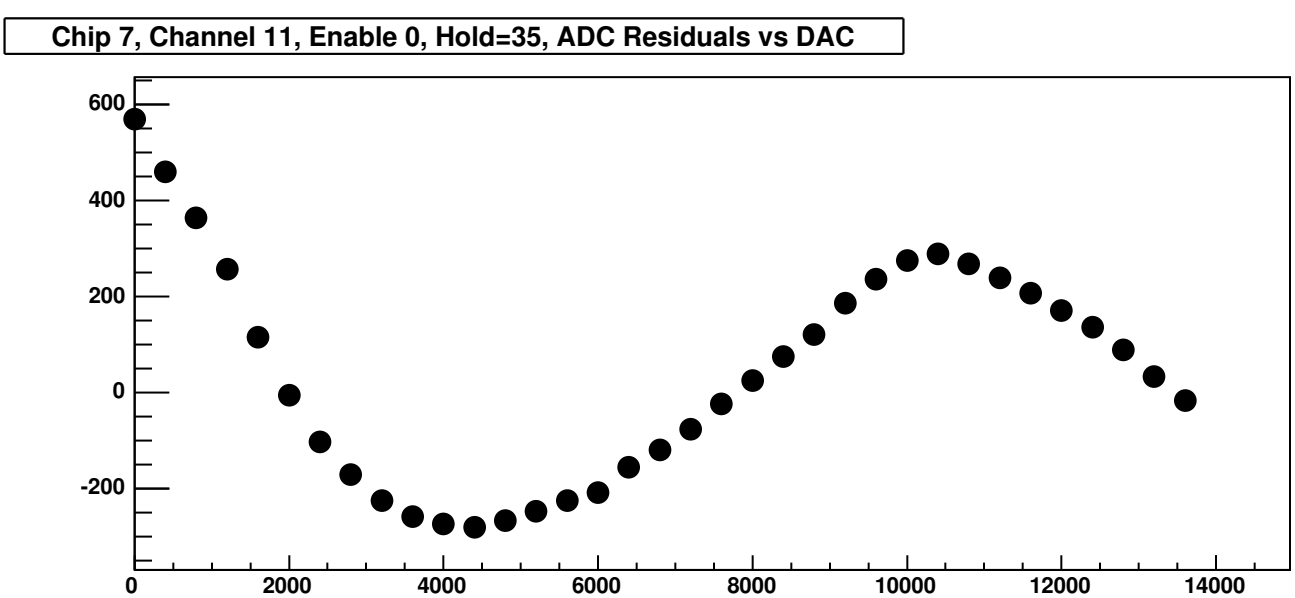


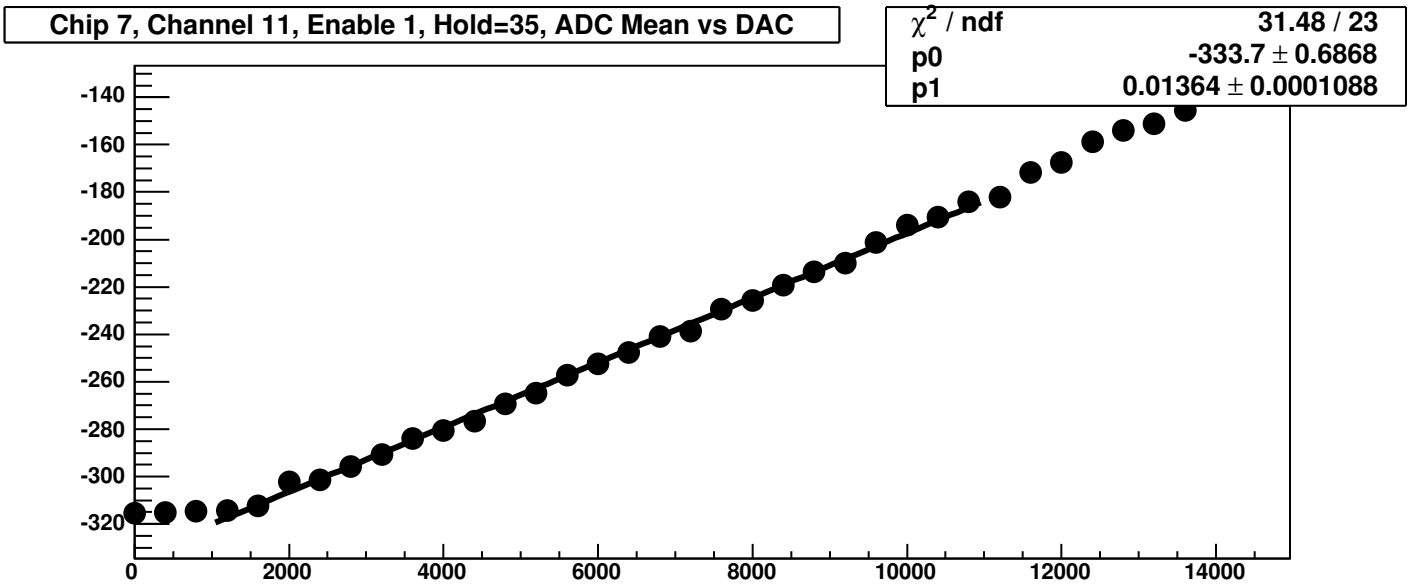
Chip 7, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

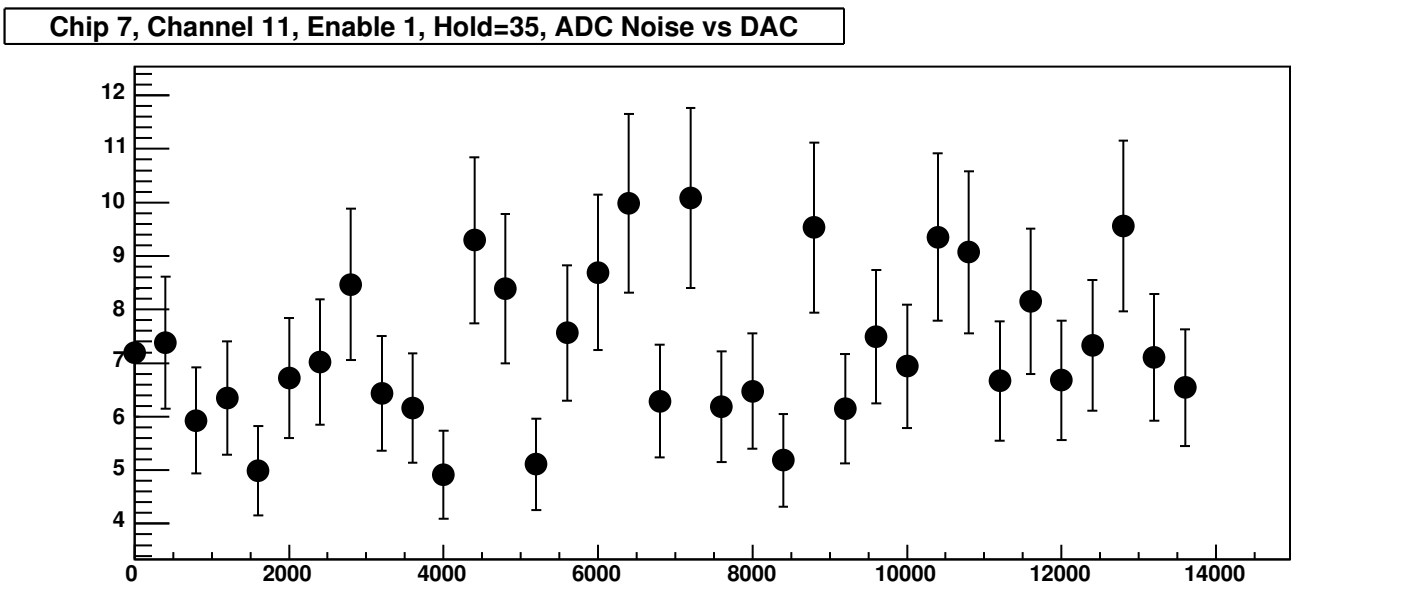


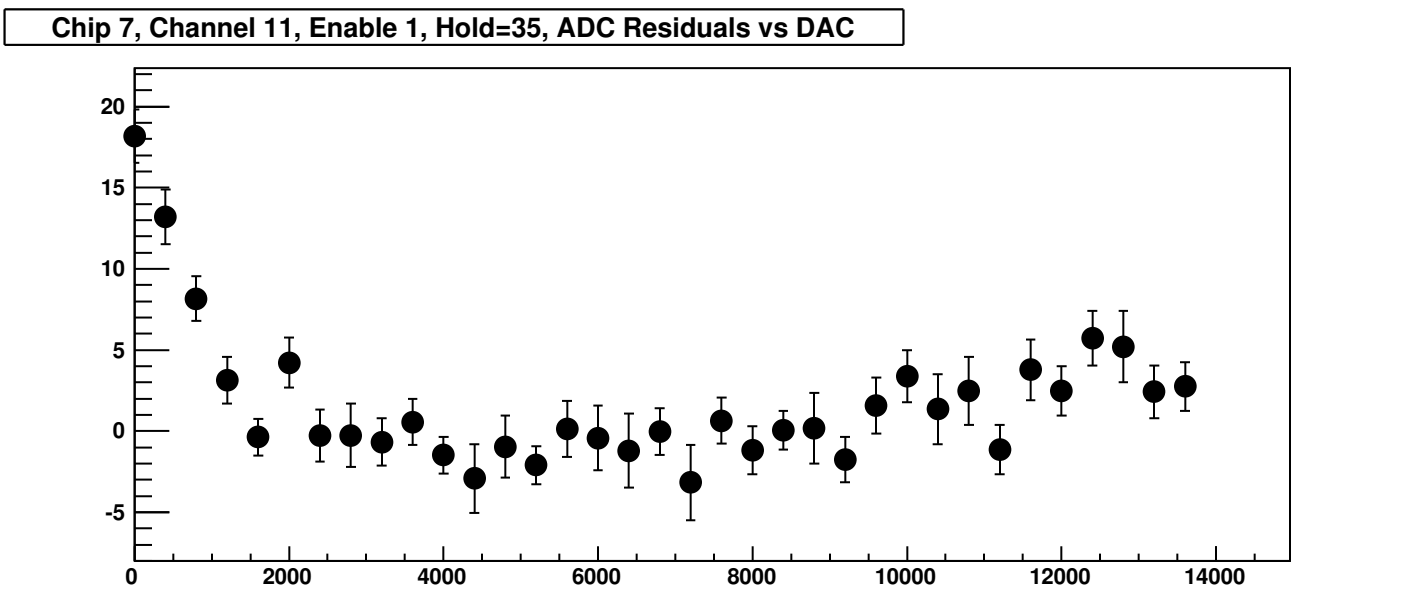

Chip 7, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.233e+05 / 23
p0 -834.4 ± 0.8872
p1 0.2697 ± 0.0002
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

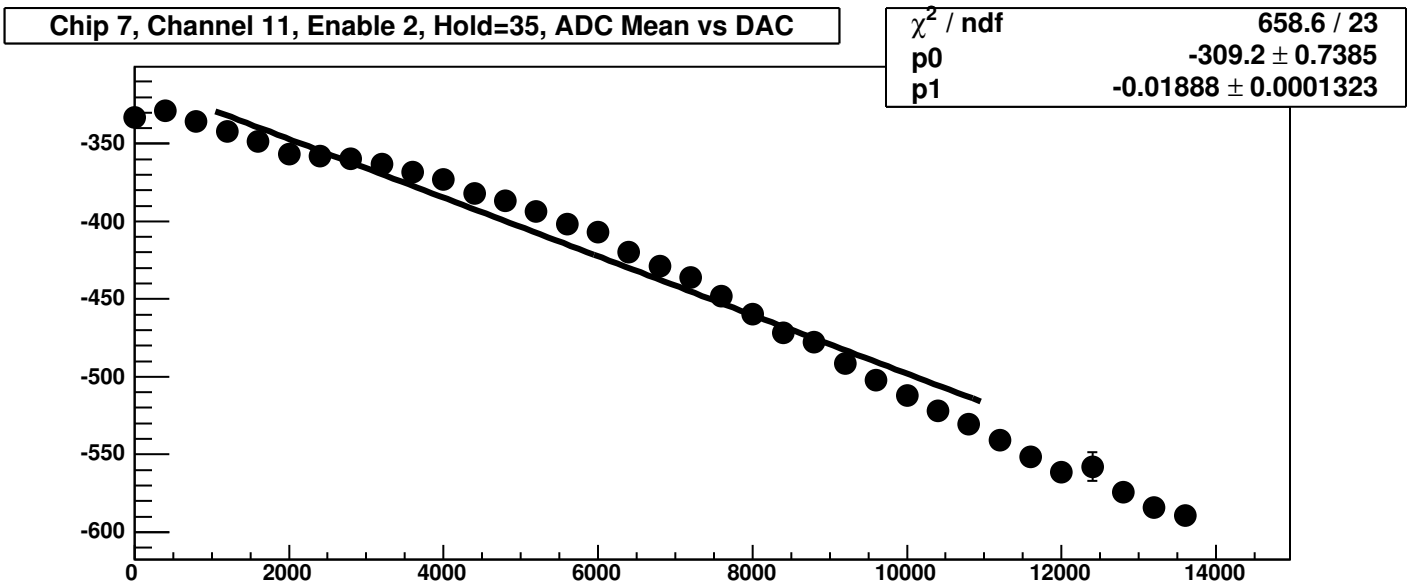

Chip 7, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

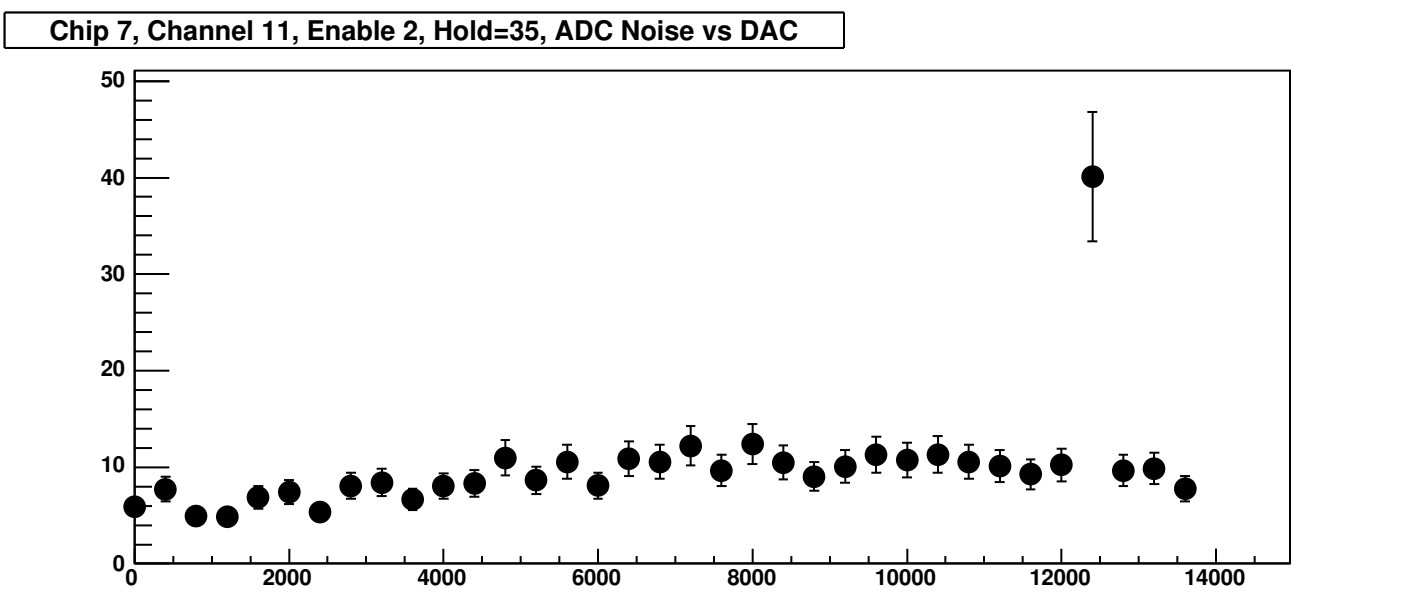

Chip 7, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

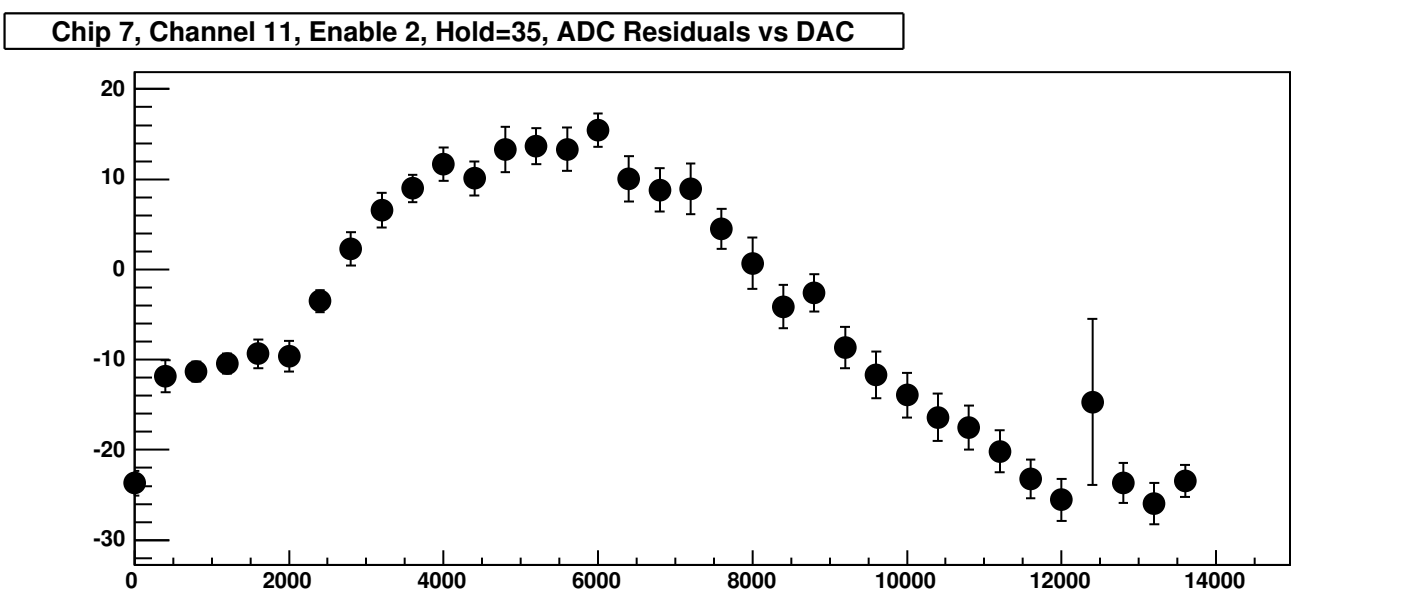

Chip 7, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.48 / 23
p0 -333.7 ± 0.6868
p1 0.01364 ± 0.0001088
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

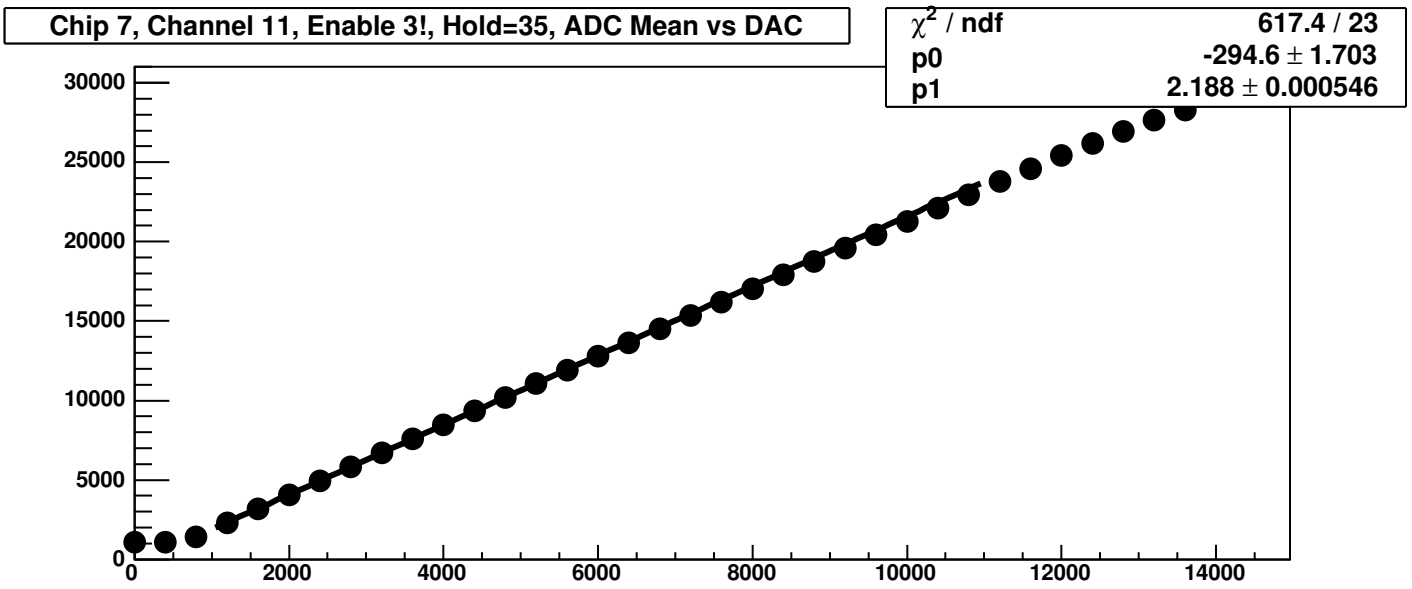

Chip 7, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

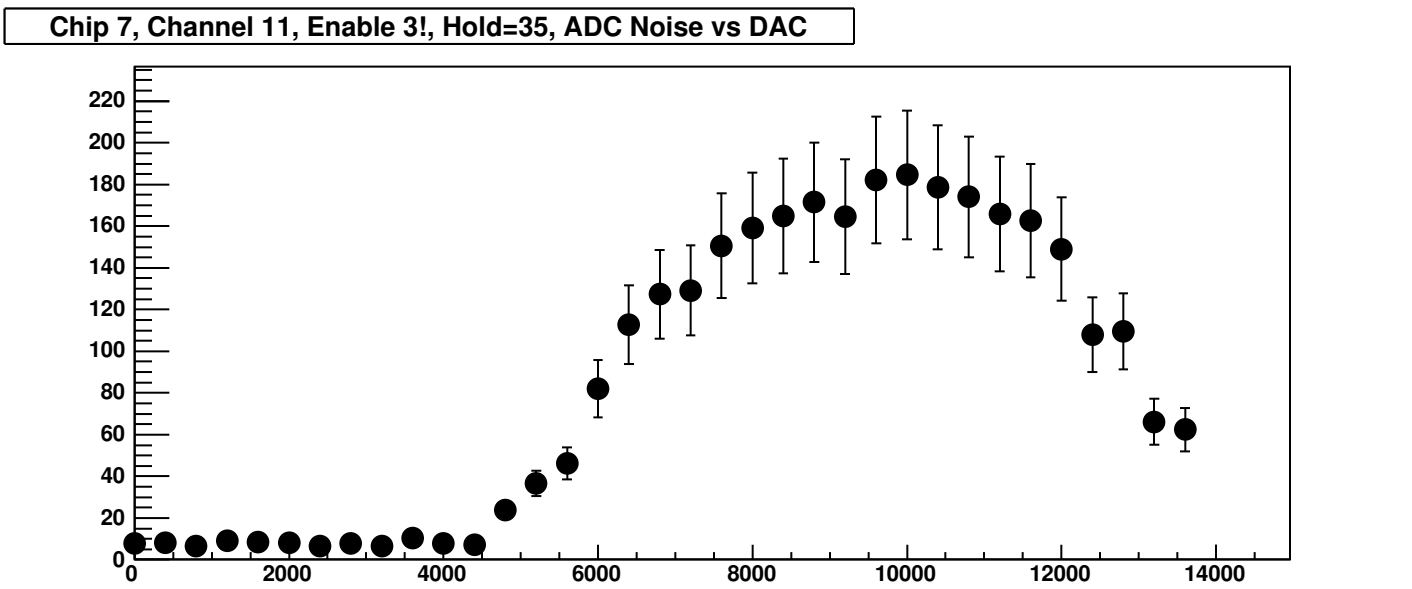

Chip 7, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

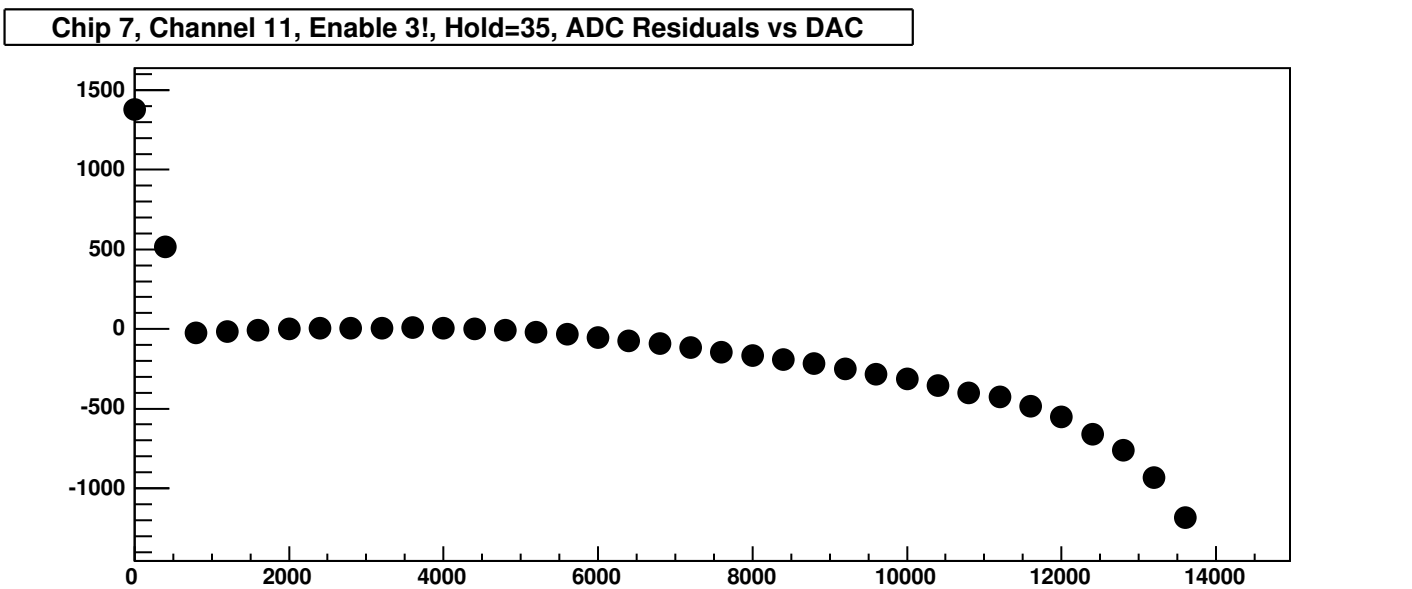

Chip 7, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 658.6 / 23
p0 -309.2 ± 0.7385
p1 -0.01888 ± 0.0001323
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

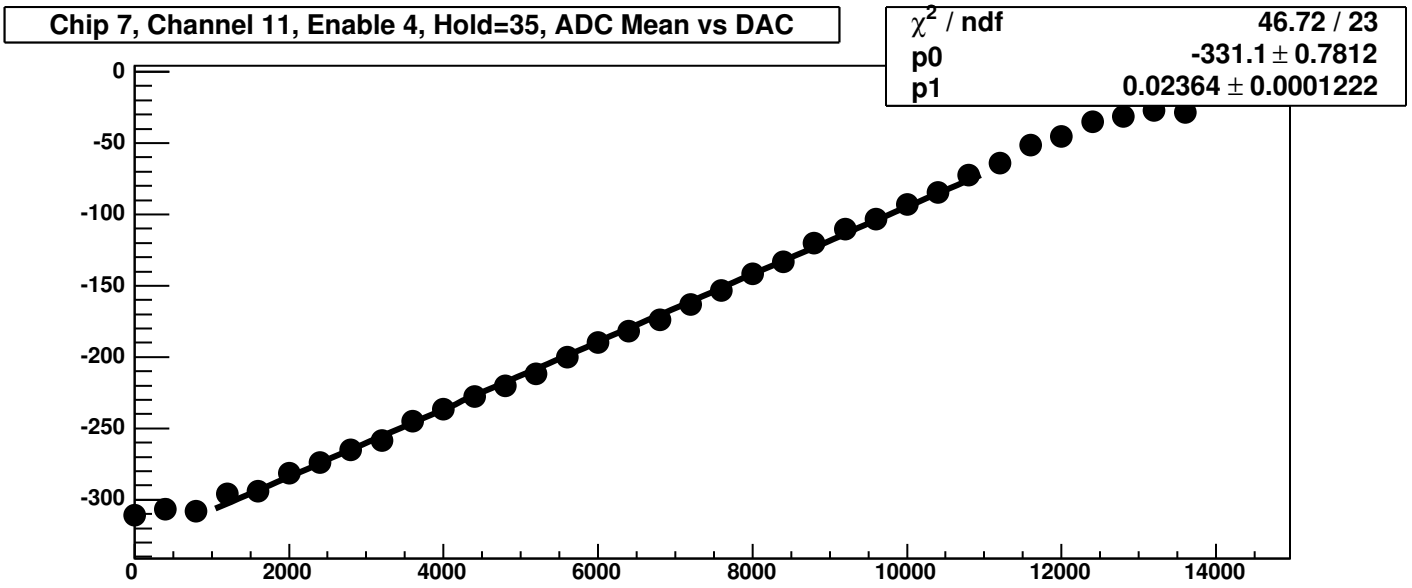

Chip 7, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

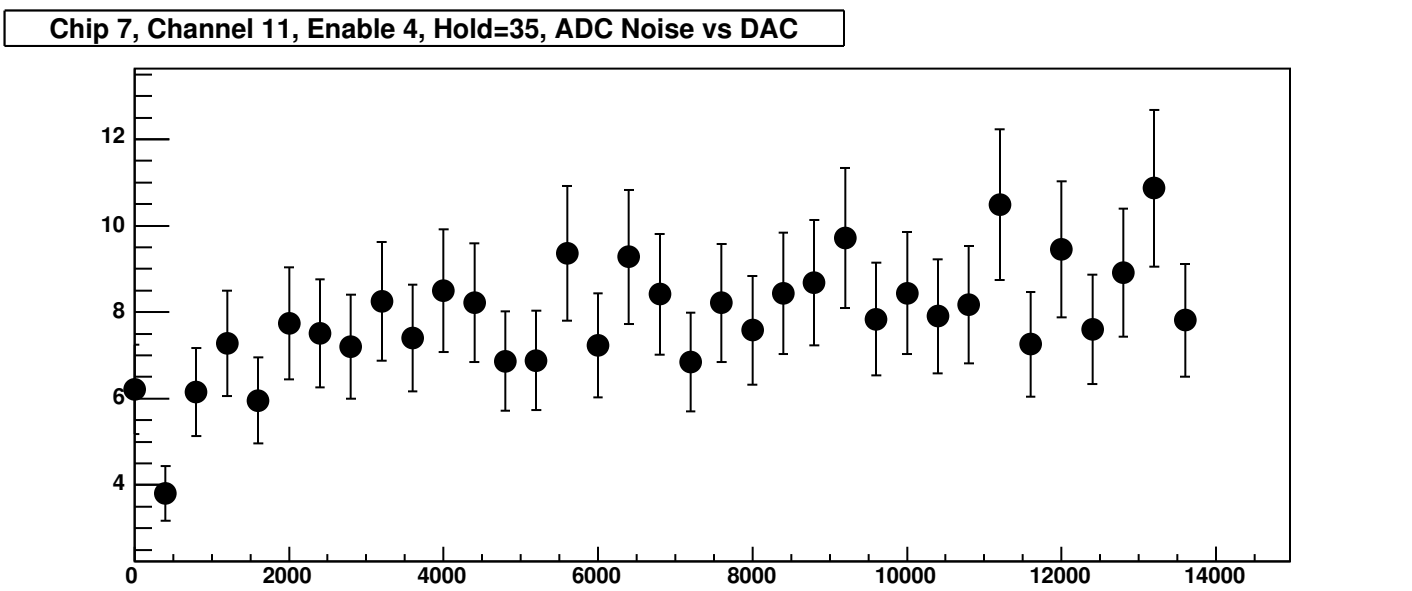

Chip 7, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

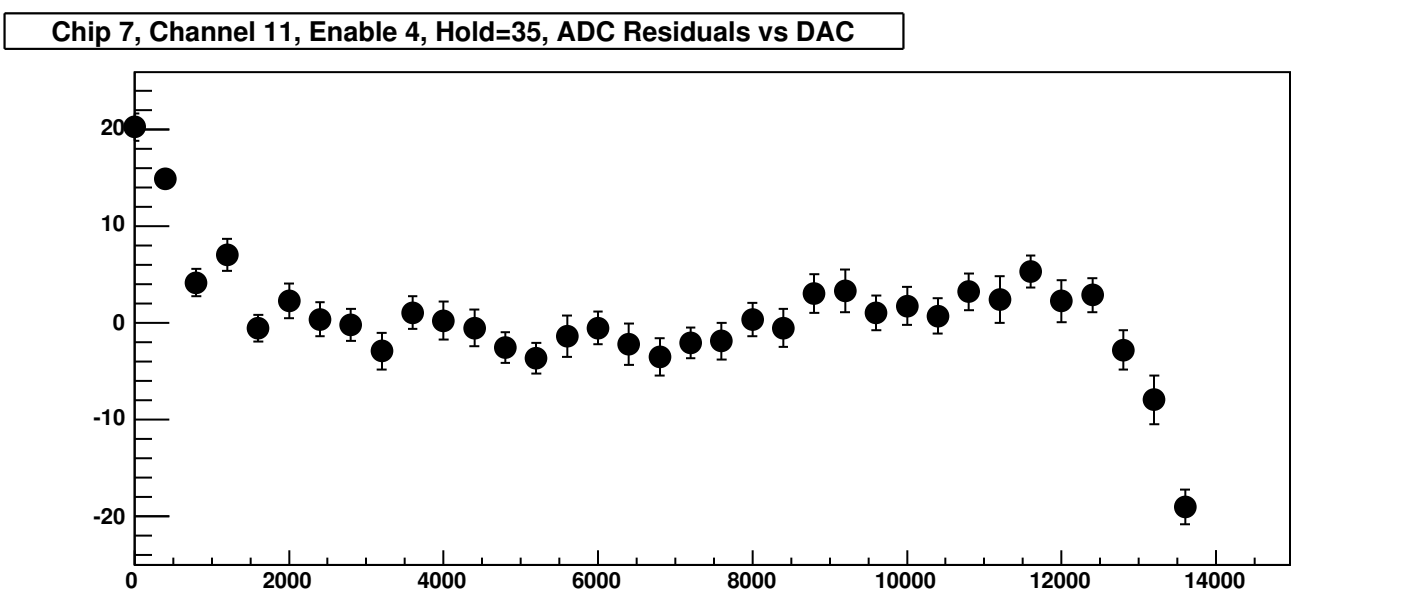

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 617.4 / 23
p0 -294.6 ± 1.703
p1 2.188 ± 0.000546
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

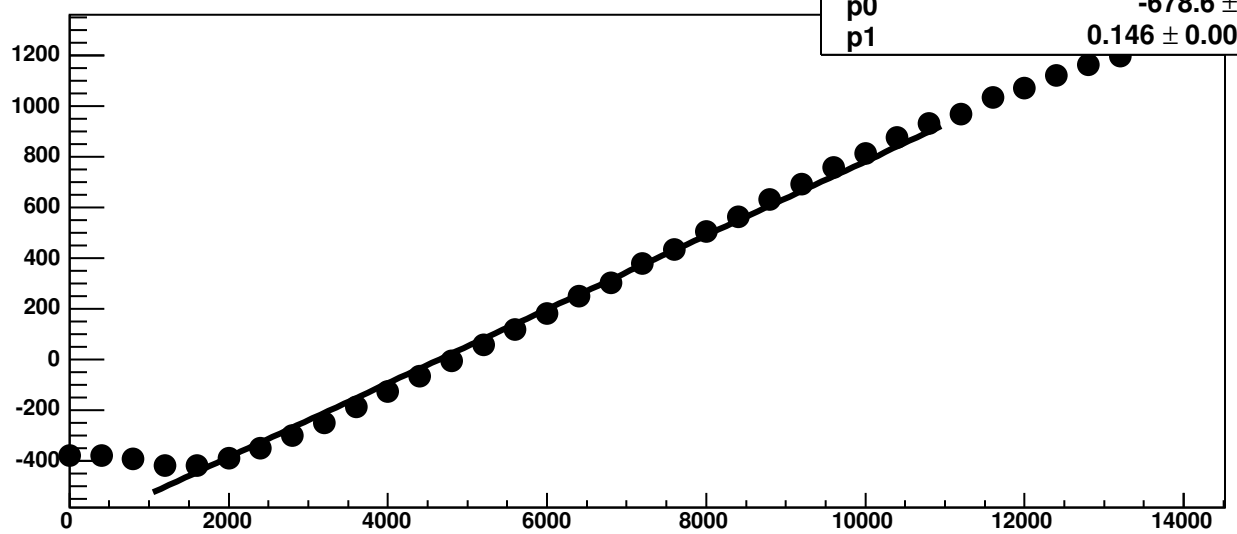

Chip 7, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 46.72 / 23
p0 -331.1 ± 0.7812
p1 0.02364 ± 0.0001222
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

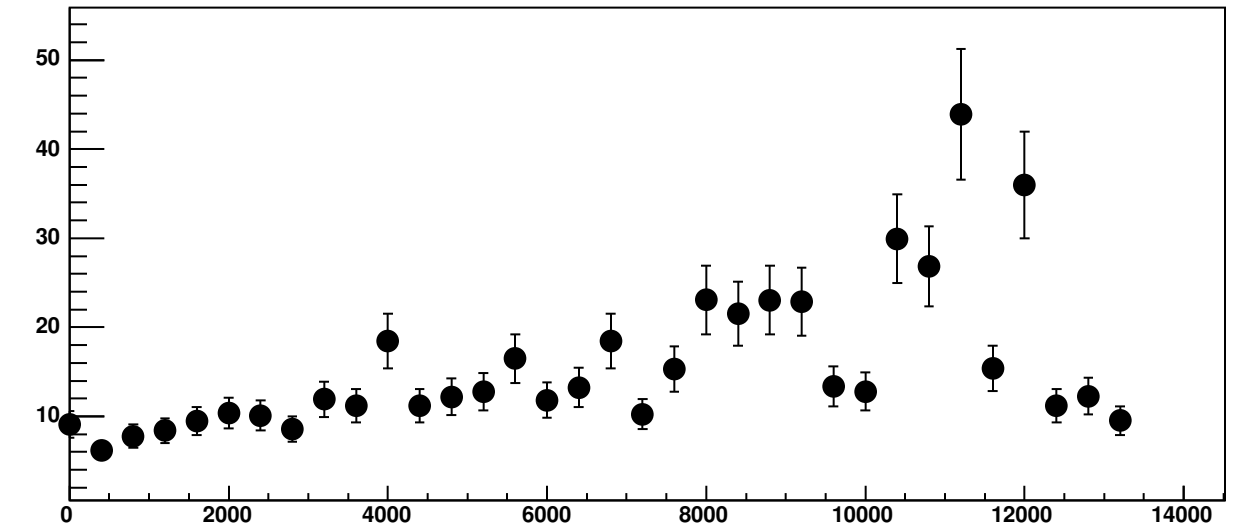

Chip 7, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

Chip 7, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

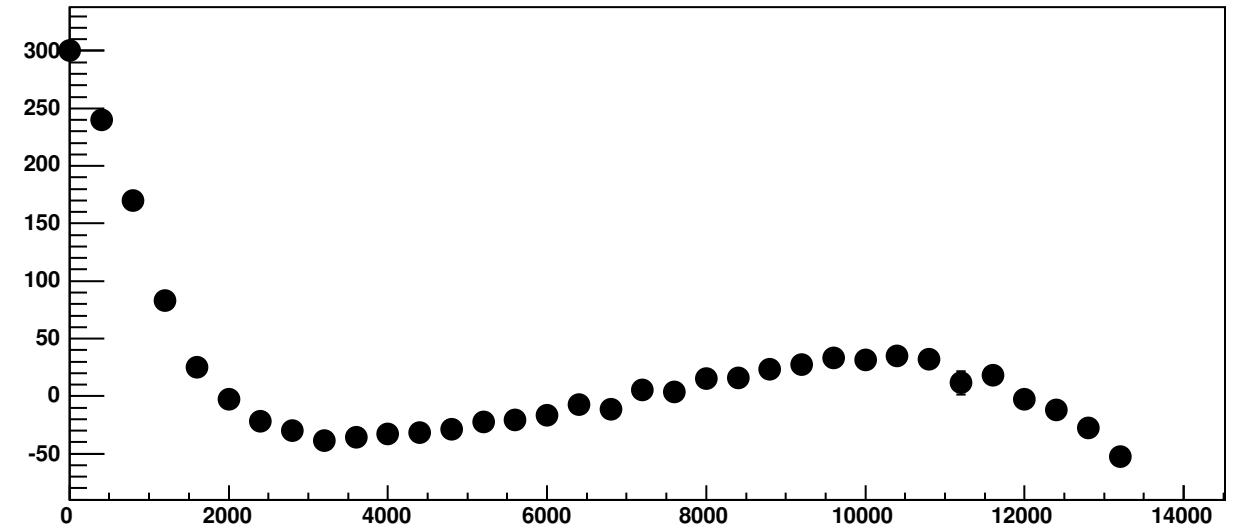
χ^2 / ndf	3514 / 23
p0	-678.6 ± 1.162
p1	0.146 ± 0.0002156

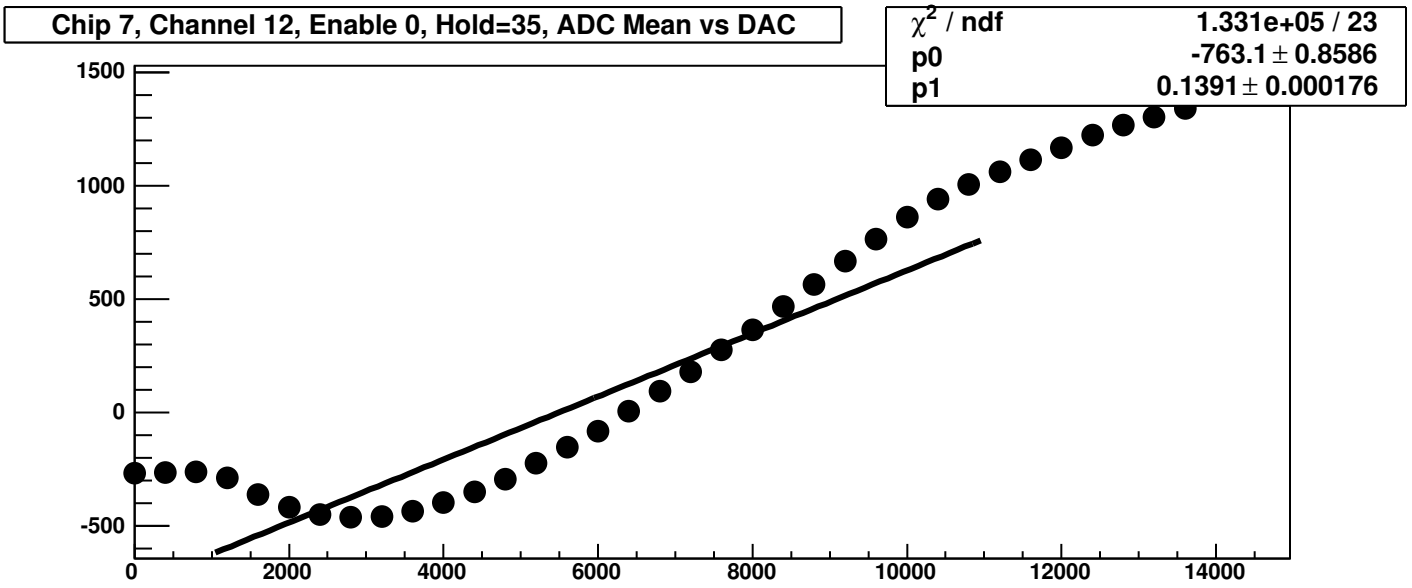


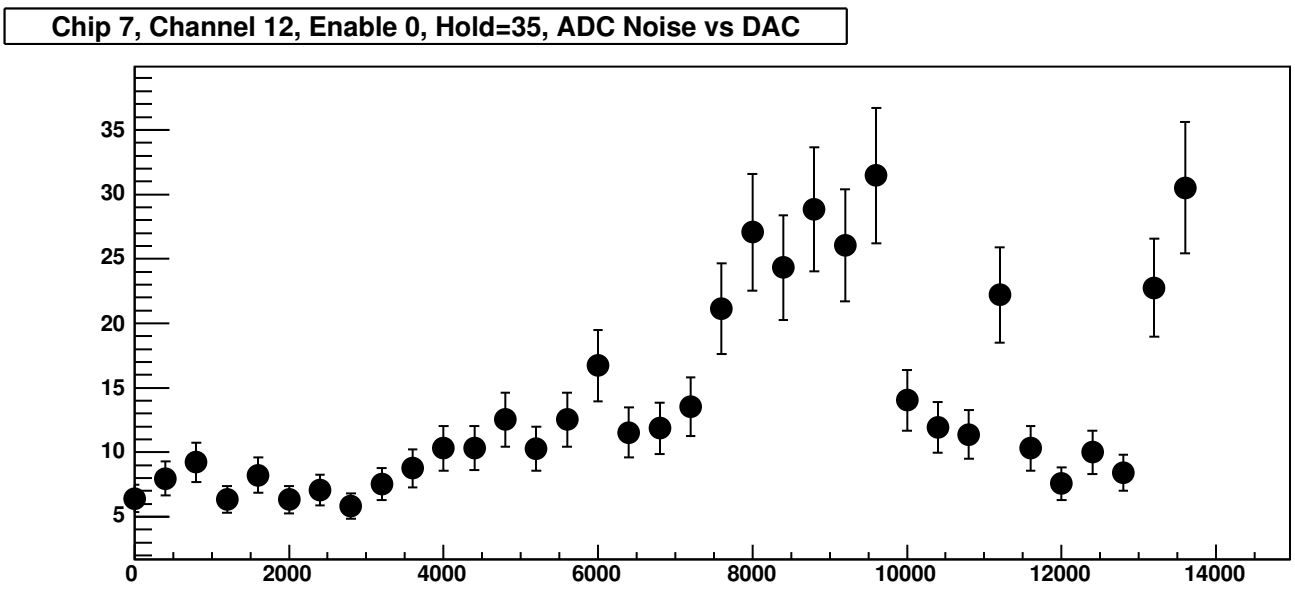
Chip 7, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

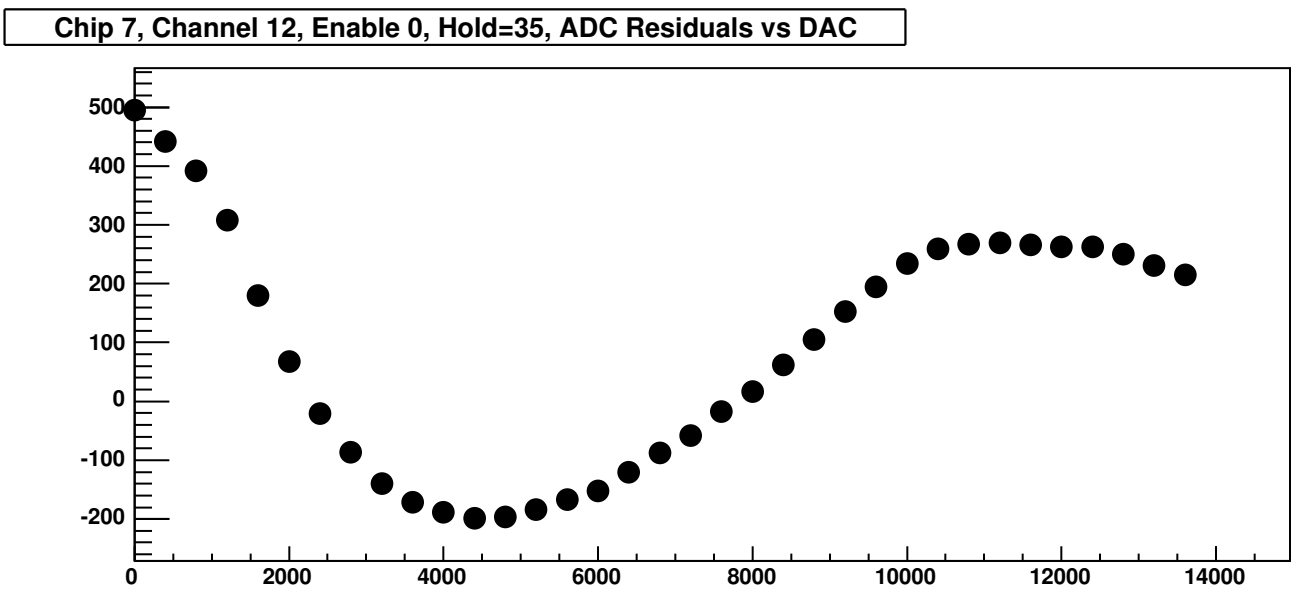


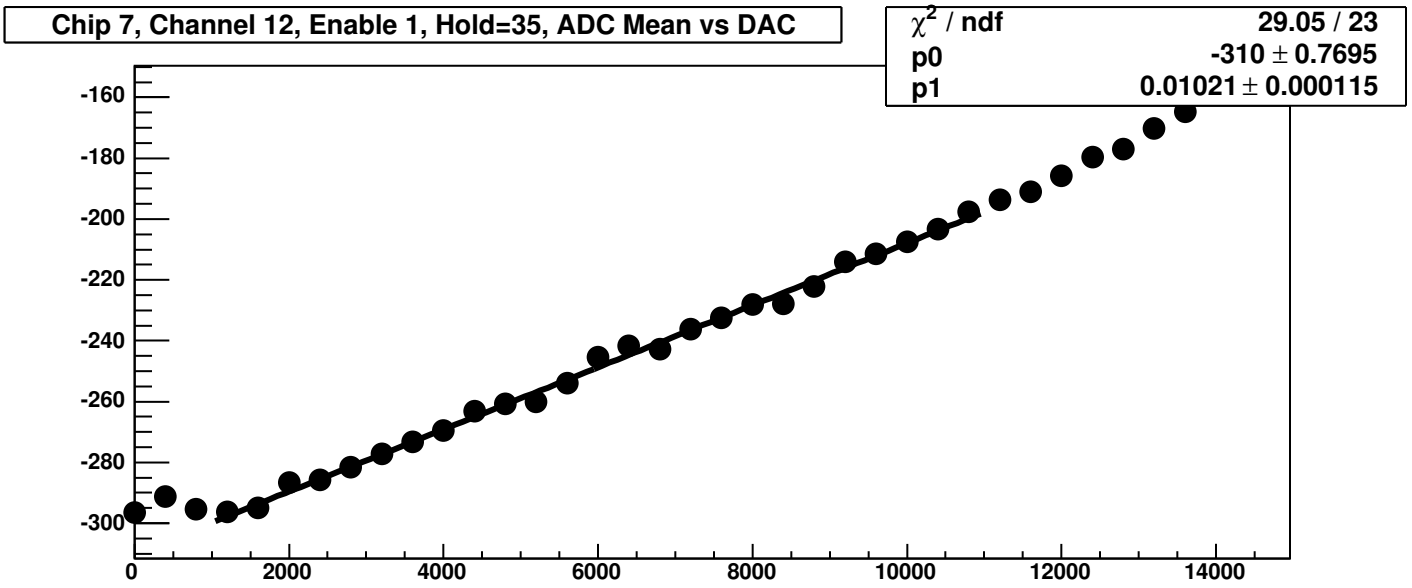
Chip 7, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

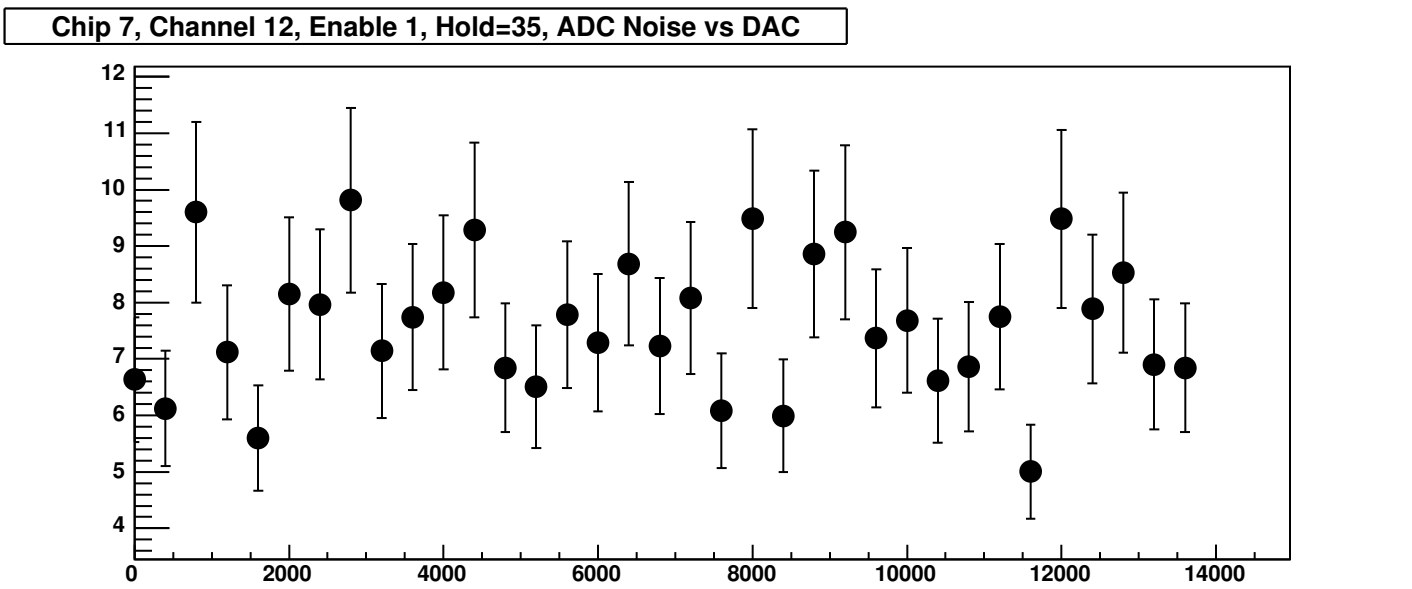


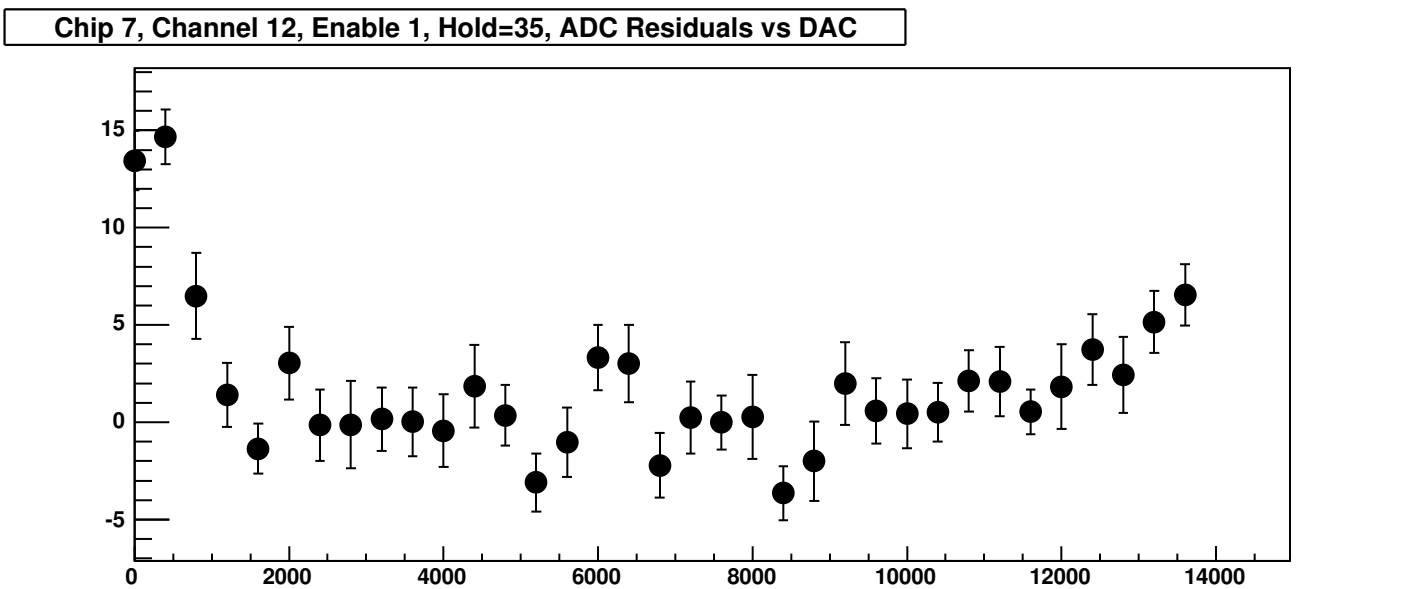

Chip 7, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.331e+05 / 23
p0 -763.1 ± 0.8586
p1 0.1391 ± 0.000176
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

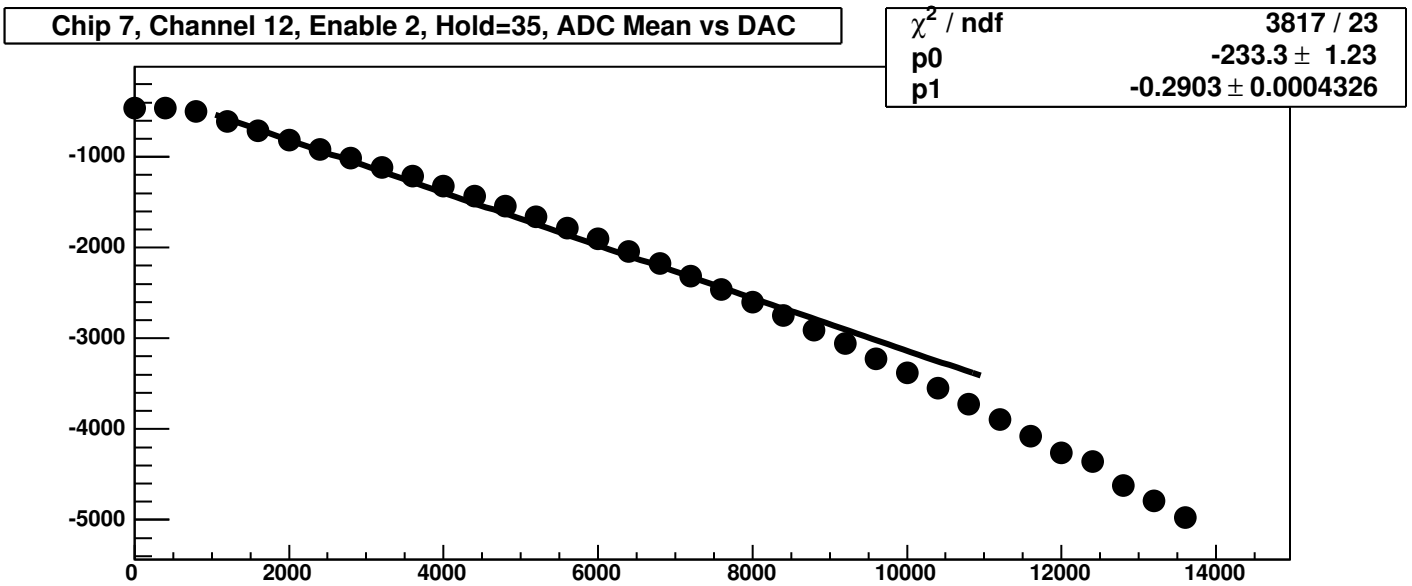

Chip 7, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

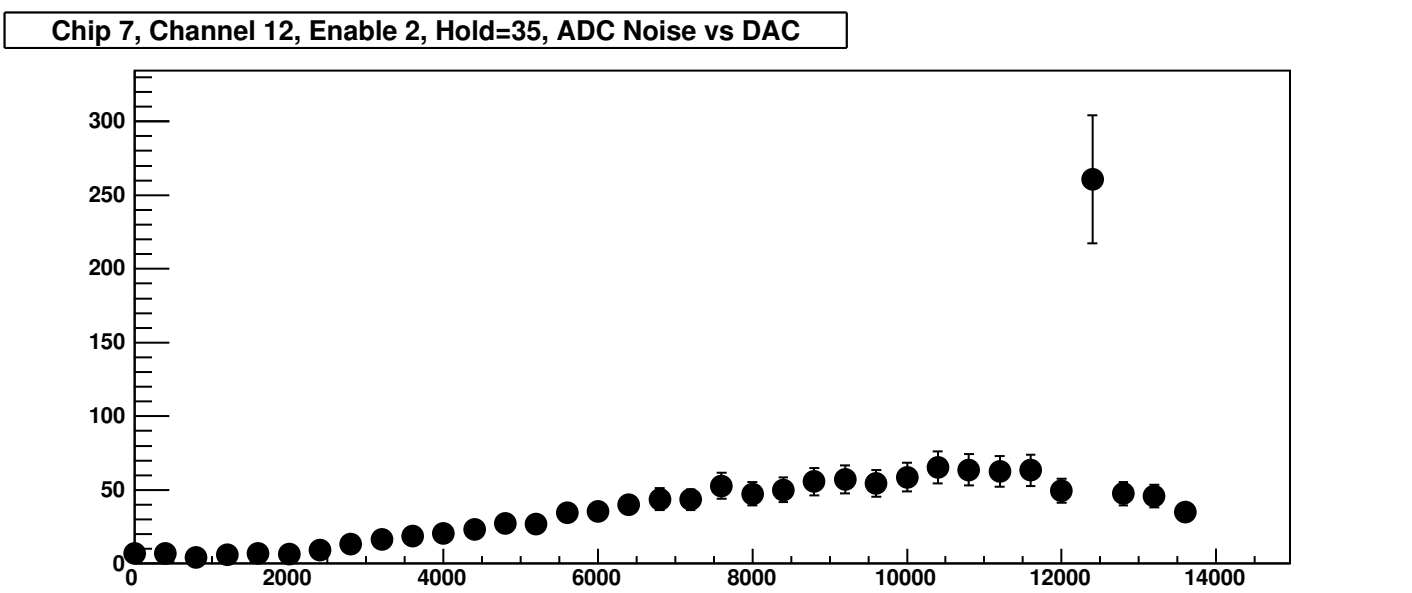

Chip 7, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

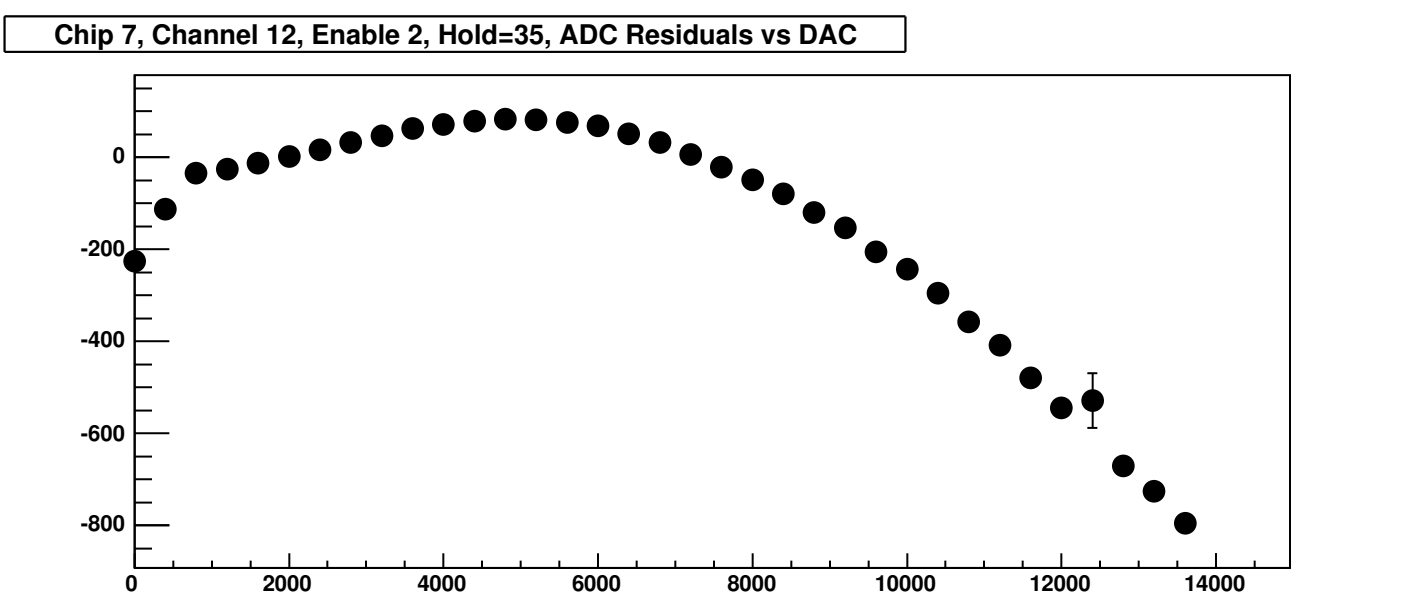

Chip 7, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.05 / 23
p0 -310 ± 0.7695
p1 0.01021 ± 0.000115
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000

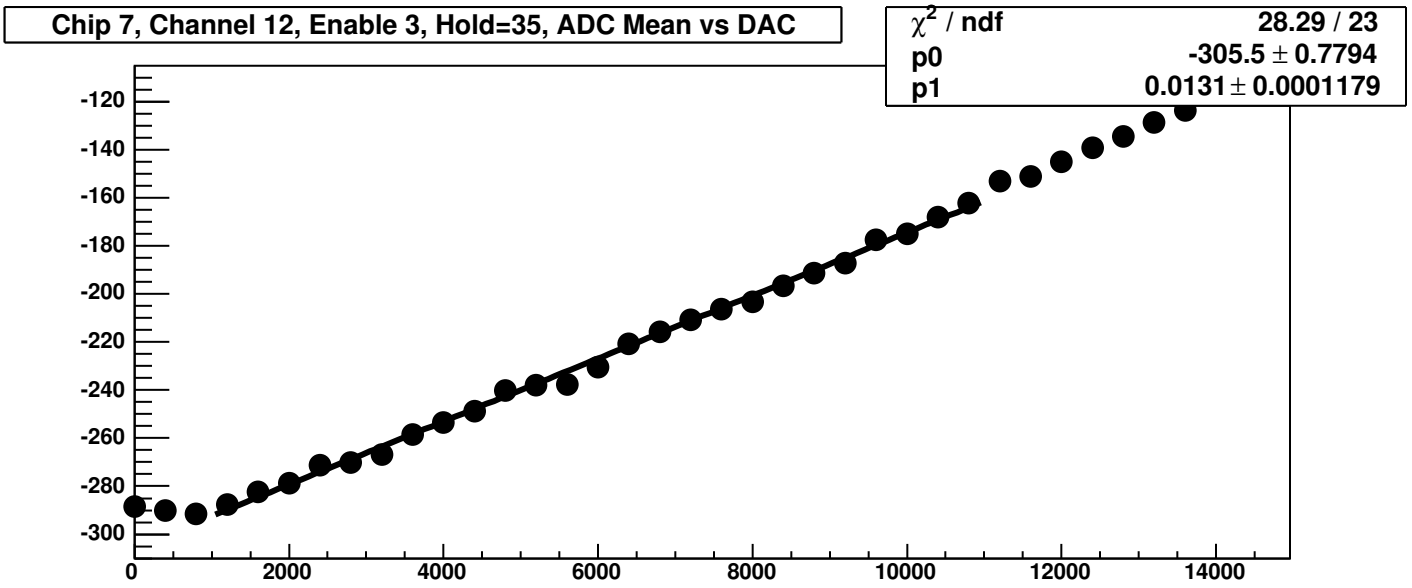

Chip 7, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

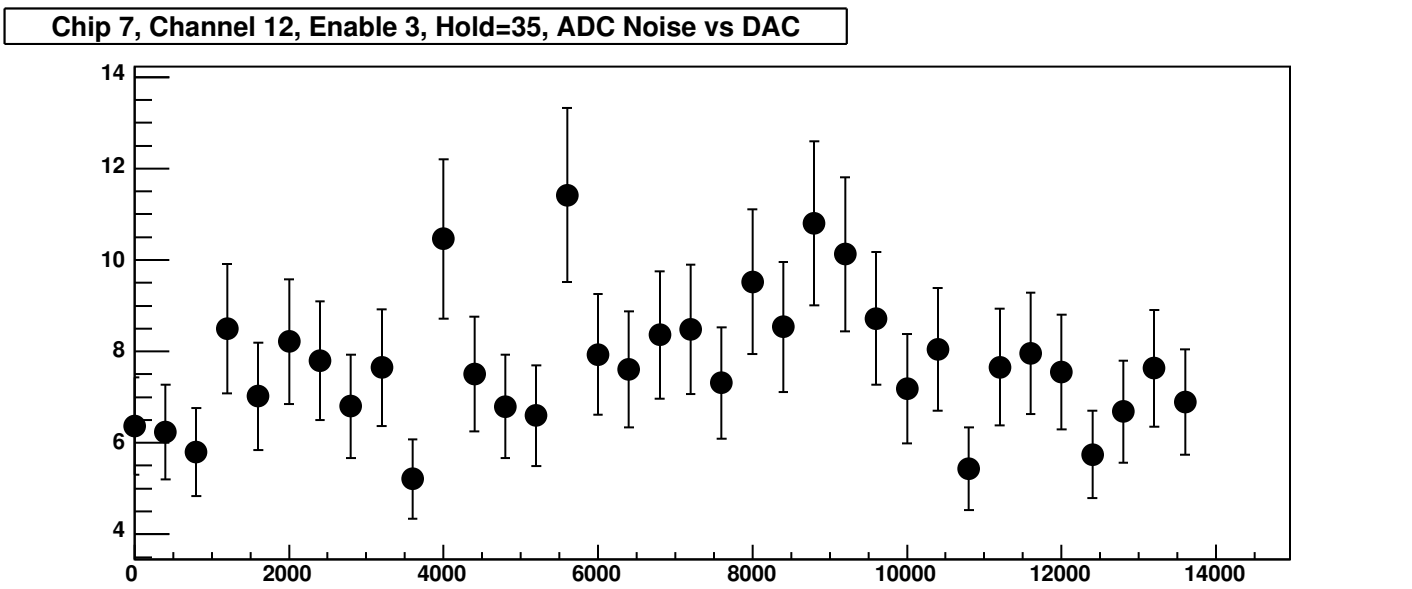

Chip 7, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

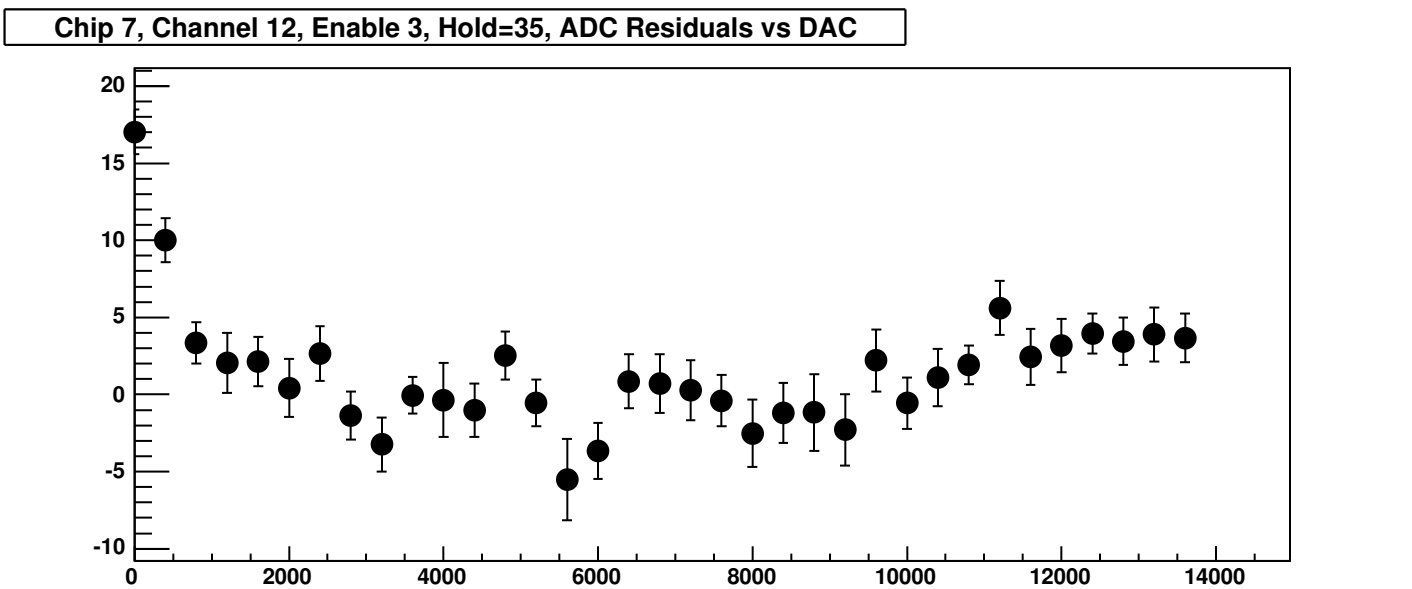

Chip 7, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3817 / 23
p0 -233.3 ± 1.23
p1 -0.2903 ± 0.0004326
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

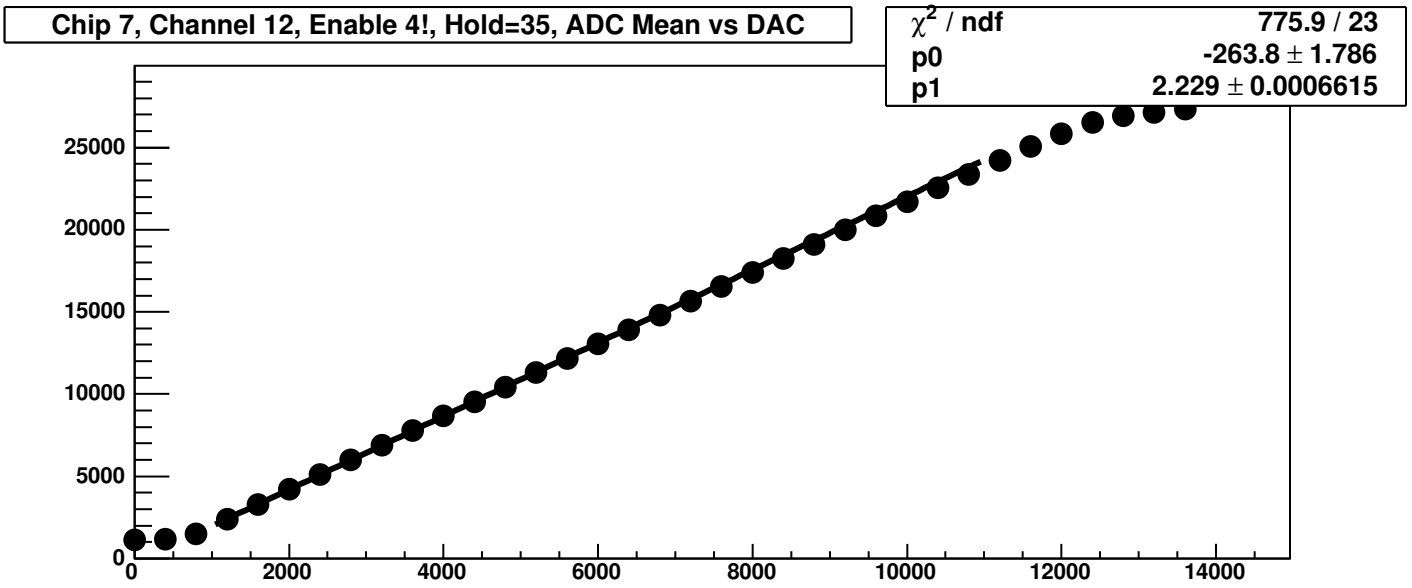

Chip 7, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

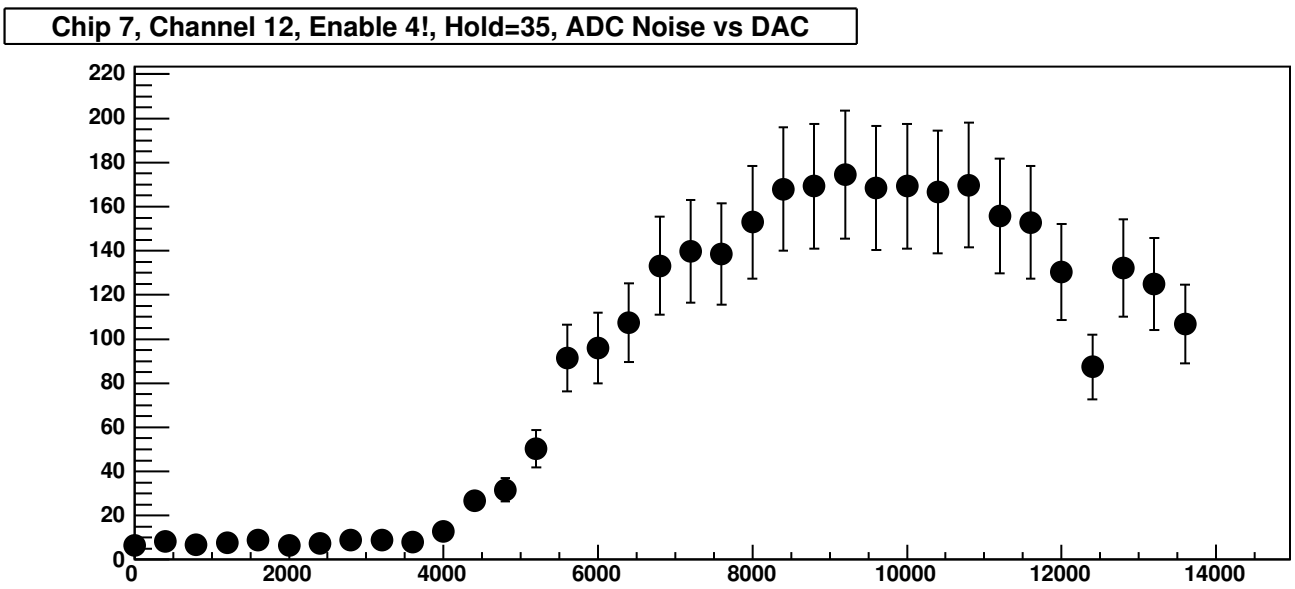

Chip 7, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

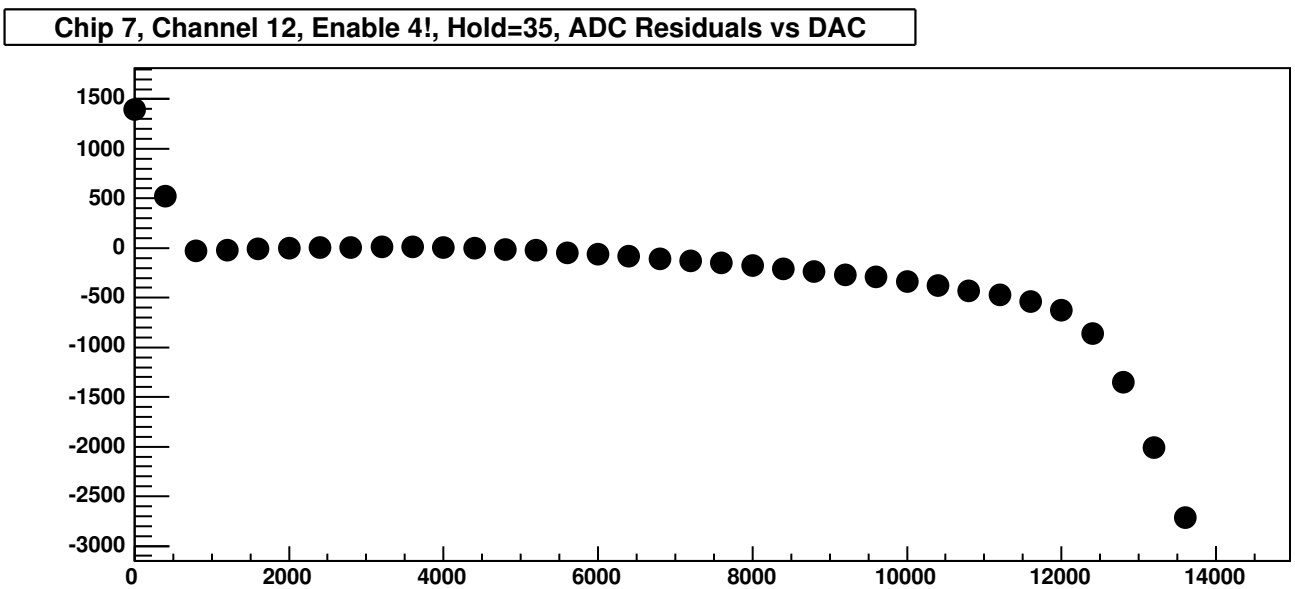

Chip 7, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.29 / 23
p0 -305.5 ± 0.7794
p1 0.0131 ± 0.0001179
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

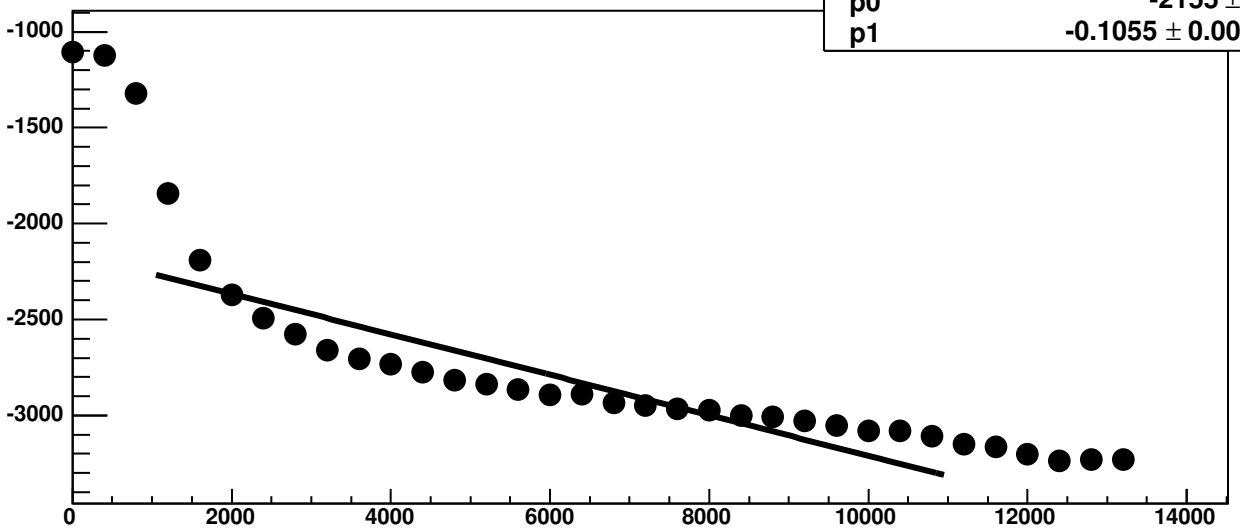

Chip 7, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 775.9 / 23
p0 -263.8 ± 1.786
p1 2.229 ± 0.0006615
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

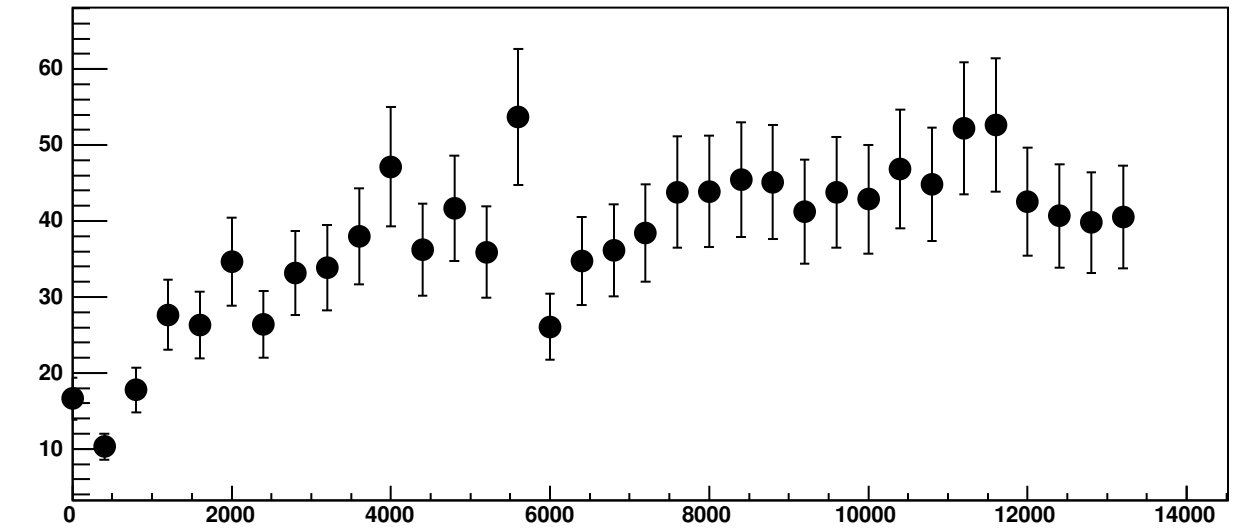

Chip 7, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

Chip 7, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

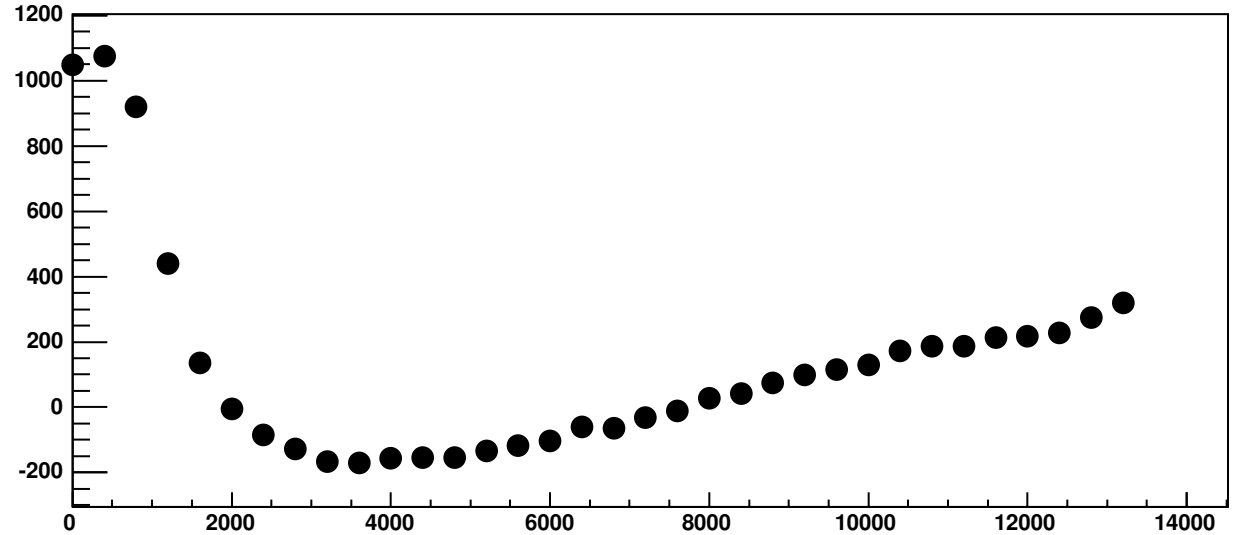
χ^2 / ndf	9284 / 23
p0	-2155 ± 3.446
p1	-0.1055 ± 0.0005803

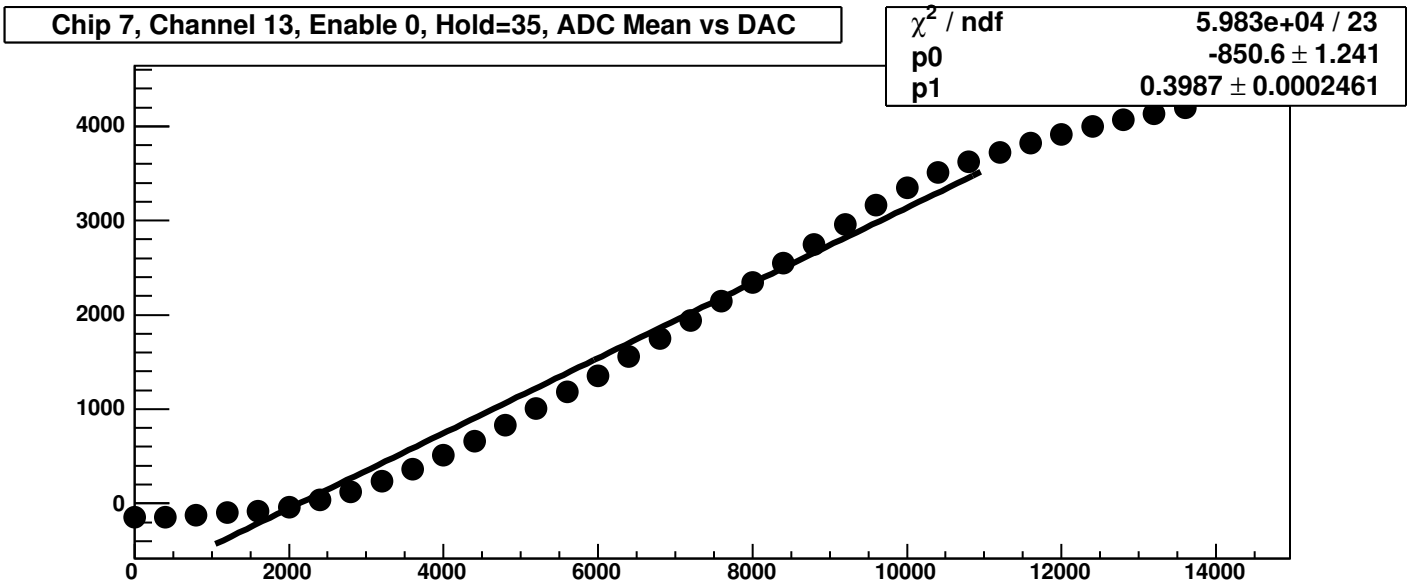


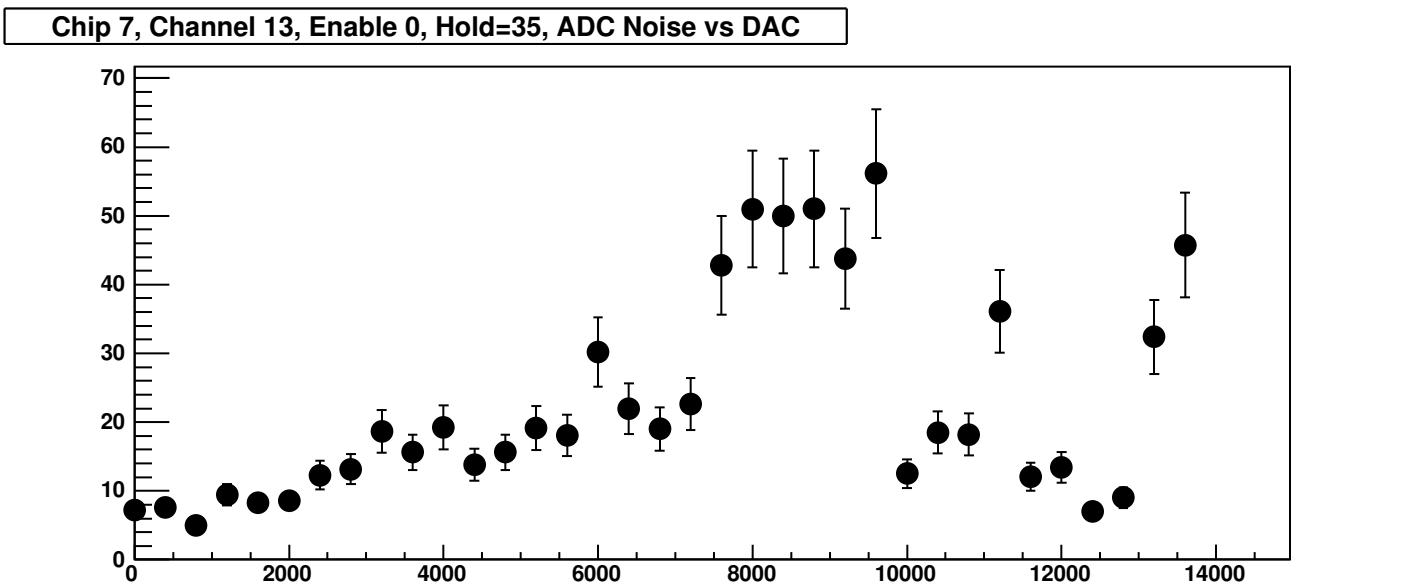
Chip 7, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

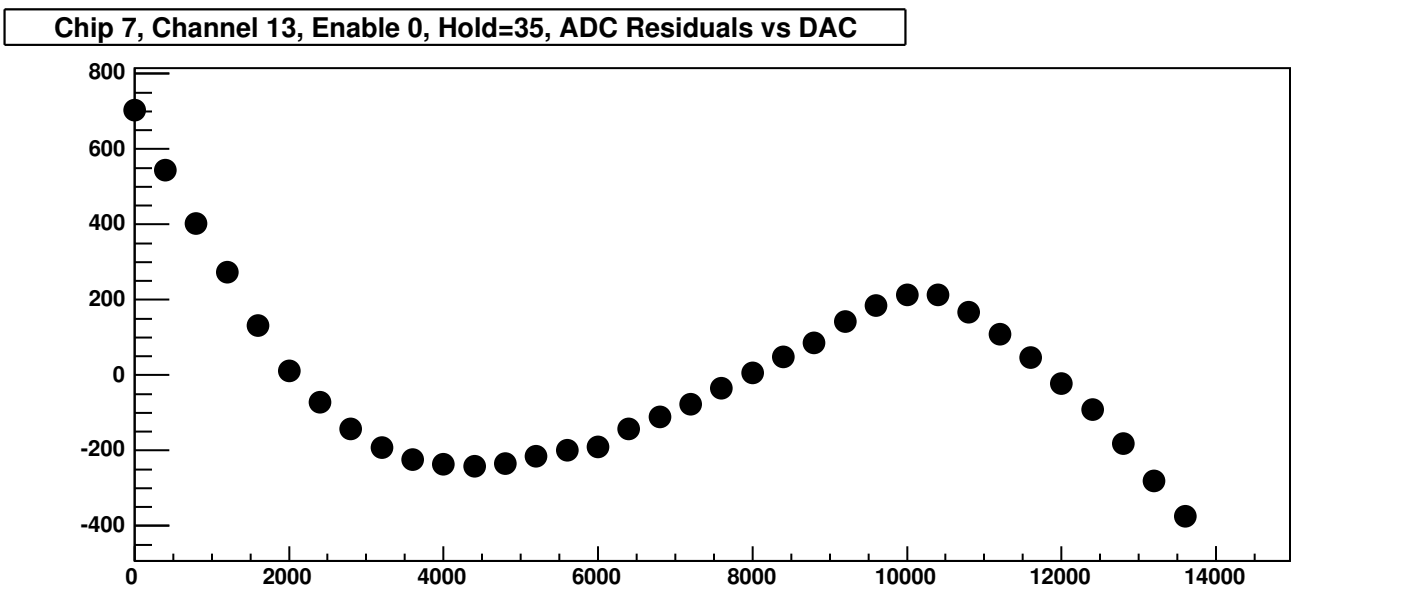


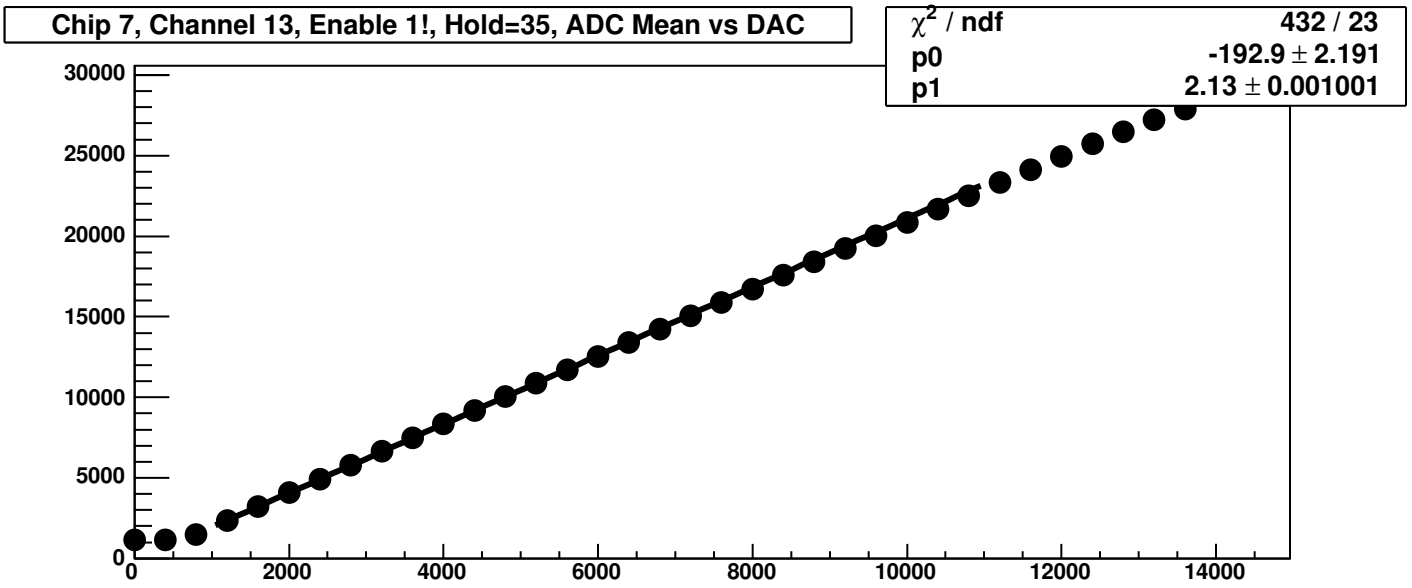
Chip 7, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

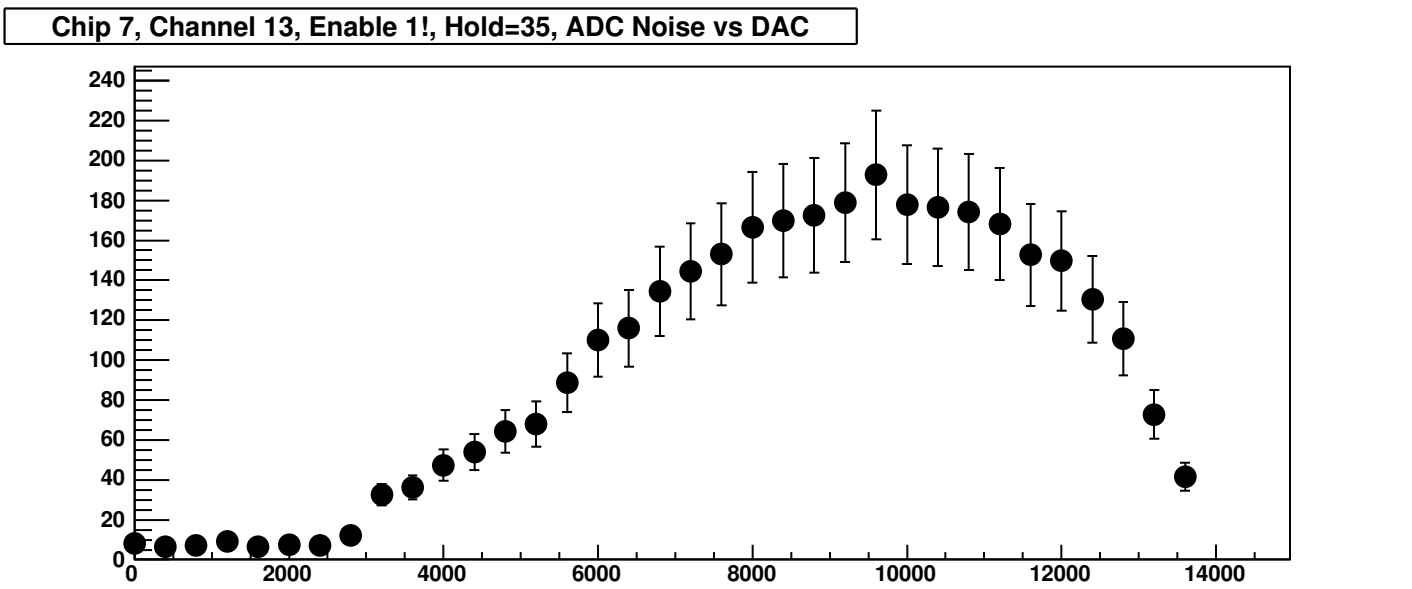


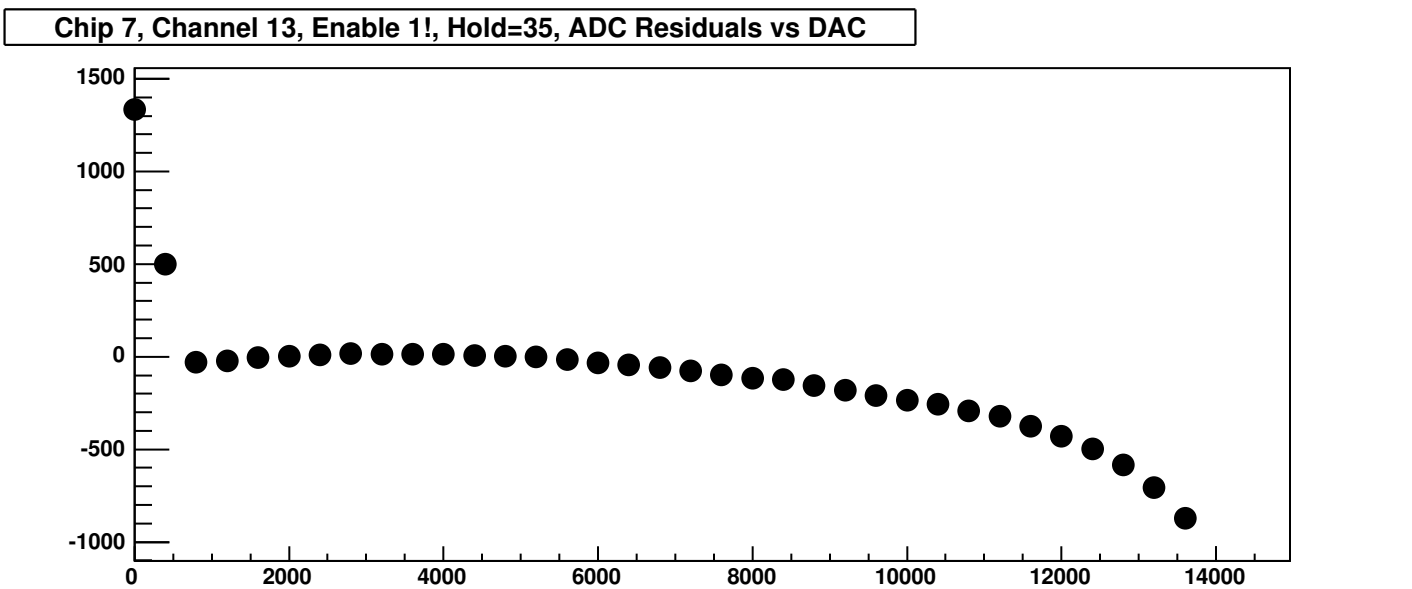

Chip 7, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.983e+04 / 23
p0 -850.6 ± 1.241
p1 0.3987 ± 0.0002461
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

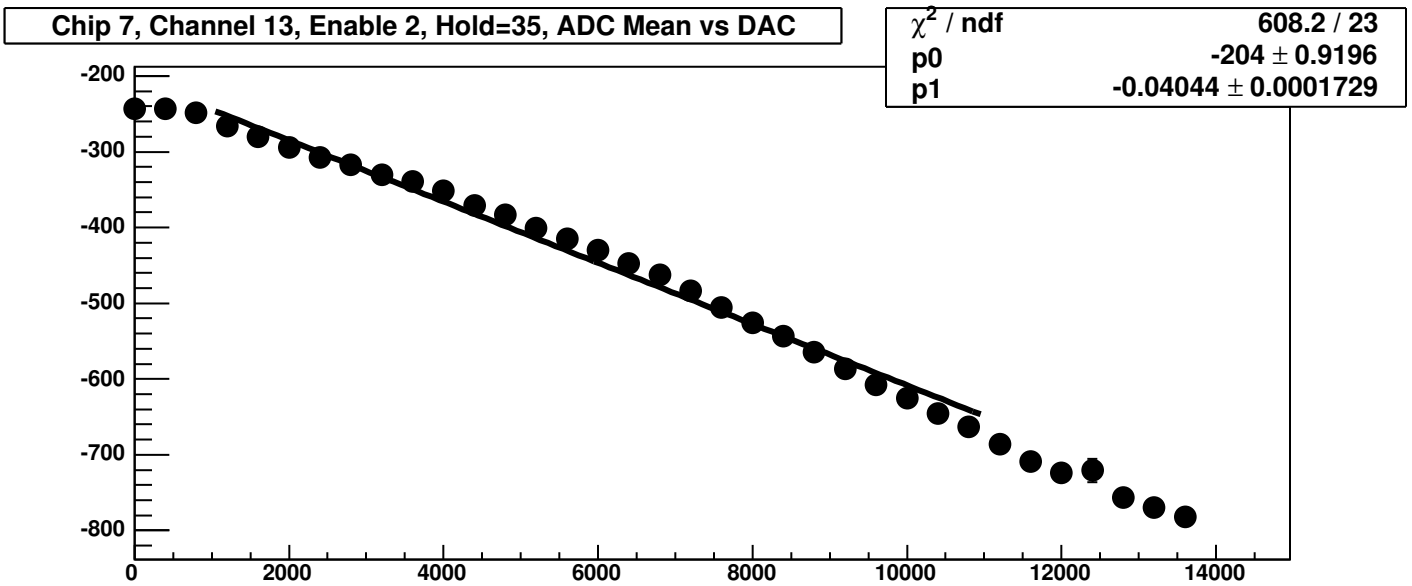

Chip 7, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

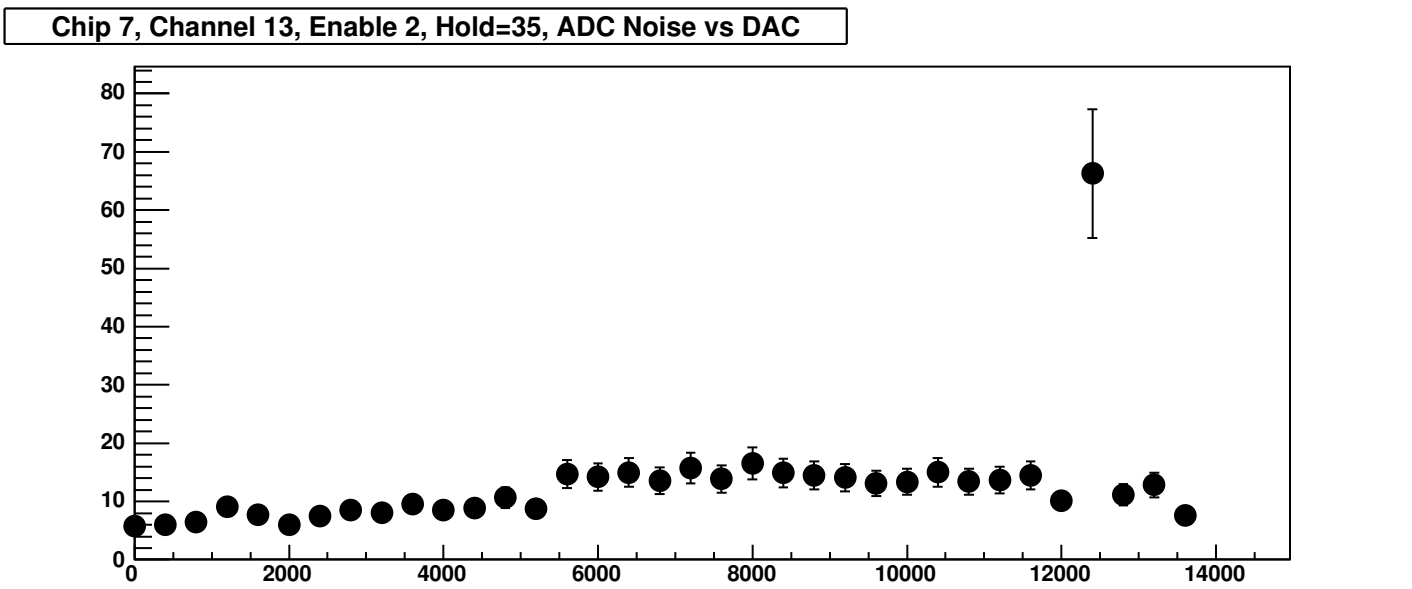

Chip 7, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

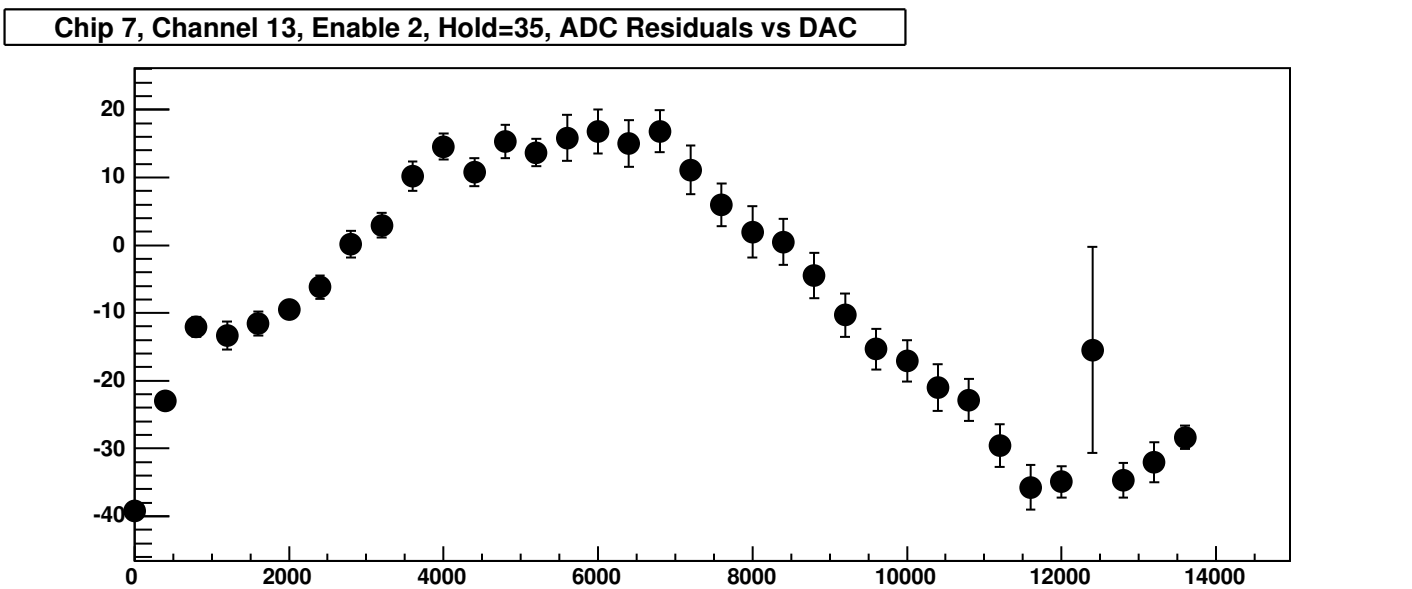

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 432 / 23
p0 -192.9 ± 2.191
p1 2.13 ± 0.001001
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

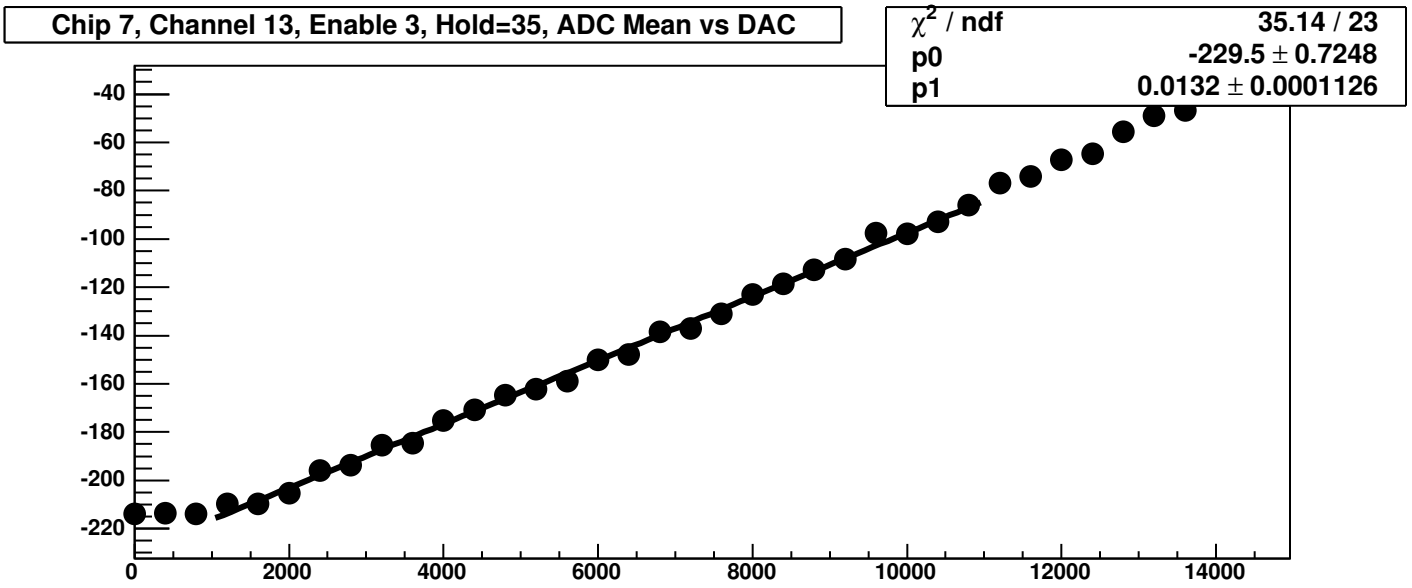

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

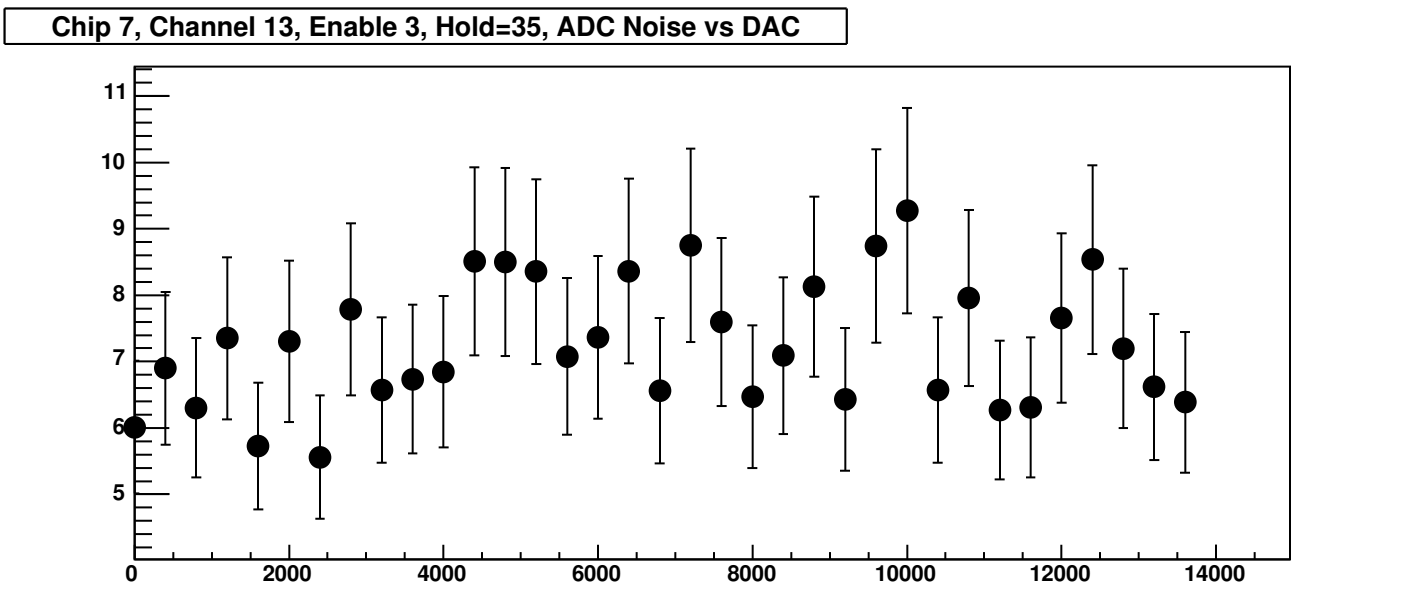

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

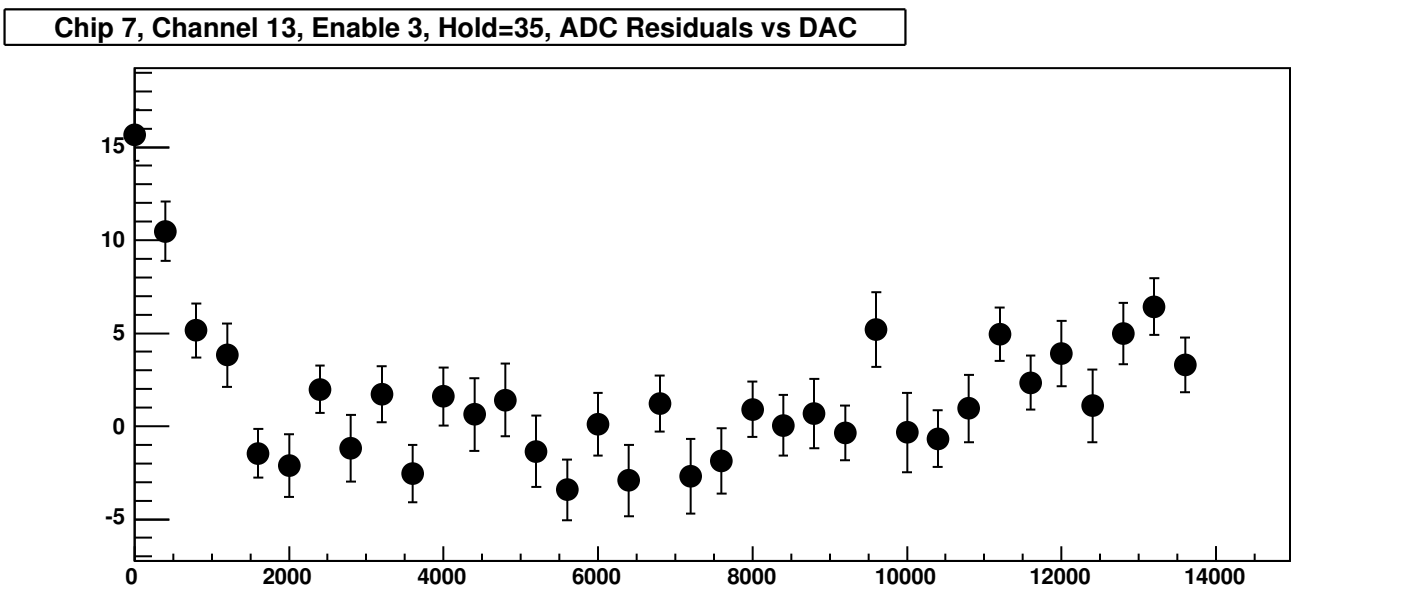

Chip 7, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 608.2 / 23
p0 -204 ± 0.9196
p1 -0.04044 ± 0.0001729
-200
-300
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

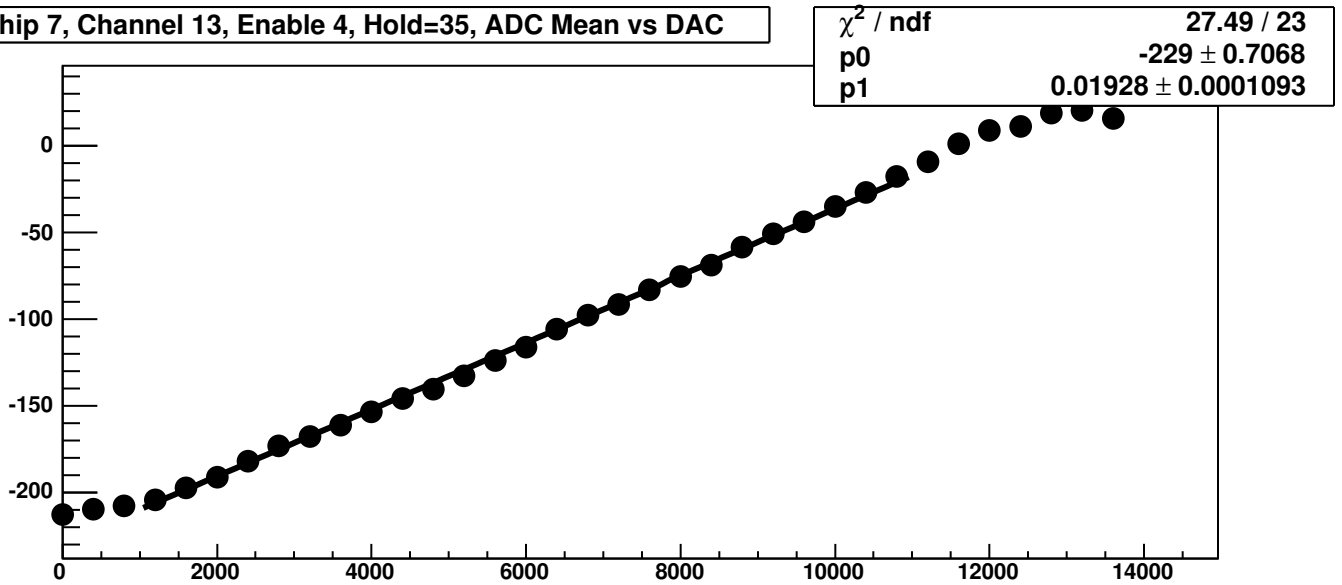

Chip 7, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.14 / 23
p0 -229.5 ± 0.7248
p1 0.0132 ± 0.0001126
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000

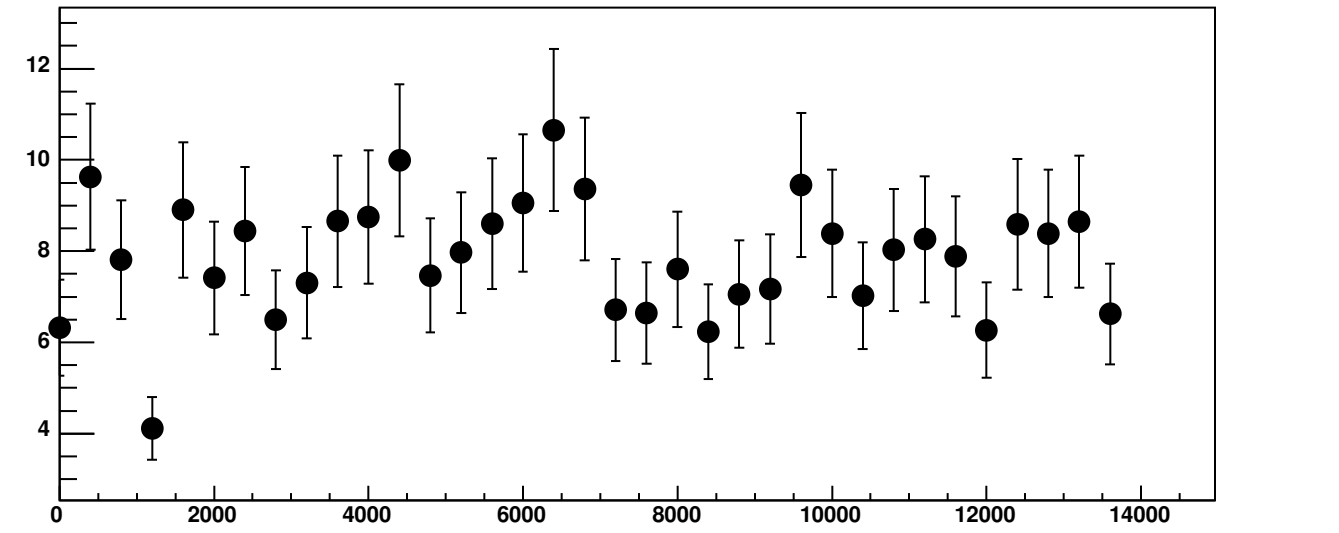

Chip 7, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

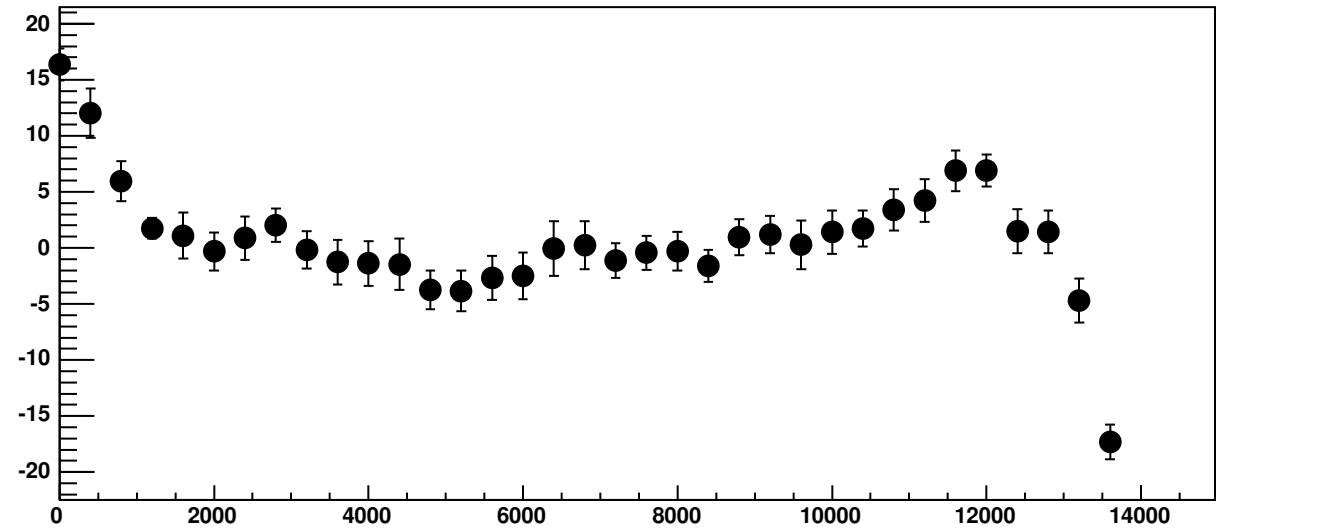
Chip 7, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

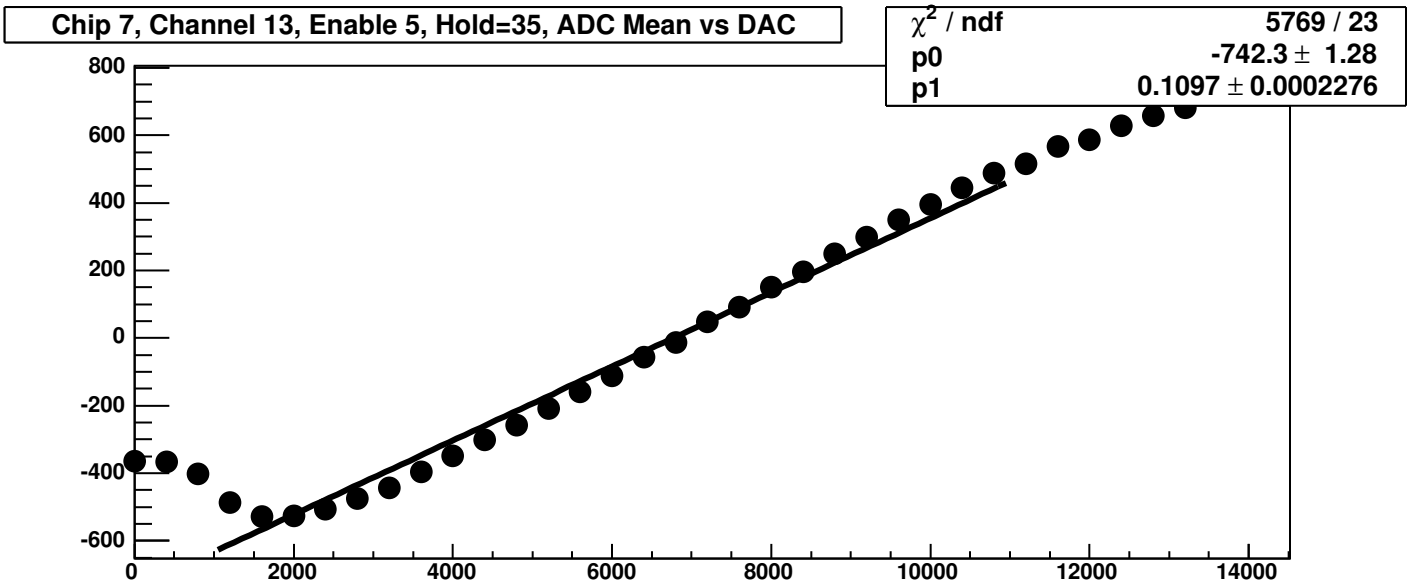


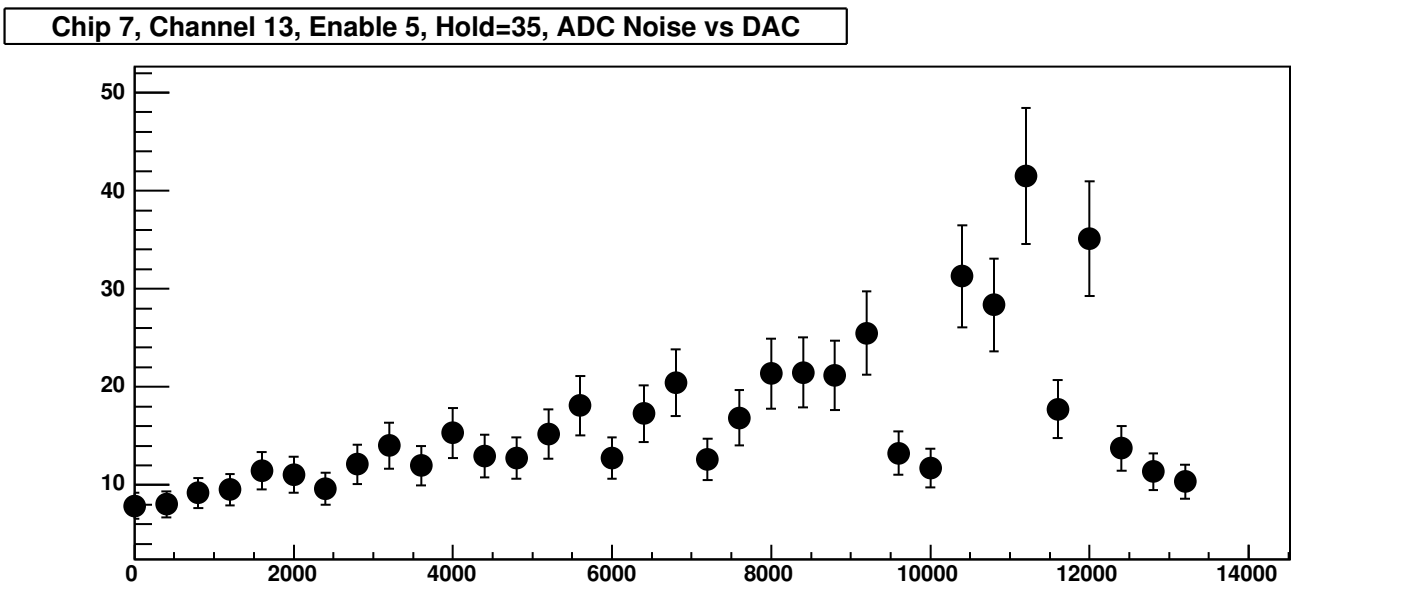
Chip 7, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

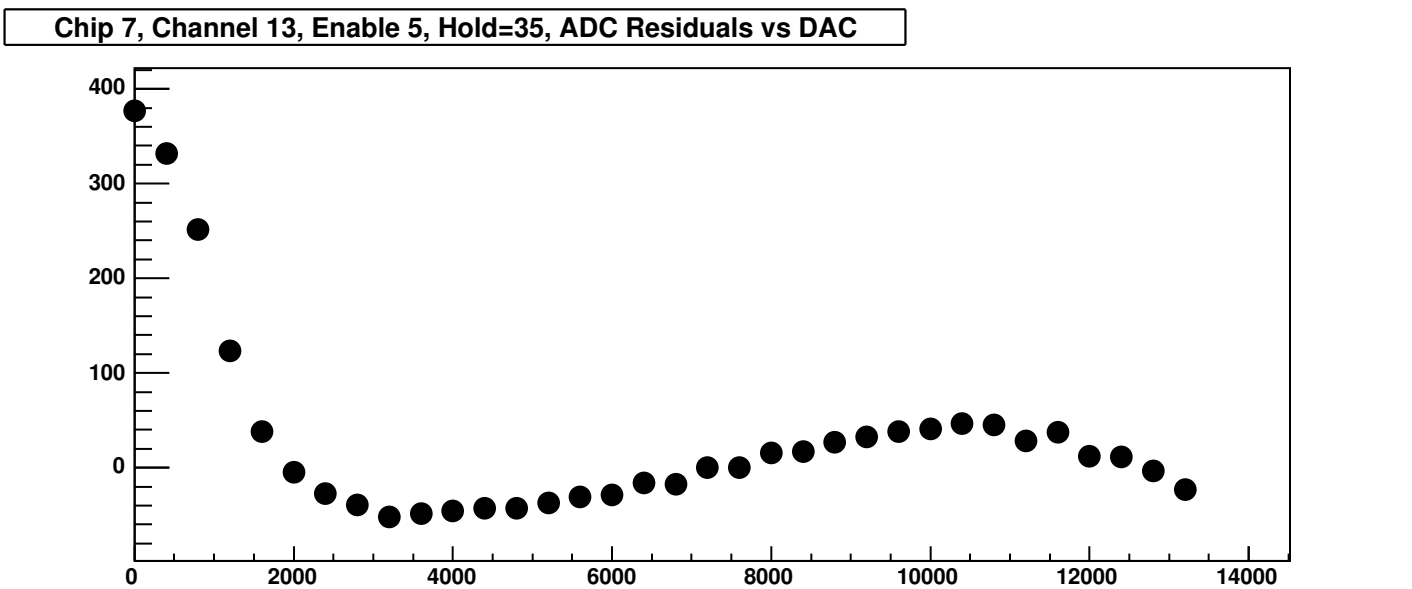


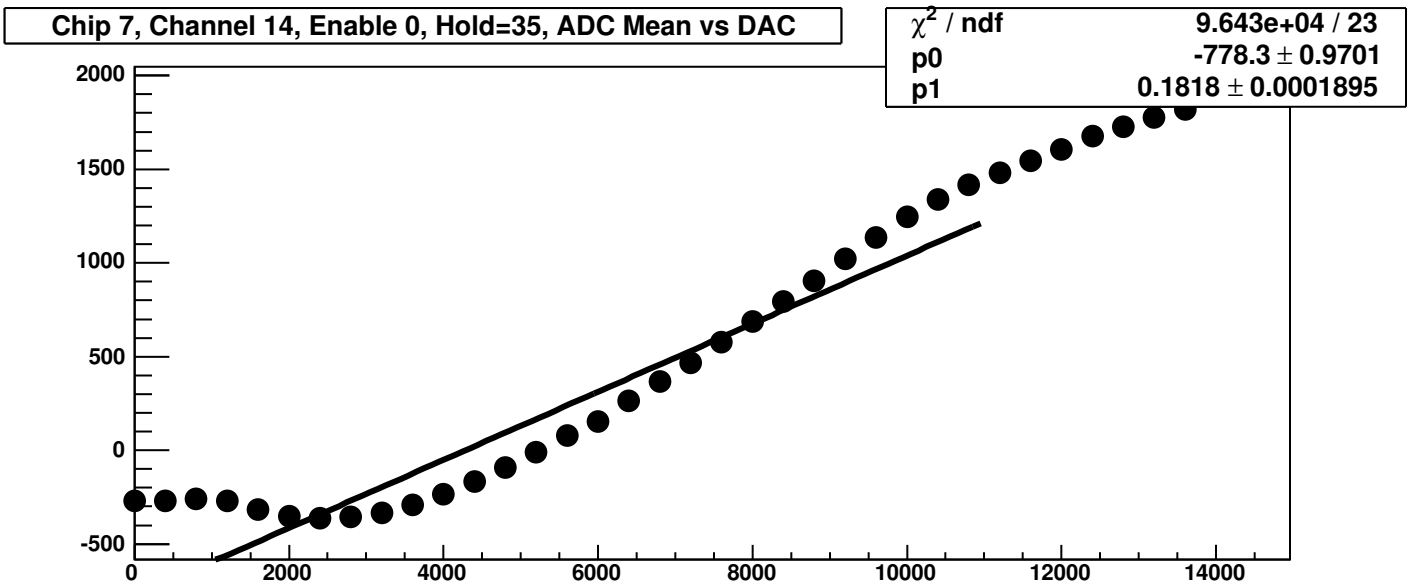
Chip 7, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

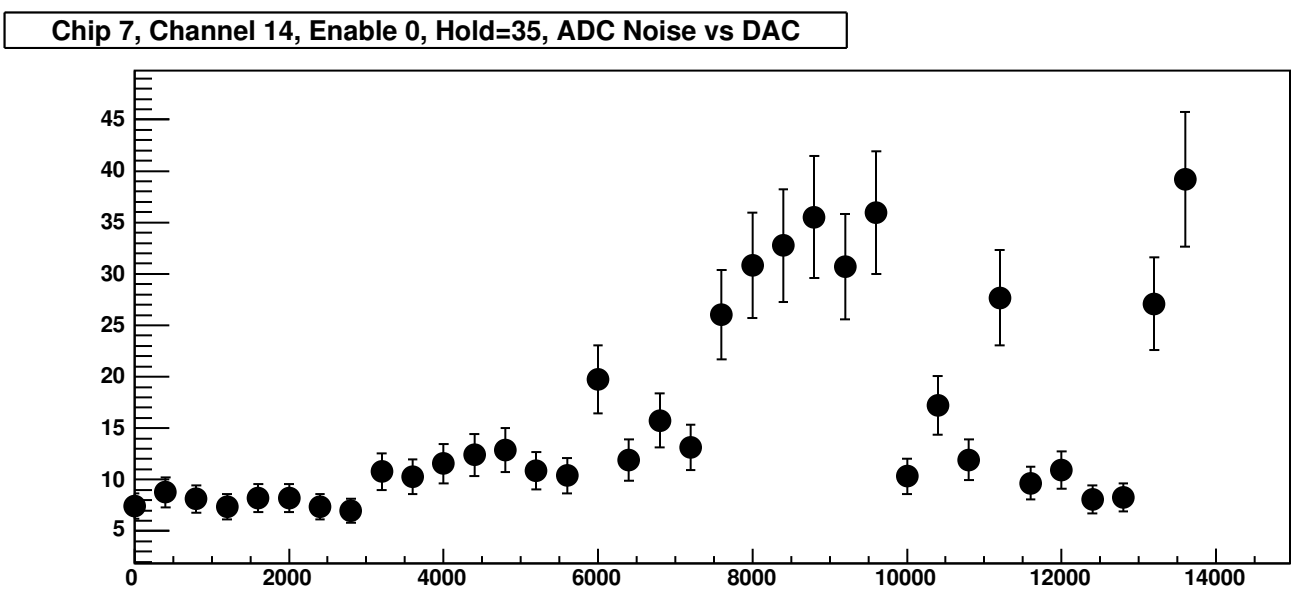


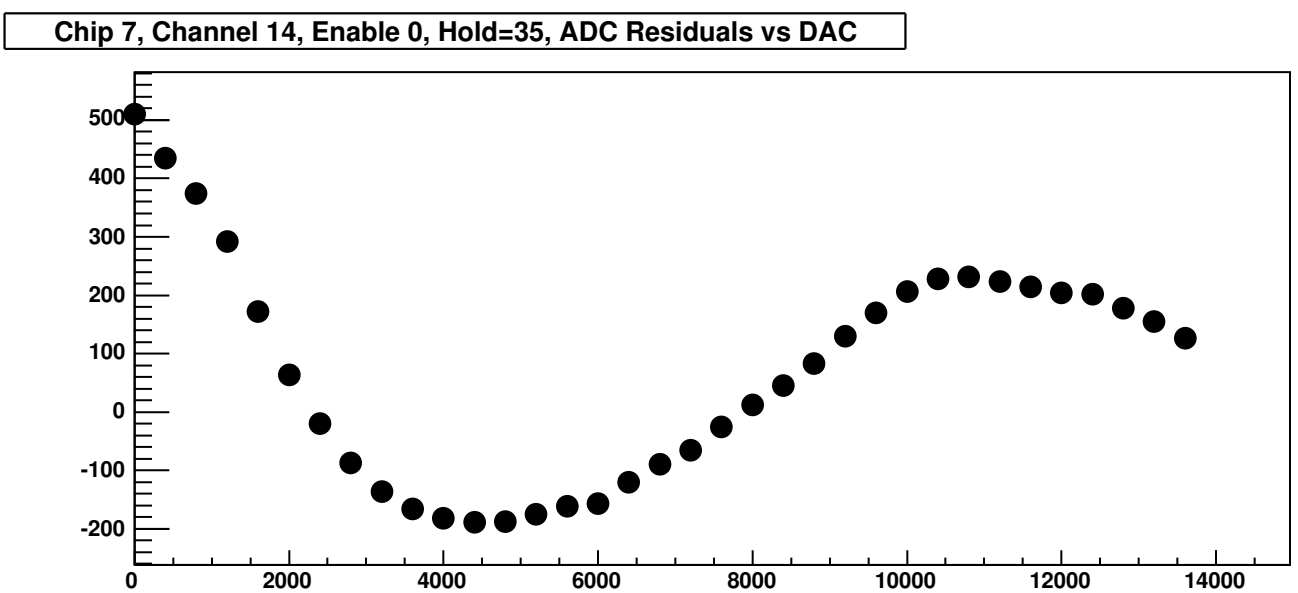

Chip 7, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5769 / 23
p0 -742.3 ± 1.28
p1 0.1097 ± 0.0002276
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

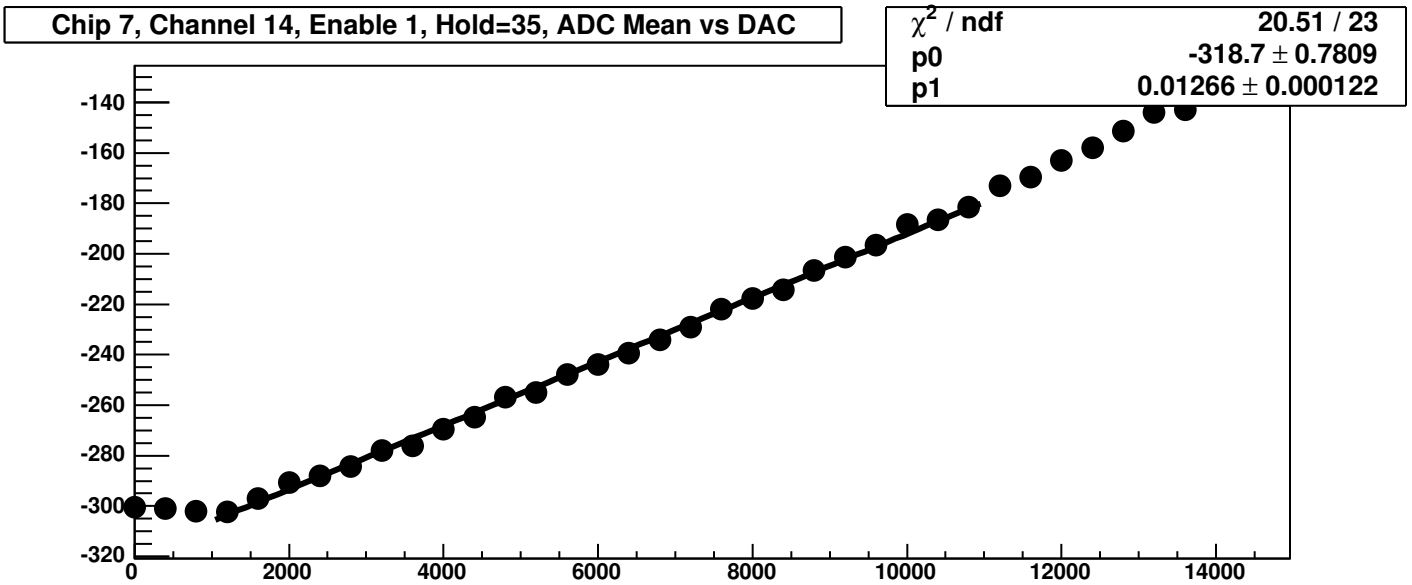

Chip 7, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

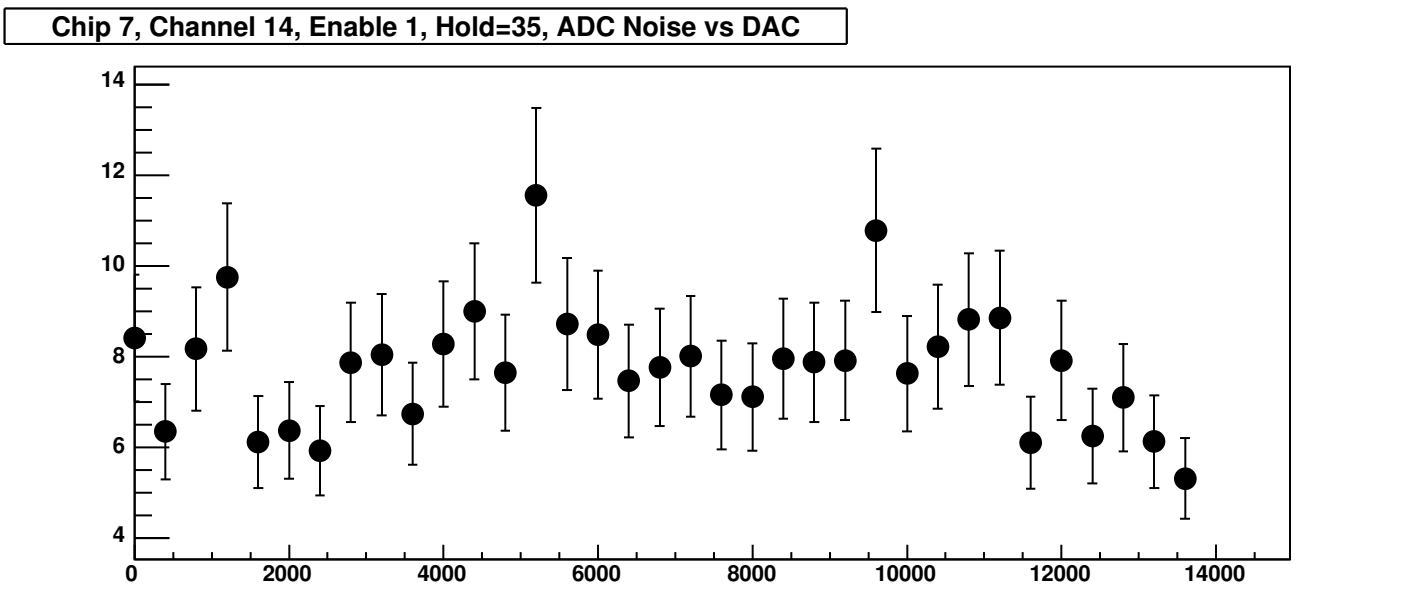

Chip 7, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

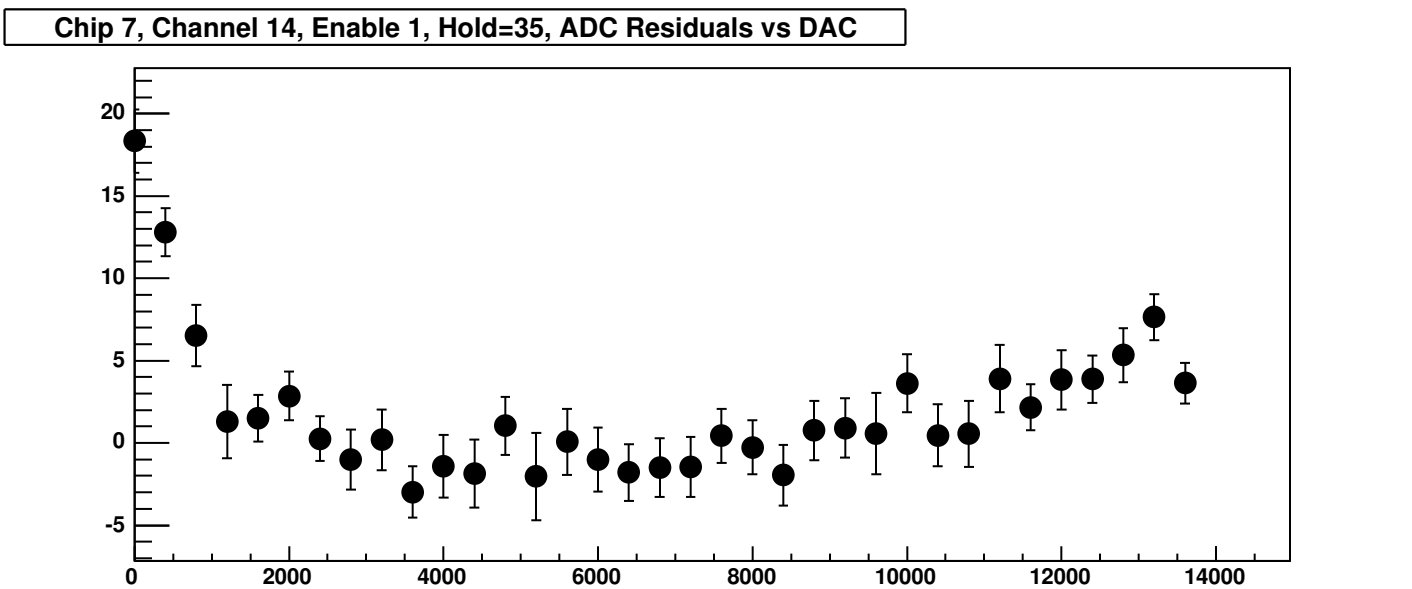

Chip 7, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9.643e+04 / 23
p0 -778.3 ± 0.9701
p1 0.1818 ± 0.0001895
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

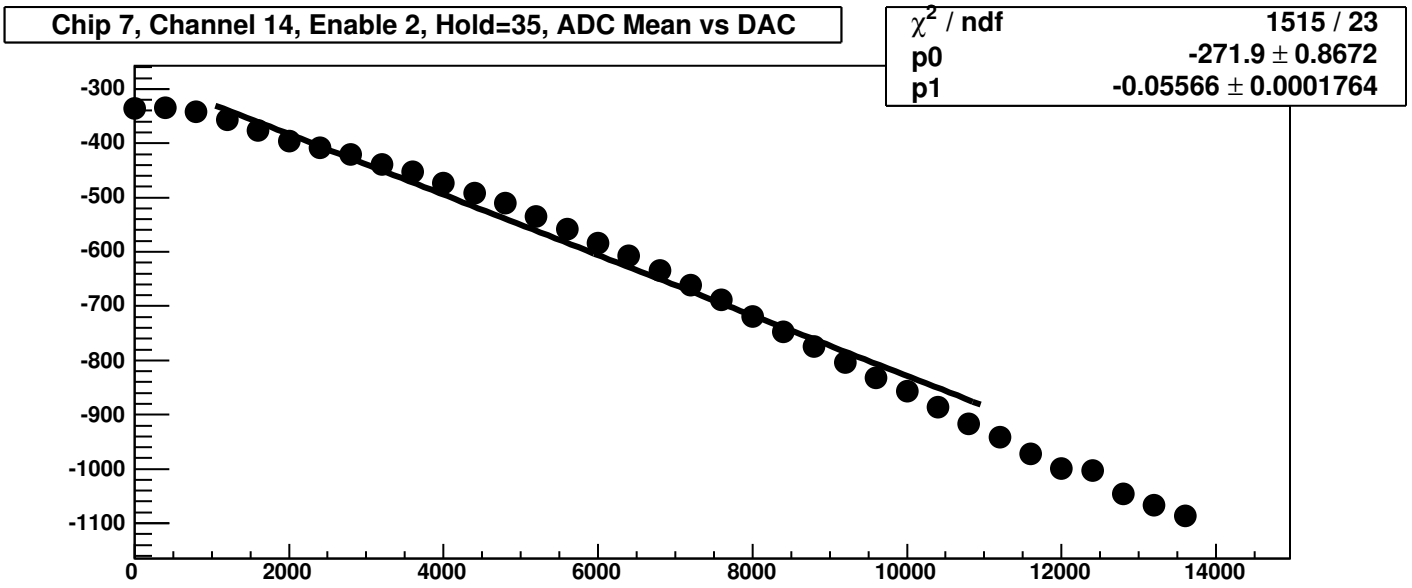

Chip 7, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

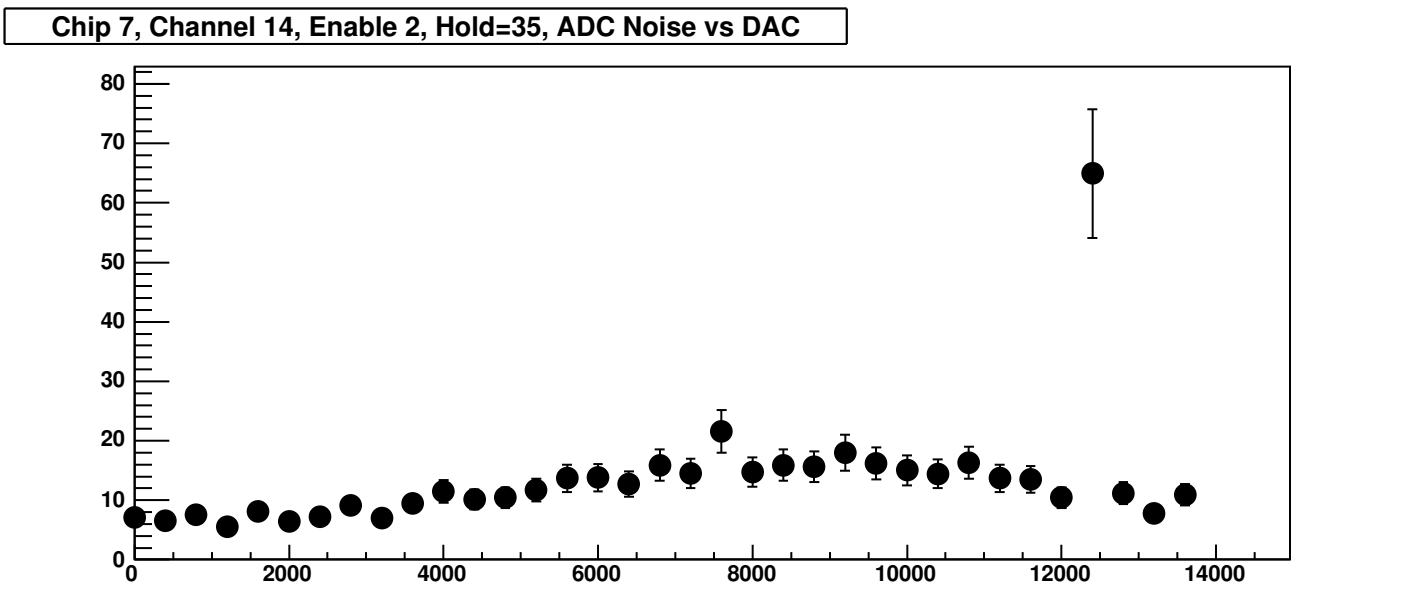

Chip 7, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

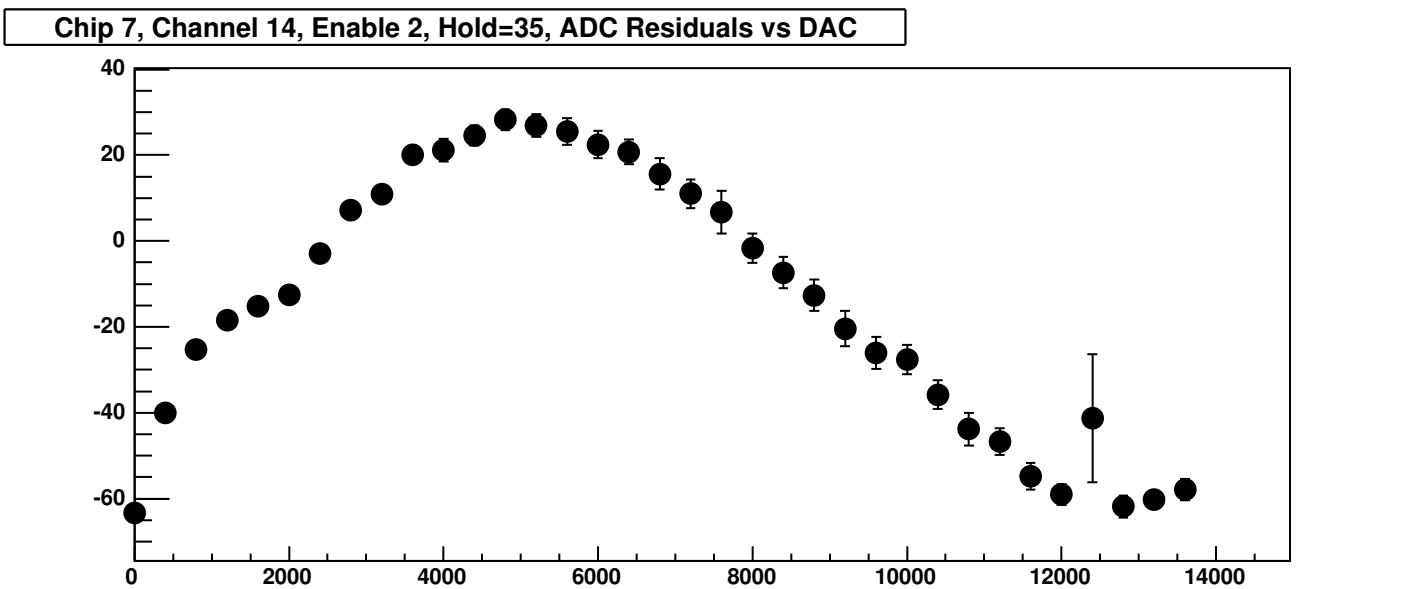

Chip 7, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.51 / 23
p0 -318.7 ± 0.7809
p1 0.01266 ± 0.000122
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

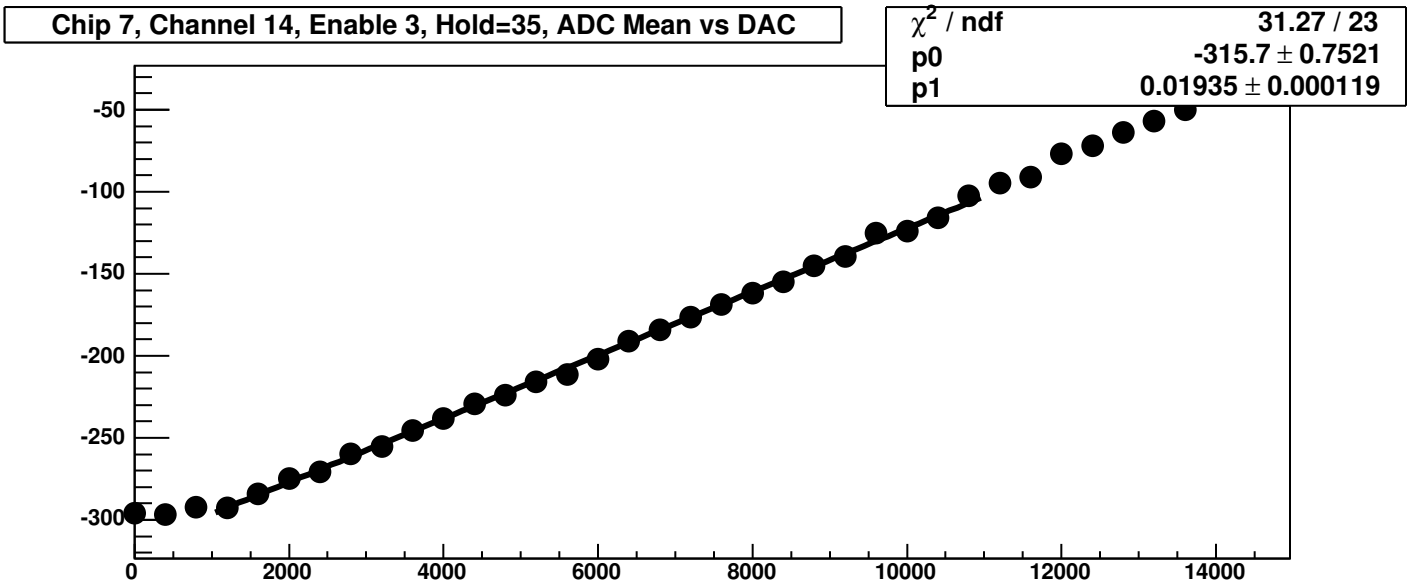

Chip 7, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

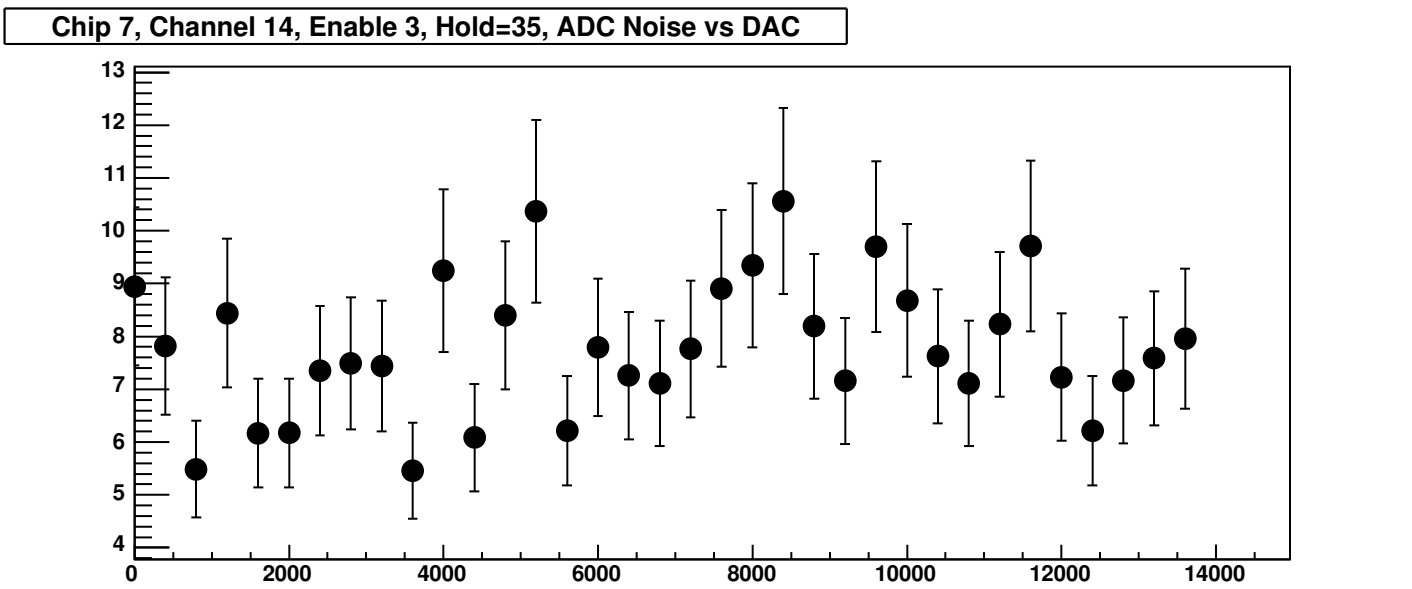

Chip 7, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

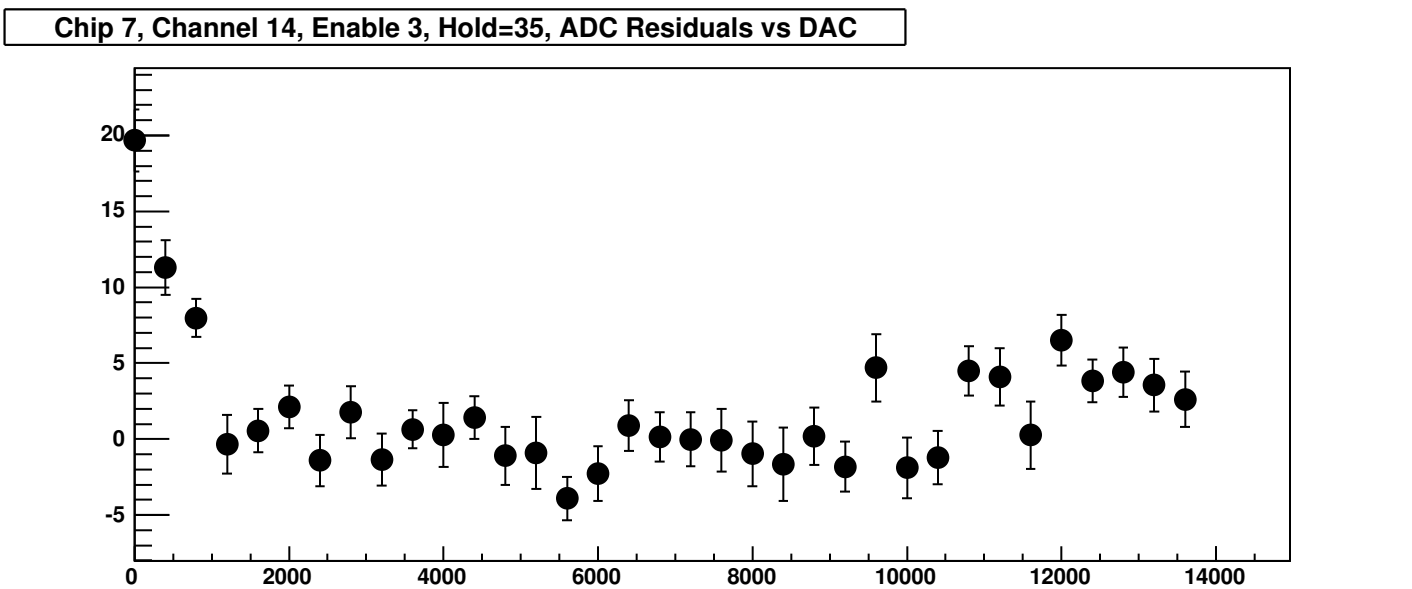

Chip 7, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1515 / 23
p0 -271.9 ± 0.8672
p1 -0.05566 ± 0.0001764
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

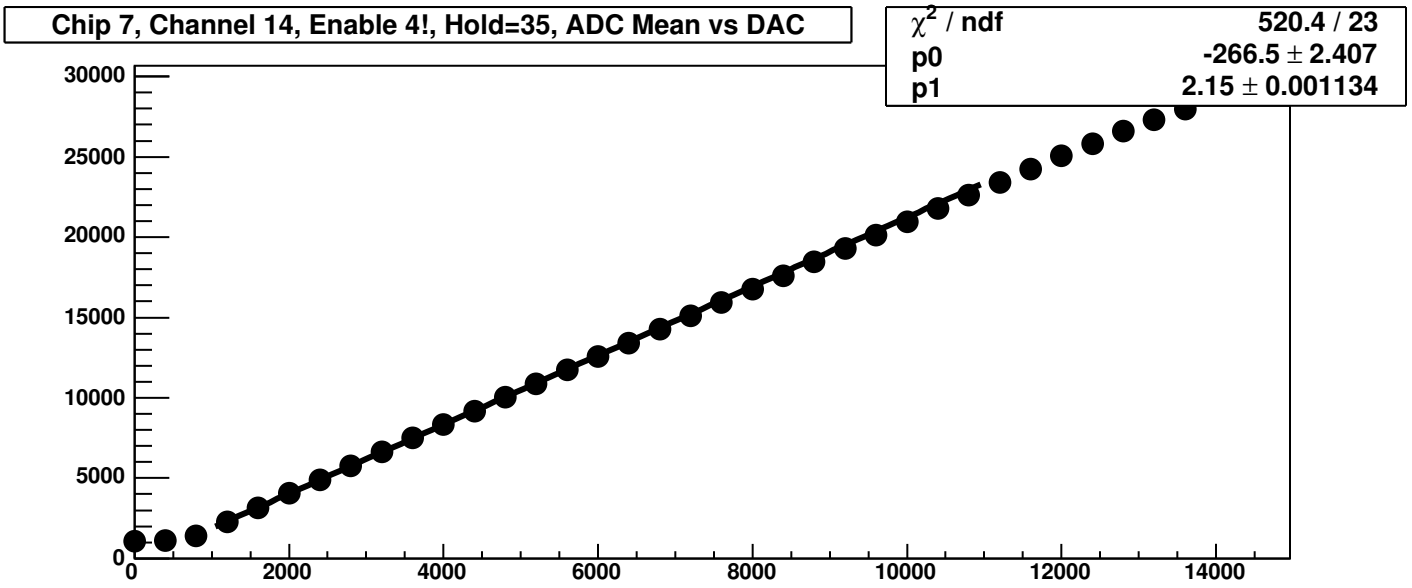

Chip 7, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

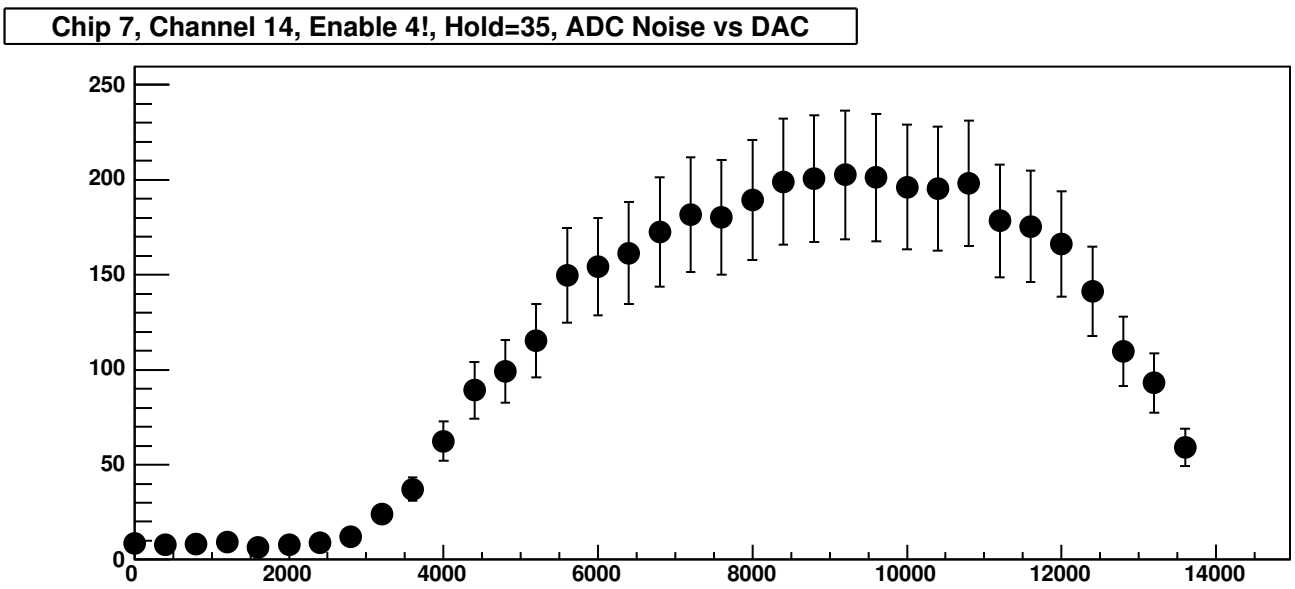

Chip 7, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

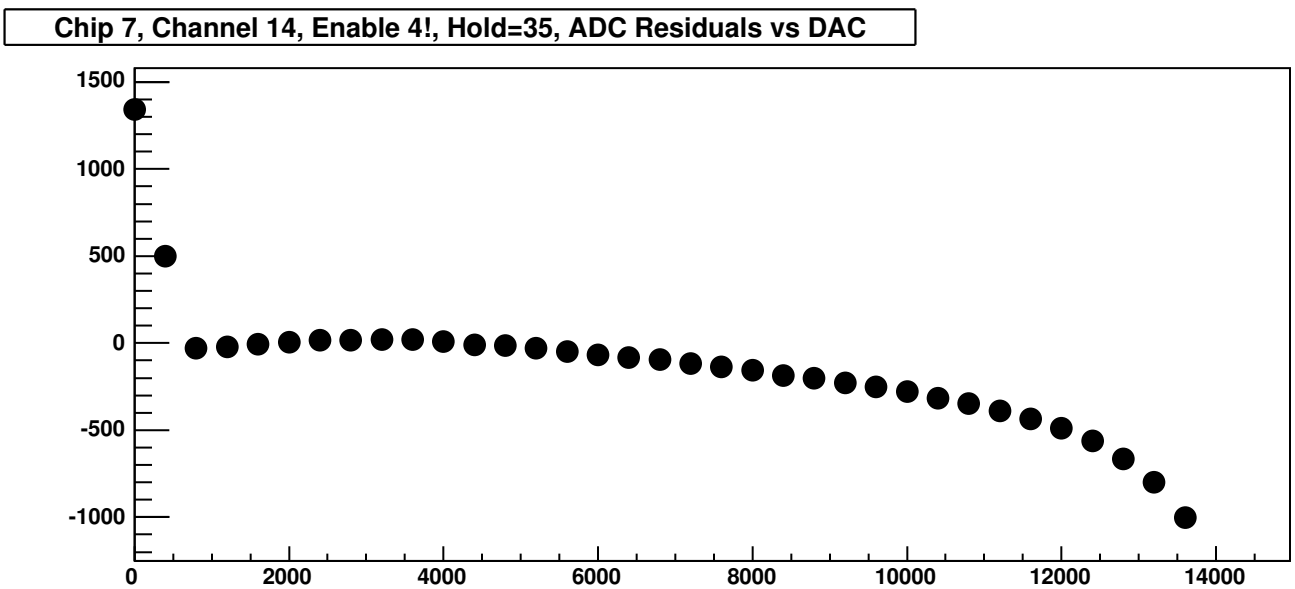

Chip 7, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.27 / 23
p0 -315.7 ± 0.7521
p1 0.01935 ± 0.000119
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

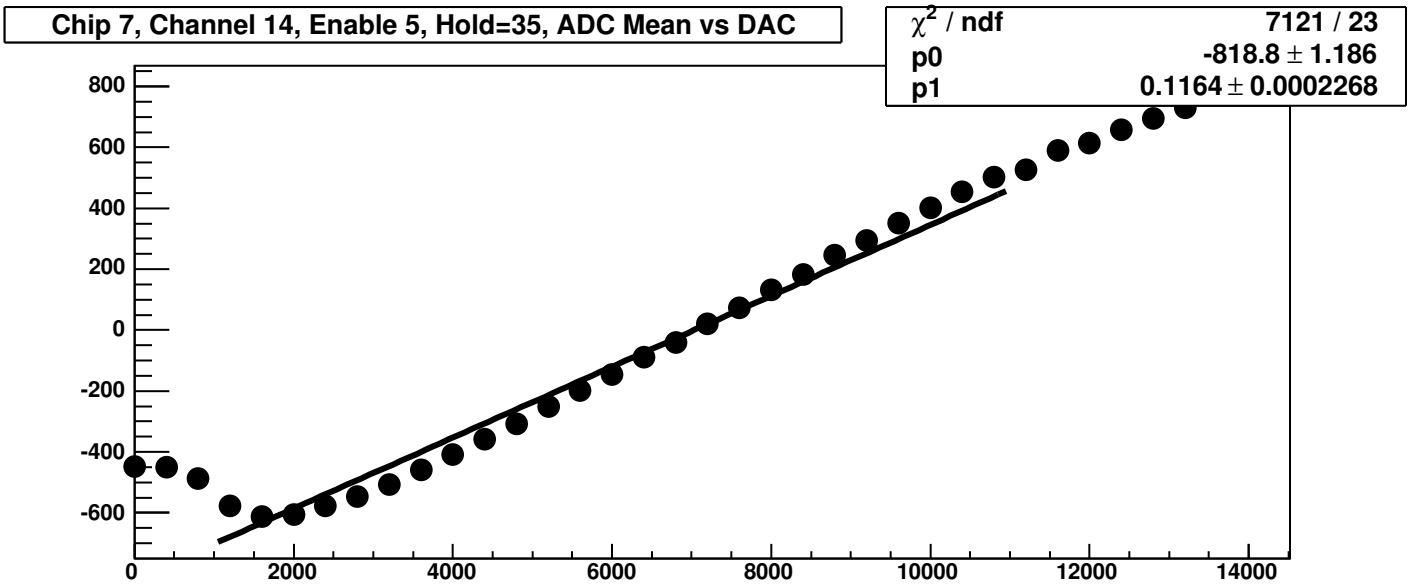

Chip 7, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

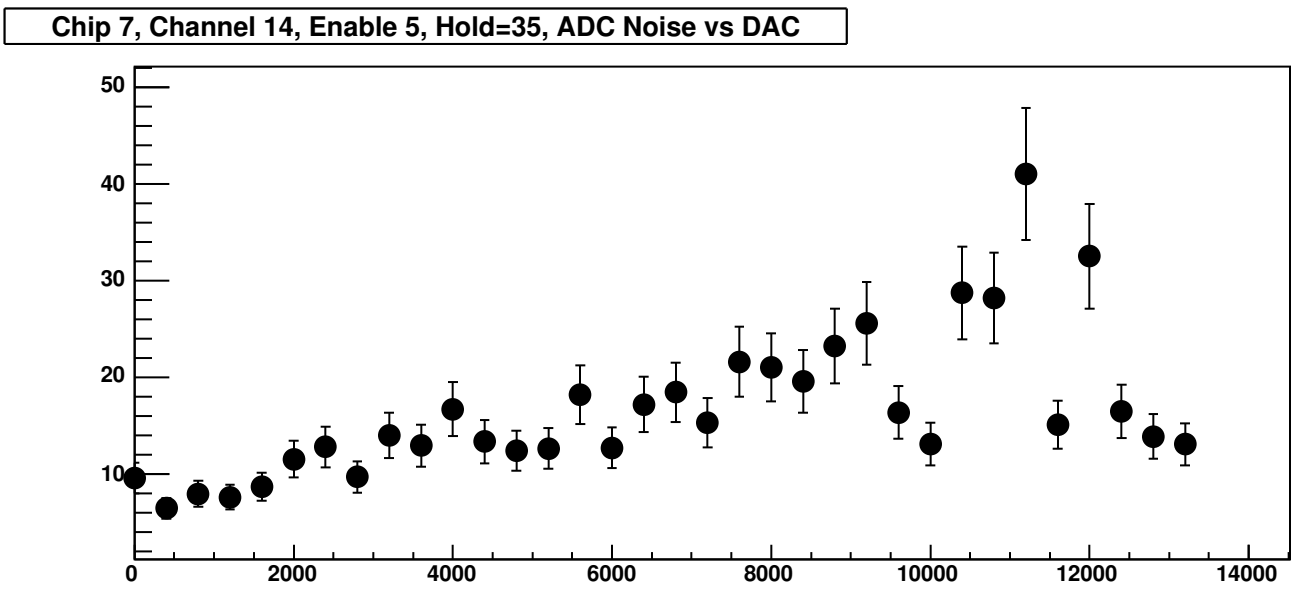

Chip 7, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

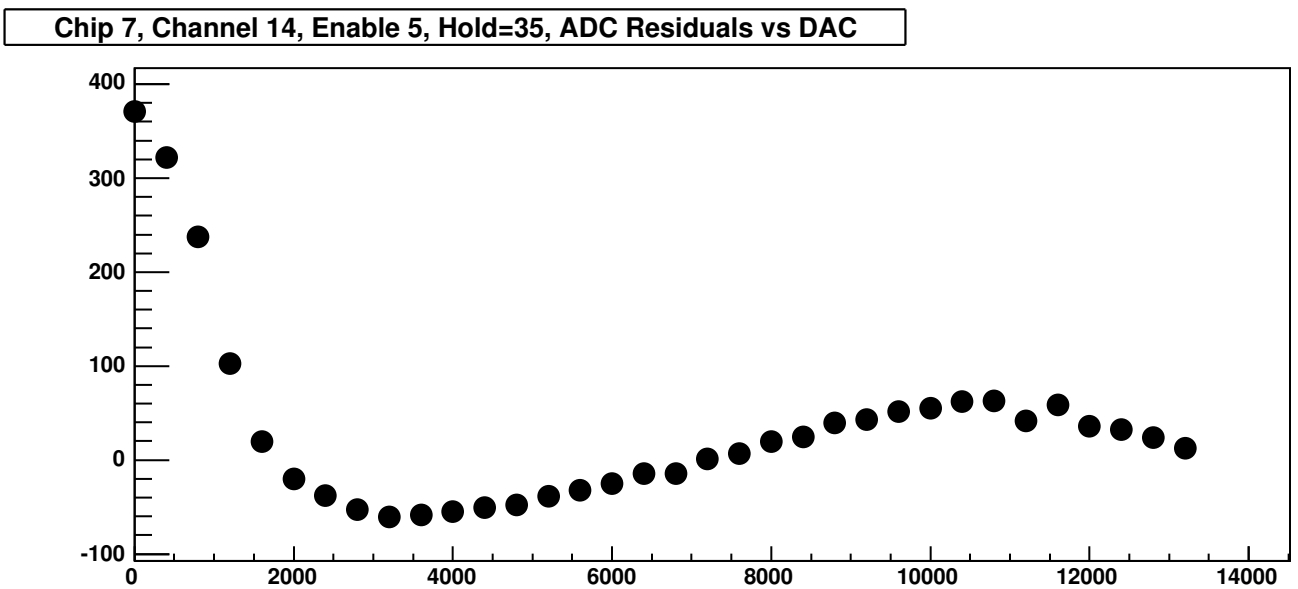

Chip 7, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 520.4 / 23
p0 -266.5 ± 2.407
p1 2.15 ± 0.001134
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

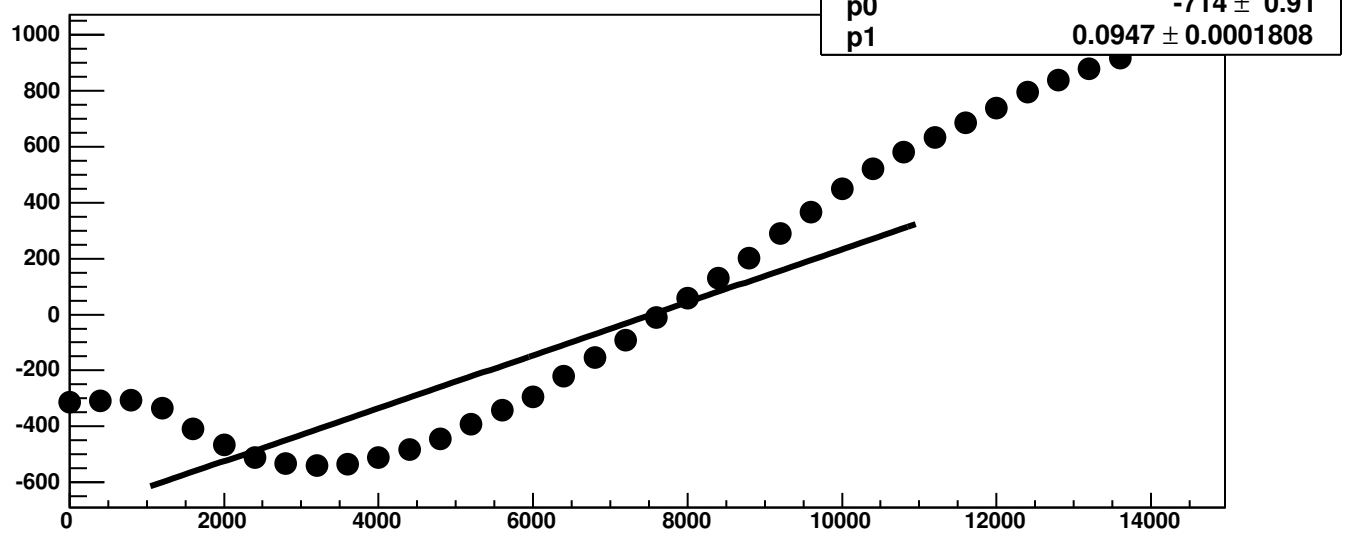

Chip 7, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7121 / 23
p0 -818.8 ± 1.186
p1 0.1164 ± 0.0002268
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

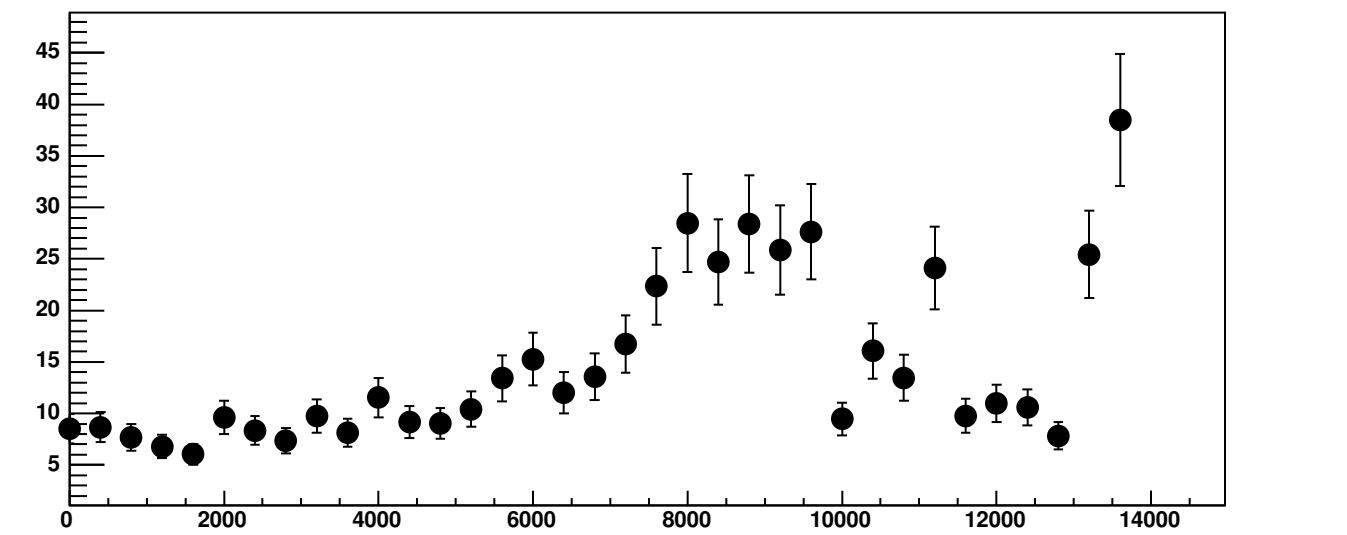

Chip 7, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

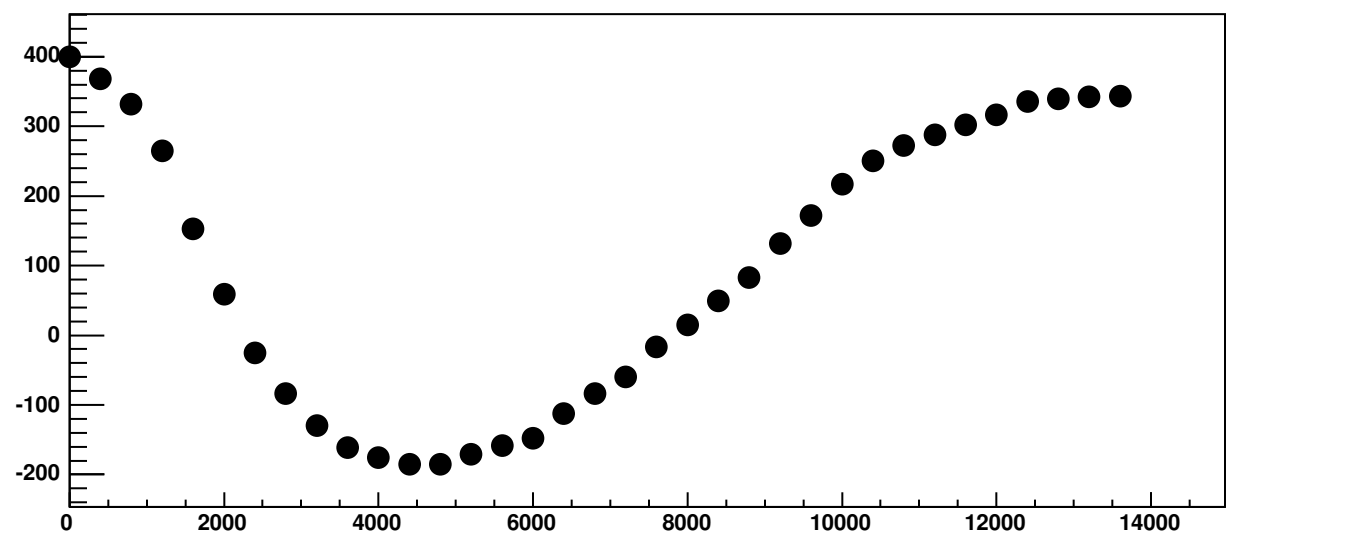
Chip 7, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

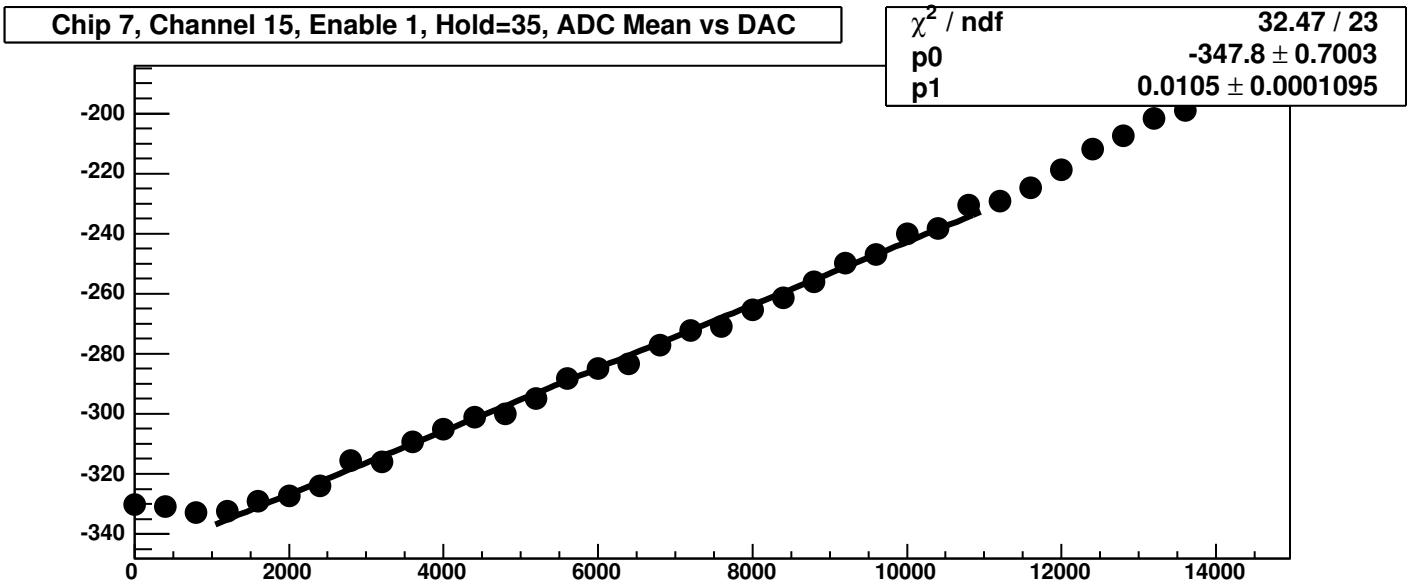


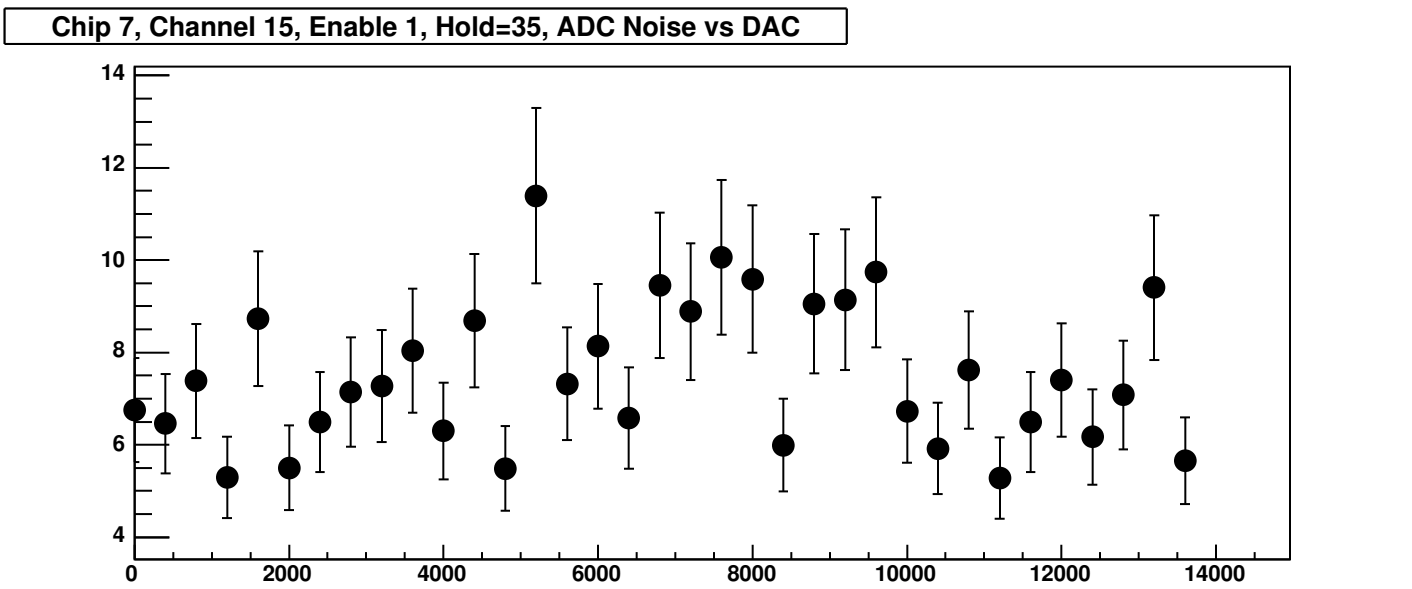
Chip 7, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

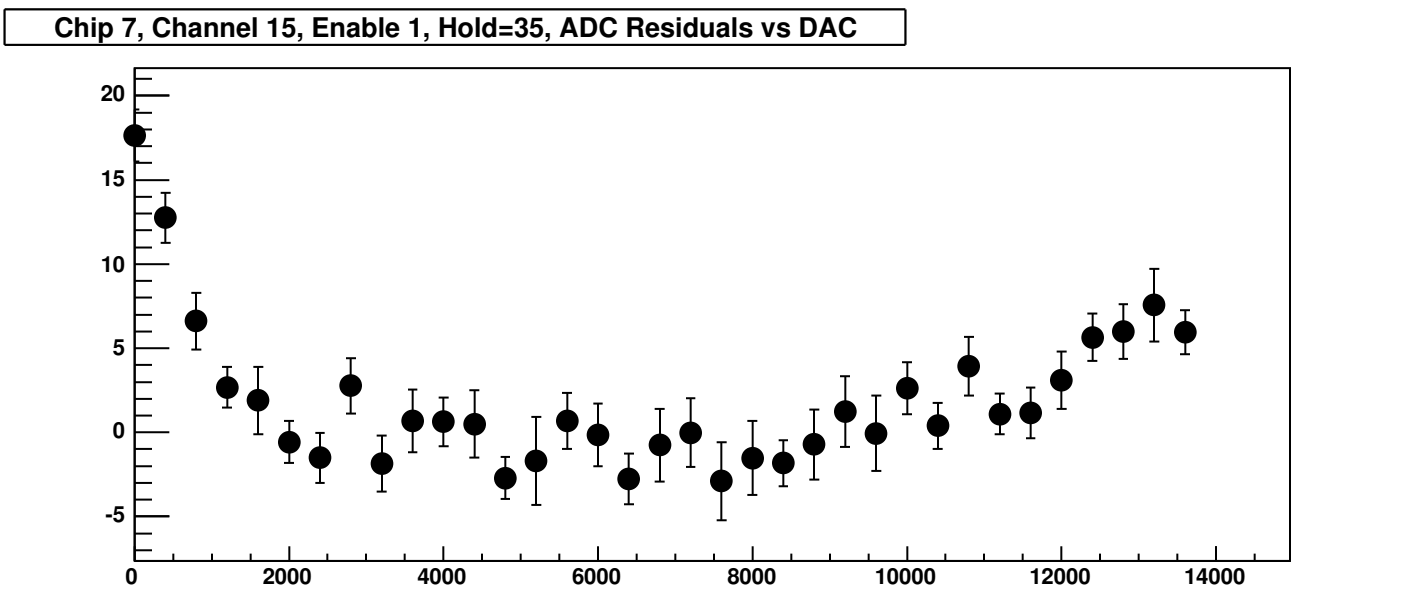


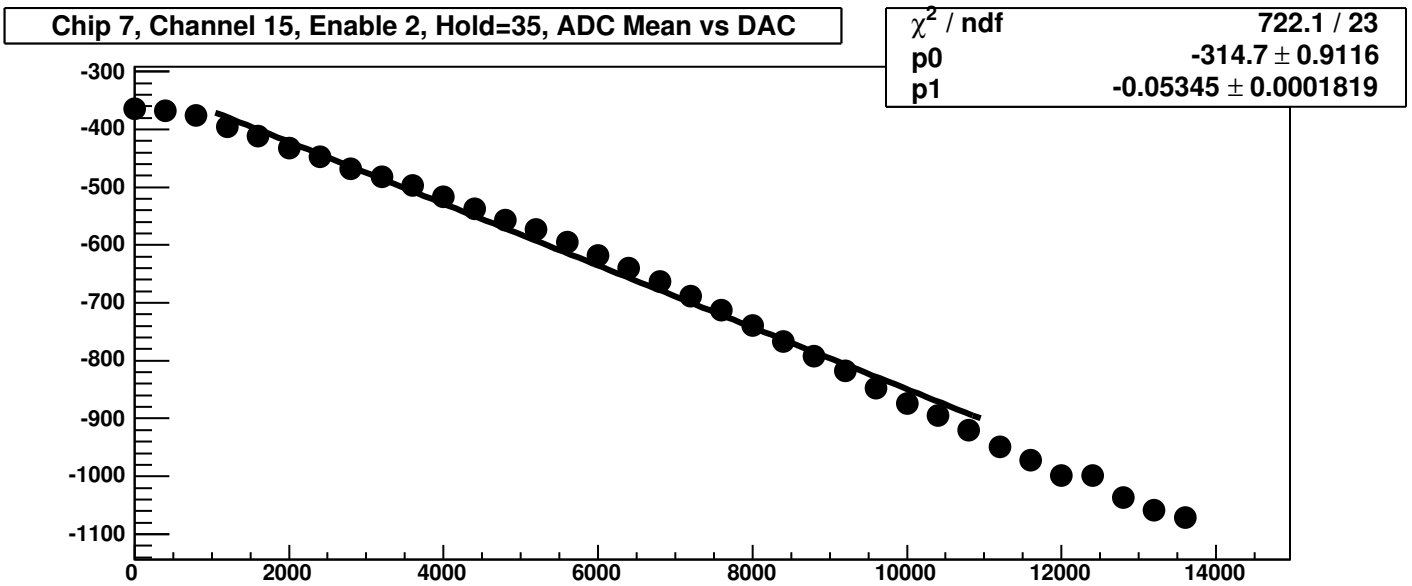
Chip 7, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

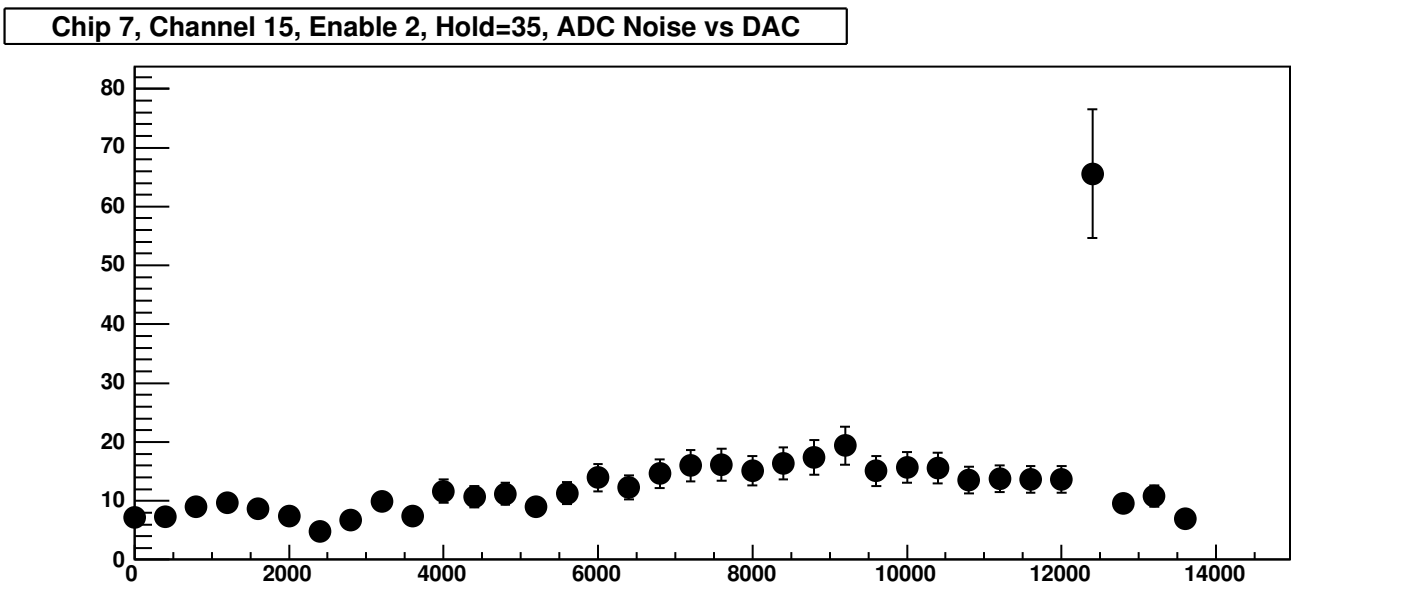


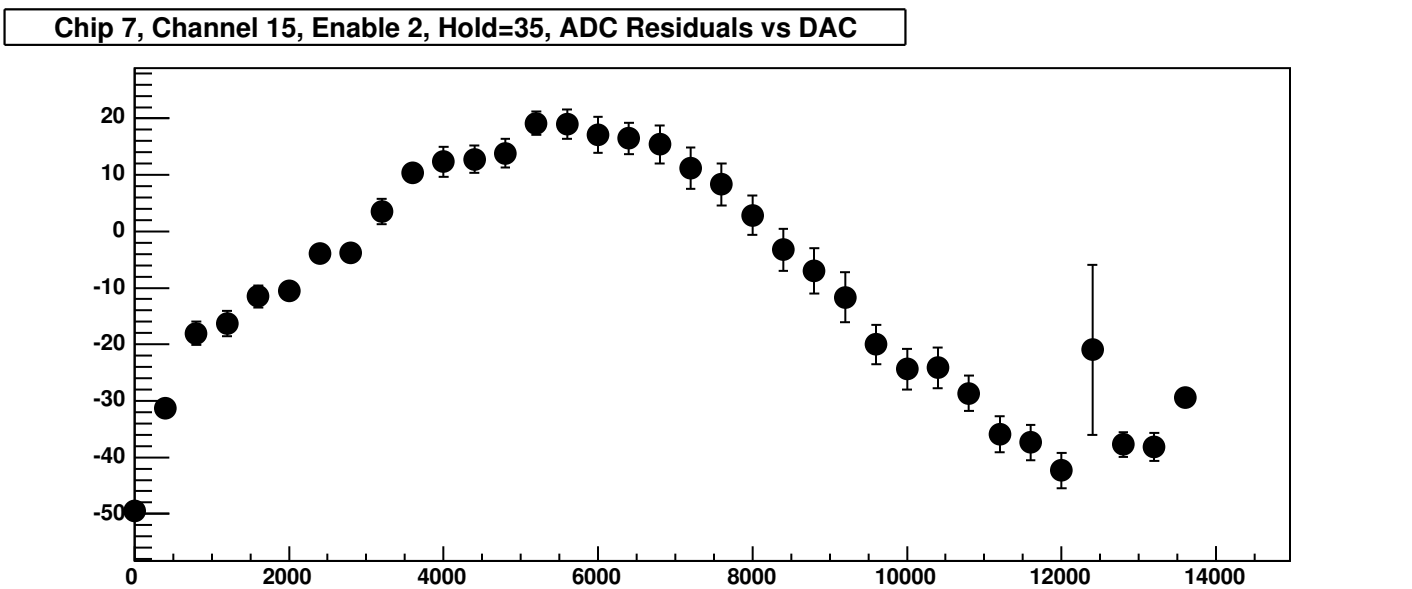

Chip 7, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.47 / 23
p0 -347.8 ± 0.7003
p1 0.0105 ± 0.0001095
-200
-220
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

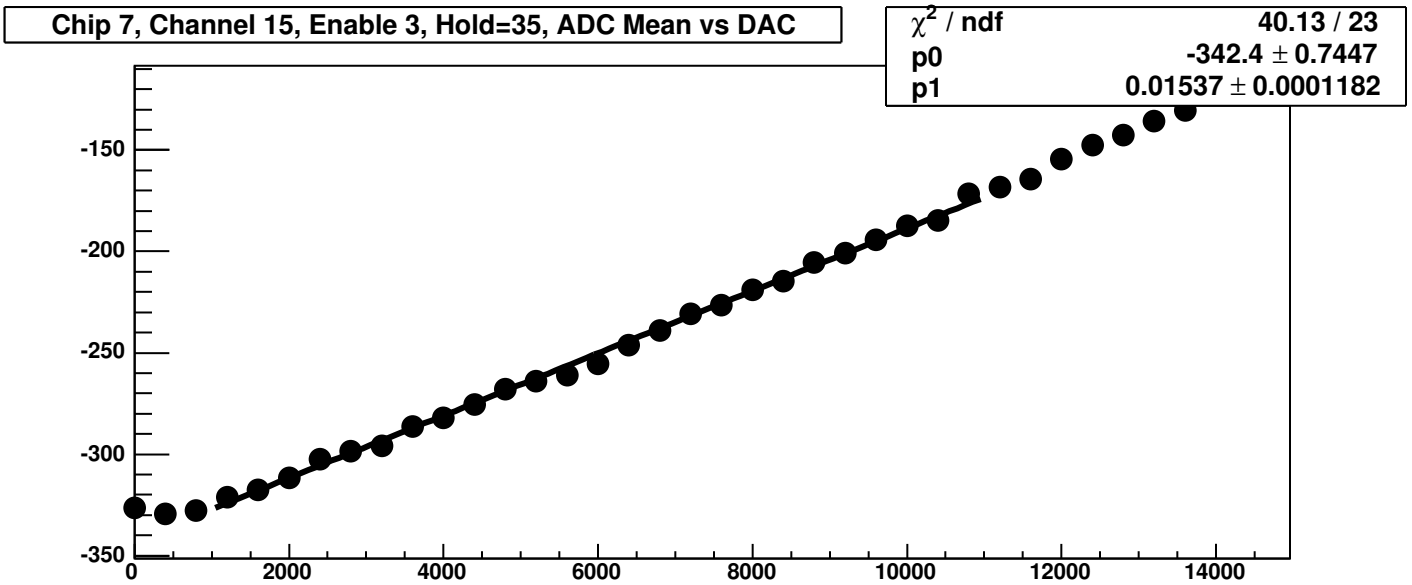

Chip 7, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

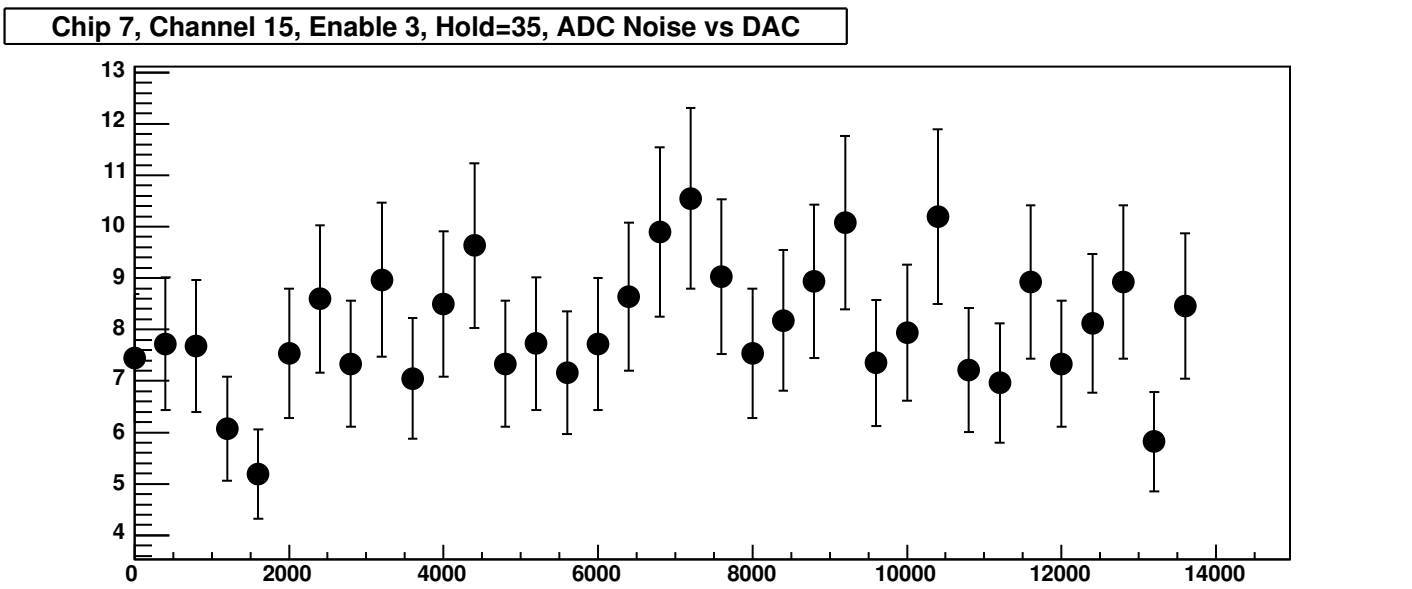

Chip 7, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

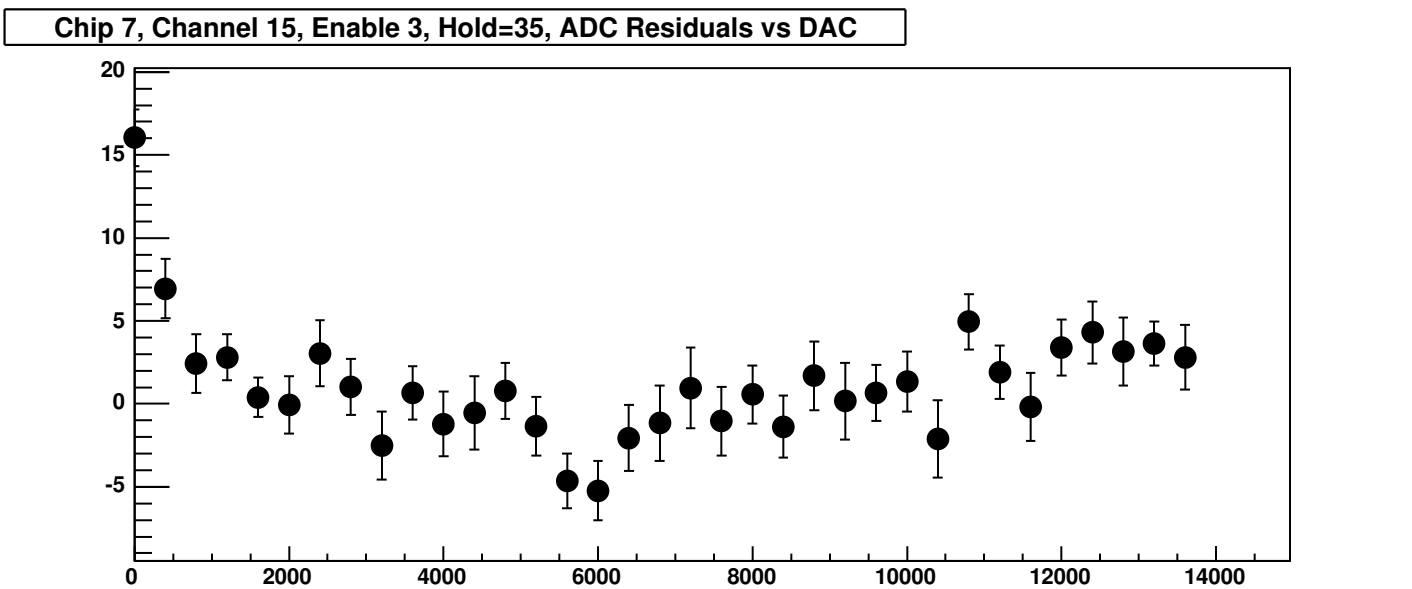

Chip 7, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 722.1 / 23
p0 -314.7 ± 0.9116
p1 -0.05345 ± 0.0001819
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

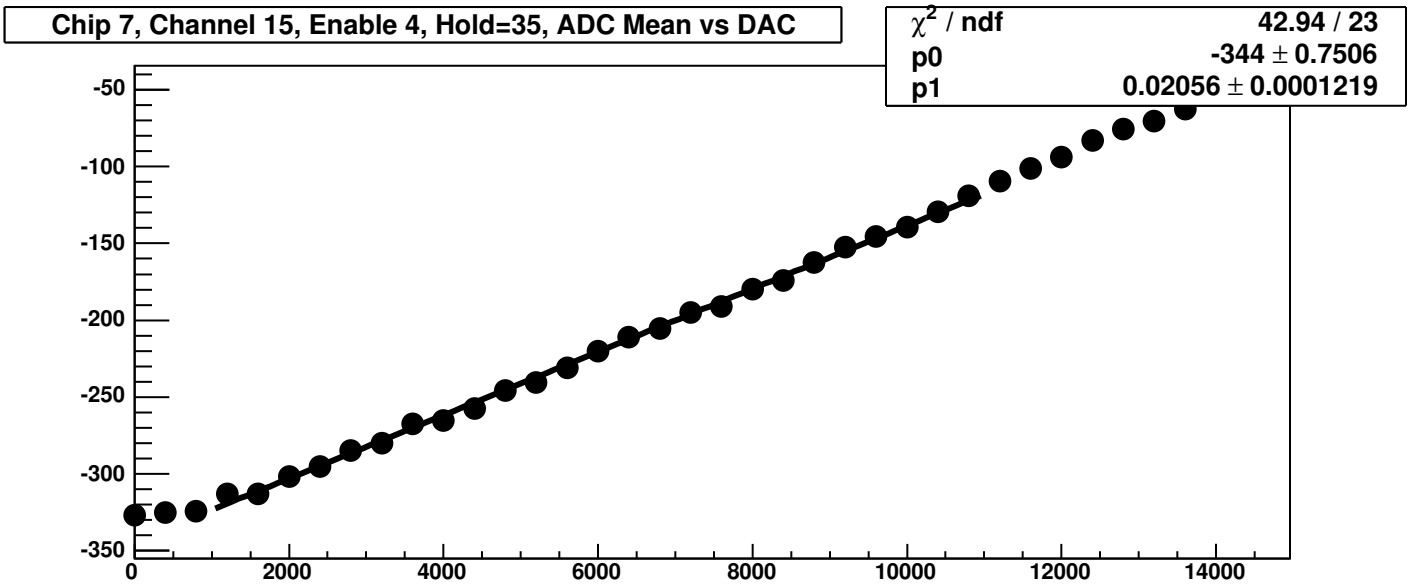

Chip 7, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

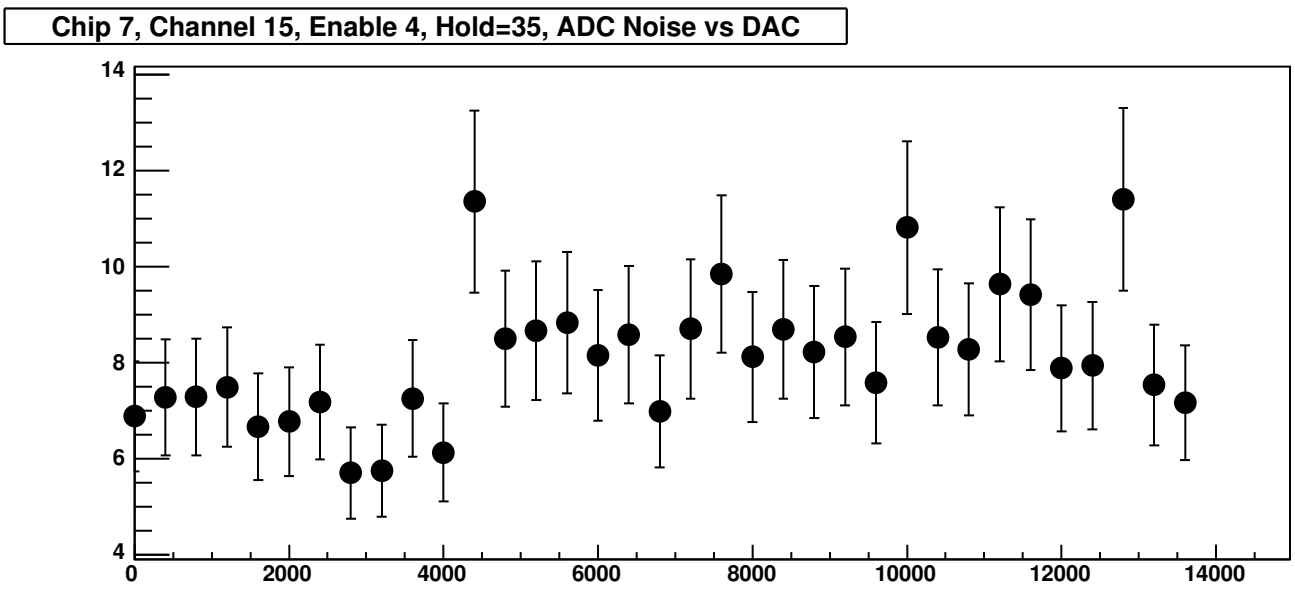

Chip 7, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

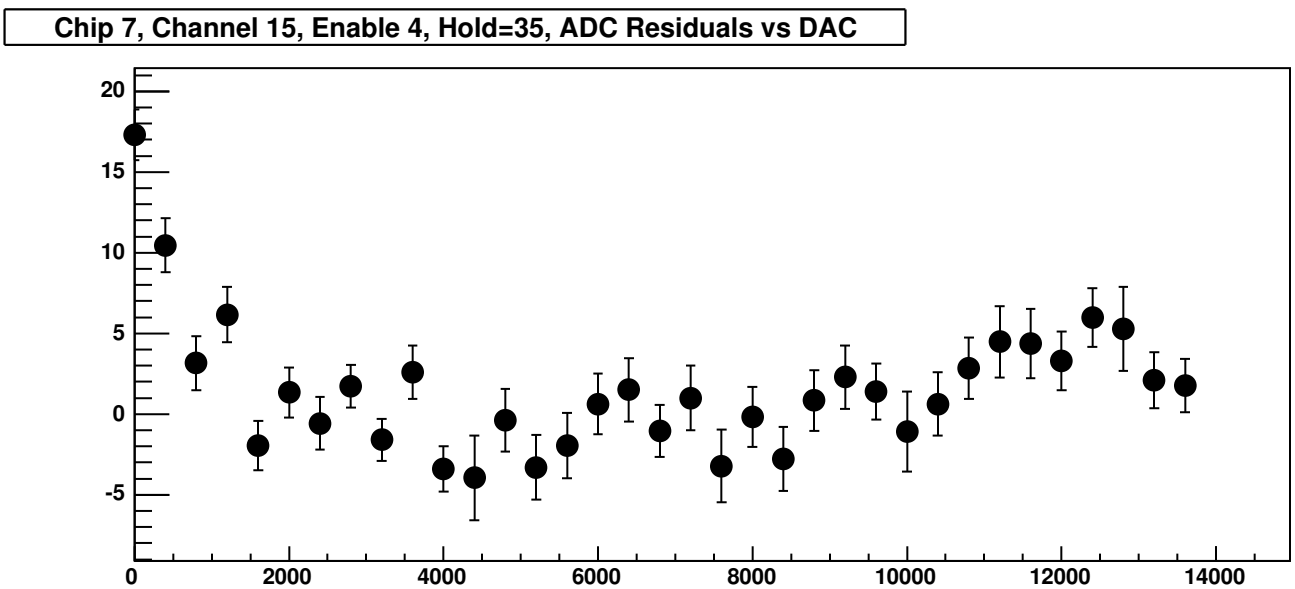

Chip 7, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.13 / 23
p0 -342.4 ± 0.7447
p1 0.01537 ± 0.0001182
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

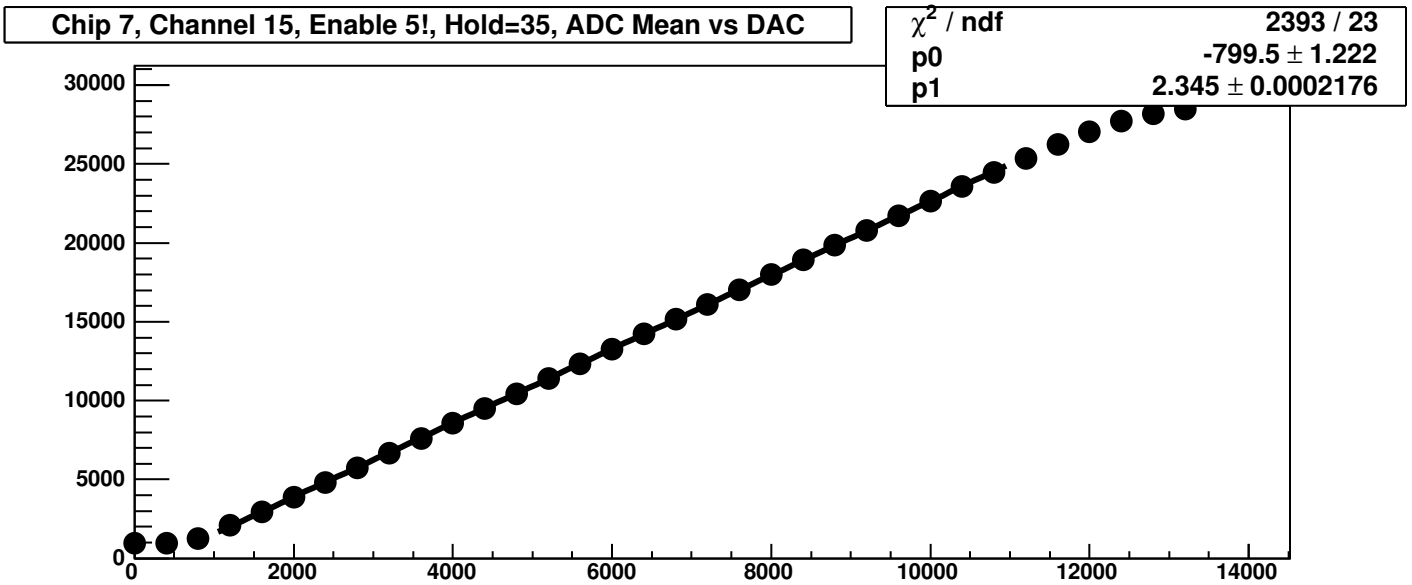

Chip 7, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

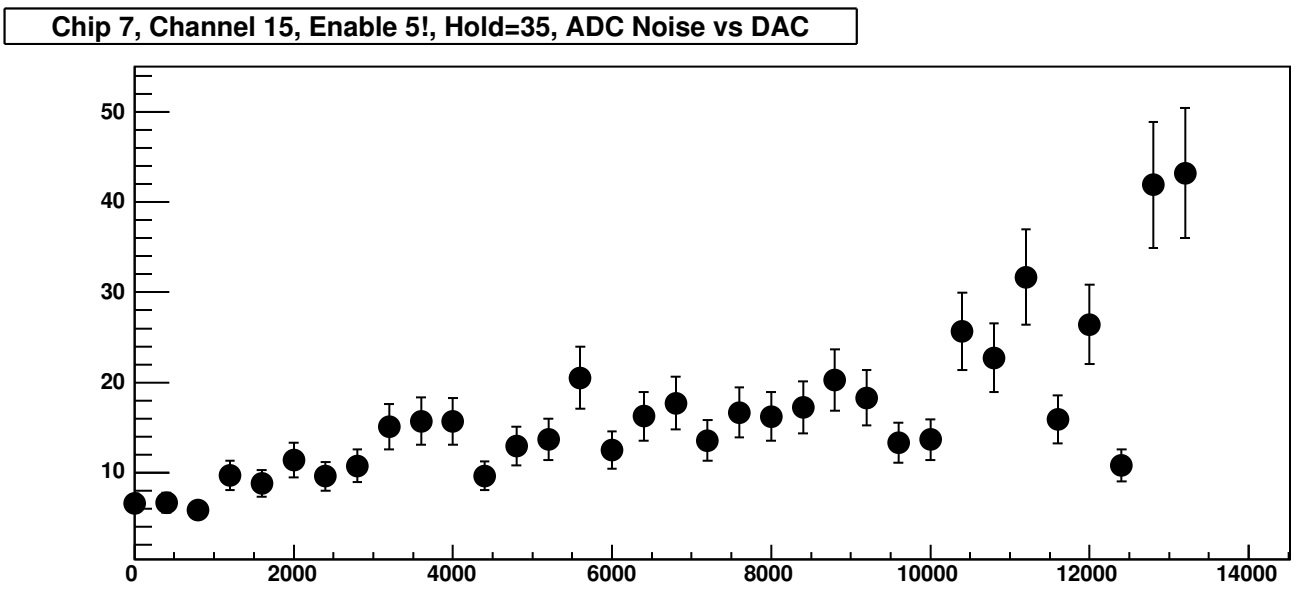

Chip 7, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

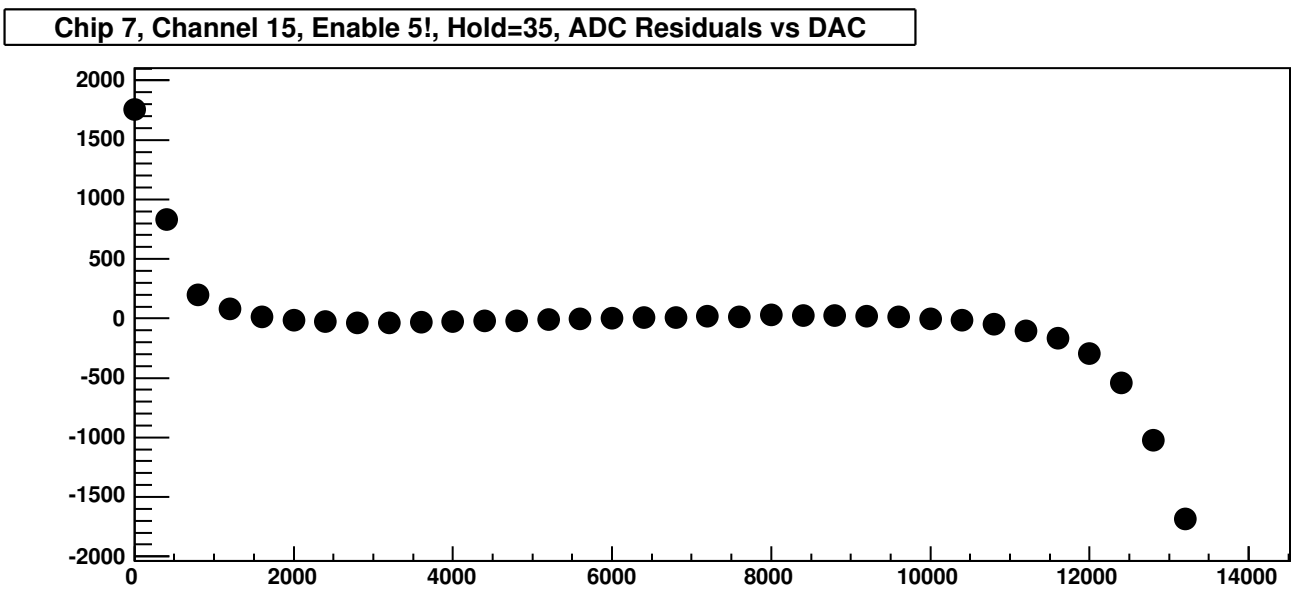

Chip 7, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.94 / 23
p0 -344 ± 0.7506
p1 0.02056 ± 0.0001219
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

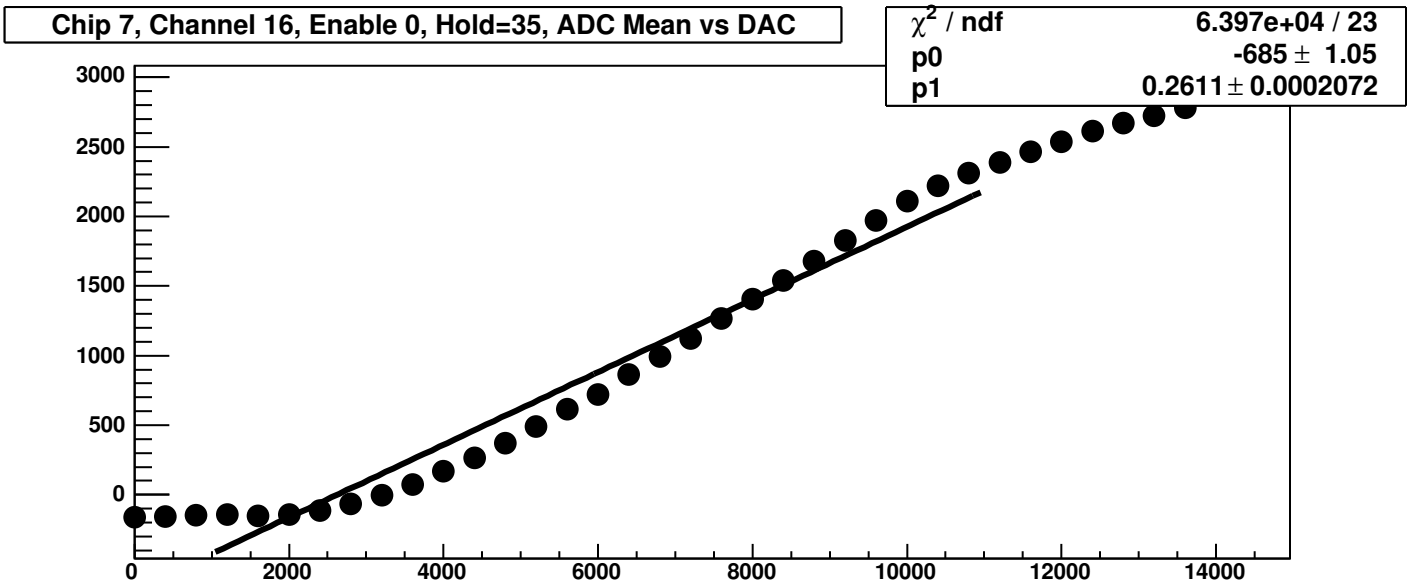

Chip 7, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

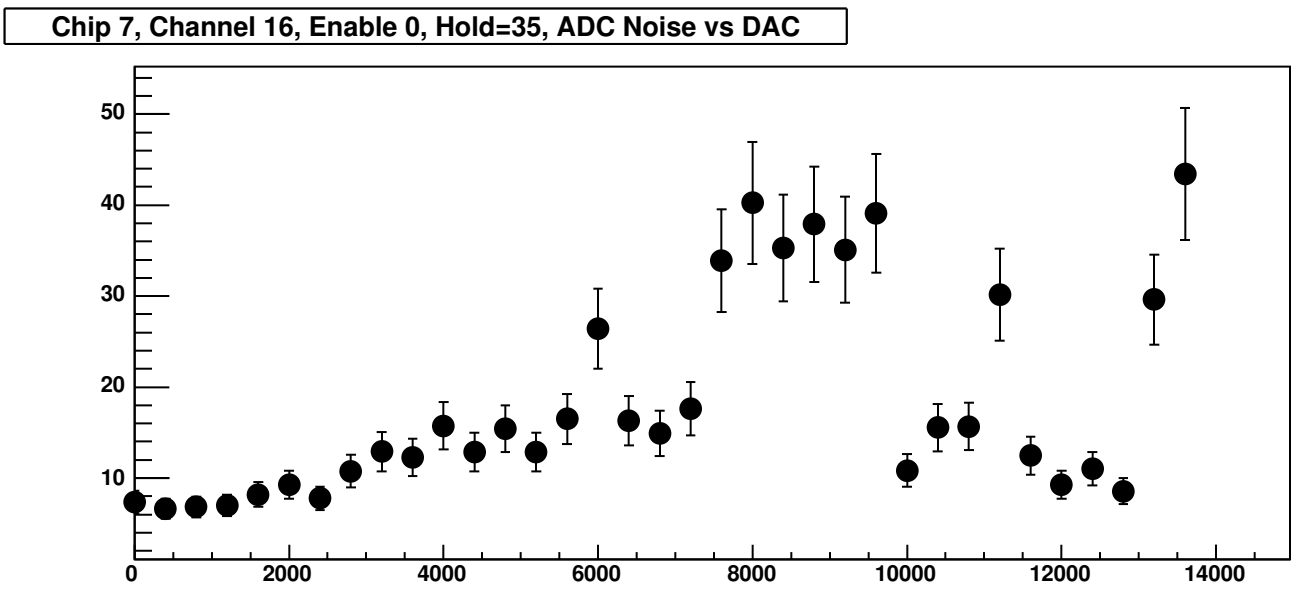

Chip 7, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

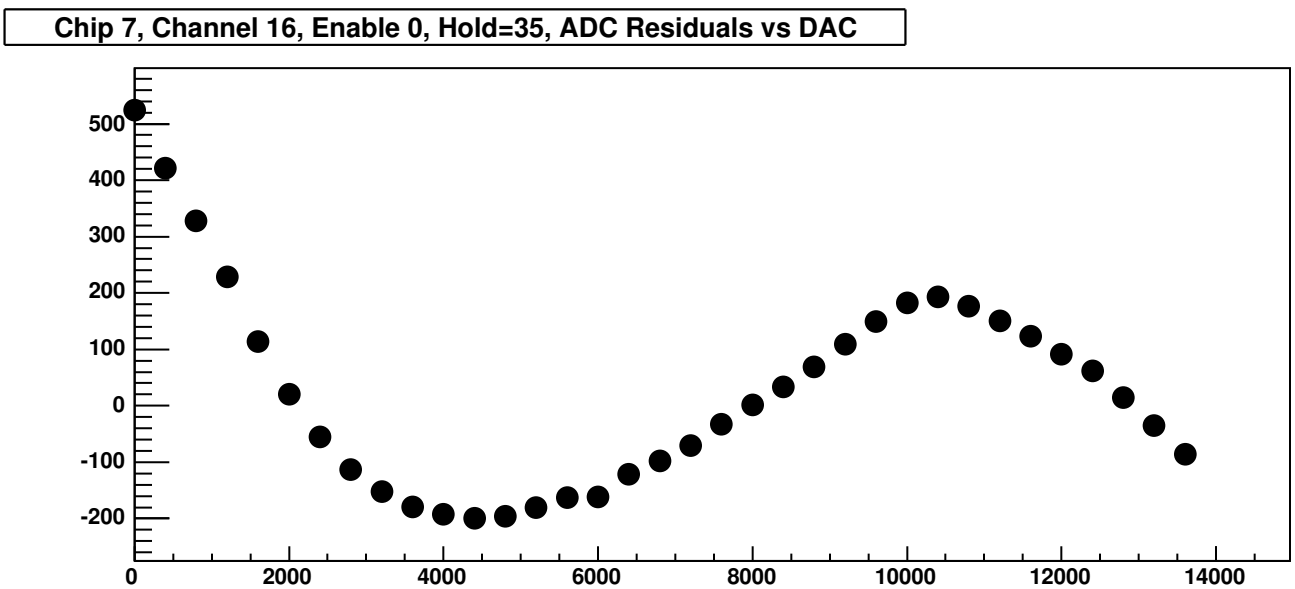

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2393 / 23
p0 -799.5 ± 1.222
p1 2.345 ± 0.0002176
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

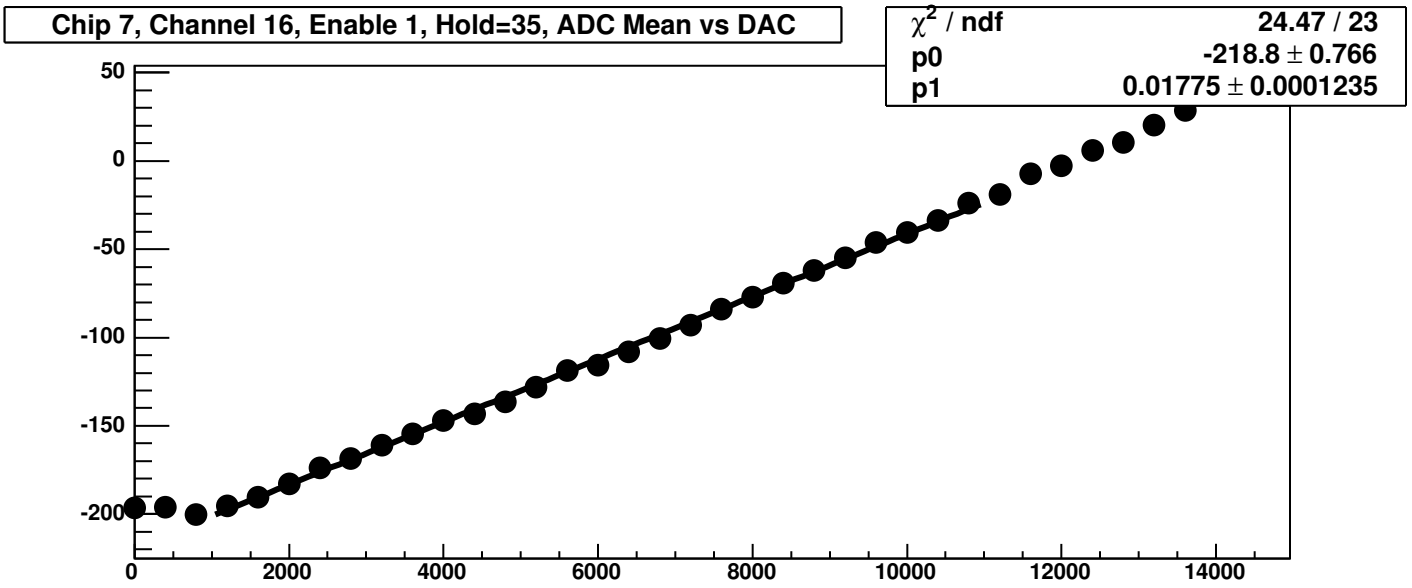

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

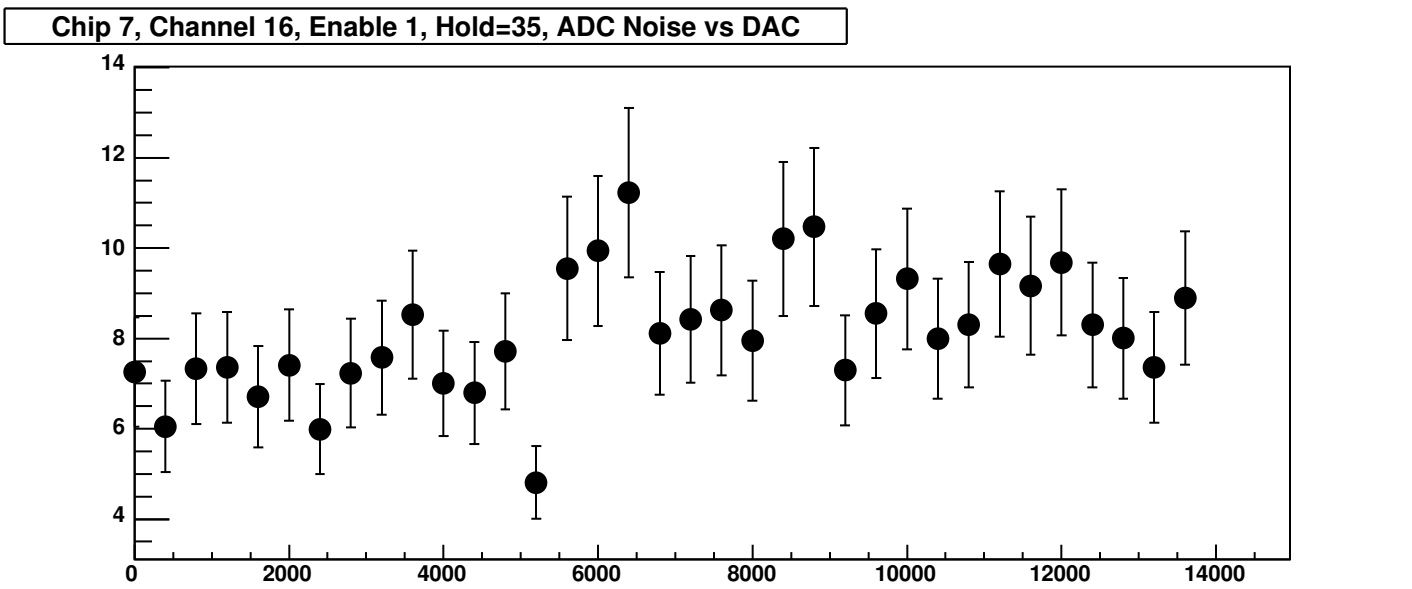

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

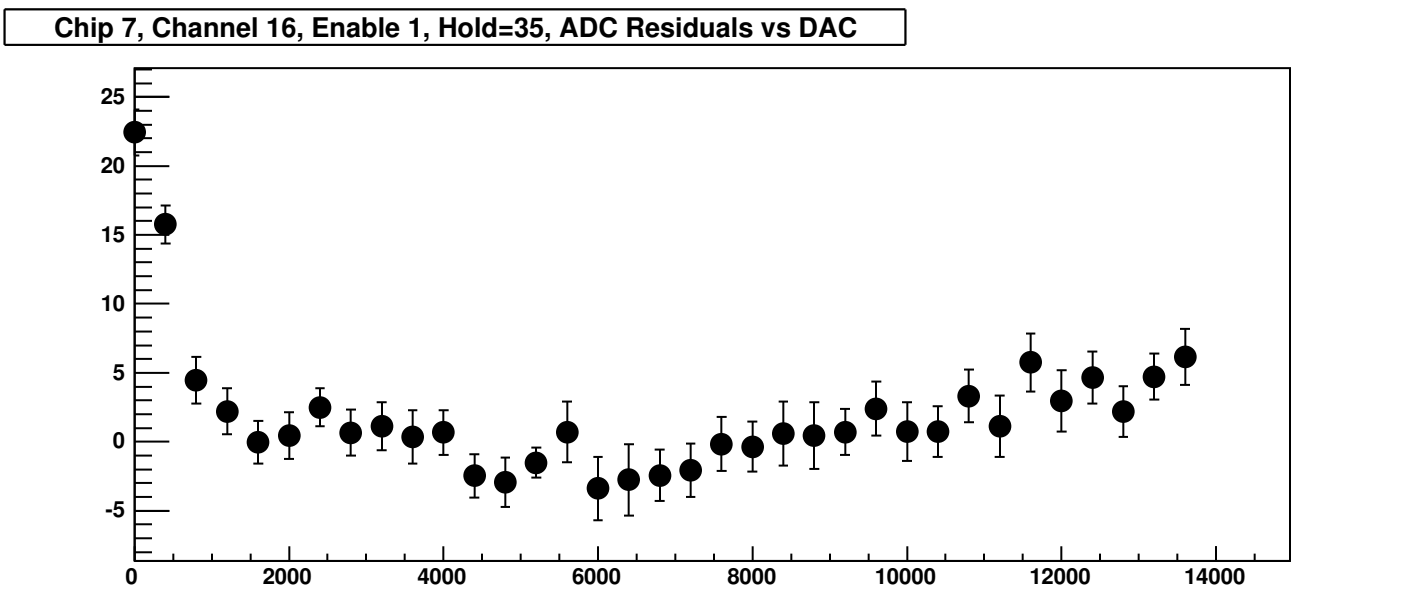

Chip 7, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.397e+04 / 23
p0 -685 ± 1.05
p1 0.2611 ± 0.0002072
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

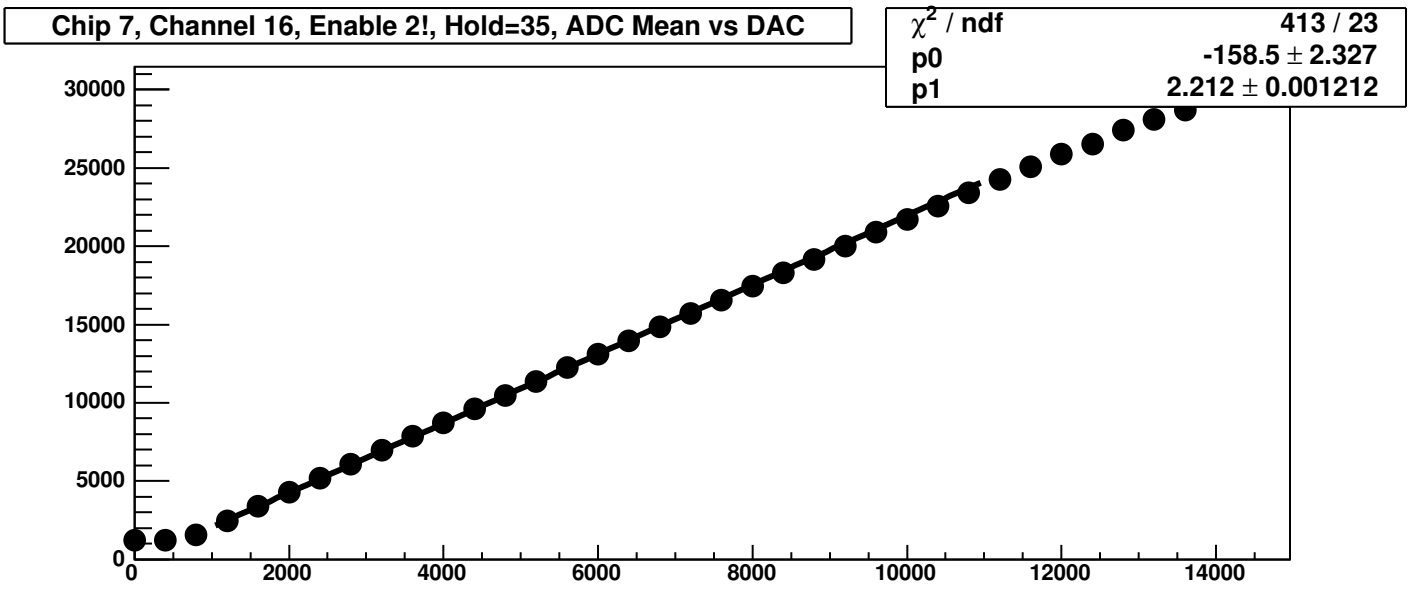

Chip 7, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

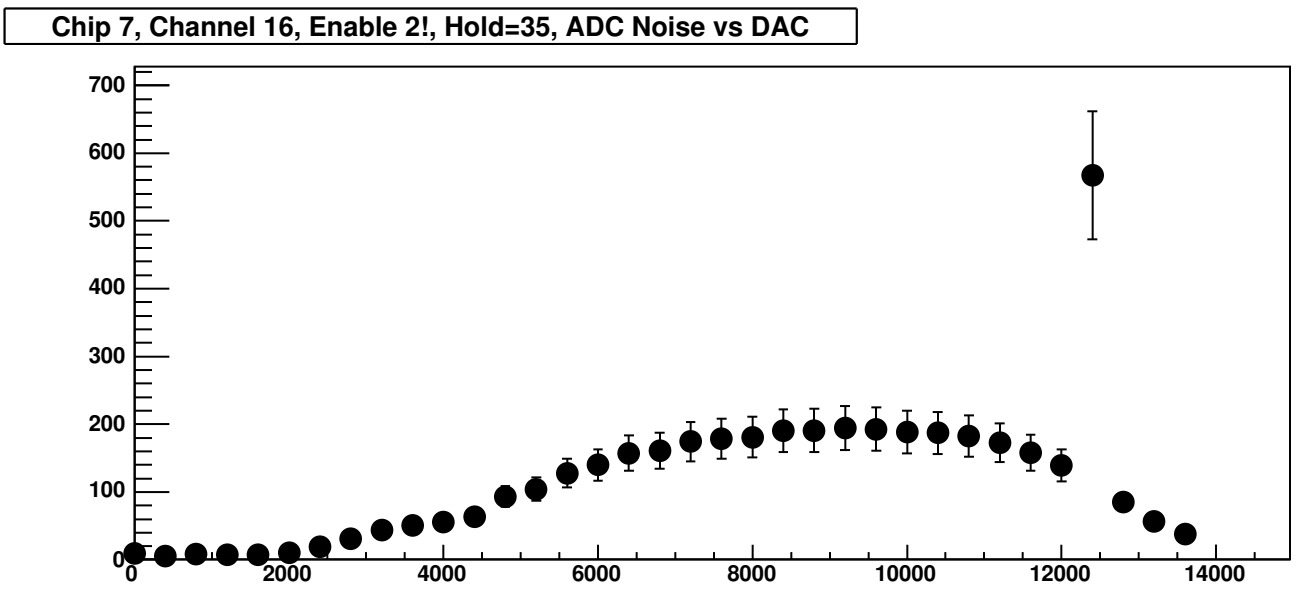

Chip 7, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

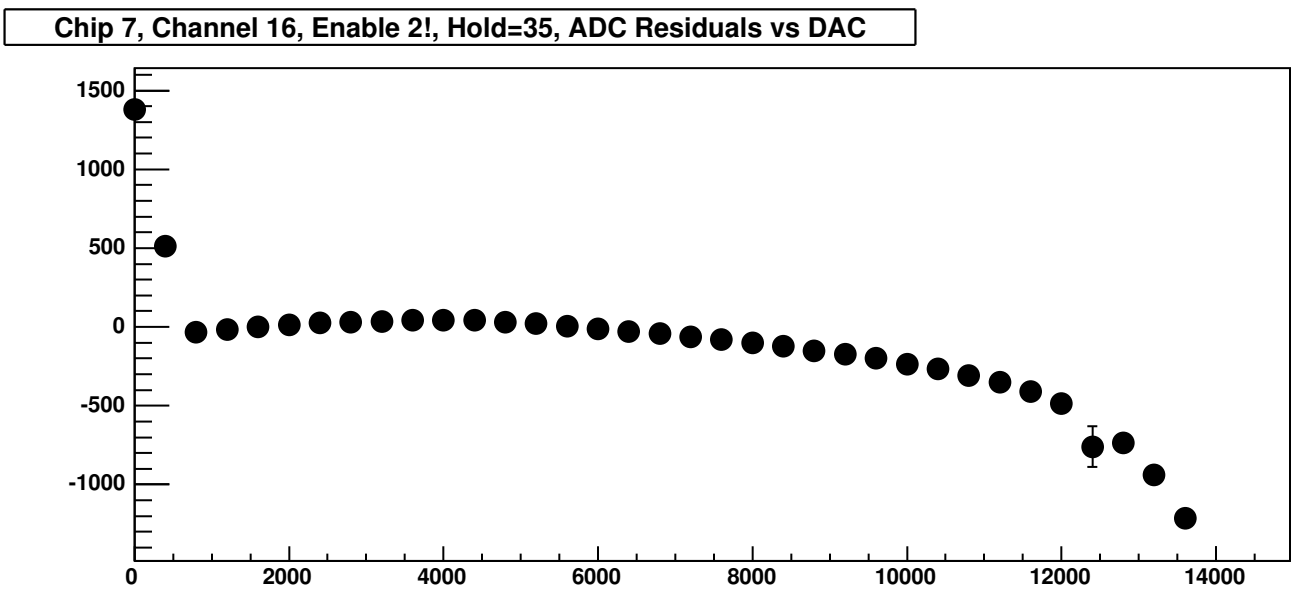

Chip 7, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.47 / 23
p0 -218.8 ± 0.766
p1 0.01775 ± 0.0001235
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

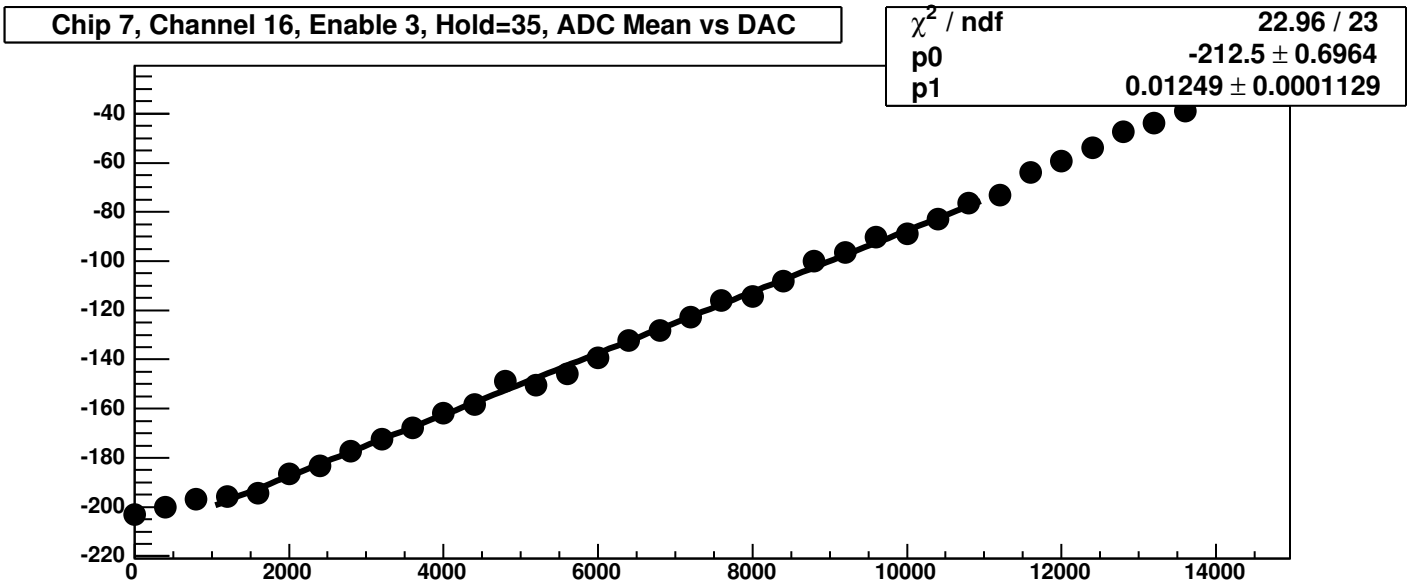

Chip 7, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

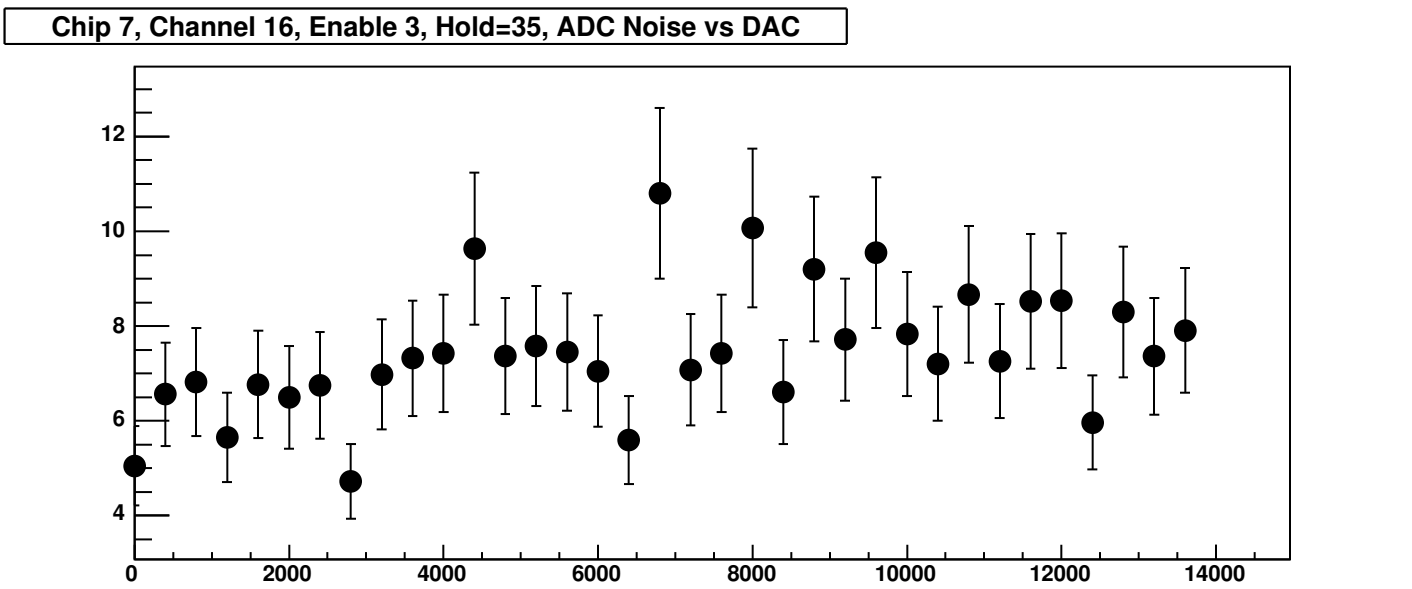

Chip 7, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

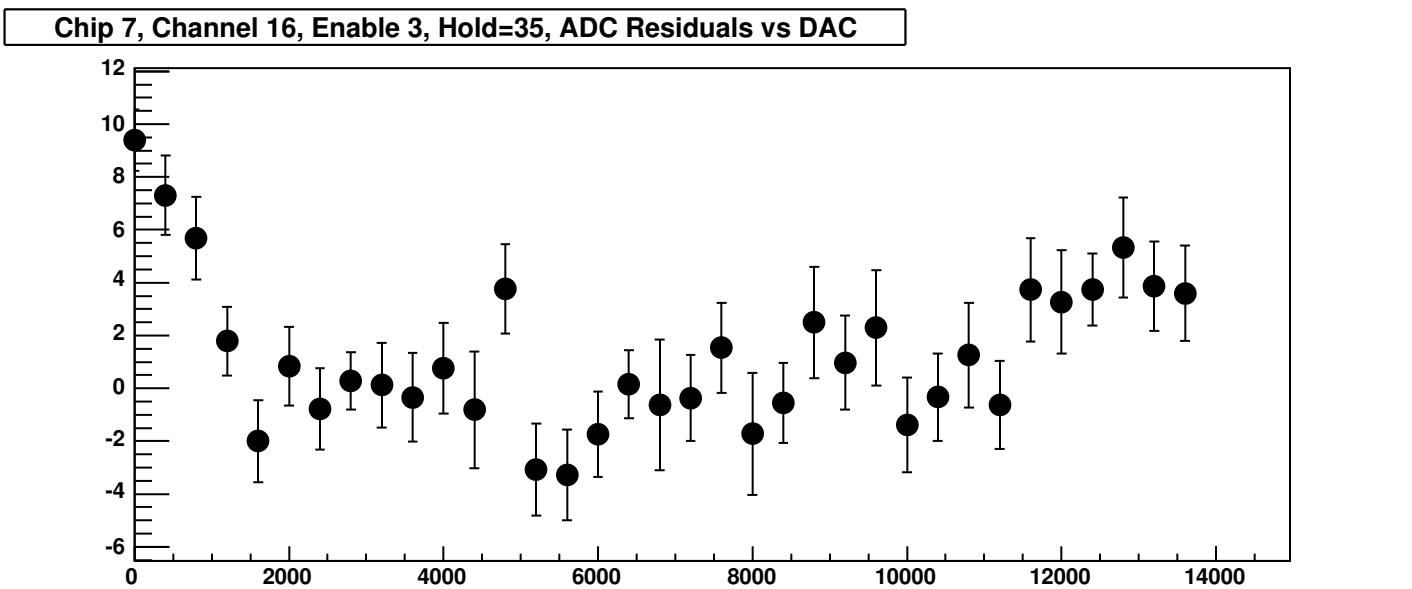

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 413 / 23
p0 -158.5 ± 2.327
p1 2.212 ± 0.001212
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

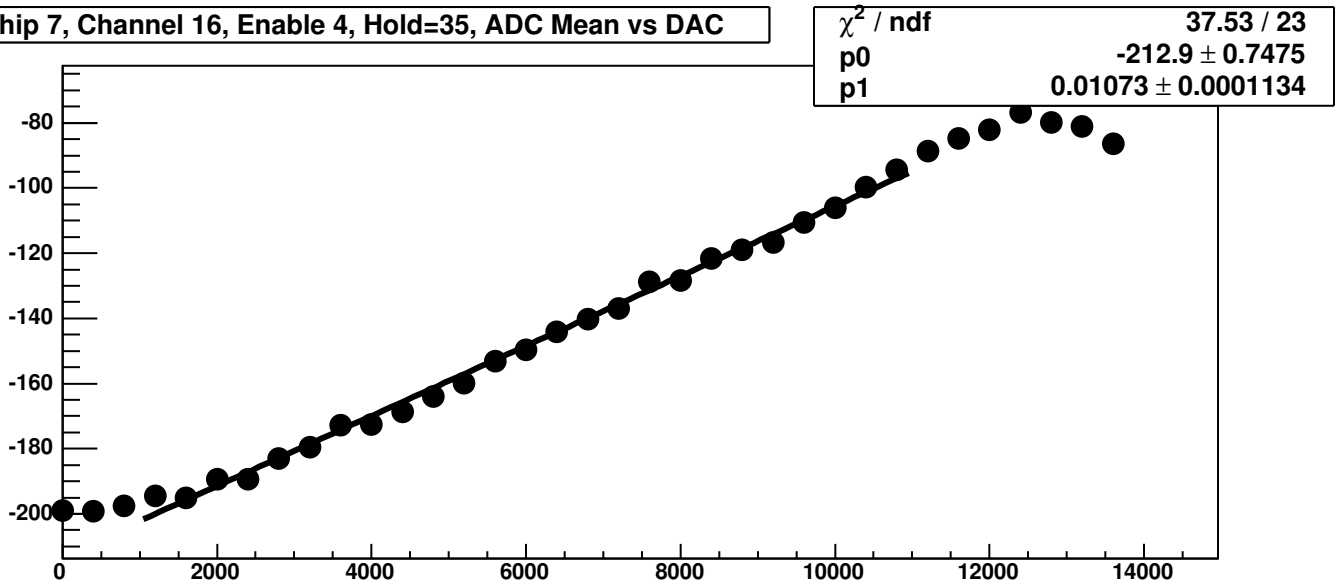

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.96 / 23
p0 -212.5 ± 0.6964
p1 0.01249 ± 0.0001129
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000

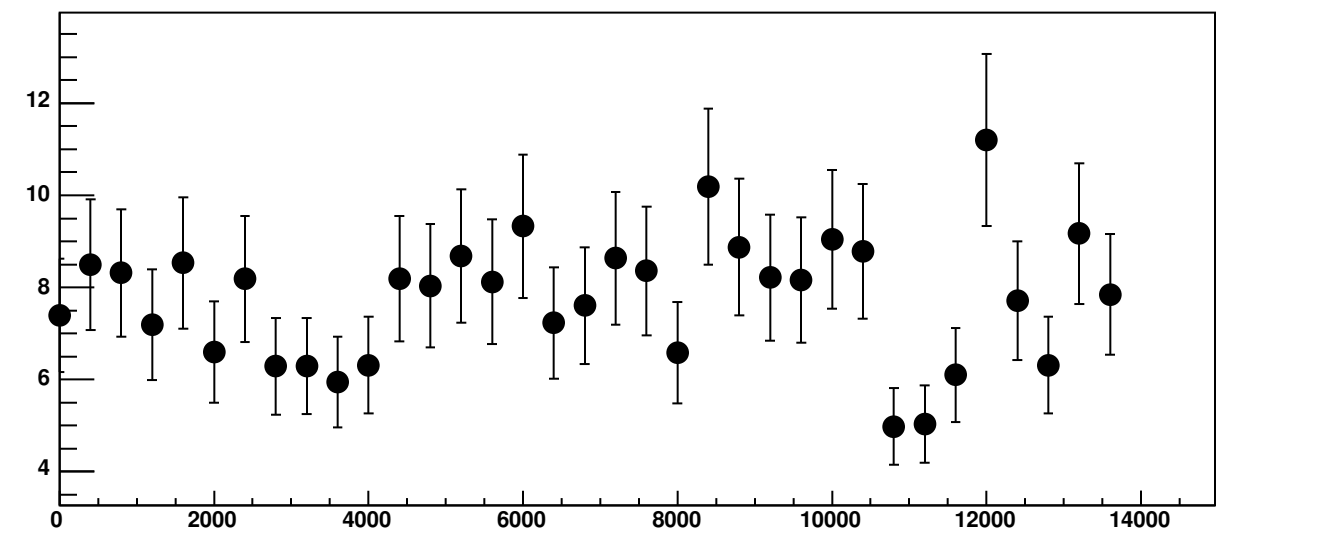

Chip 7, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

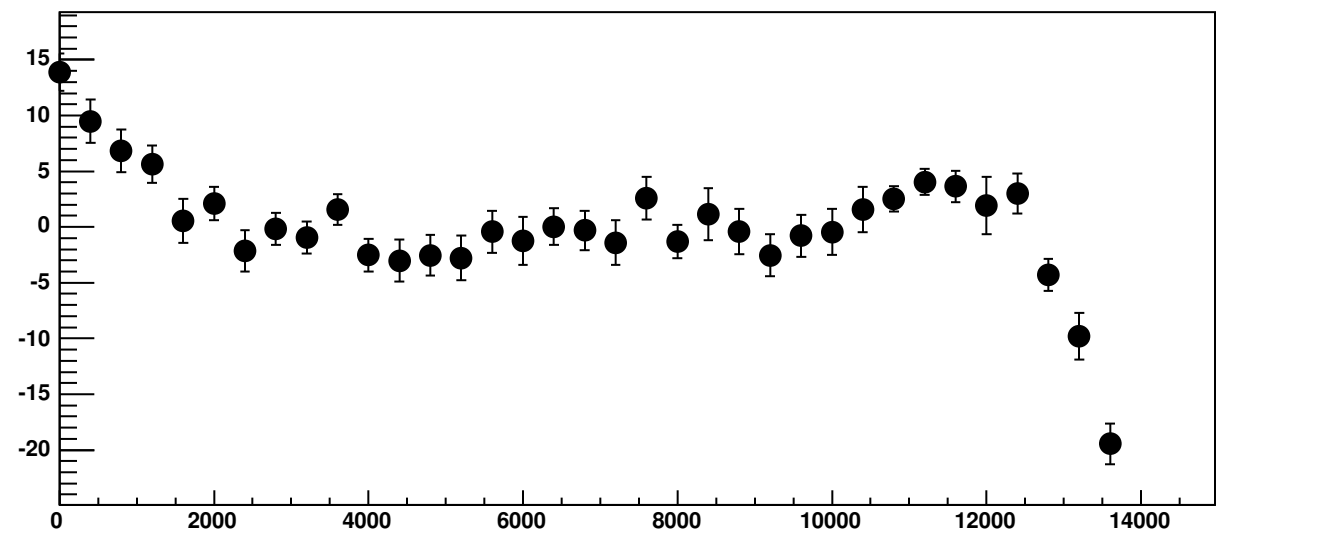
Chip 7, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

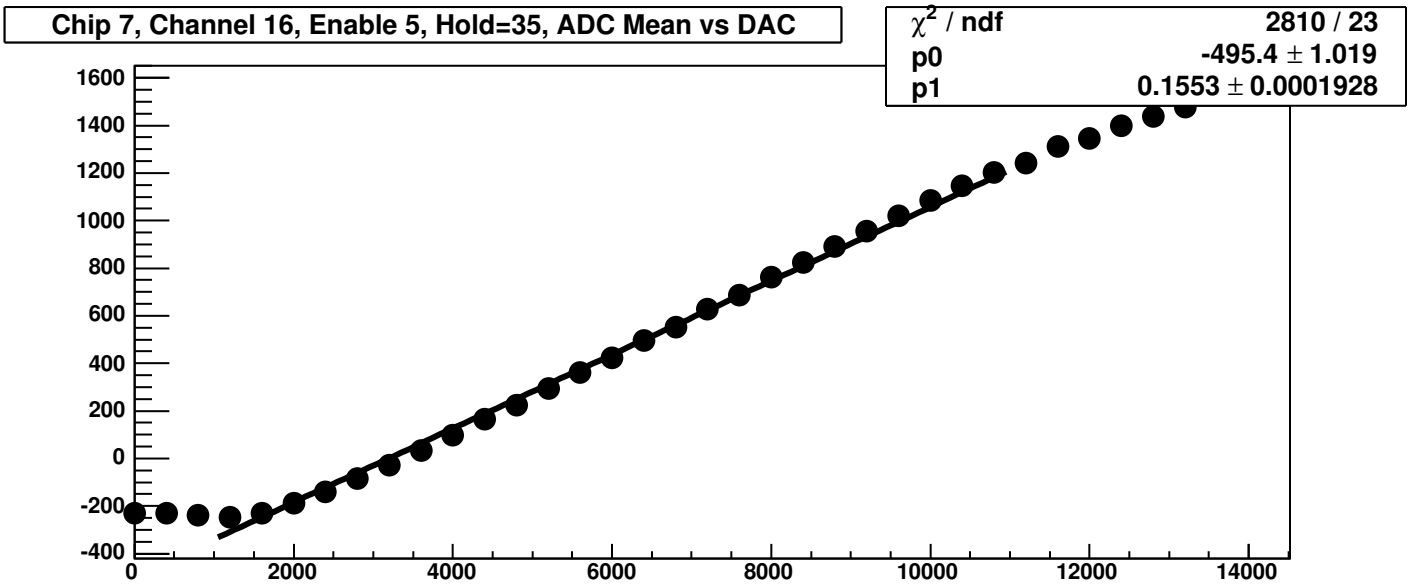


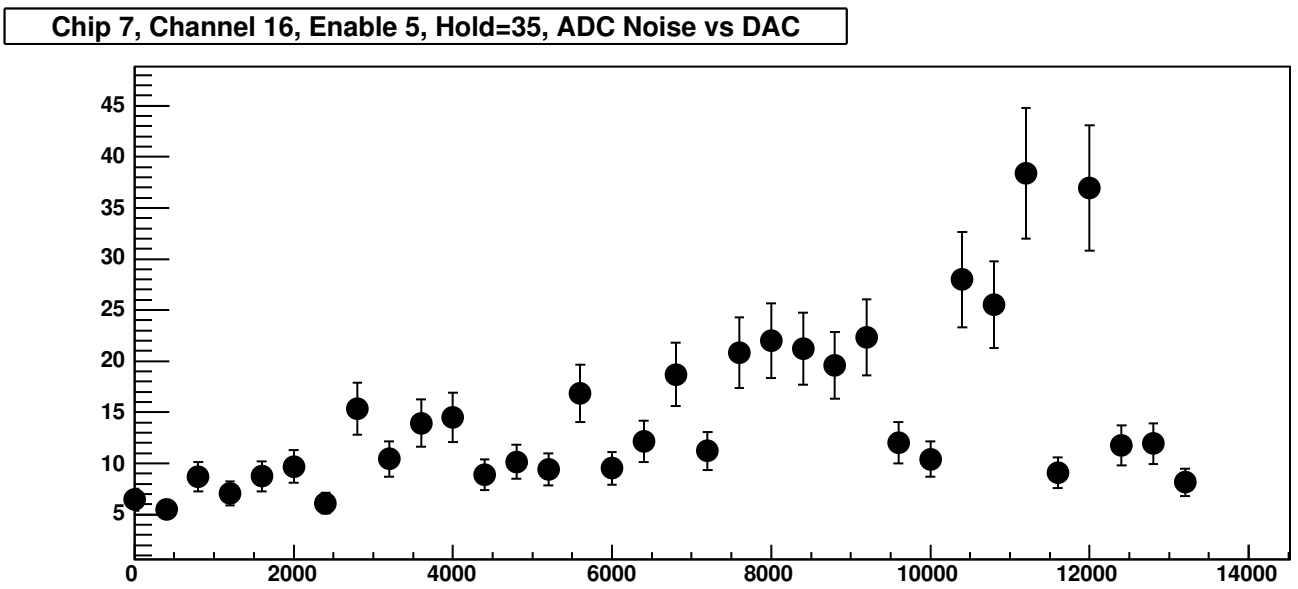
Chip 7, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

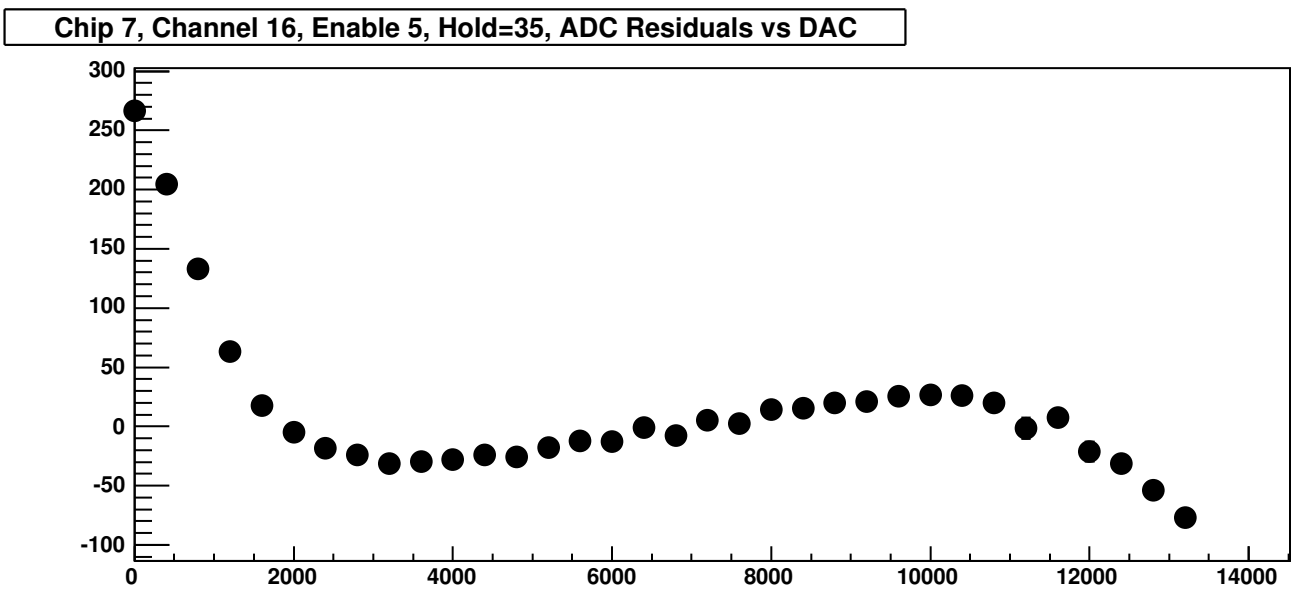


Chip 7, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

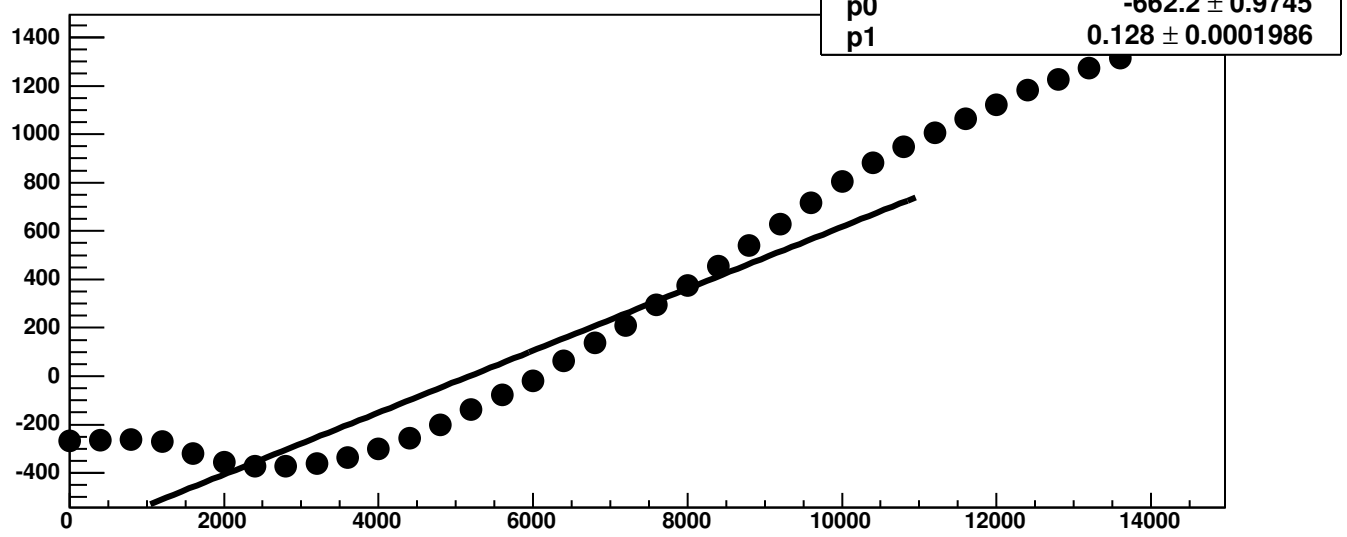



Chip 7, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2810 / 23
p0 -495.4 ± 1.019
p1 0.1553 ± 0.0001928
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

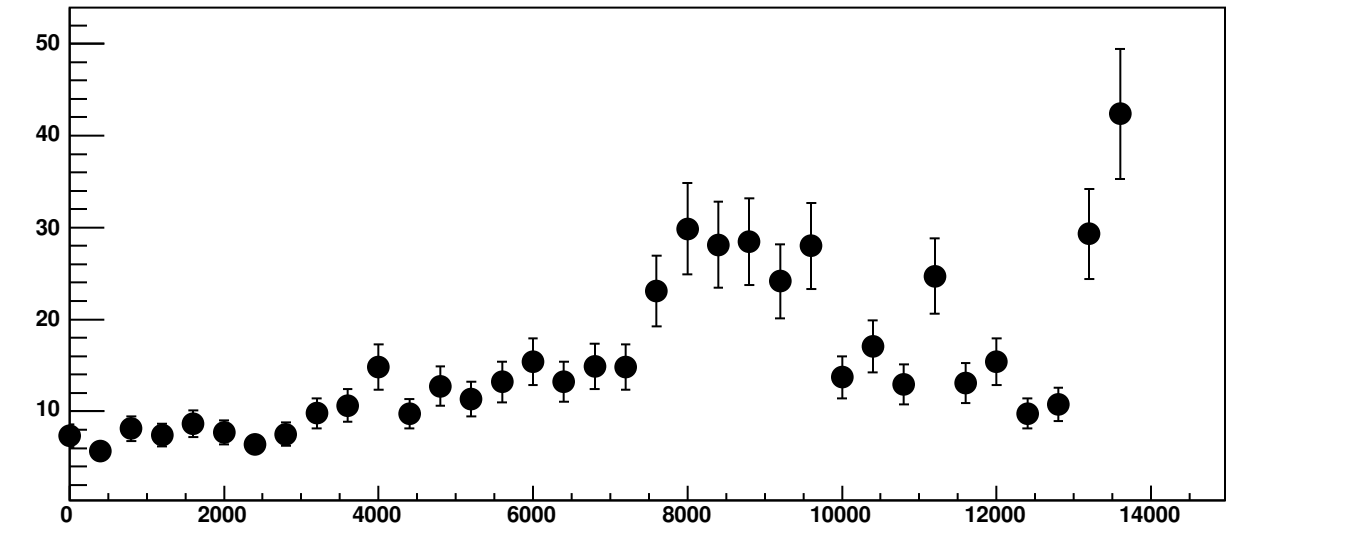

Chip 7, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

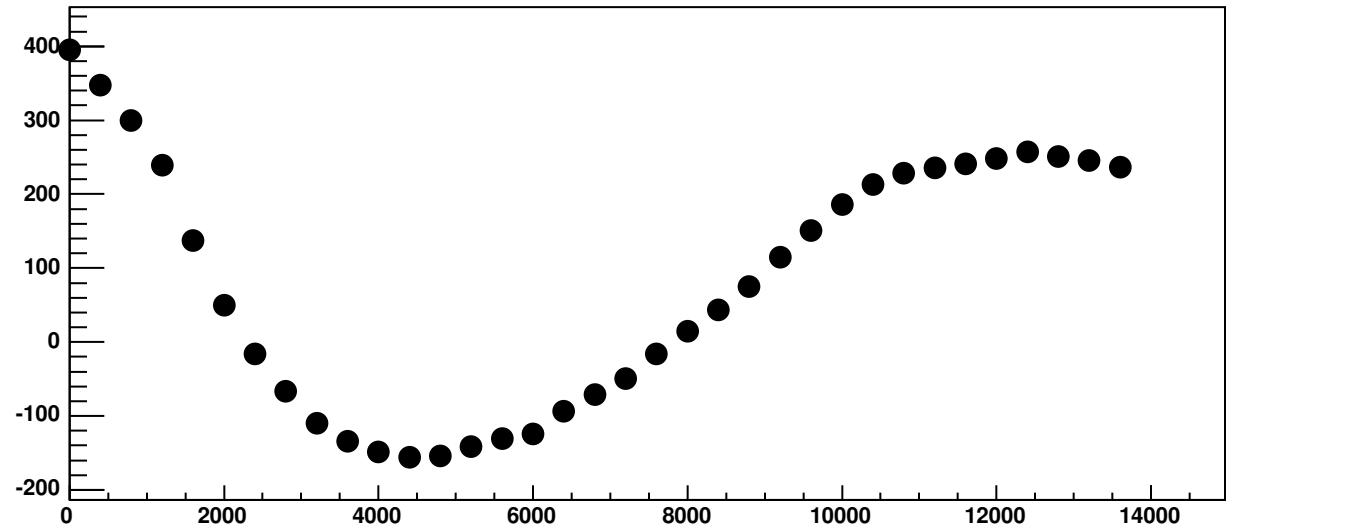
Chip 7, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

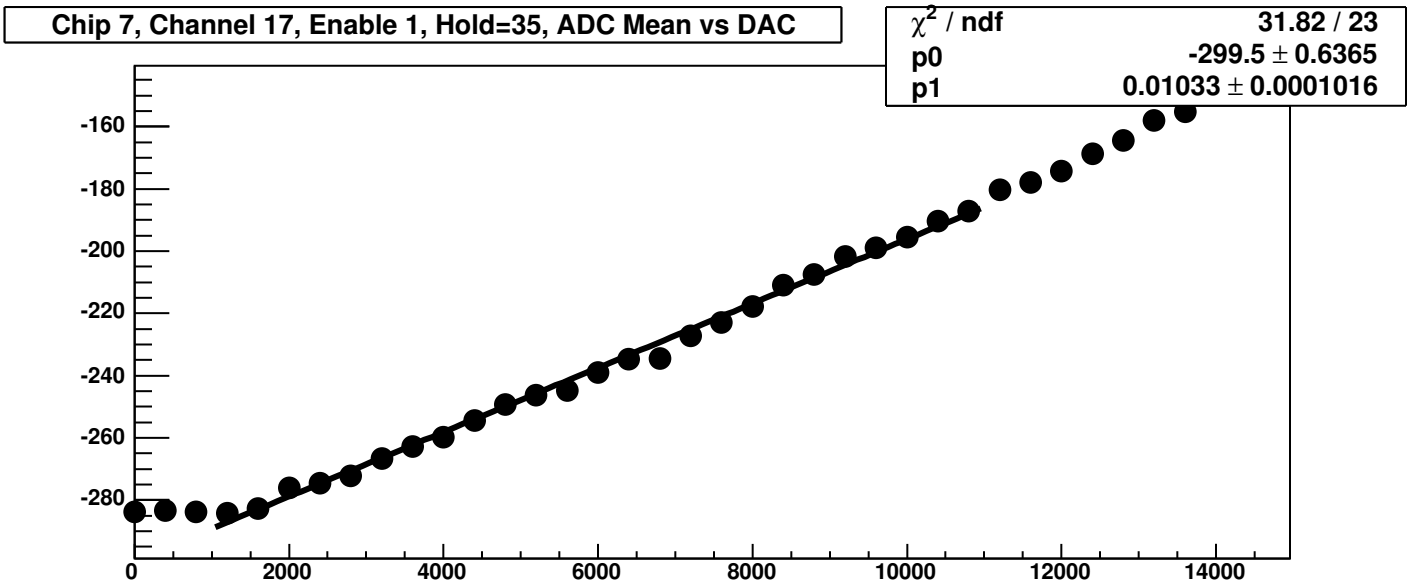


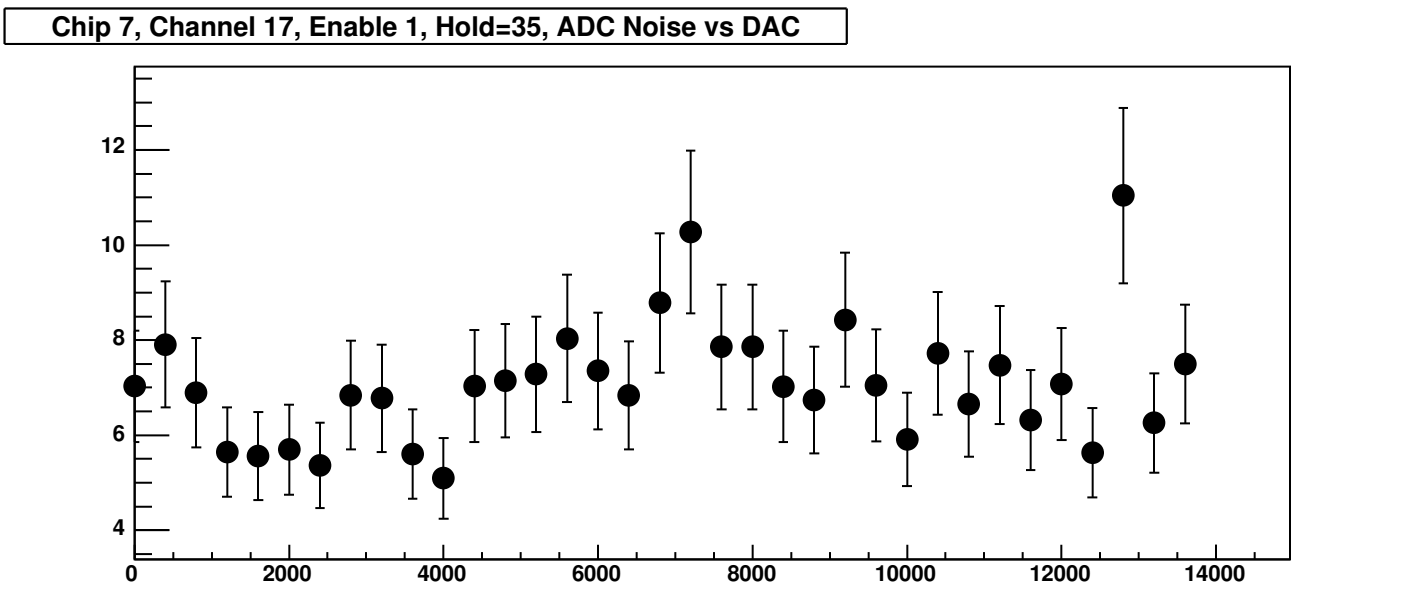
Chip 7, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

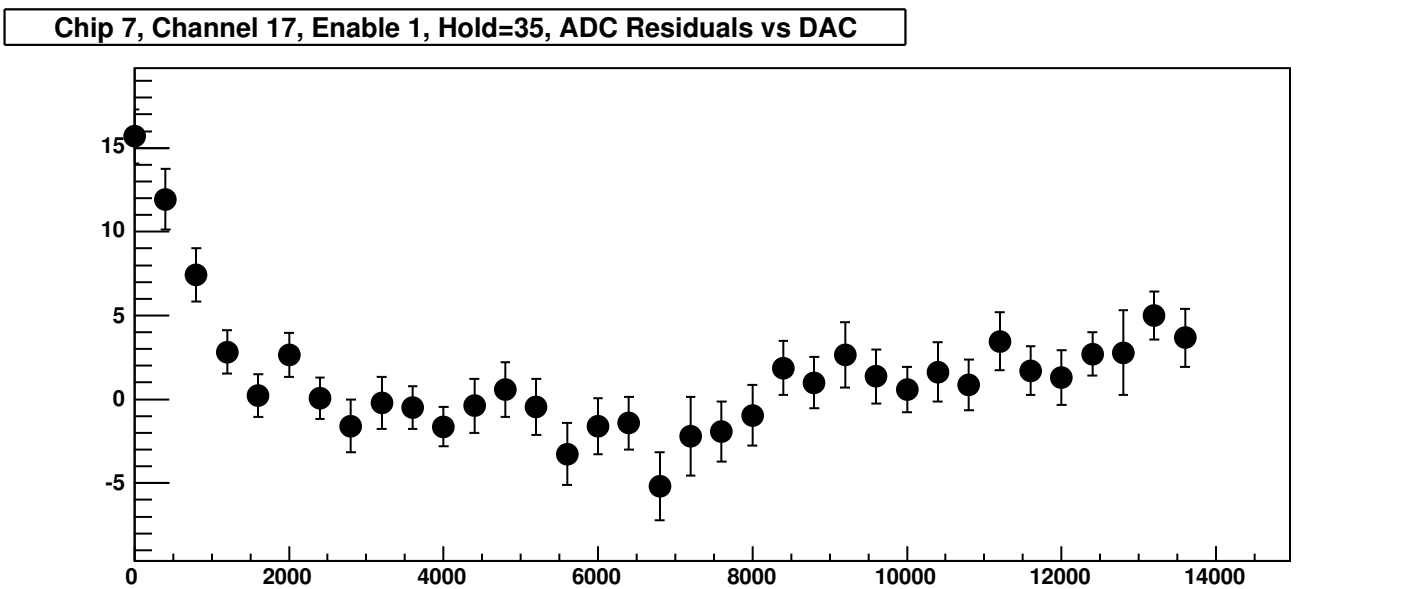


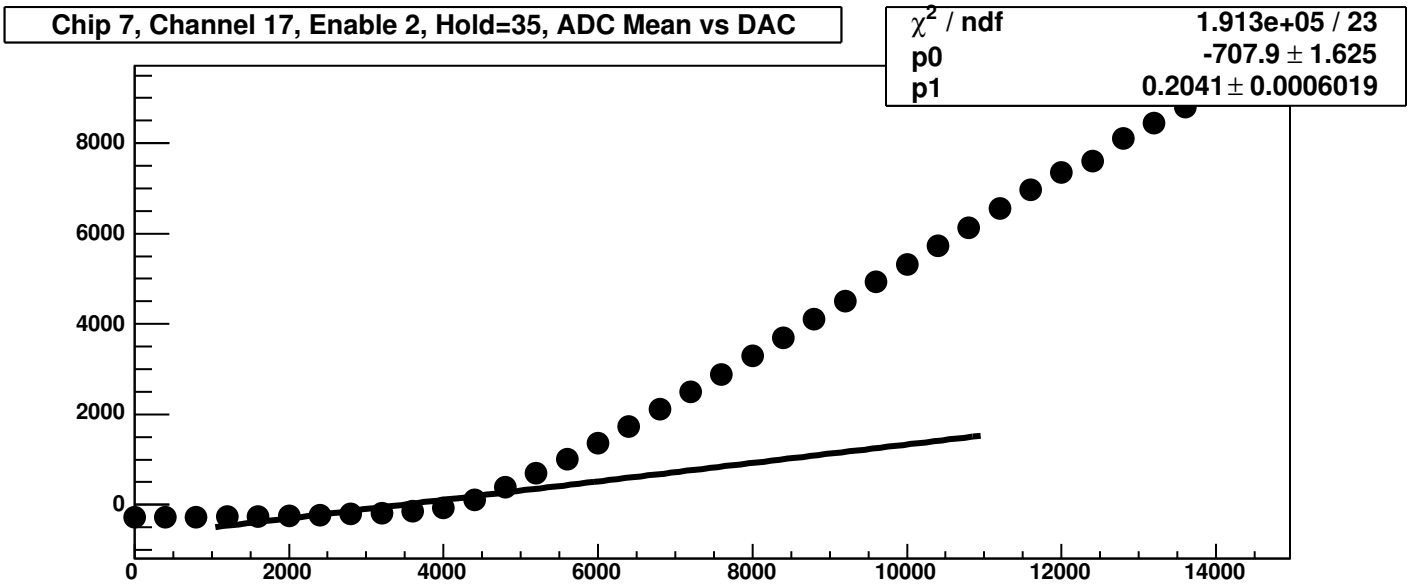
Chip 7, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

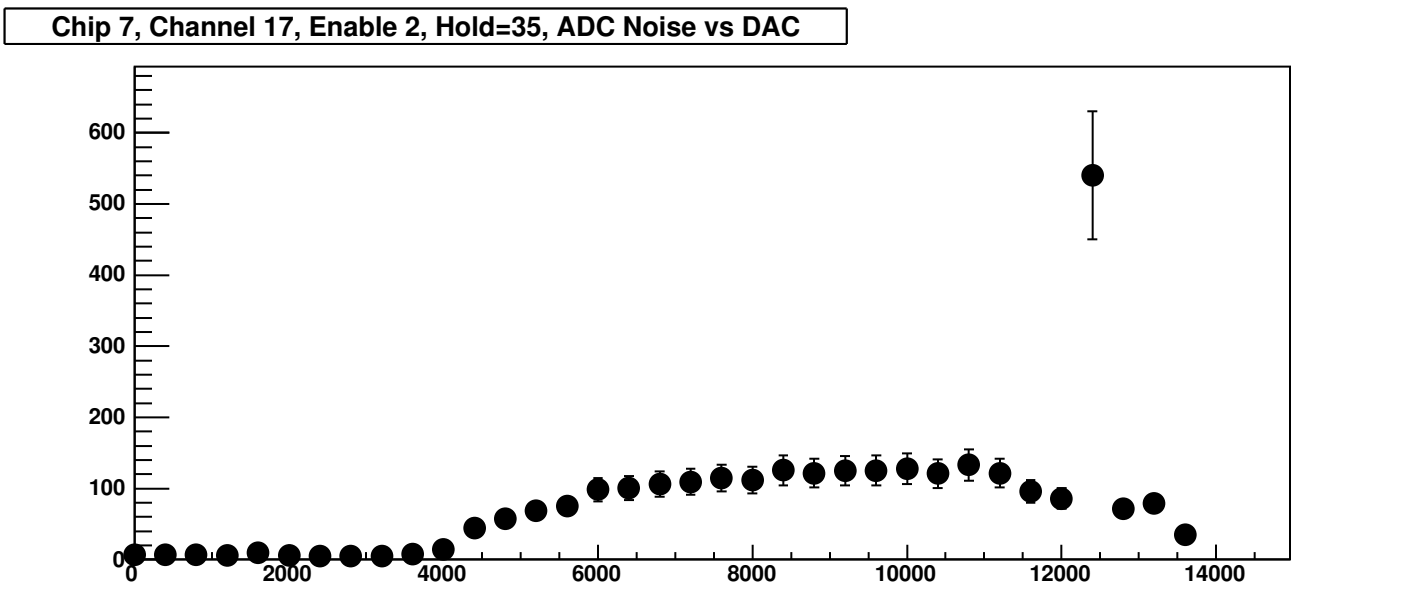


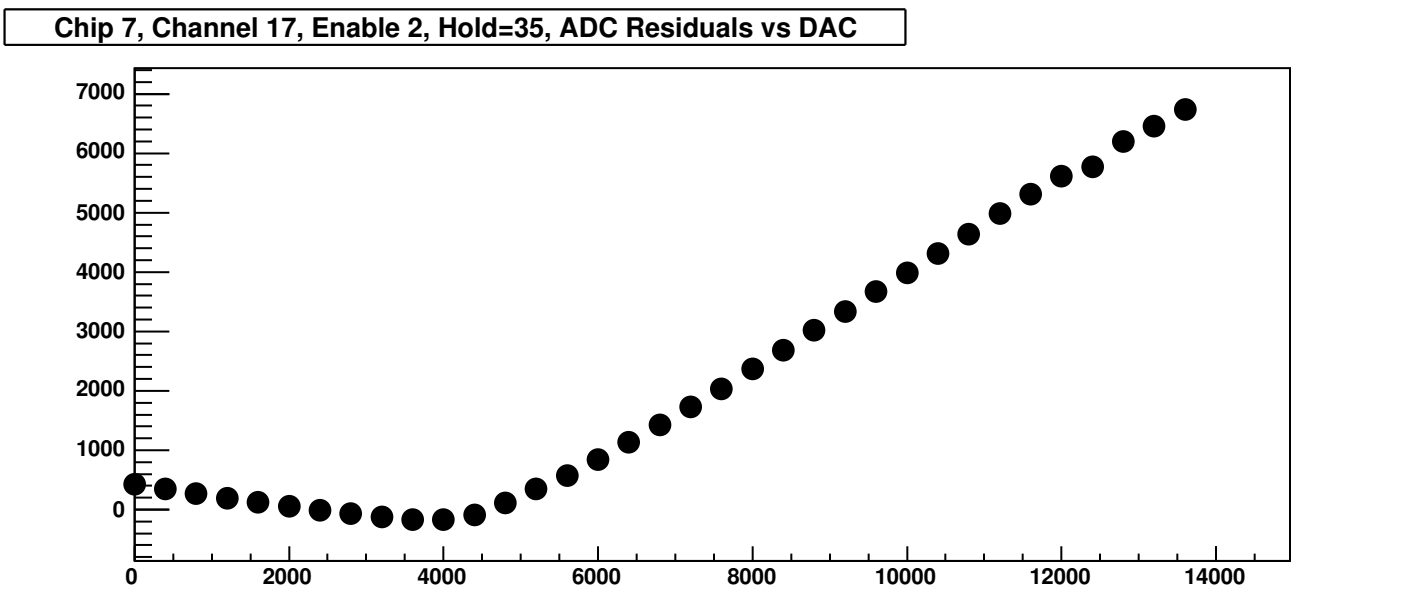

Chip 7, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.82 / 23
p0 -299.5 ± 0.6365
p1 0.01033 ± 0.0001016
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

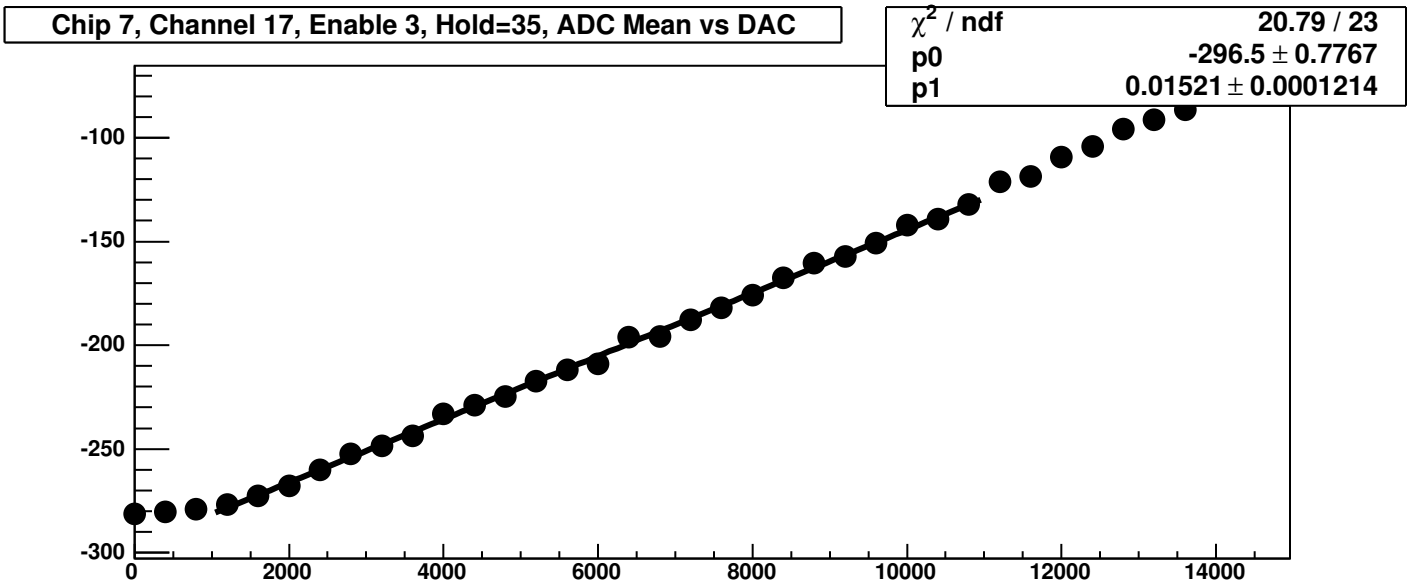

Chip 7, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

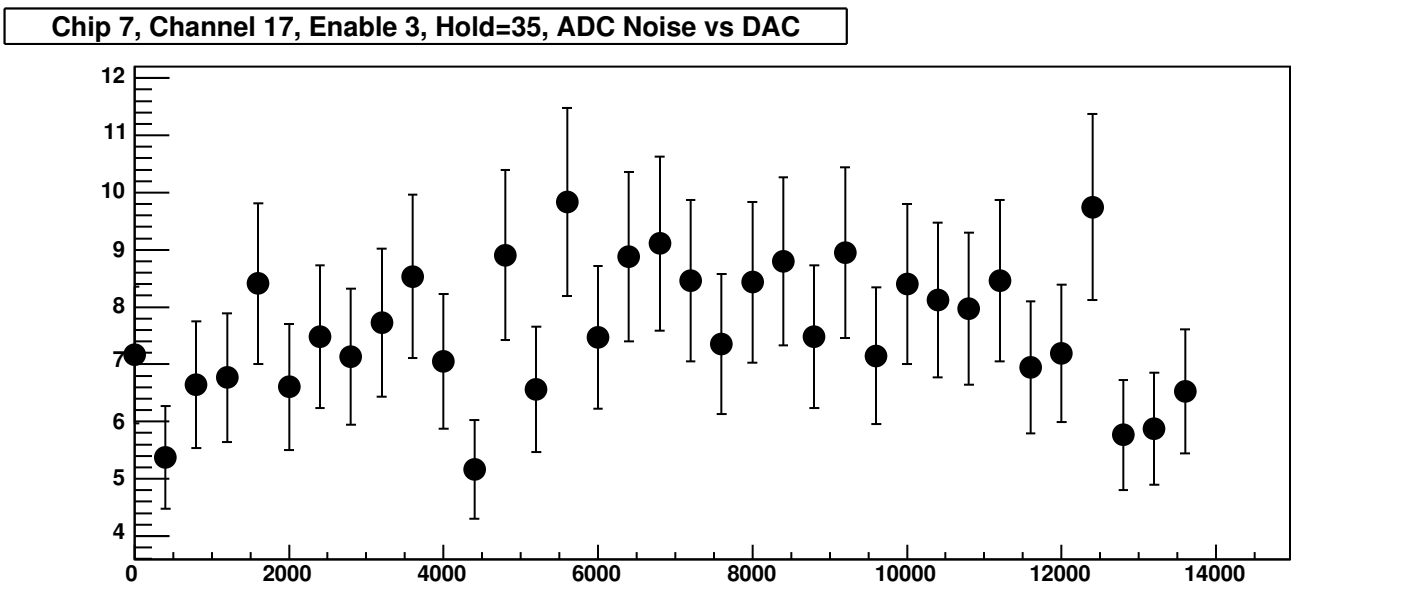

Chip 7, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

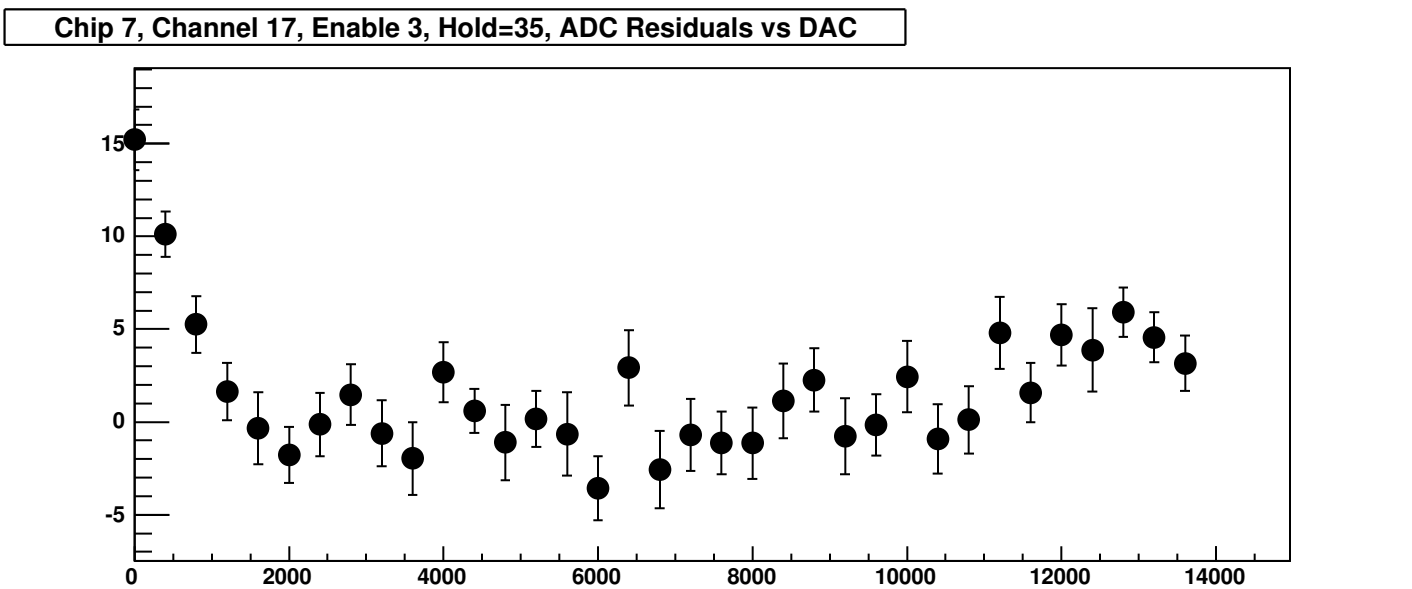

Chip 7, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.913e+05 / 23
p0 -707.9 ± 1.625
p1 0.2041 ± 0.0006019
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

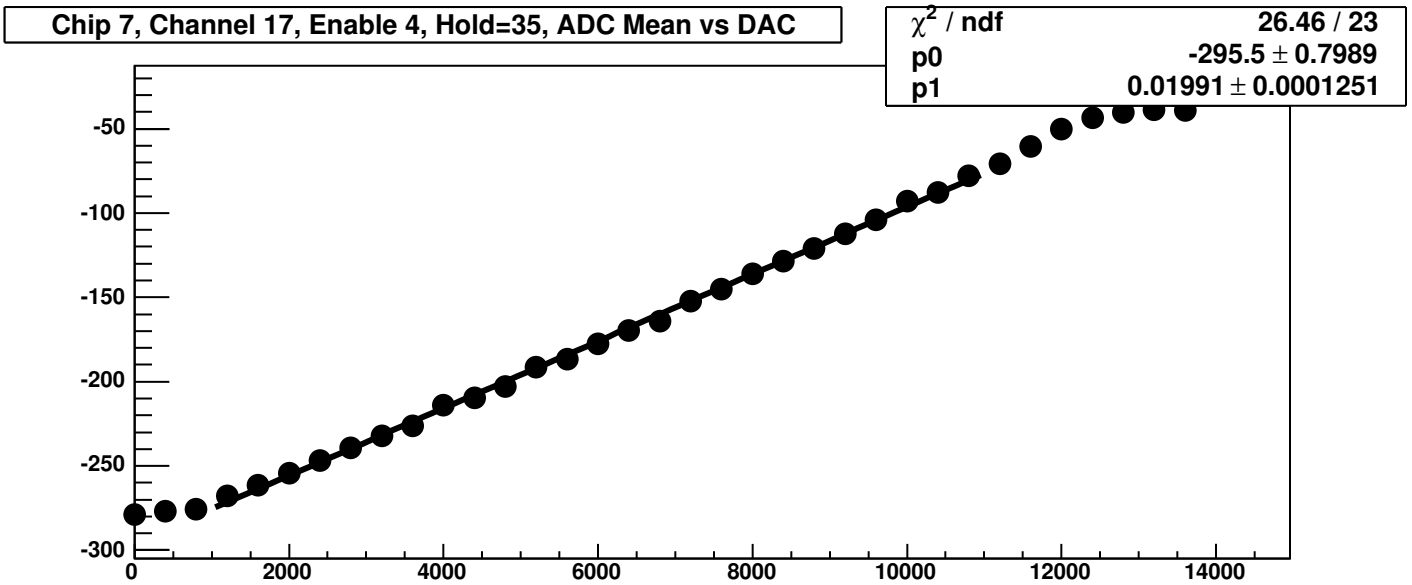

Chip 7, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

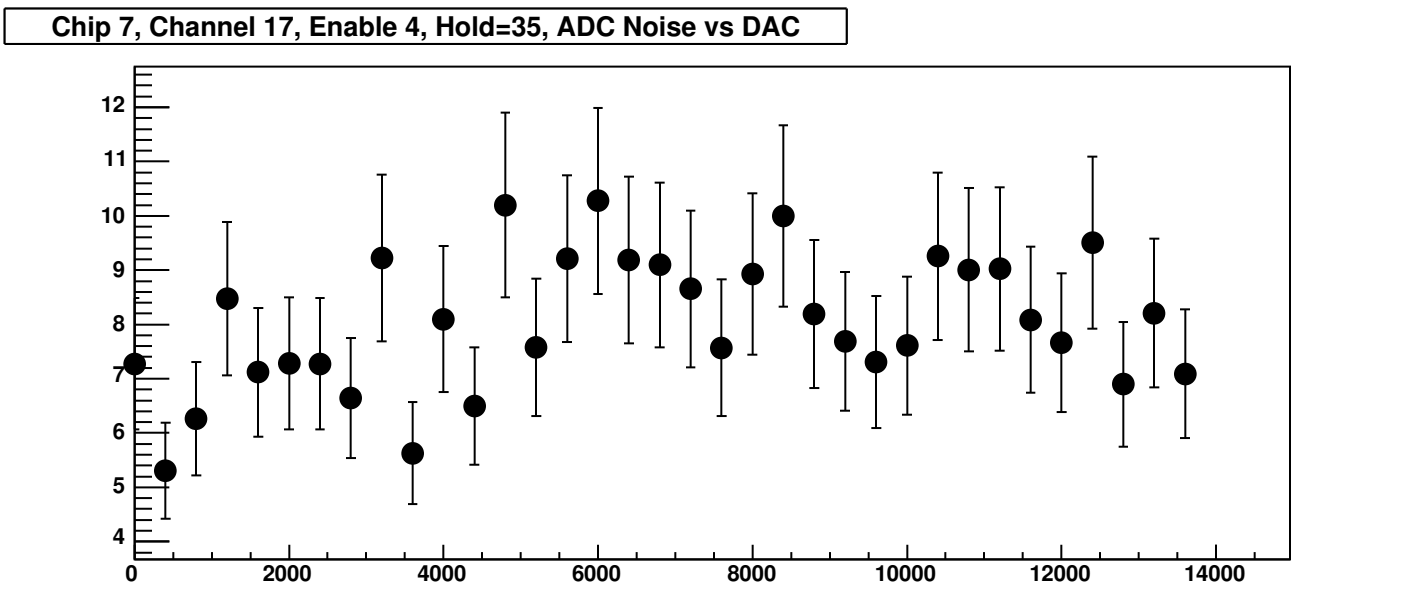

Chip 7, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

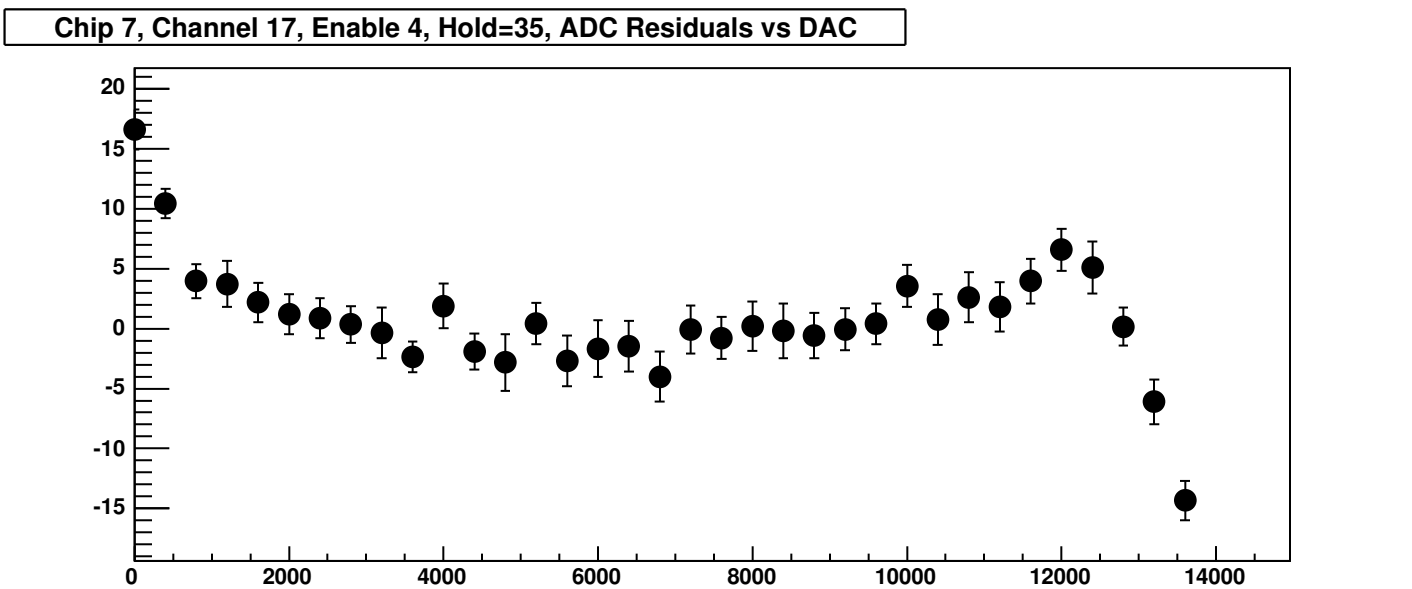

Chip 7, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.79 / 23
p0 -296.5 ± 0.7767
p1 0.01521 ± 0.0001214
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

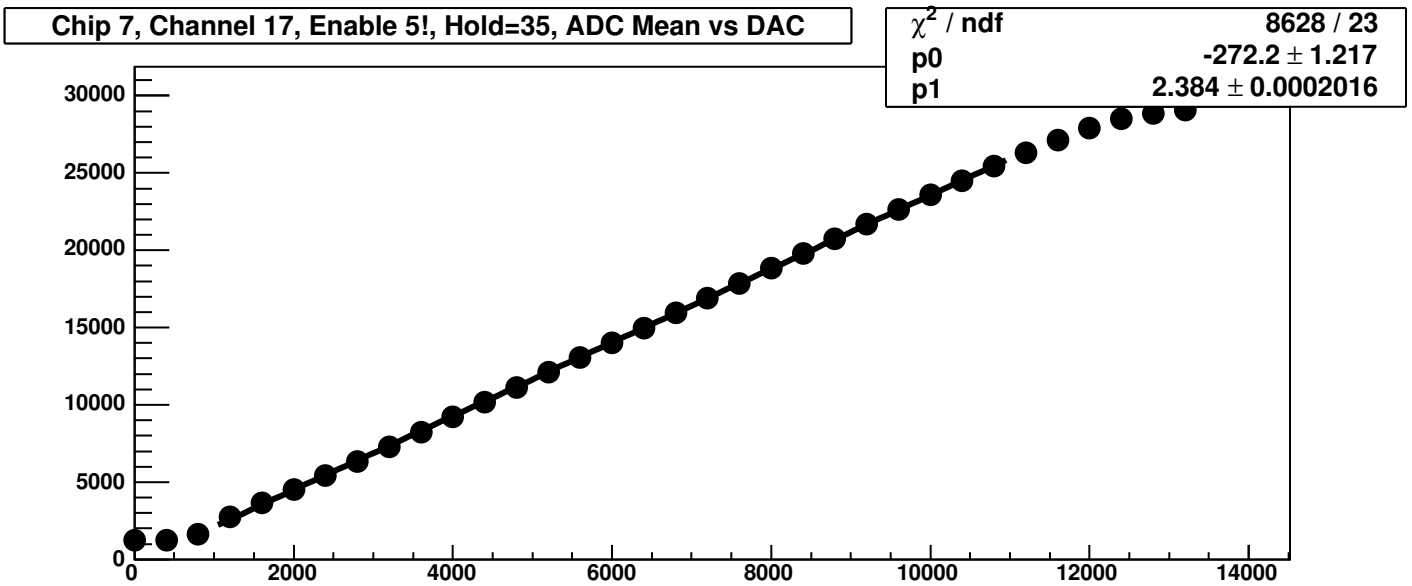

Chip 7, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

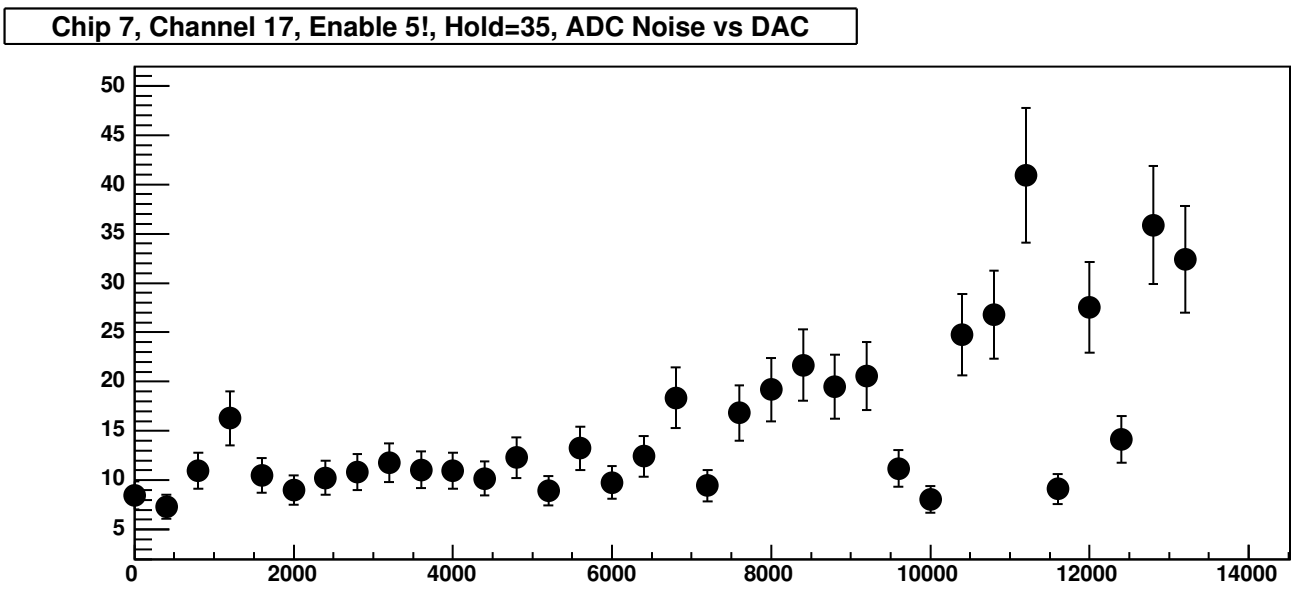

Chip 7, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

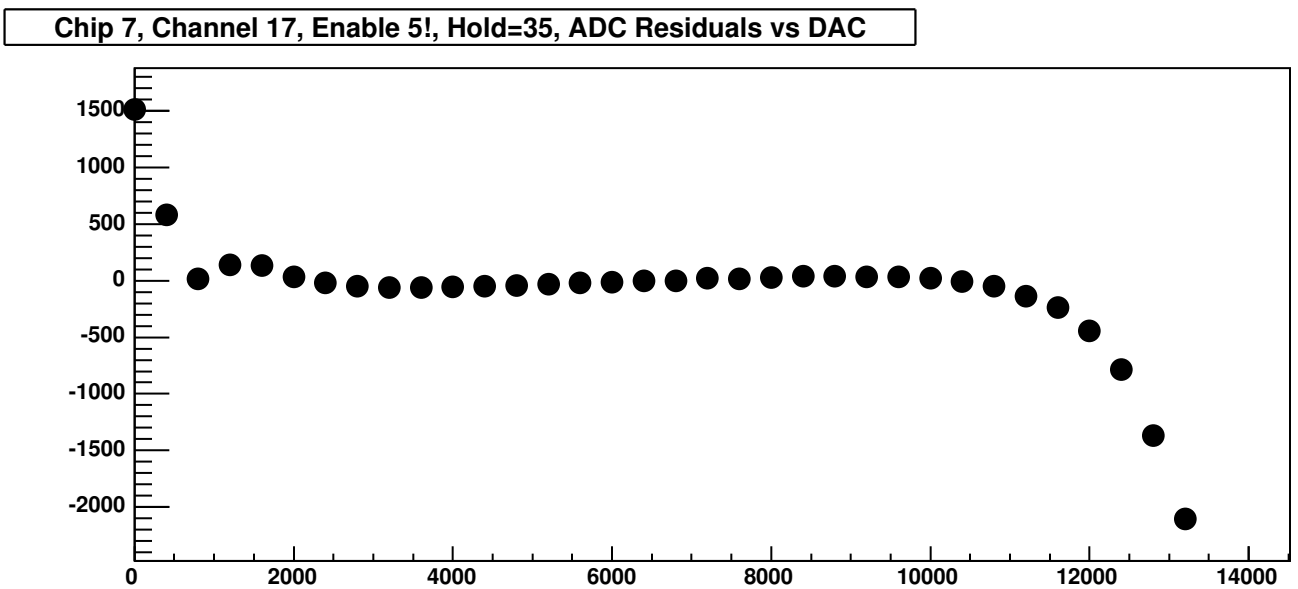

Chip 7, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.46 / 23
p0 -295.5 ± 0.7989
p1 0.01991 ± 0.0001251
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

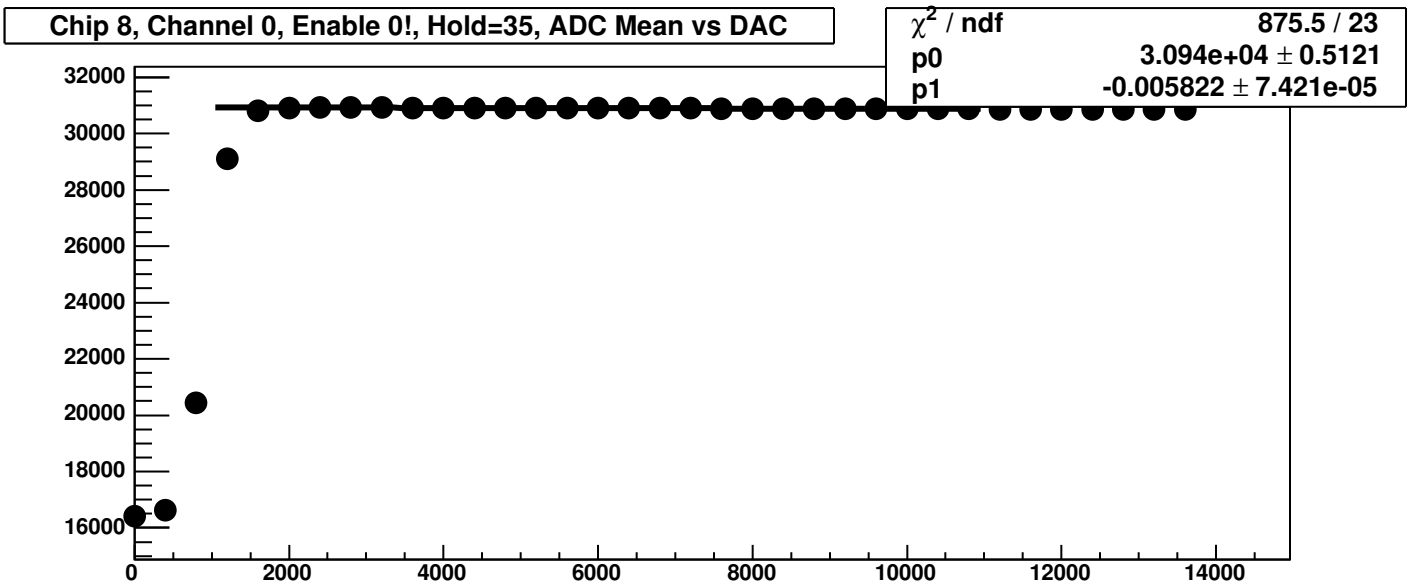

Chip 7, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

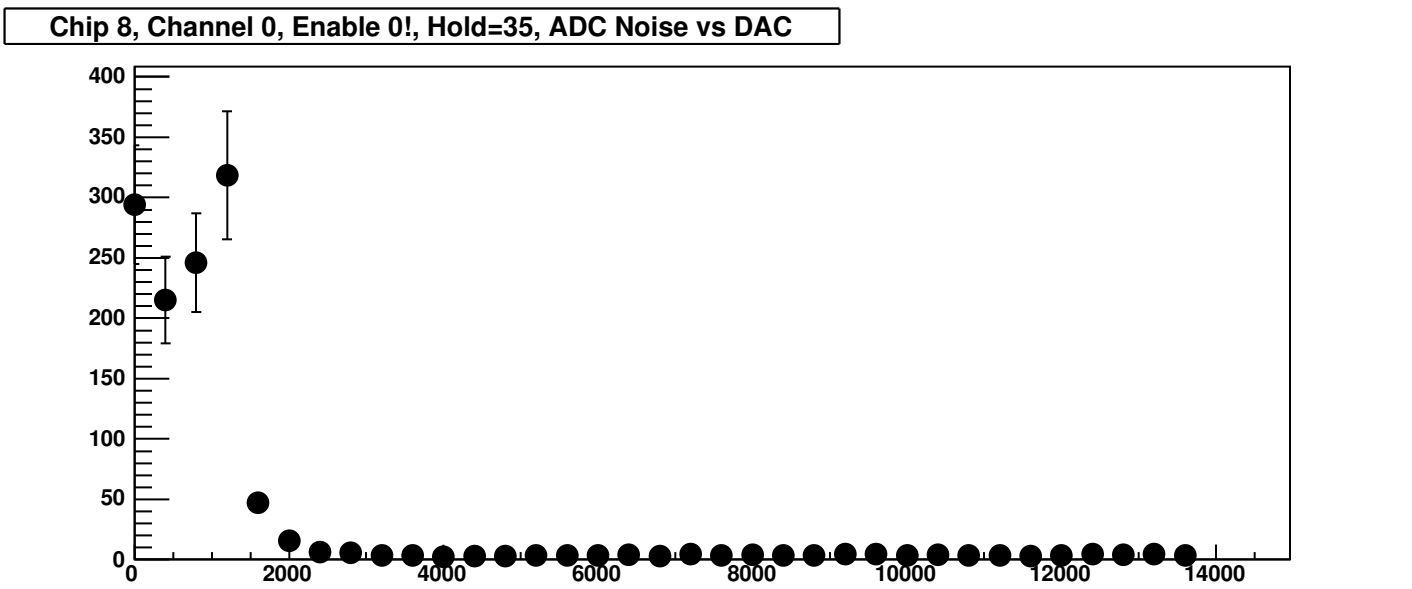

Chip 7, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

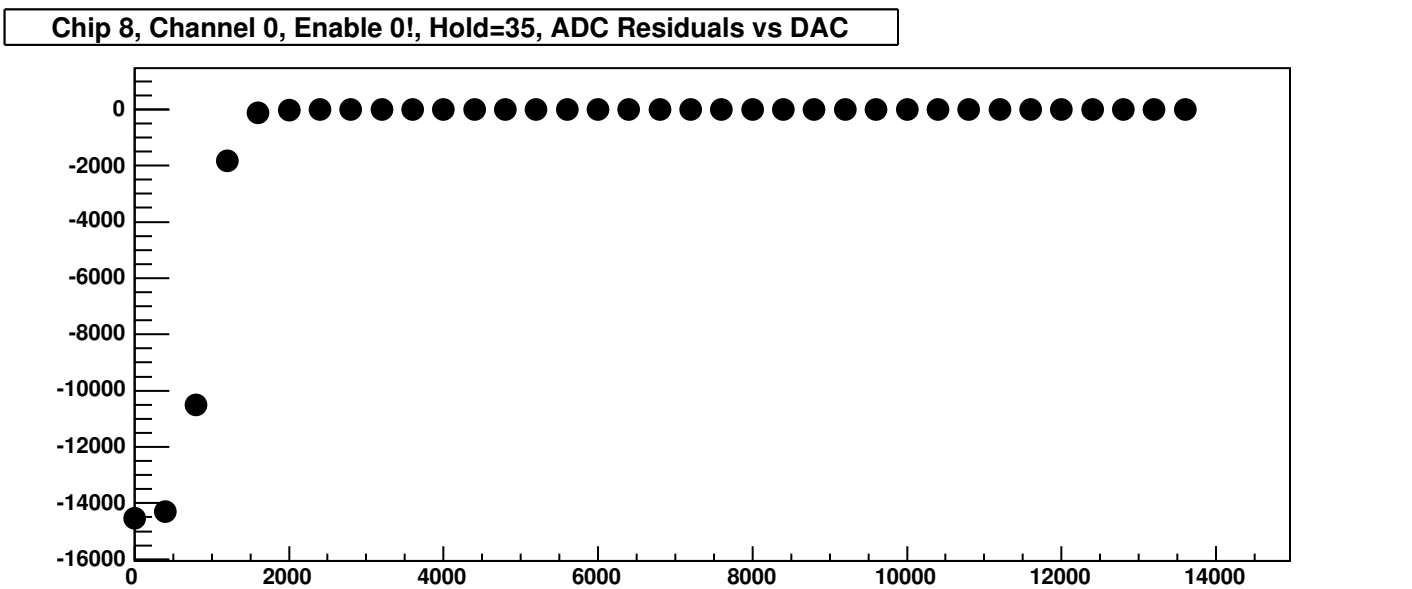

Chip 7, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 8628 / 23
p0 -272.2 ± 1.217
p1 2.384 ± 0.0002016
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

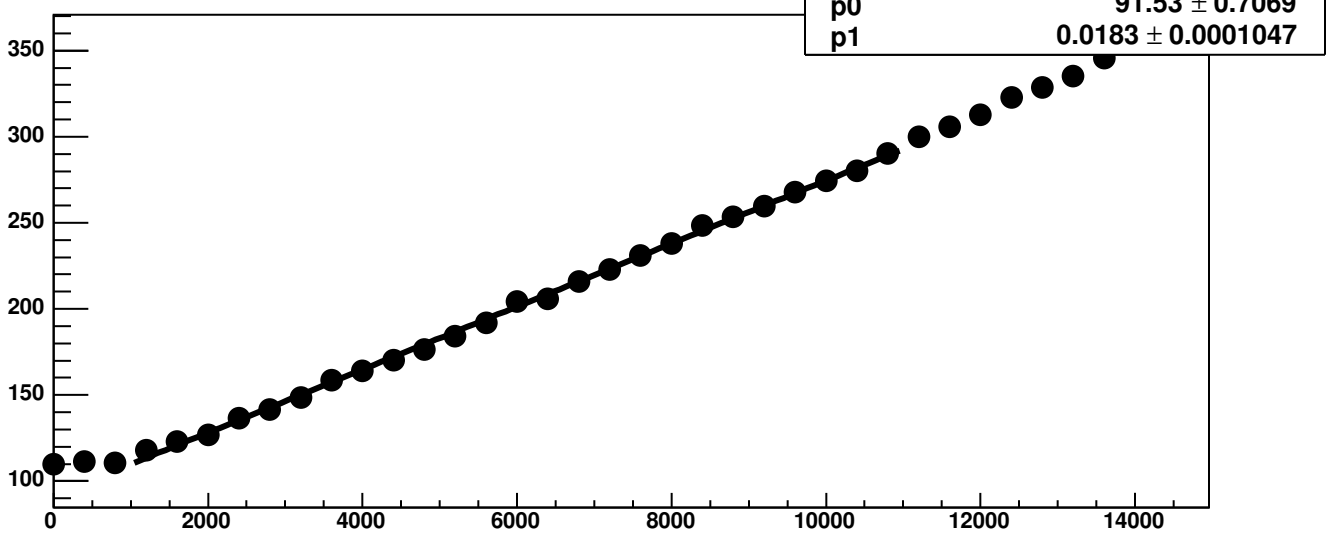

Chip 7, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 875.5 / 23
p0 3.094e+04 ± 0.5121
p1 -0.005822 ± 7.421e-05
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

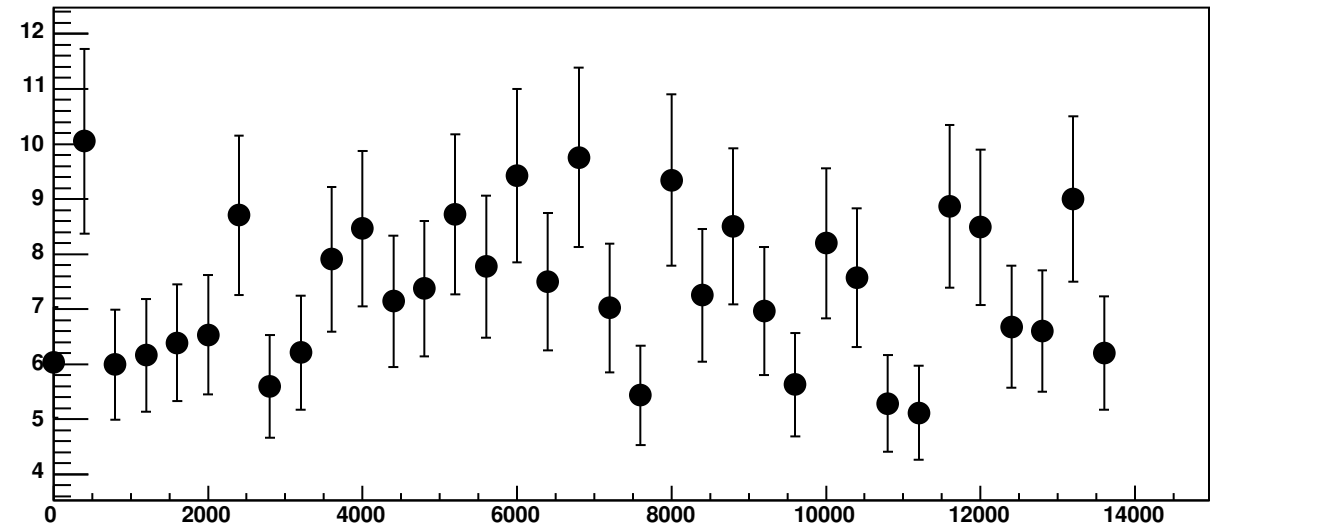

Chip 8, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

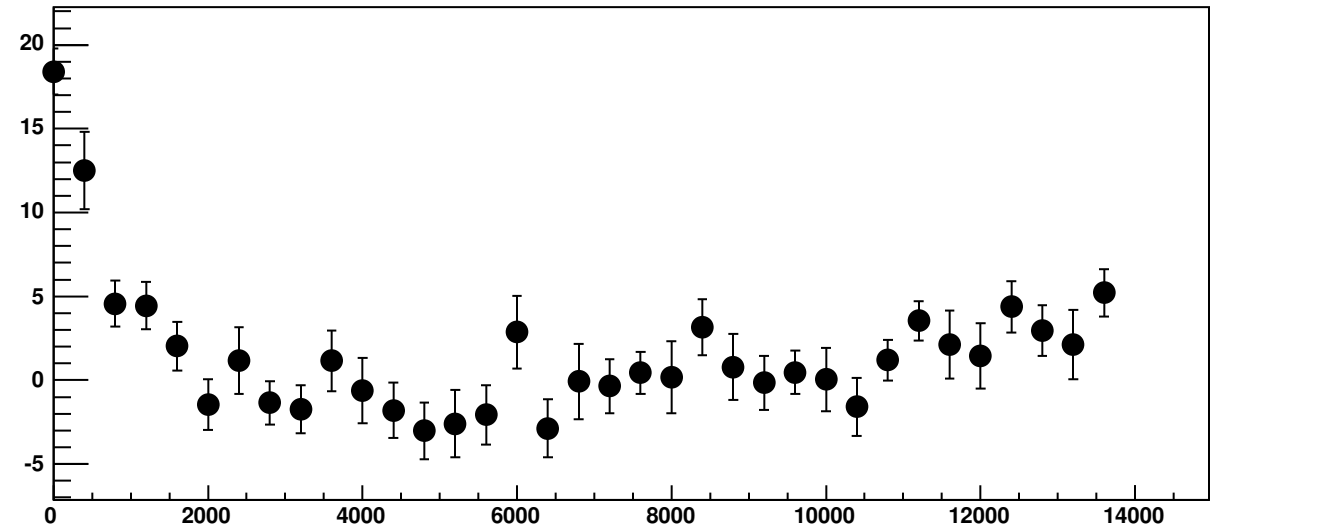
Chip 8, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

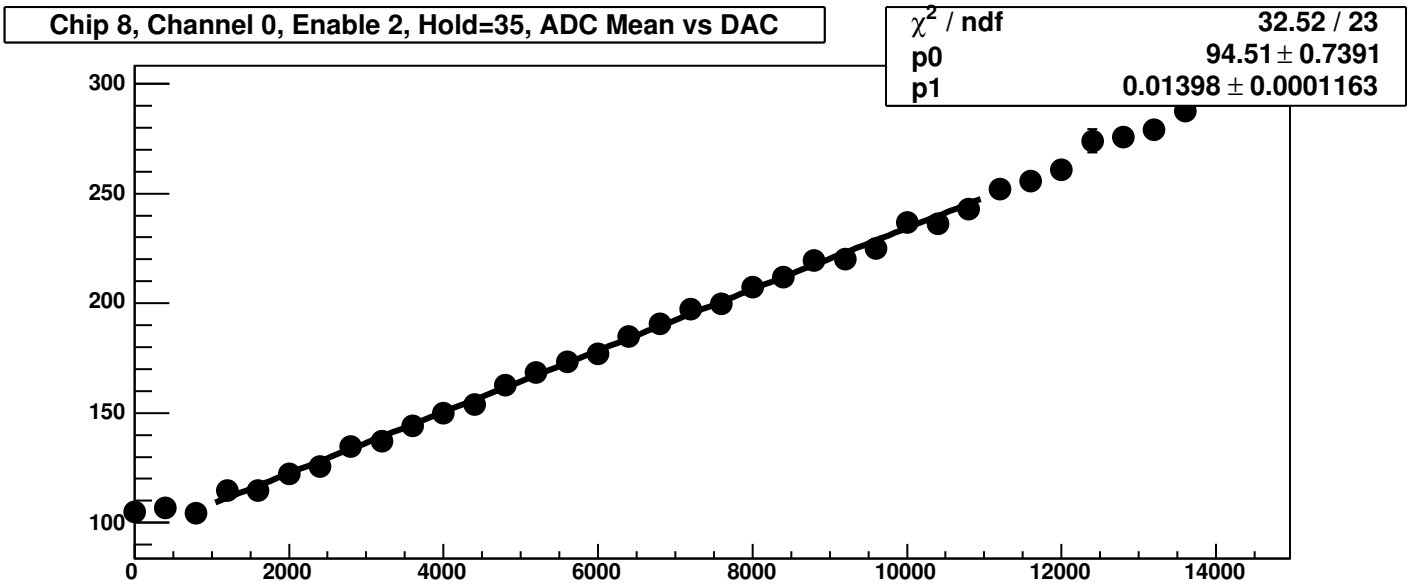


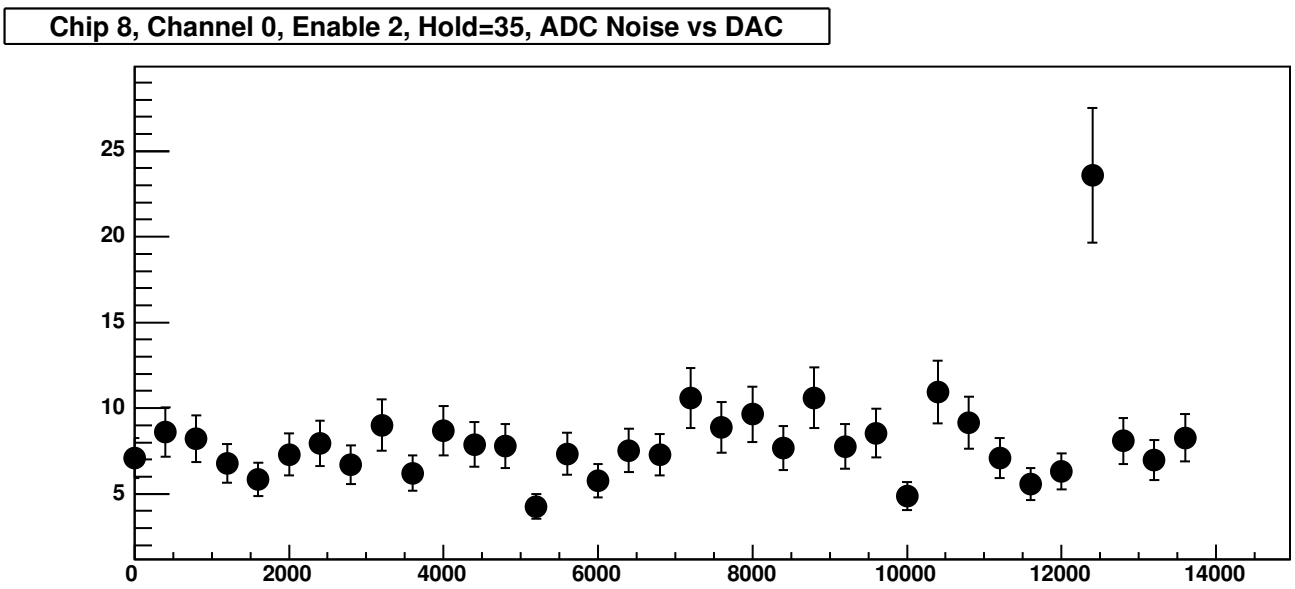
Chip 8, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

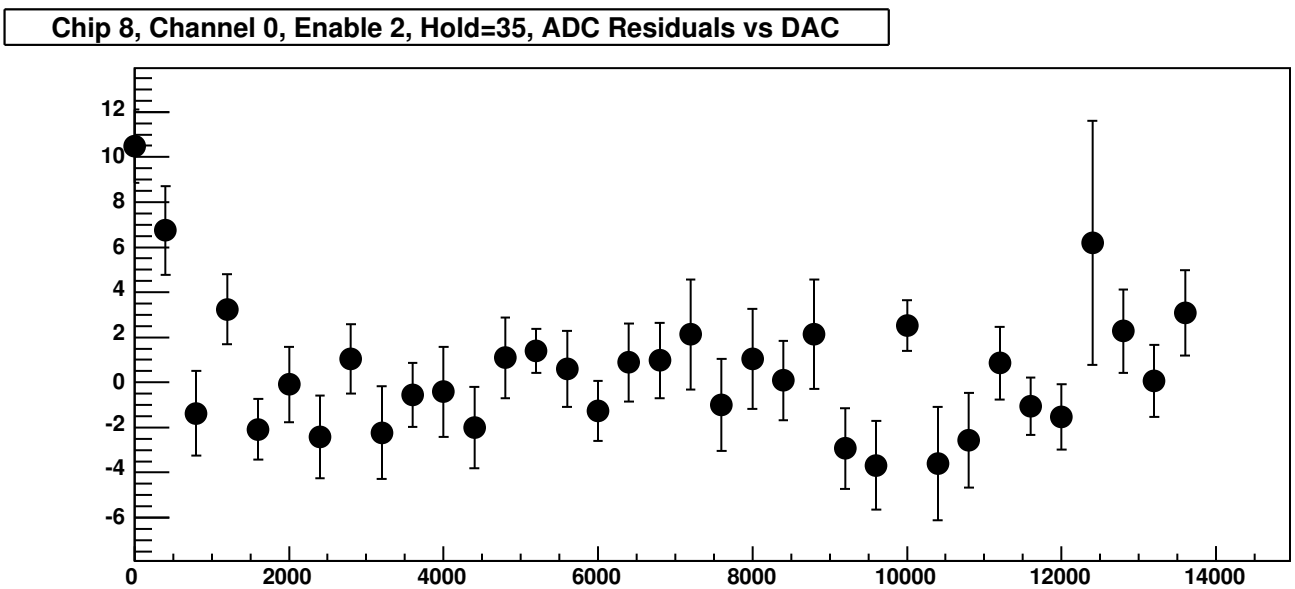


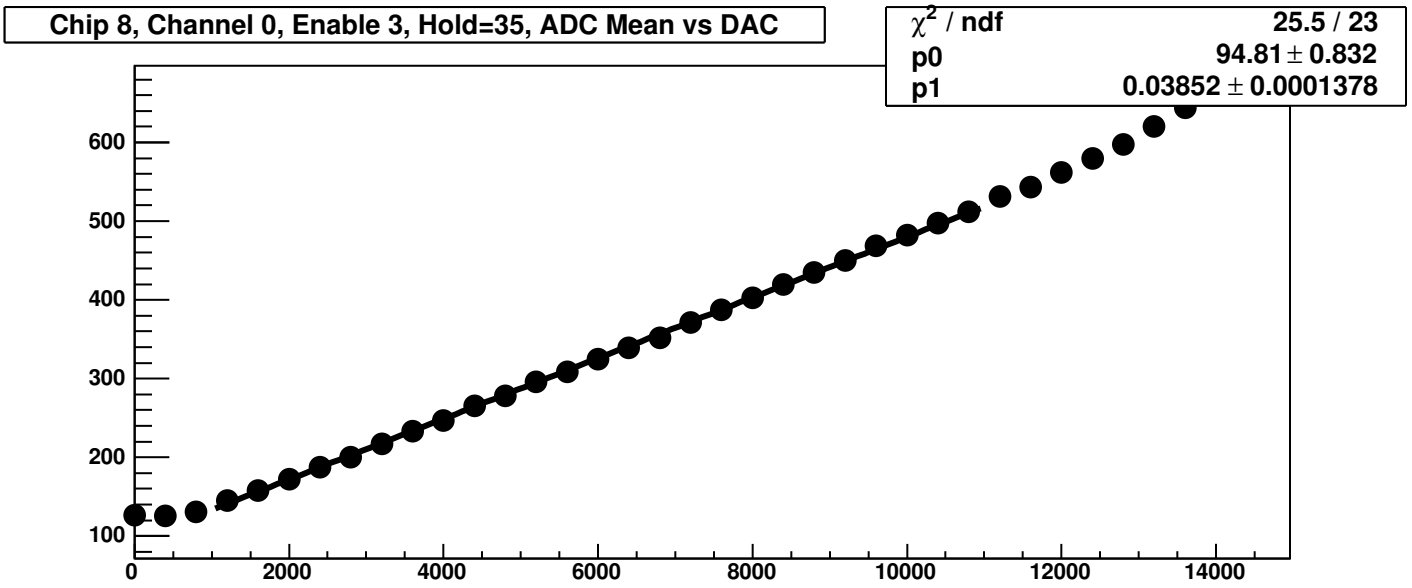
Chip 8, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

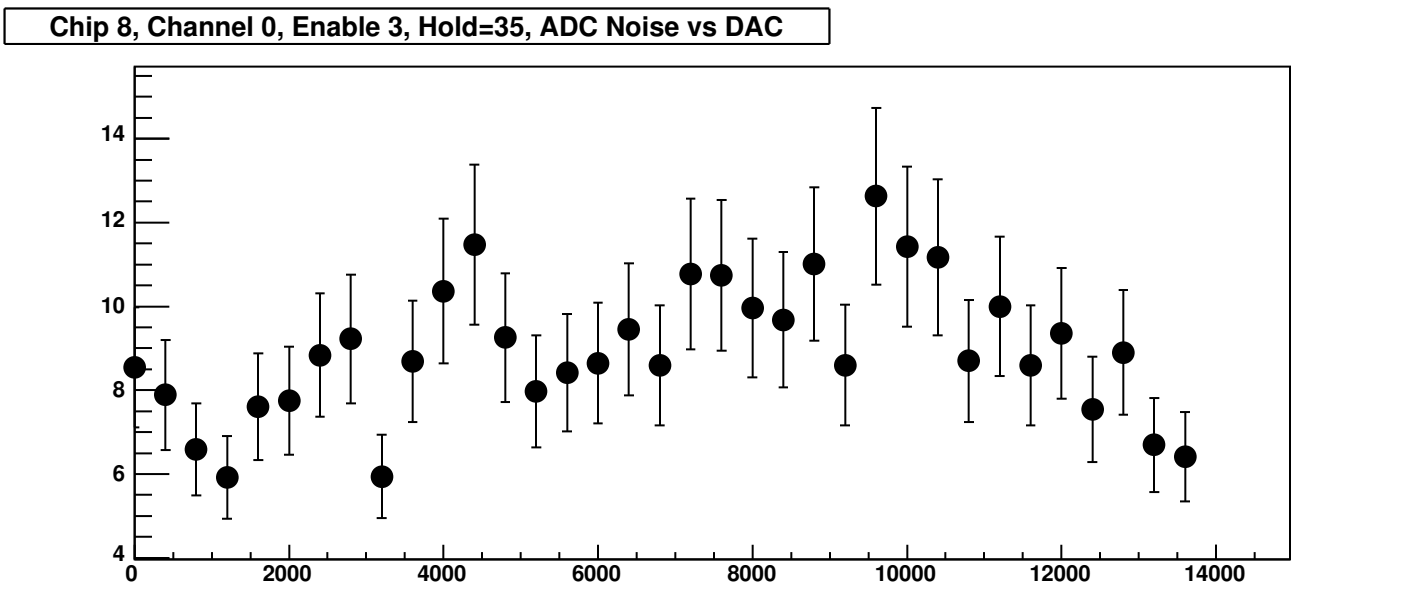


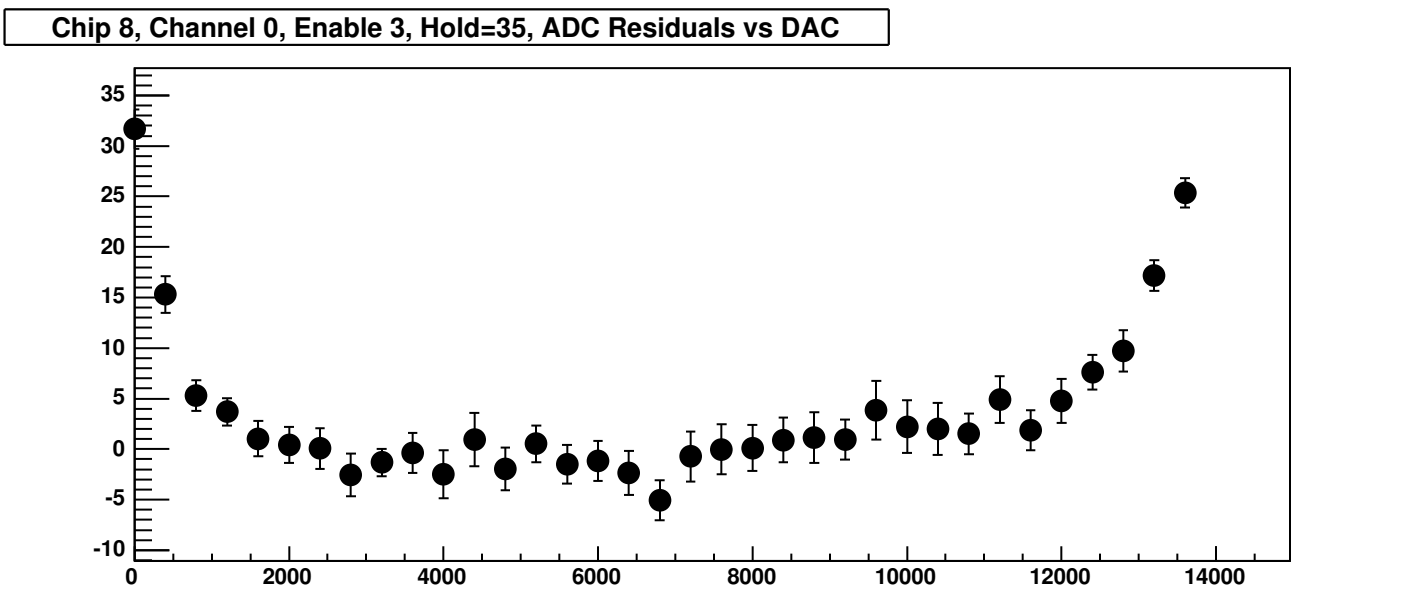

Chip 8, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.52 / 23
p0 94.51 ± 0.7391
p1 0.01398 ± 0.0001163
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

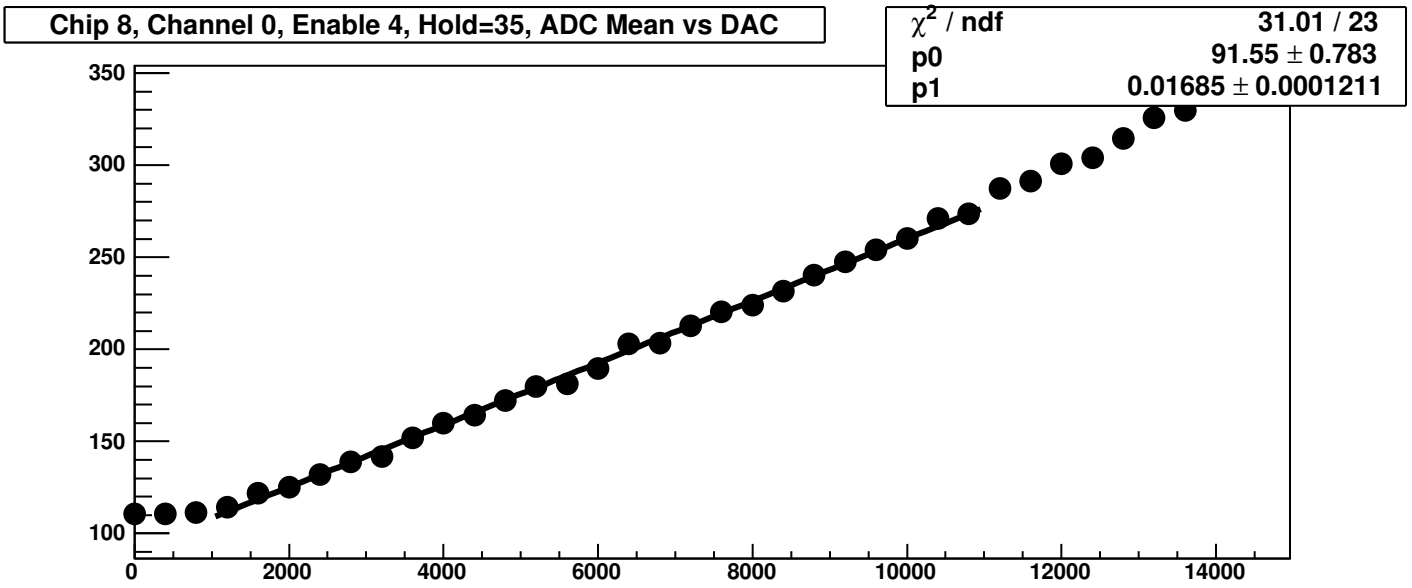

Chip 8, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

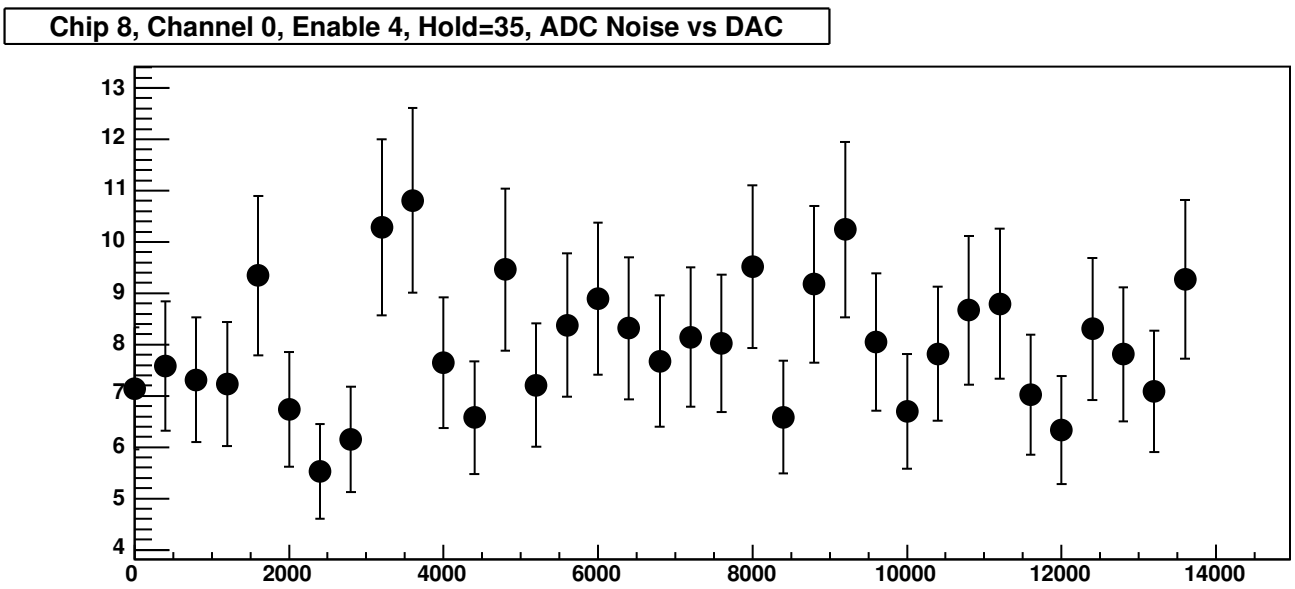

Chip 8, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

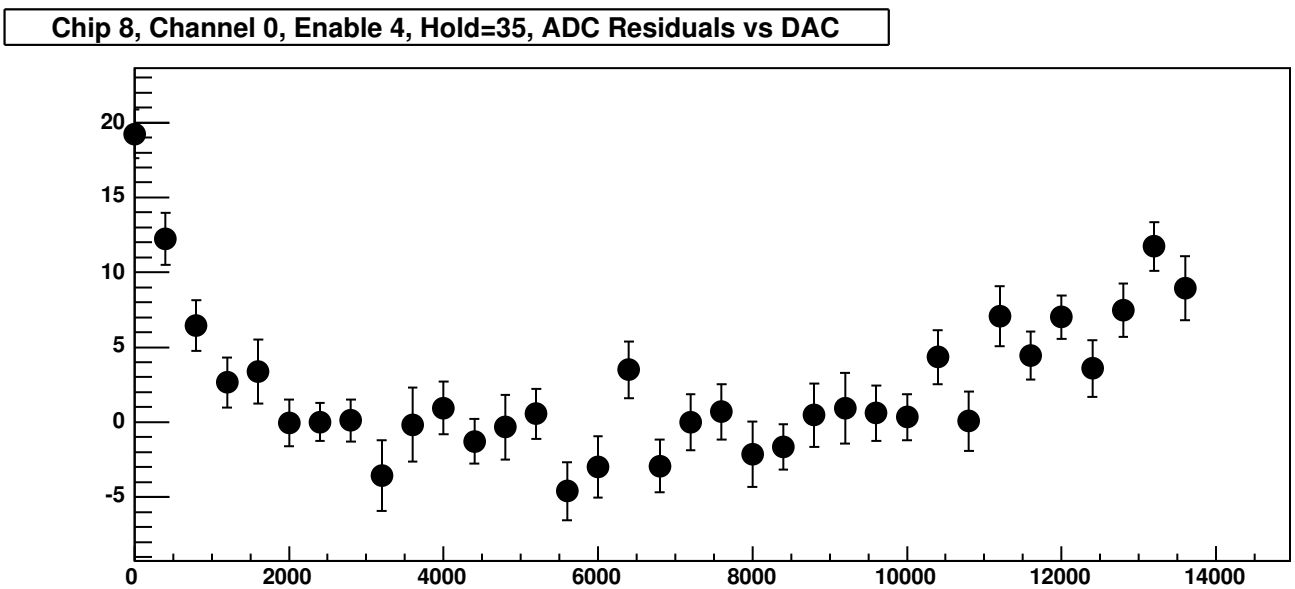

Chip 8, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.5 / 23
p0 94.81 ± 0.832
p1 0.03852 ± 0.0001378
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

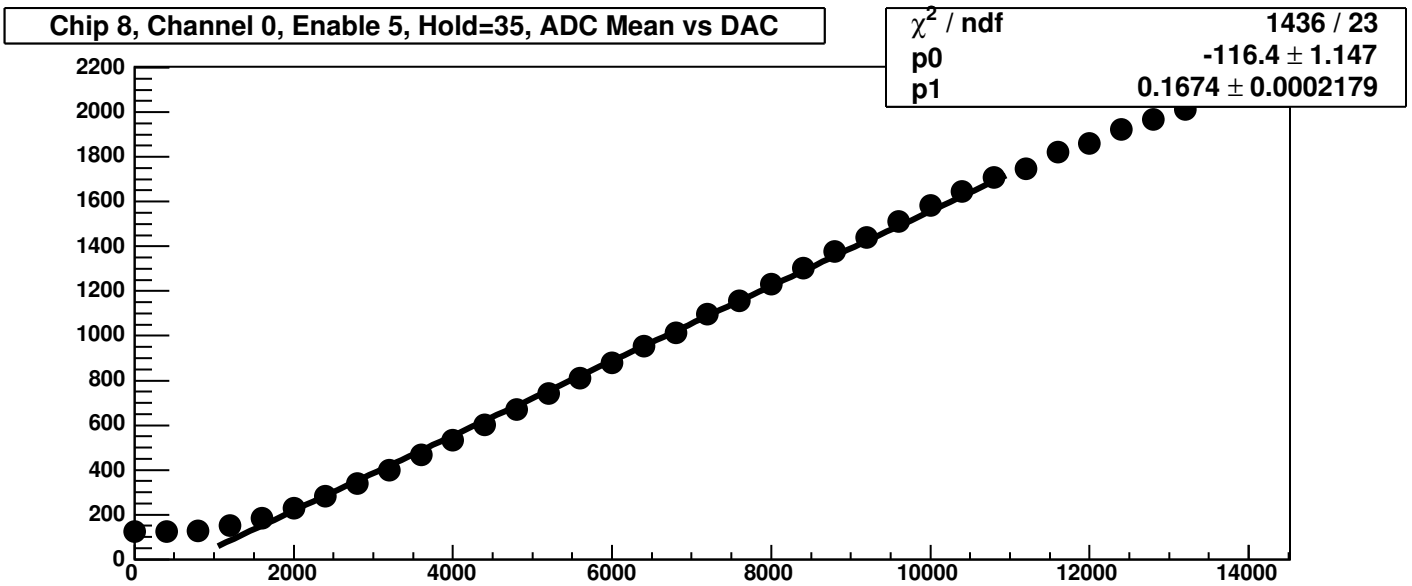

Chip 8, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

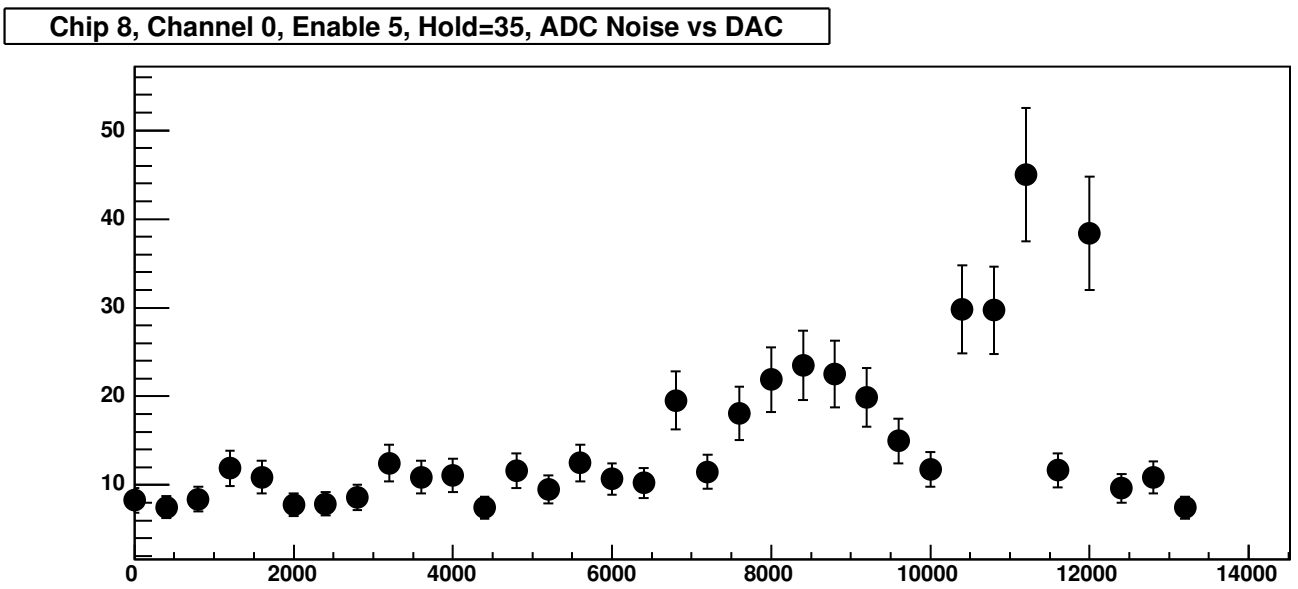

Chip 8, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

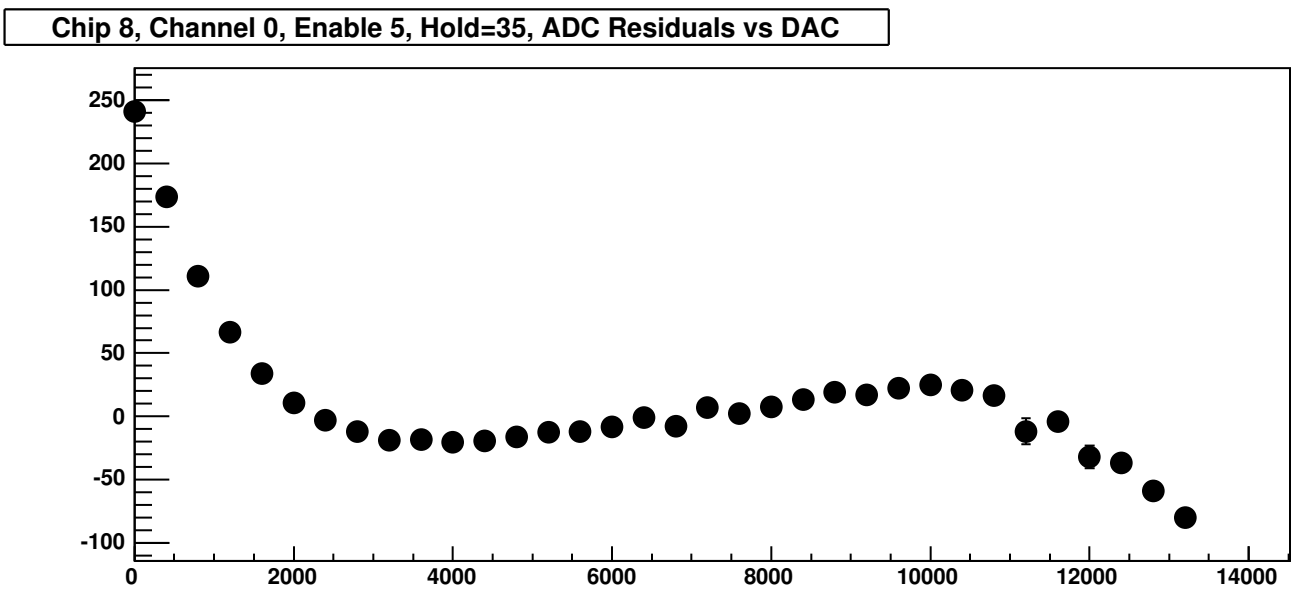

Chip 8, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.01 / 23
p0 91.55 ± 0.783
p1 0.01685 ± 0.0001211
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

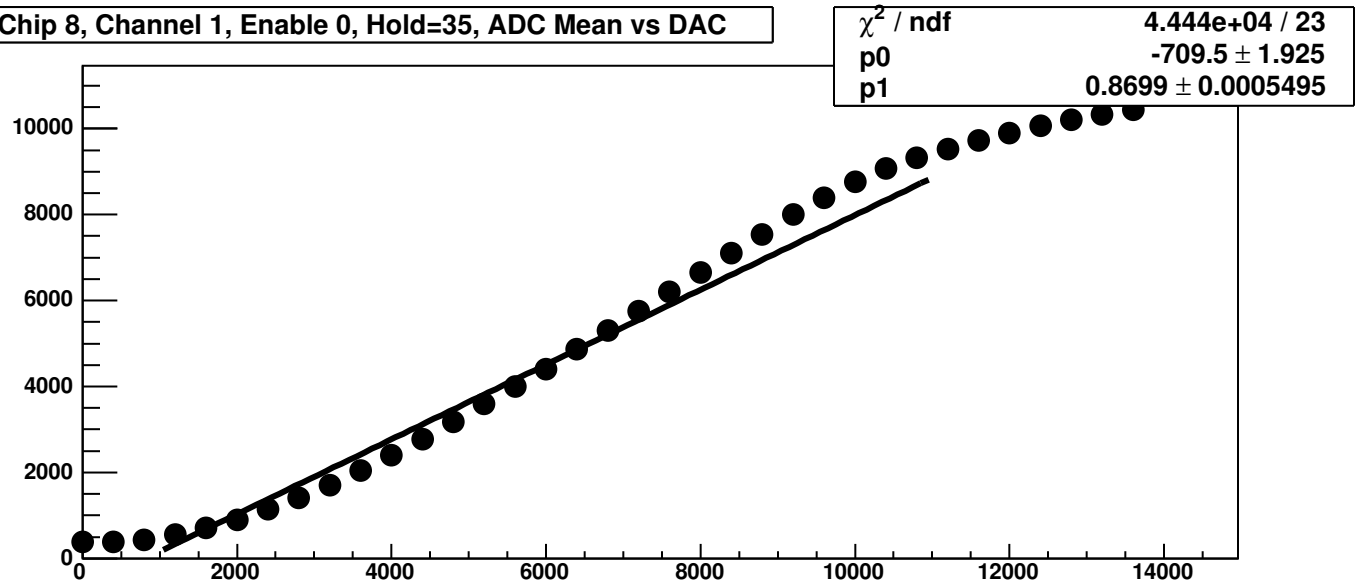

Chip 8, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1436 / 23
p0 -116.4 ± 1.147
p1 0.1674 ± 0.0002179
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

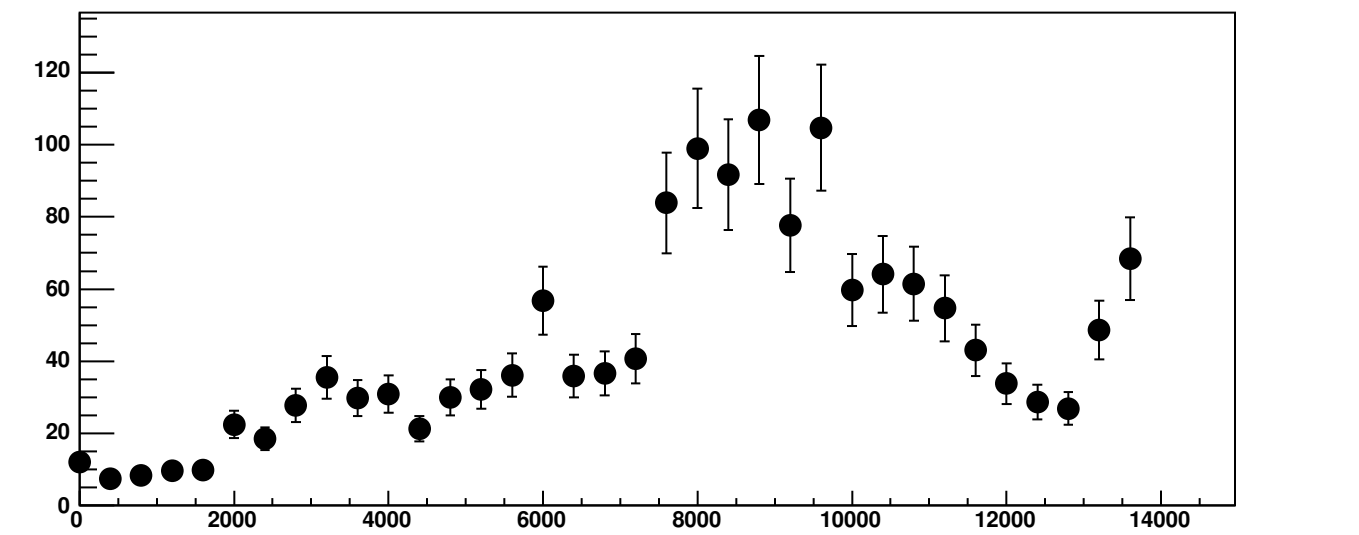

Chip 8, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

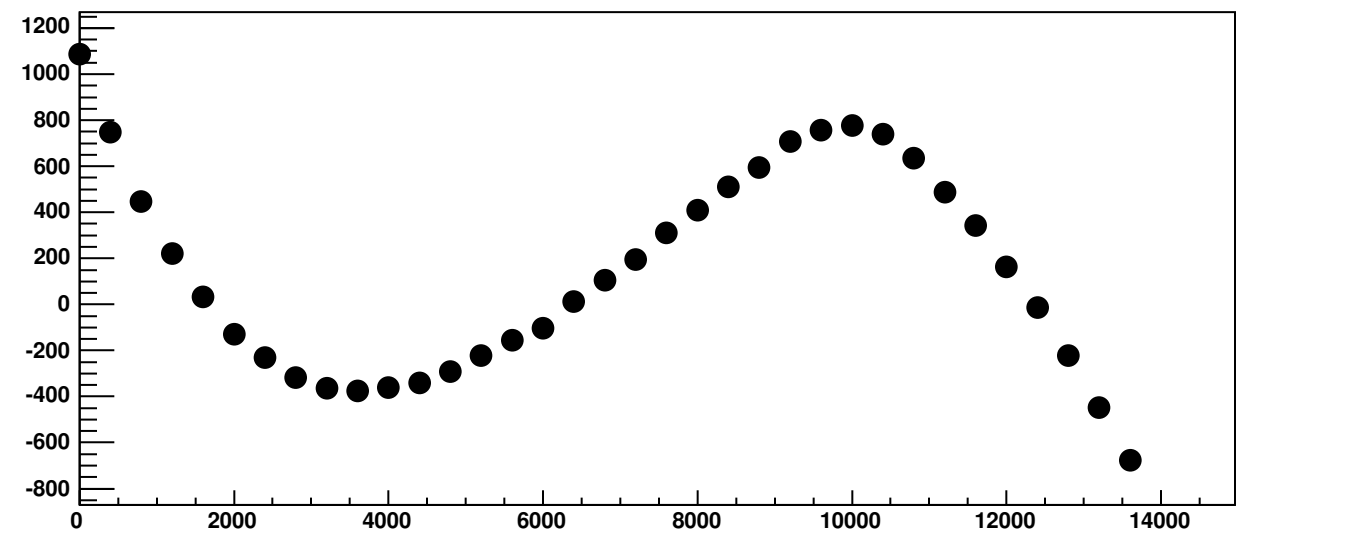
Chip 8, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

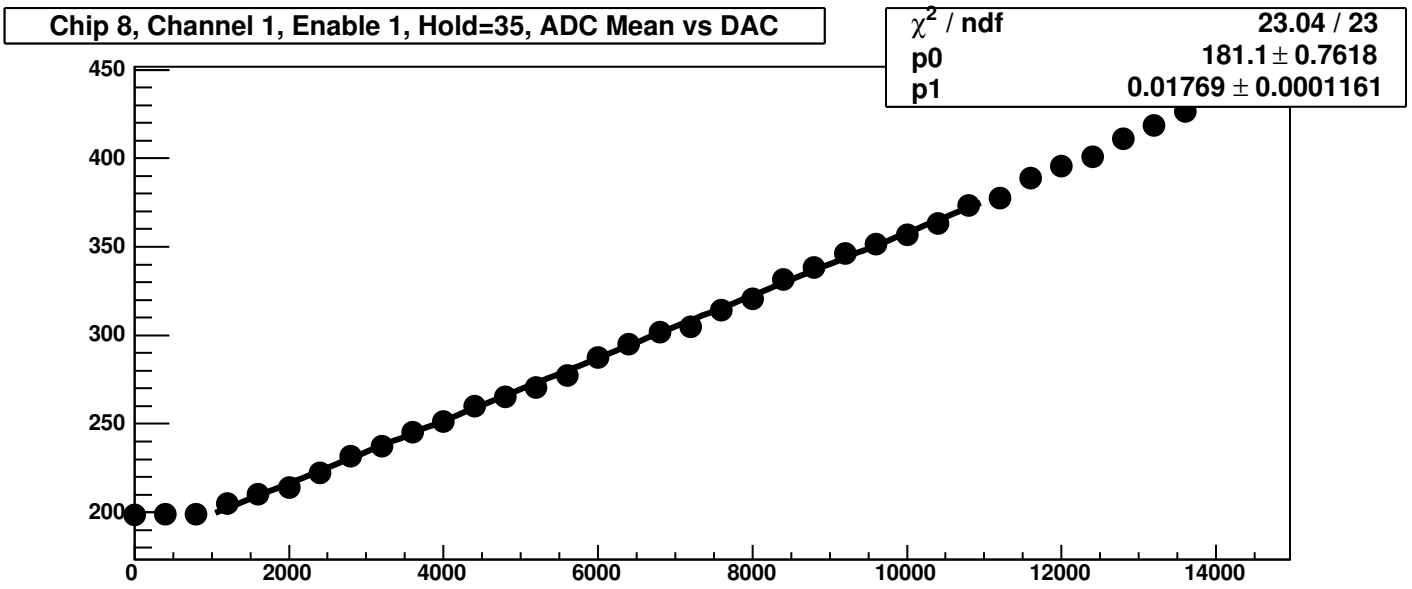


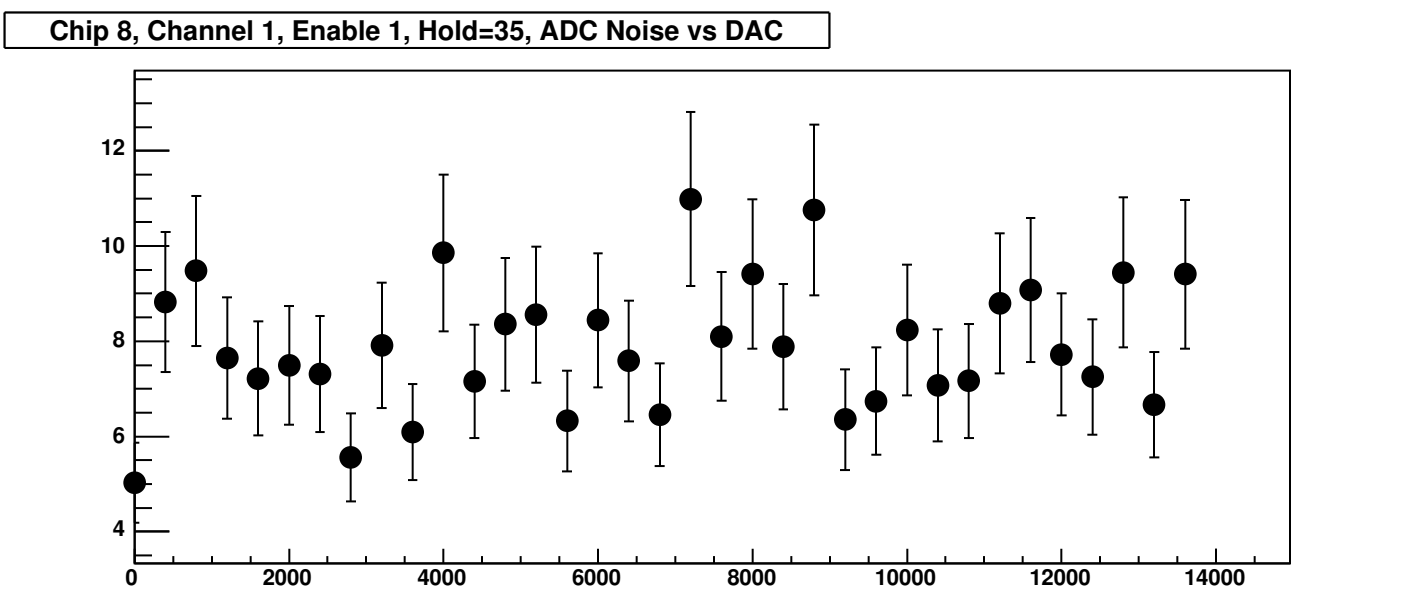
Chip 8, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

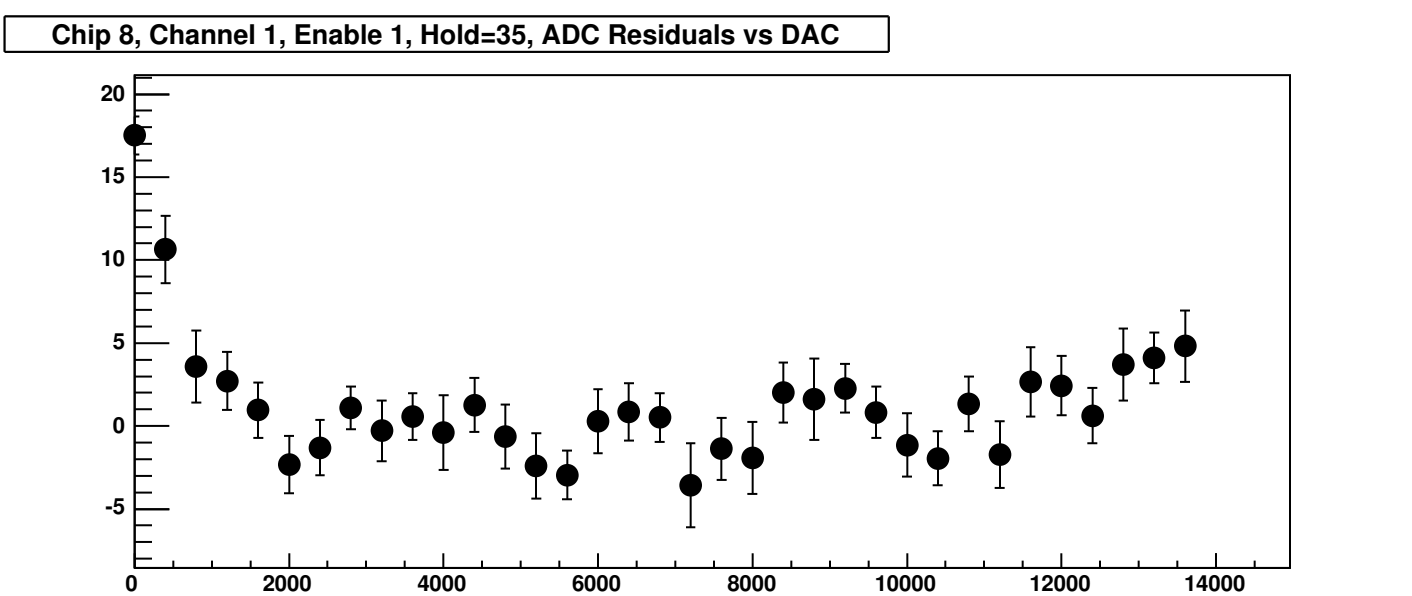


Chip 8, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

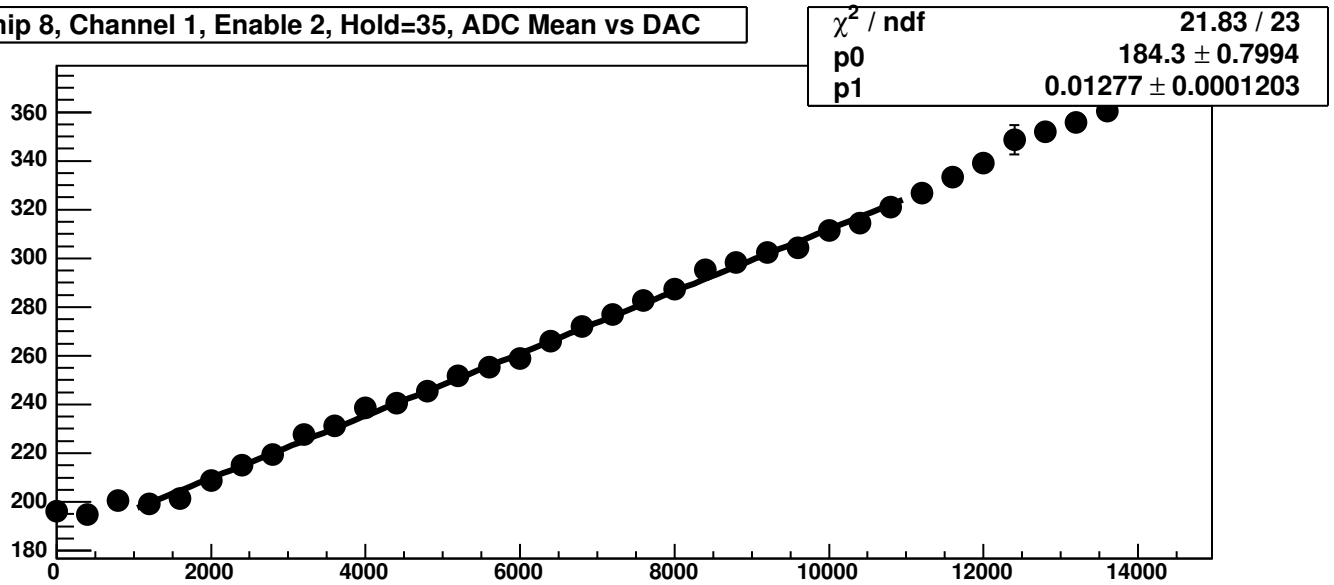



Chip 8, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.04 / 23
p0 181.1 ± 0.7618
p1 0.01769 ± 0.0001161
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

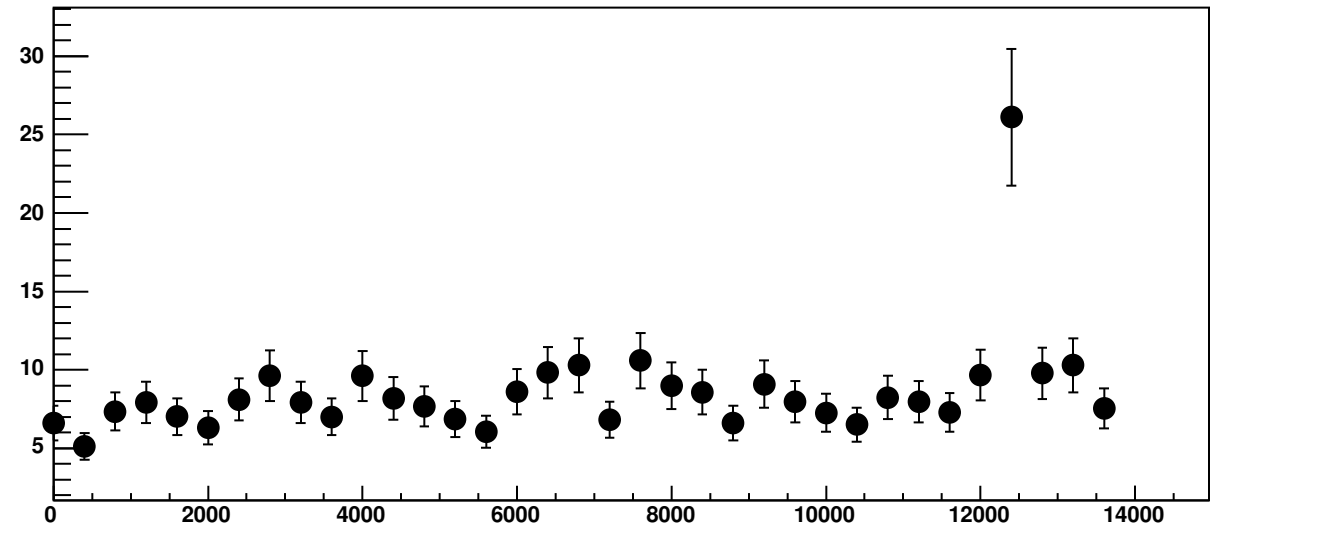

Chip 8, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

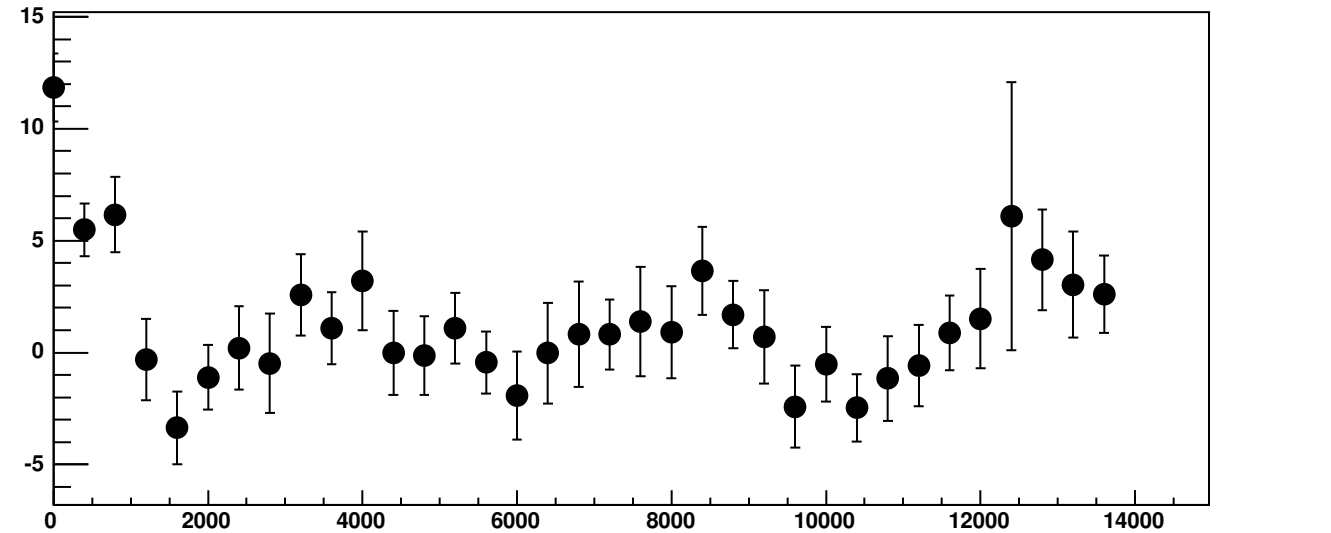
Chip 8, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

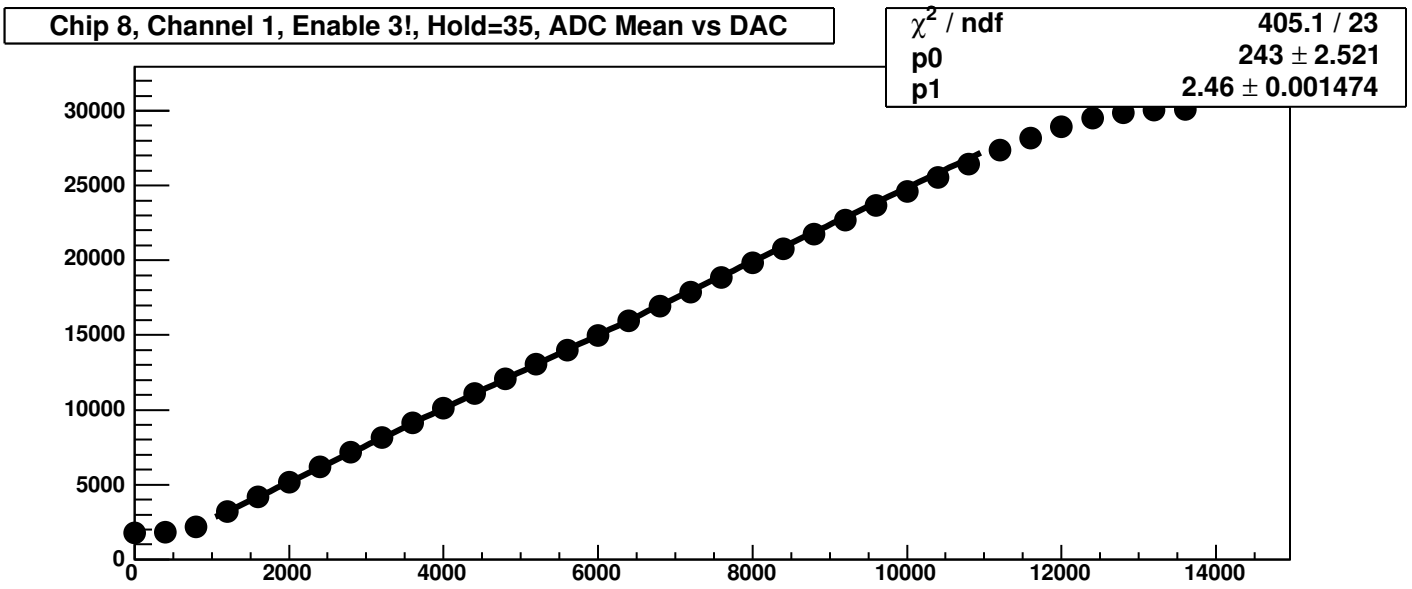


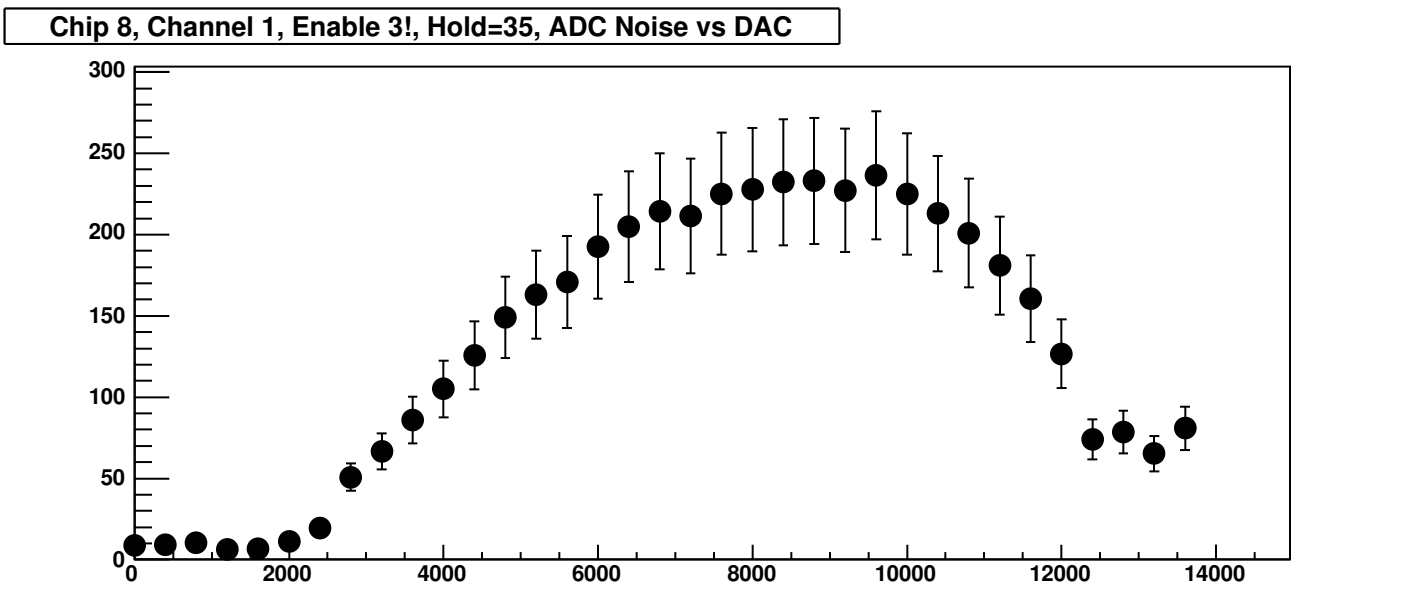
Chip 8, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

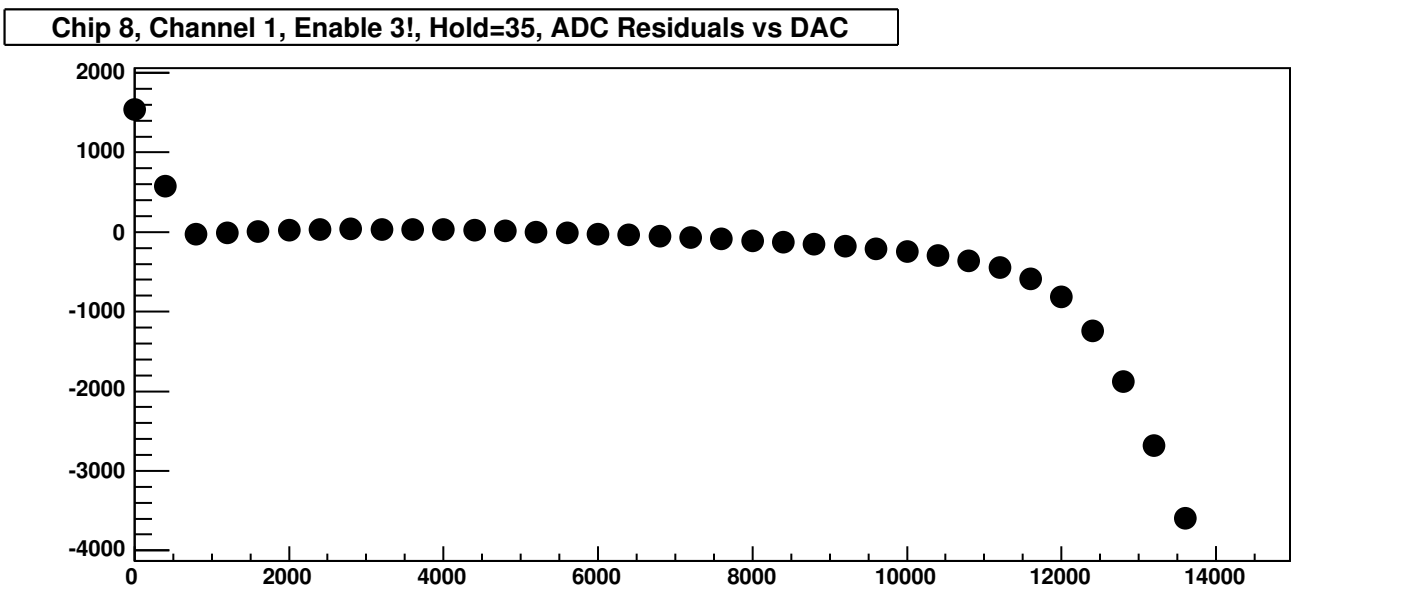


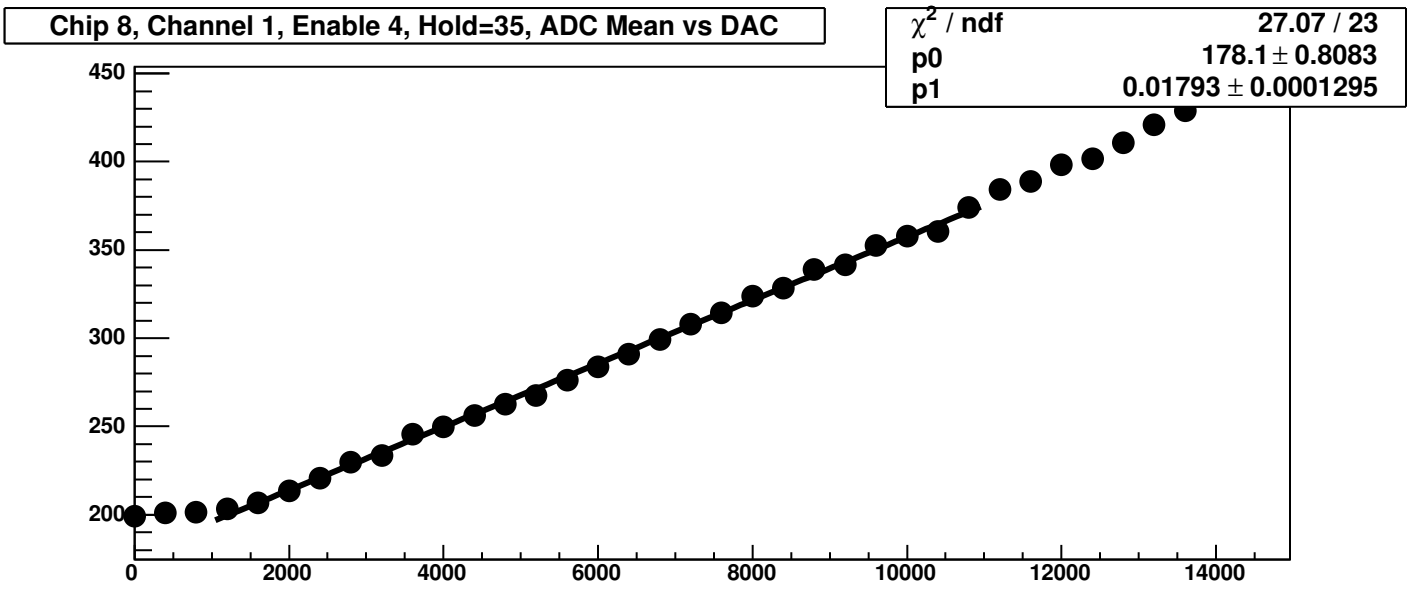
Chip 8, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

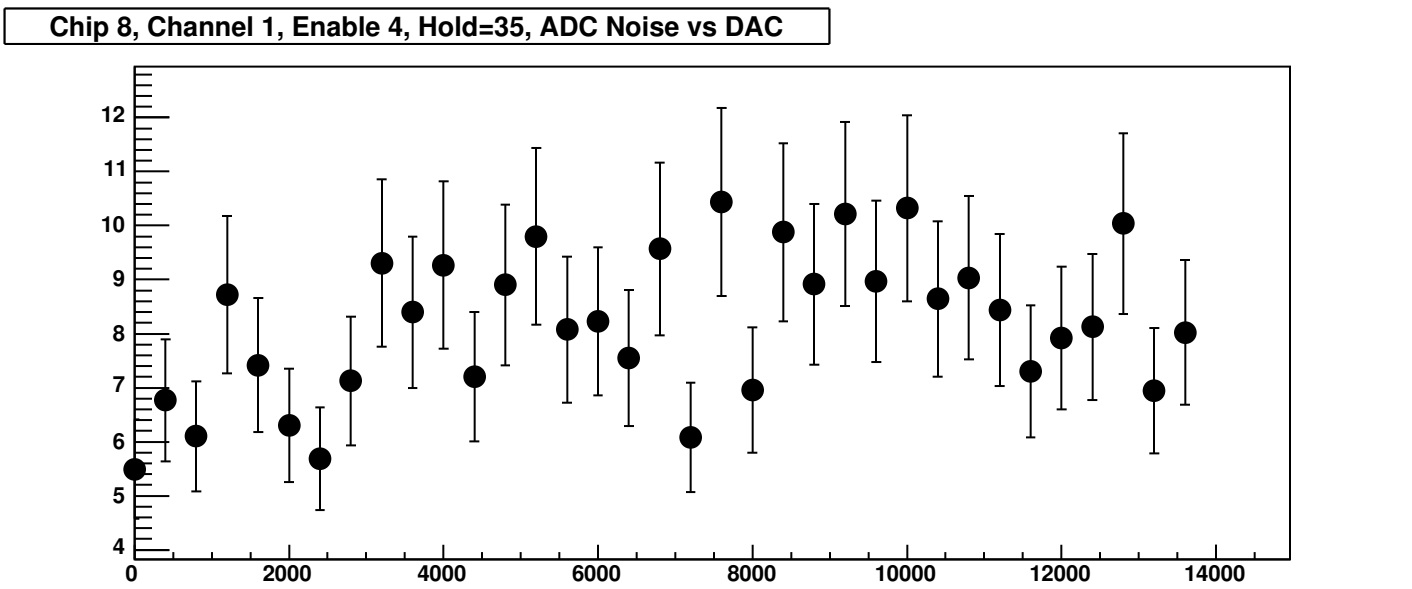


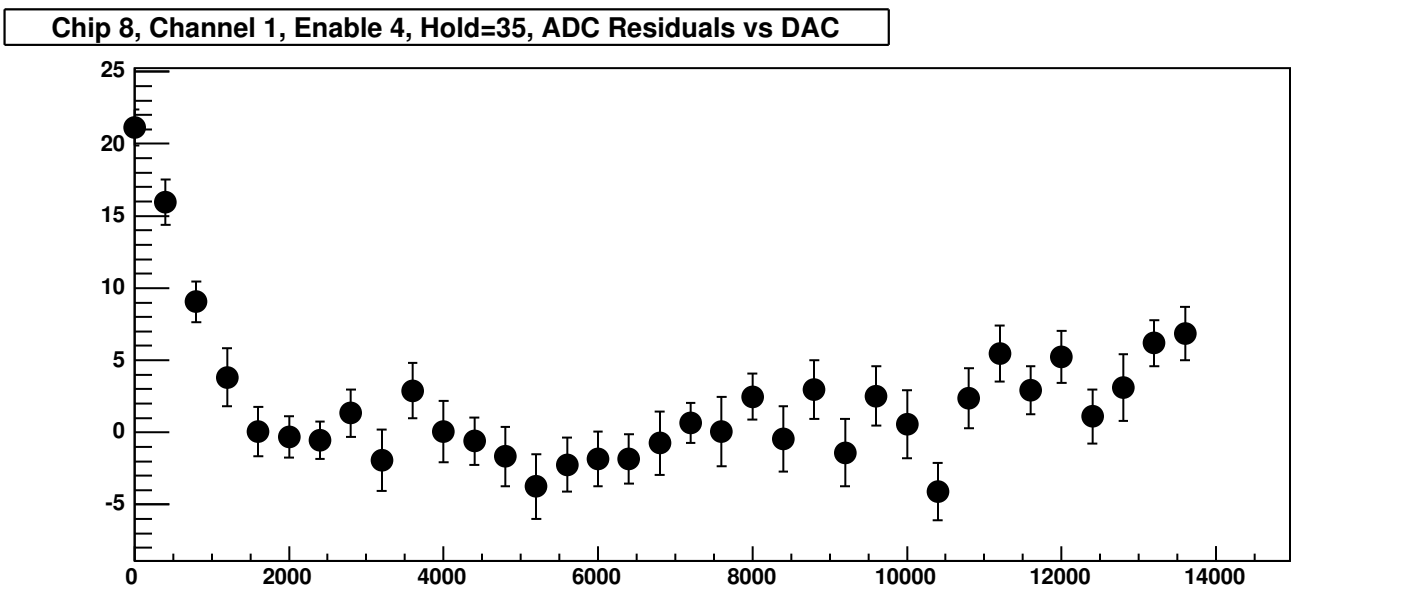

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 405.1 / 23
p0 243 ± 2.521
p1 2.46 ± 0.001474
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

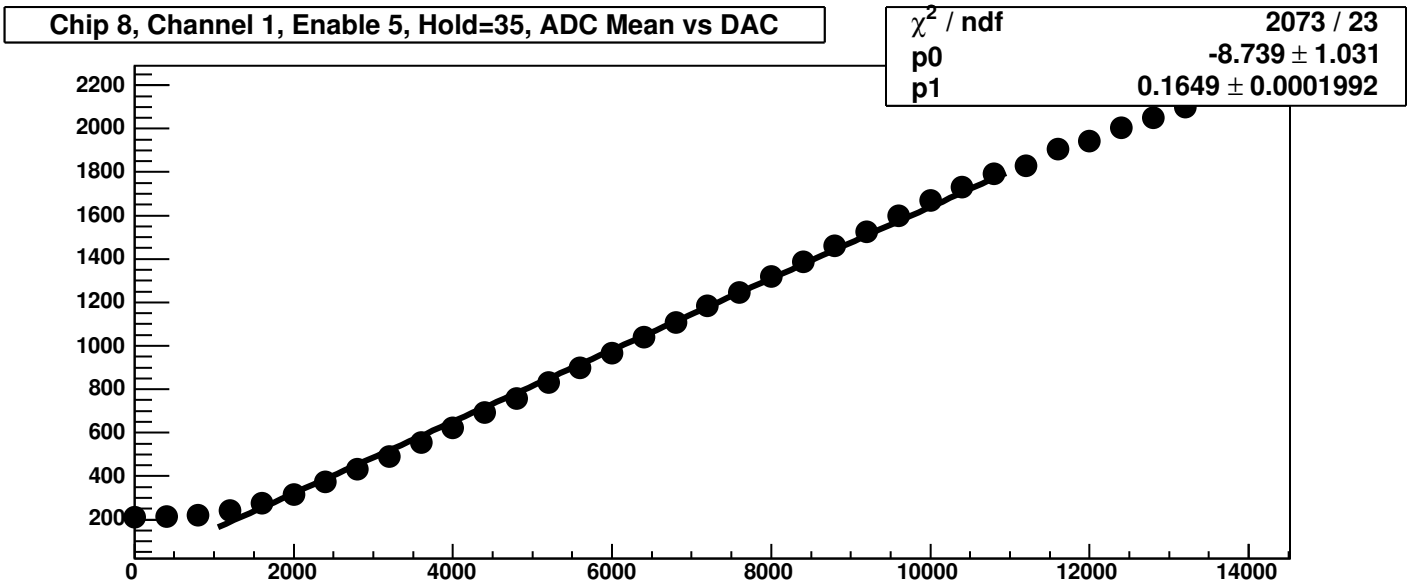

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

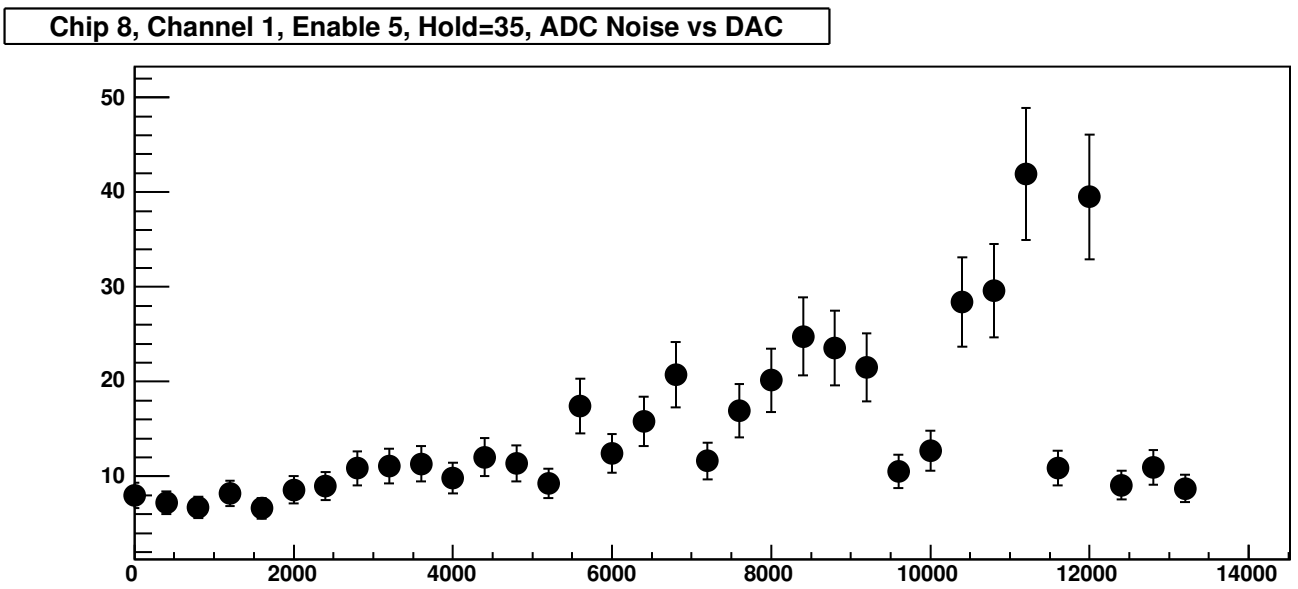

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

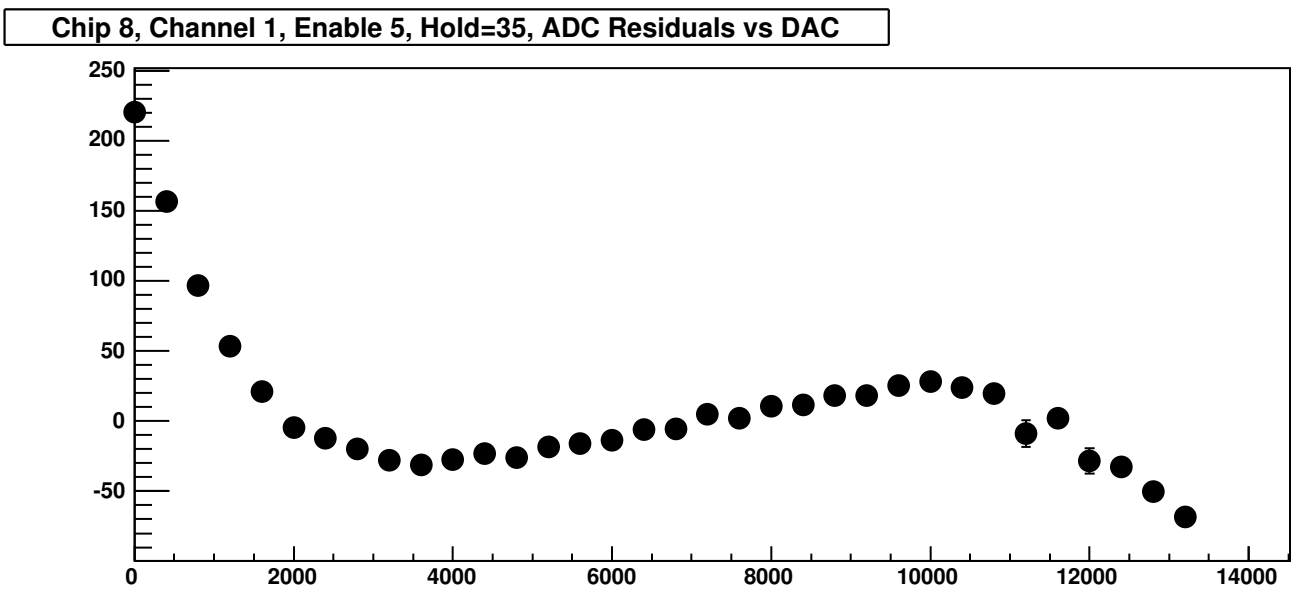

Chip 8, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.07 / 23
p0 178.1 ± 0.8083
p1 0.01793 ± 0.0001295
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

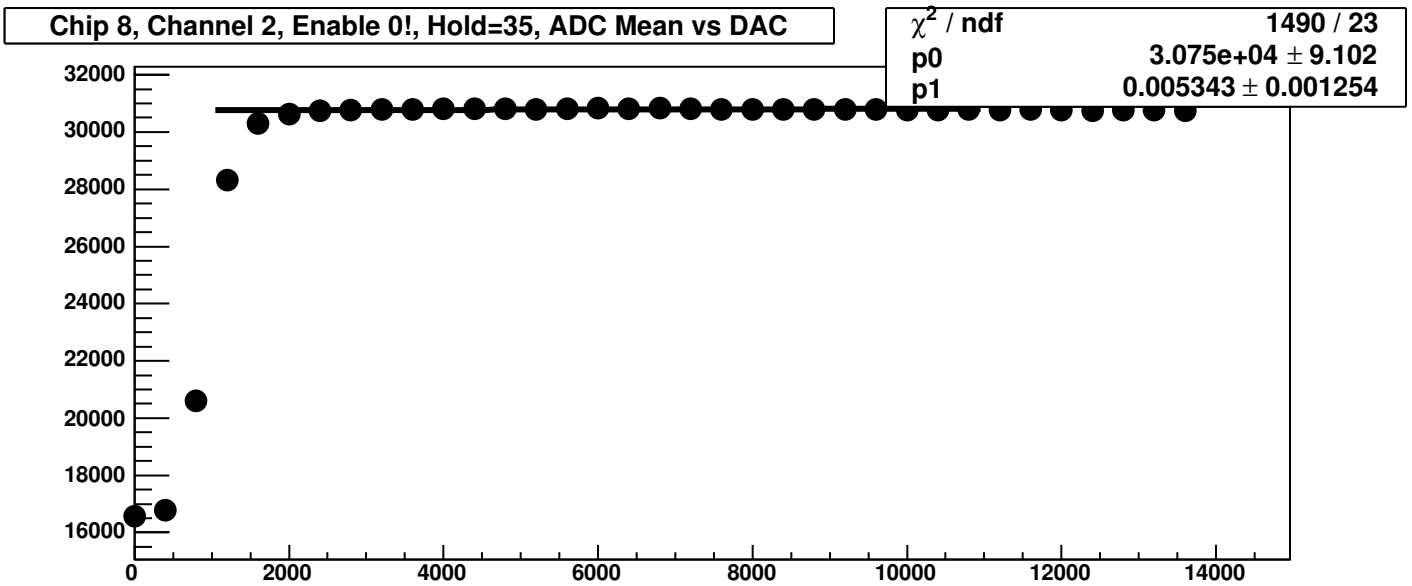

Chip 8, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

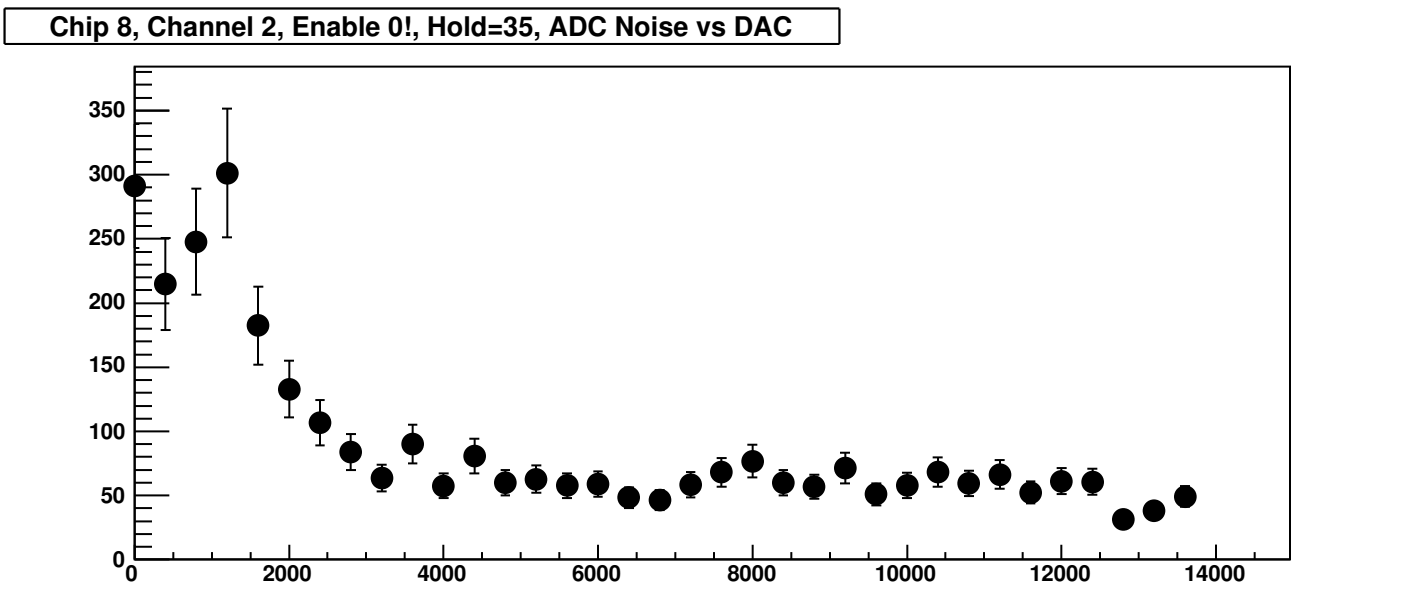

Chip 8, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

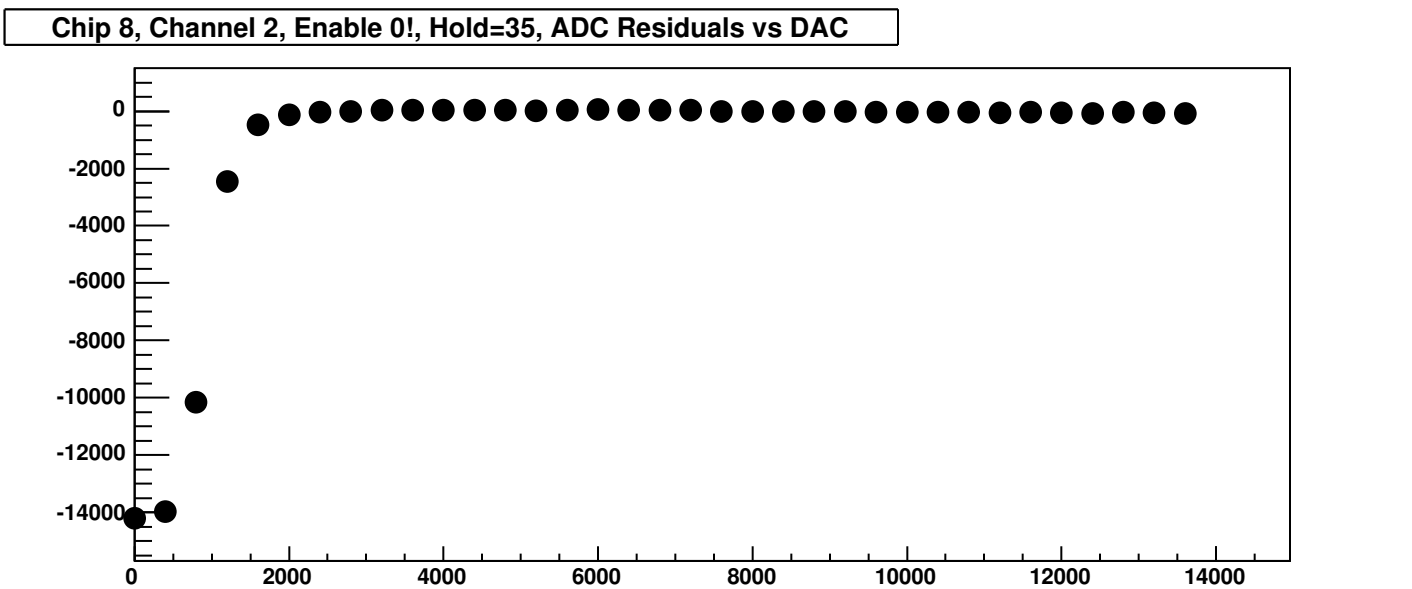

Chip 8, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2073 / 23
p0 -8.739 ± 1.031
p1 0.1649 ± 0.0001992
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

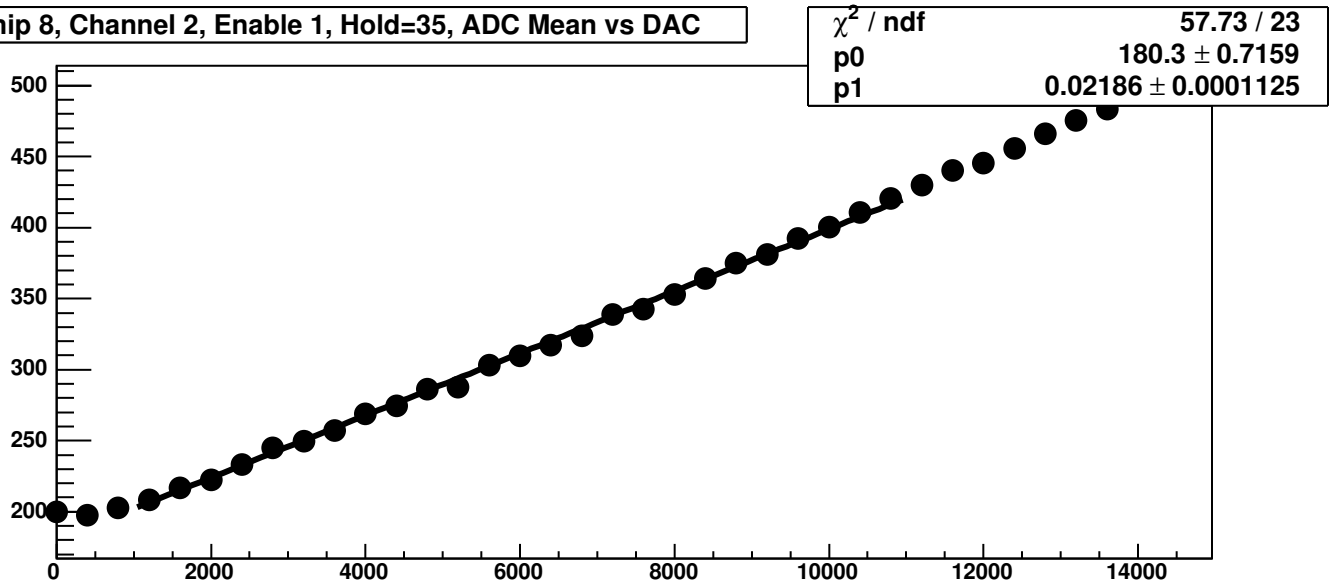

Chip 8, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1490 / 23
p0 3.075e+04 ± 9.102
p1 0.005343 ± 0.001254
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

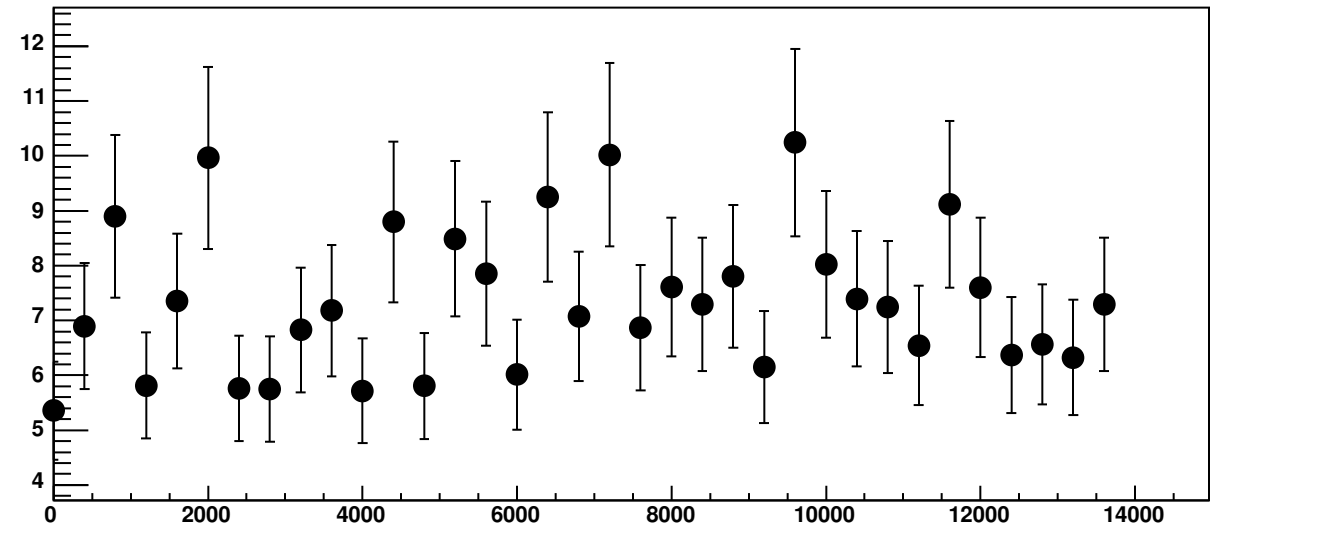

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

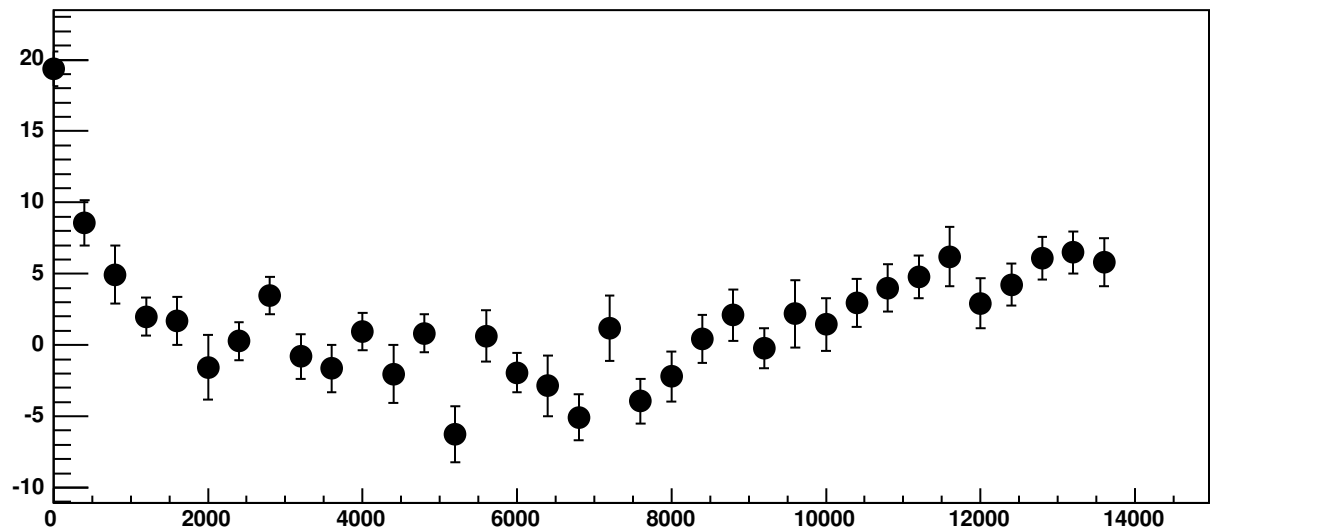
Chip 8, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



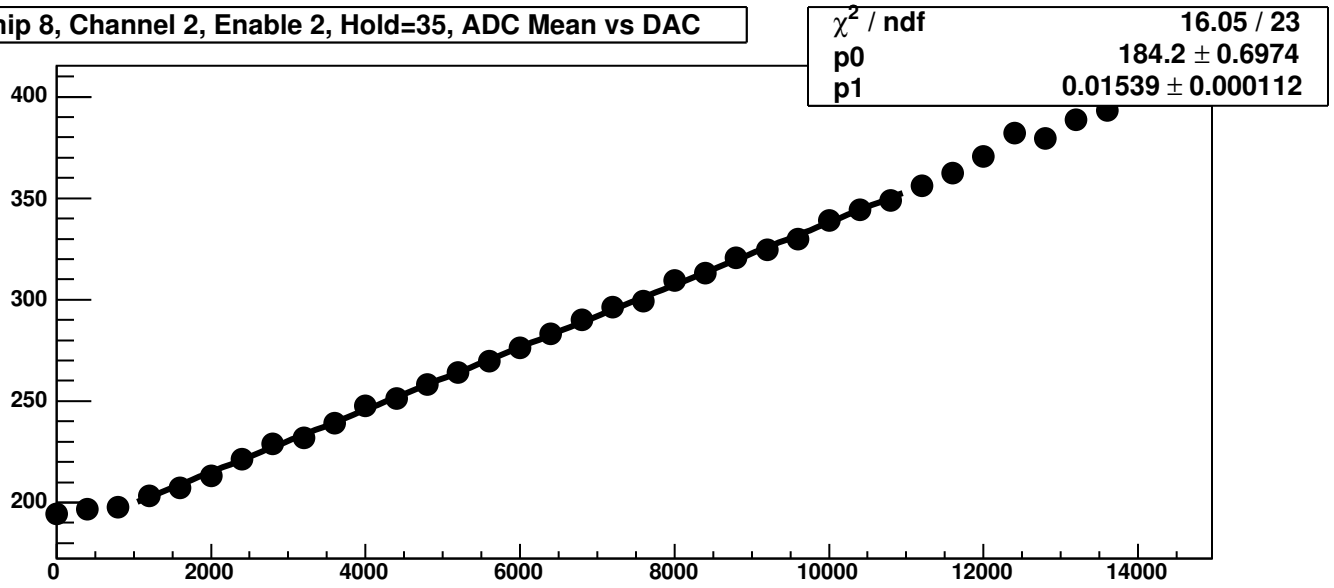
Chip 8, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



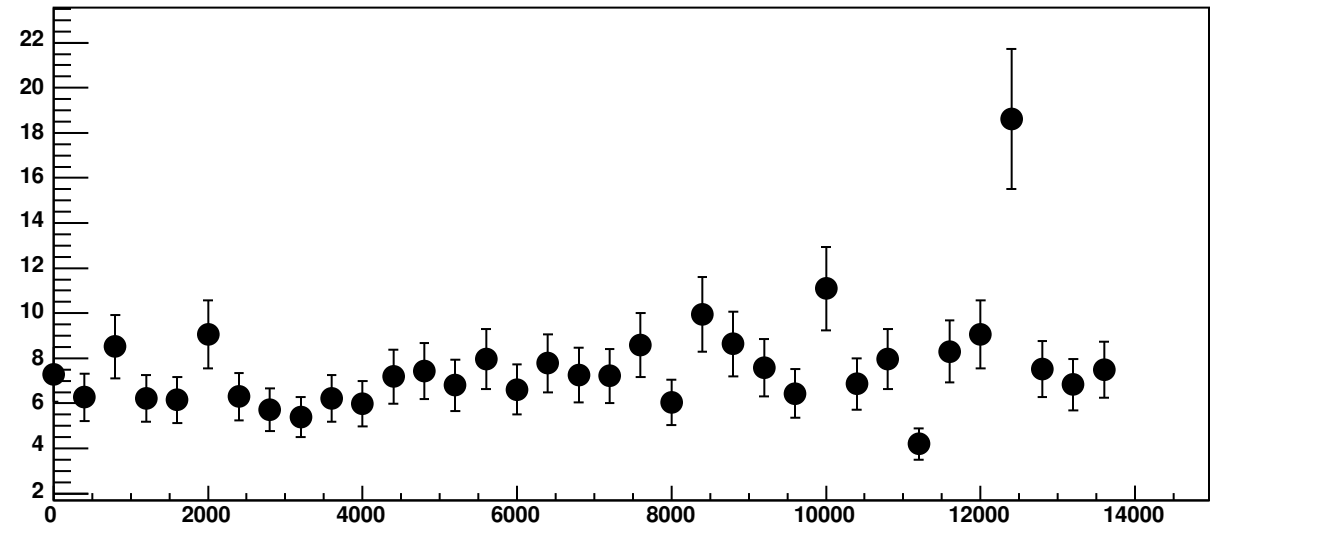
Chip 8, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



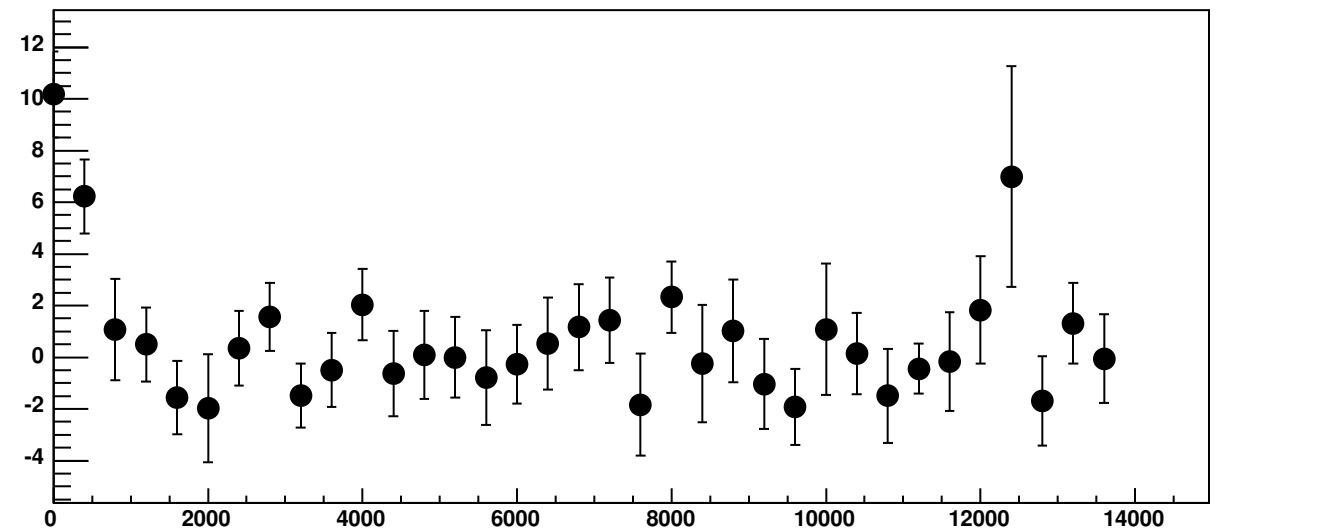
Chip 8, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

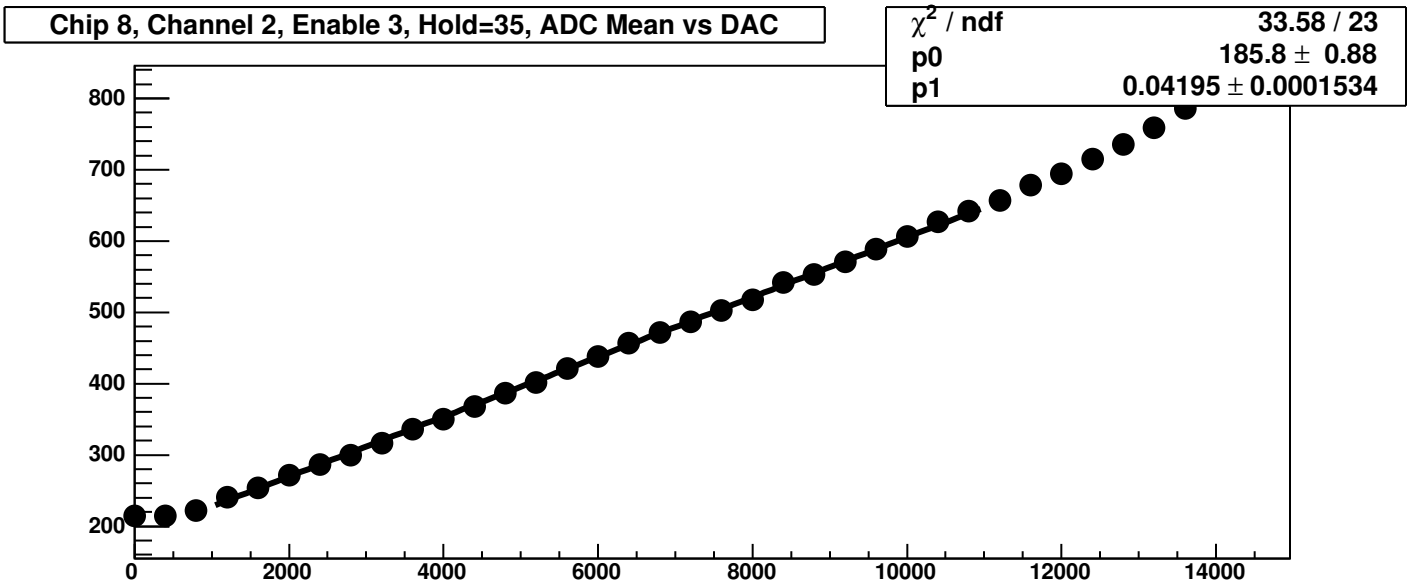


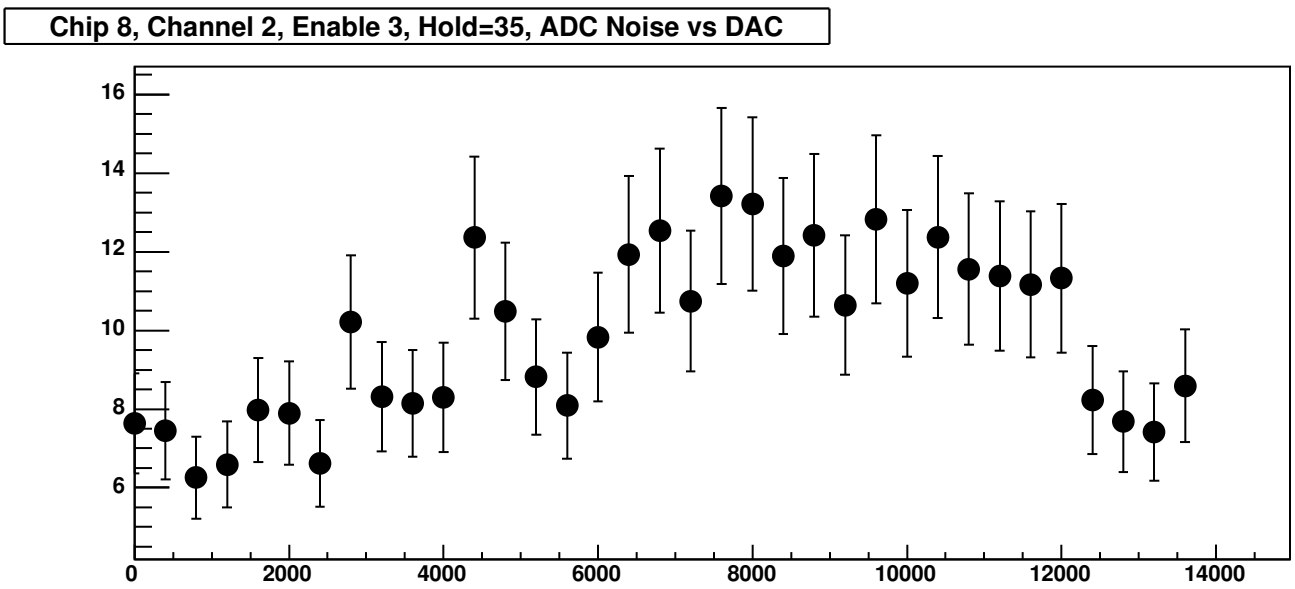
Chip 8, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

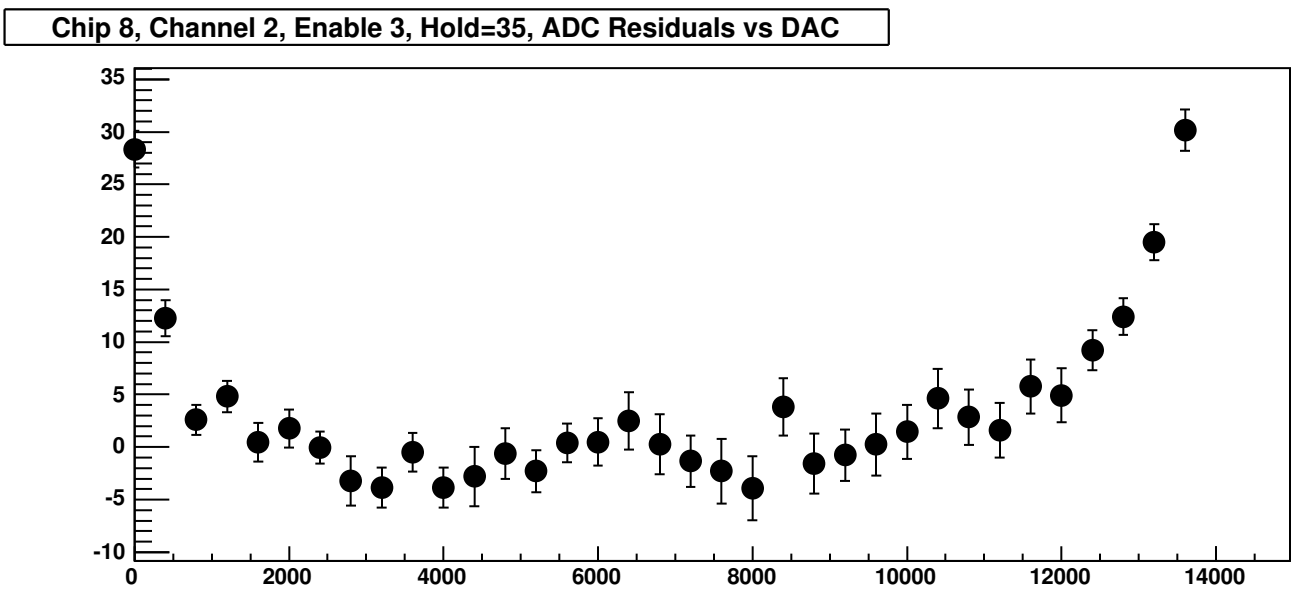


Chip 8, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

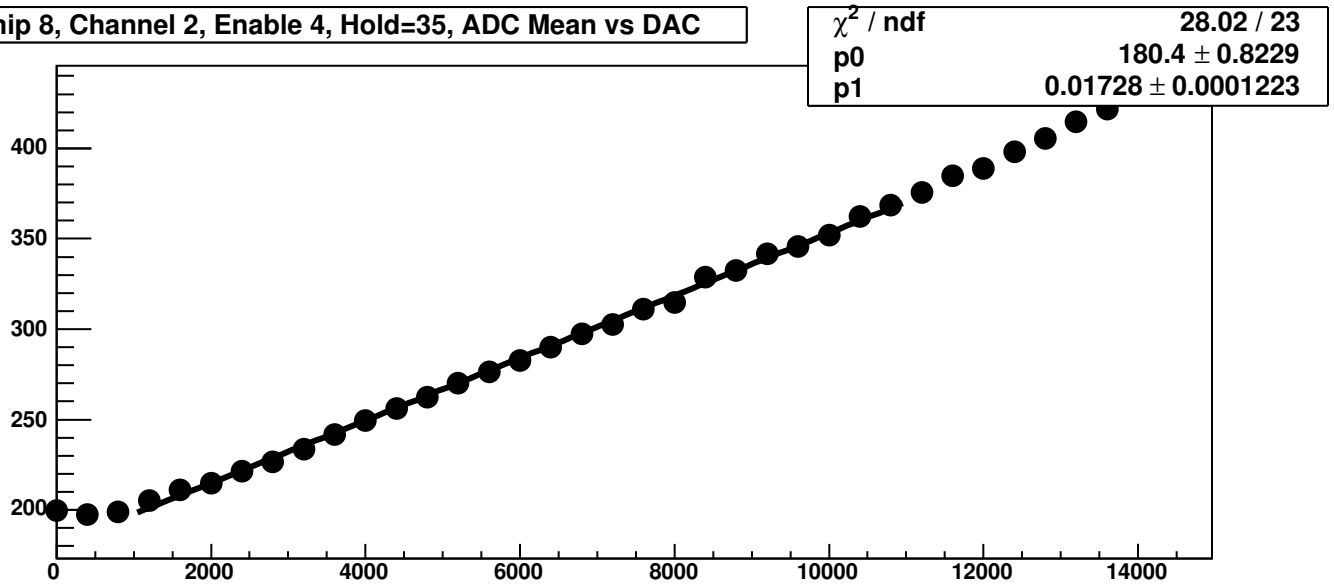



Chip 8, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.58 / 23
p0 185.8 ± 0.88
p1 0.04195 ± 0.0001534
800
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

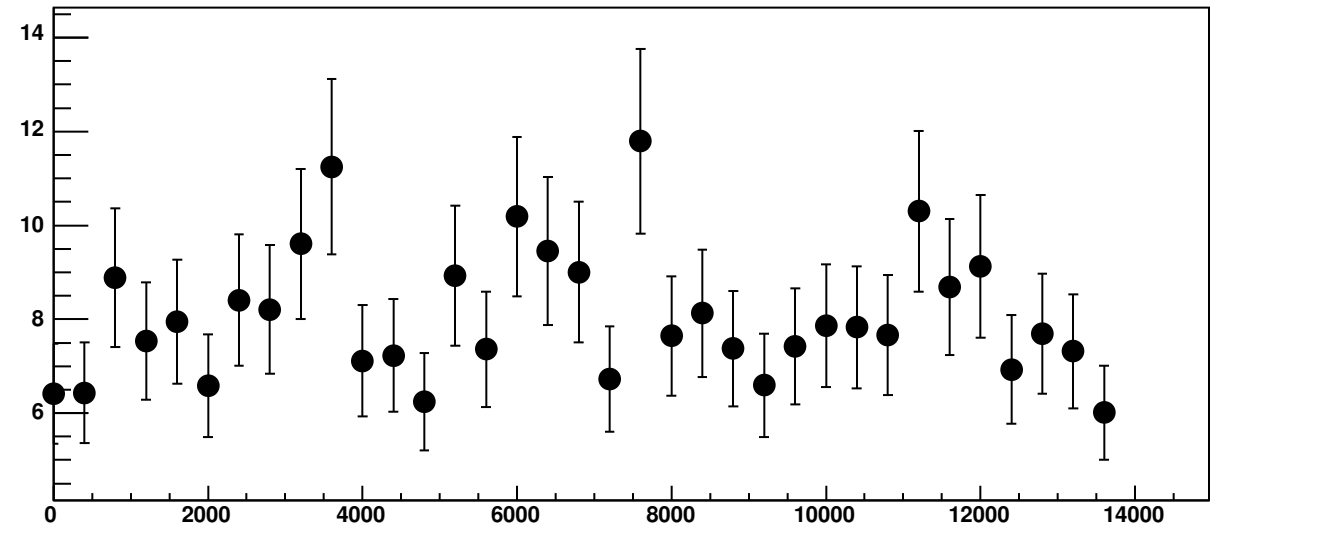

Chip 8, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

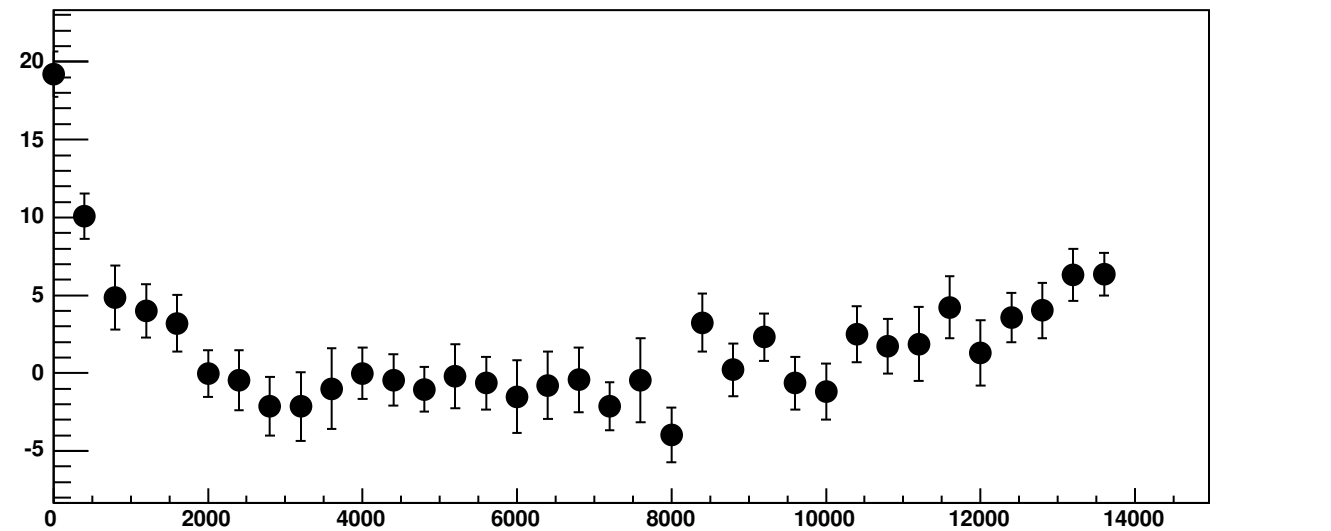
Chip 8, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

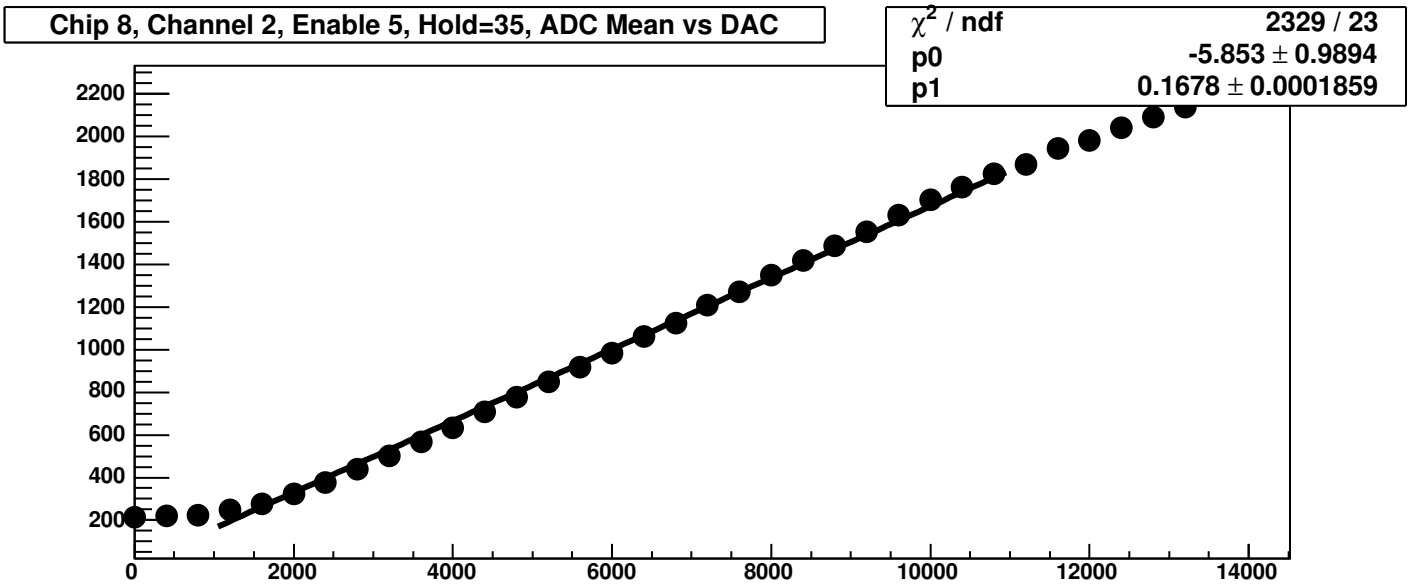


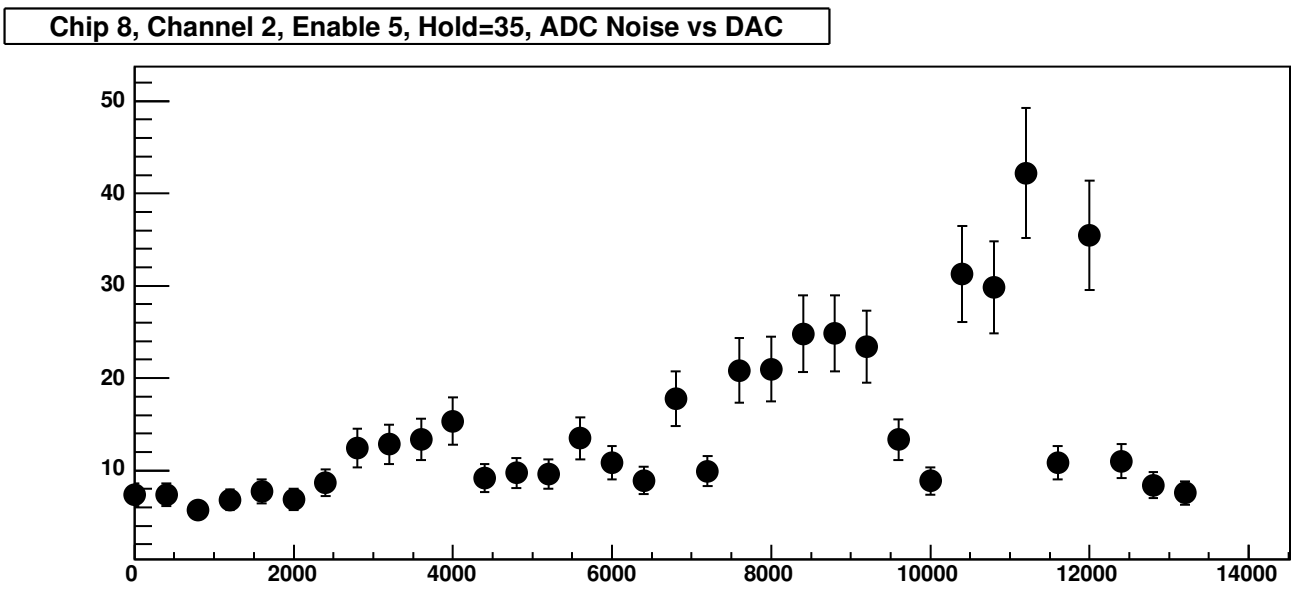
Chip 8, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

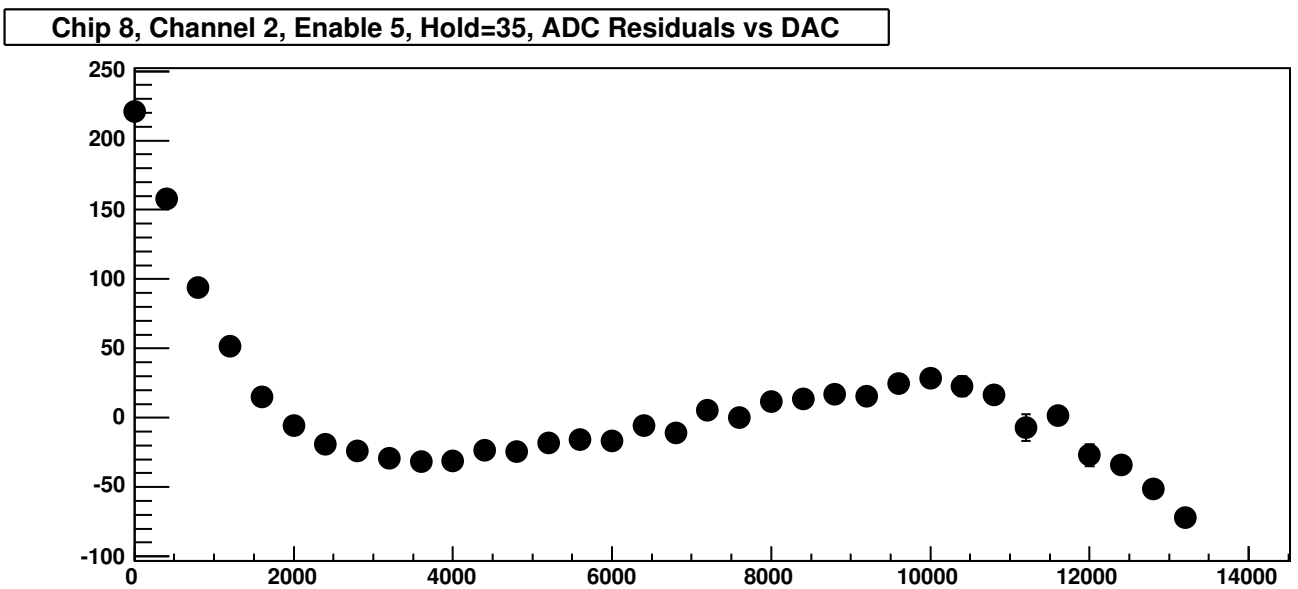


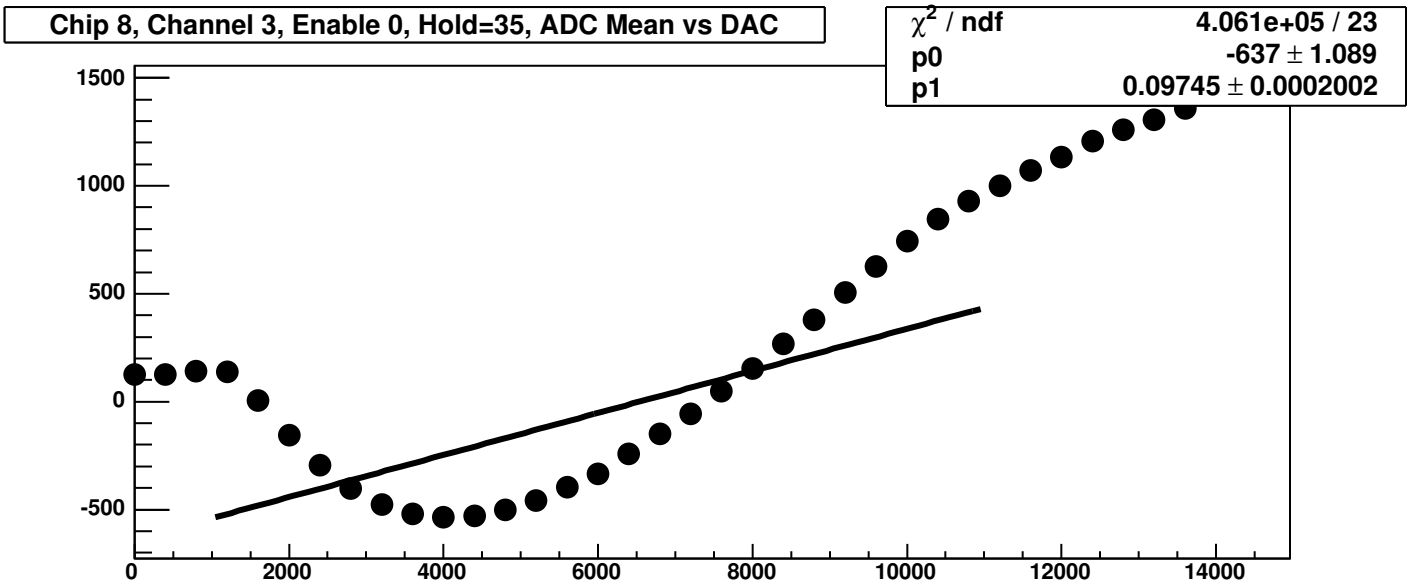
Chip 8, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

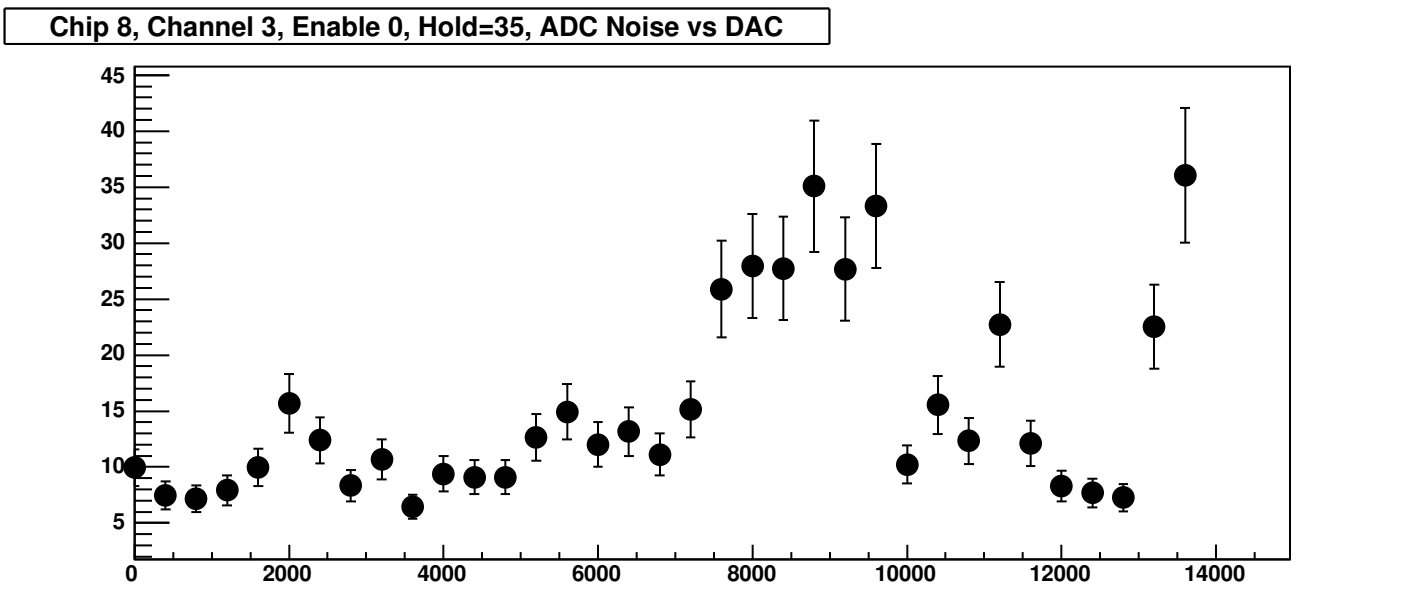


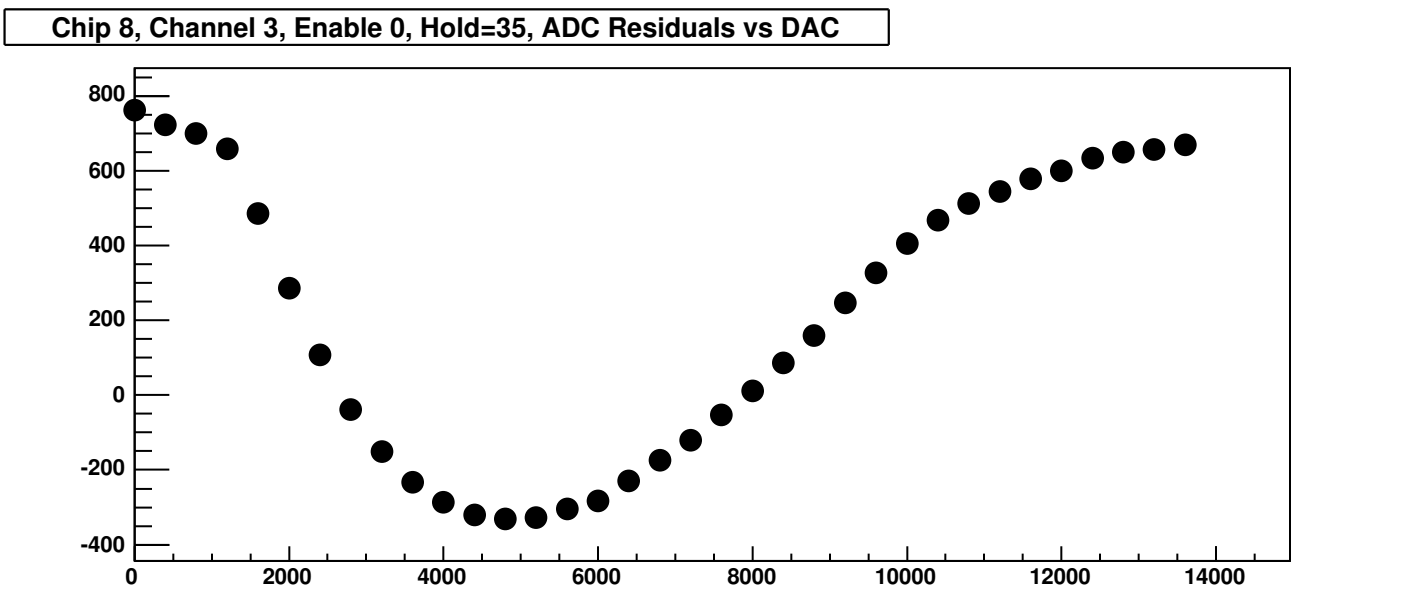

Chip 8, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2329 / 23
p0 -5.853 ± 0.9894
p1 0.1678 ± 0.0001859
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

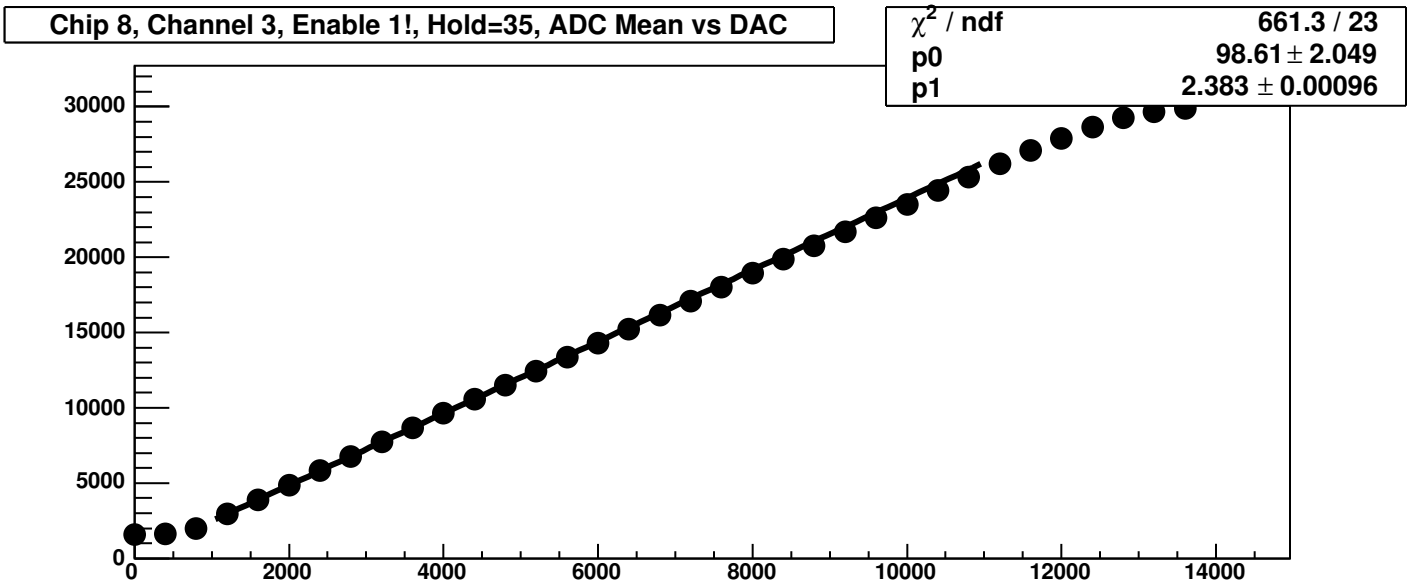

Chip 8, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

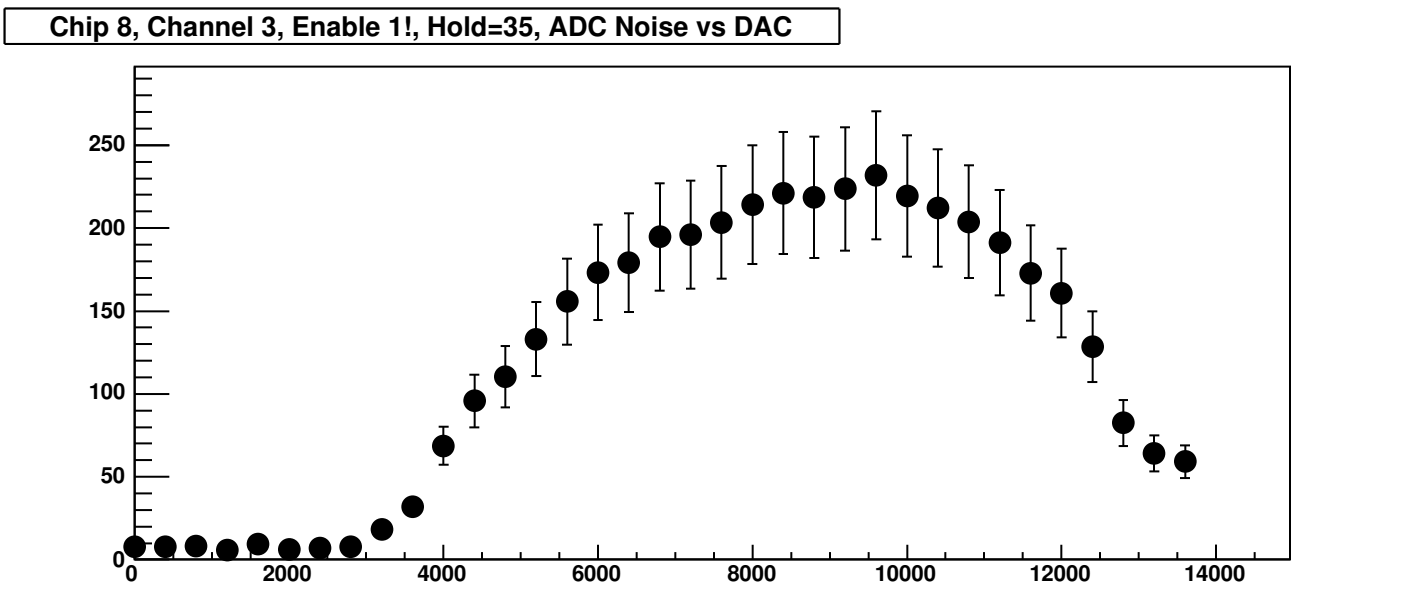

Chip 8, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

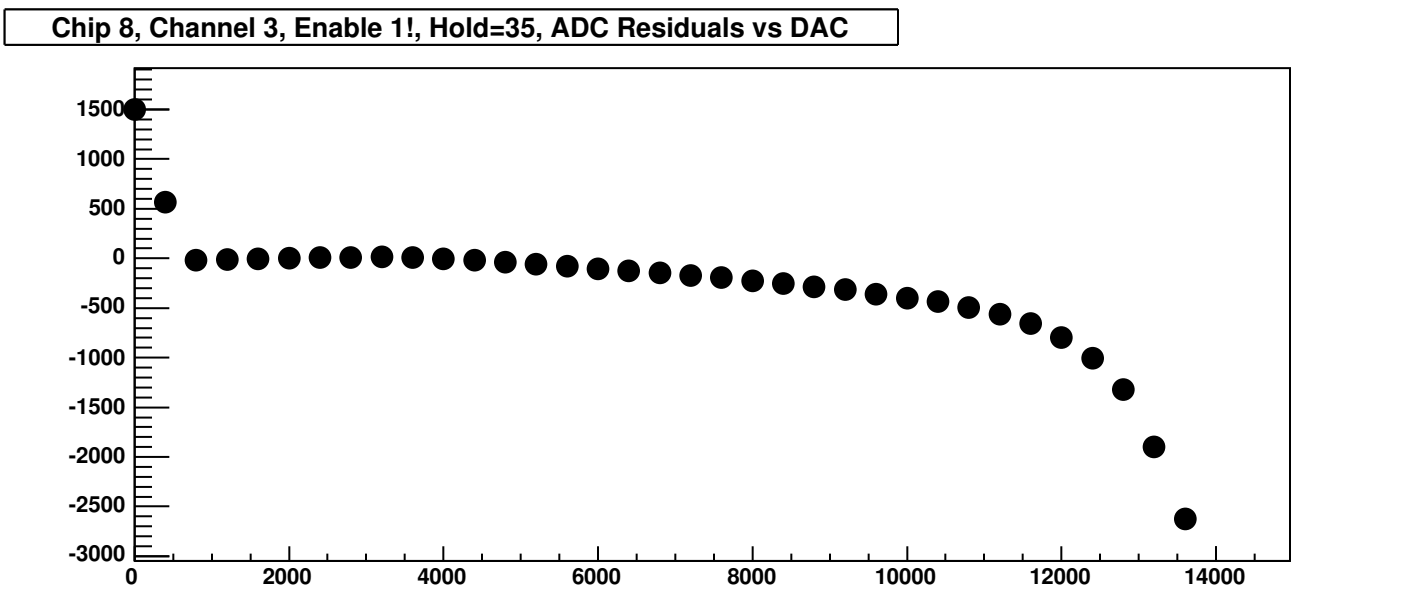

Chip 8, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.061e+05 / 23
p0 -637 ± 1.089
p1 0.09745 ± 0.0002002
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

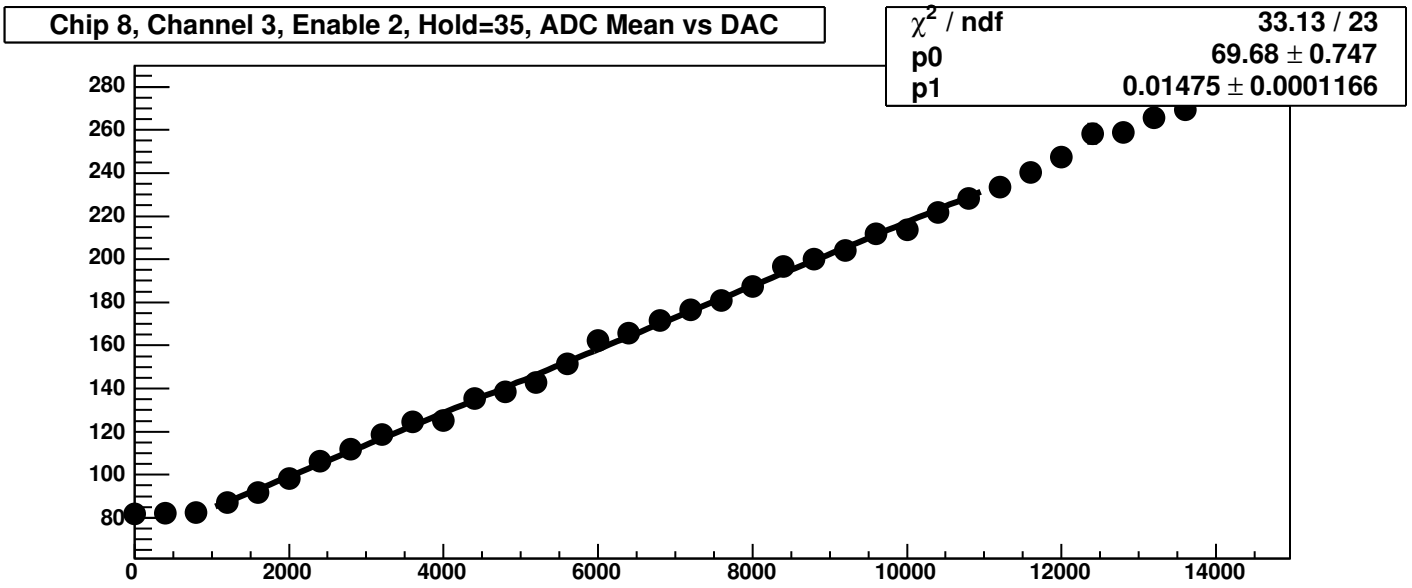

Chip 8, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

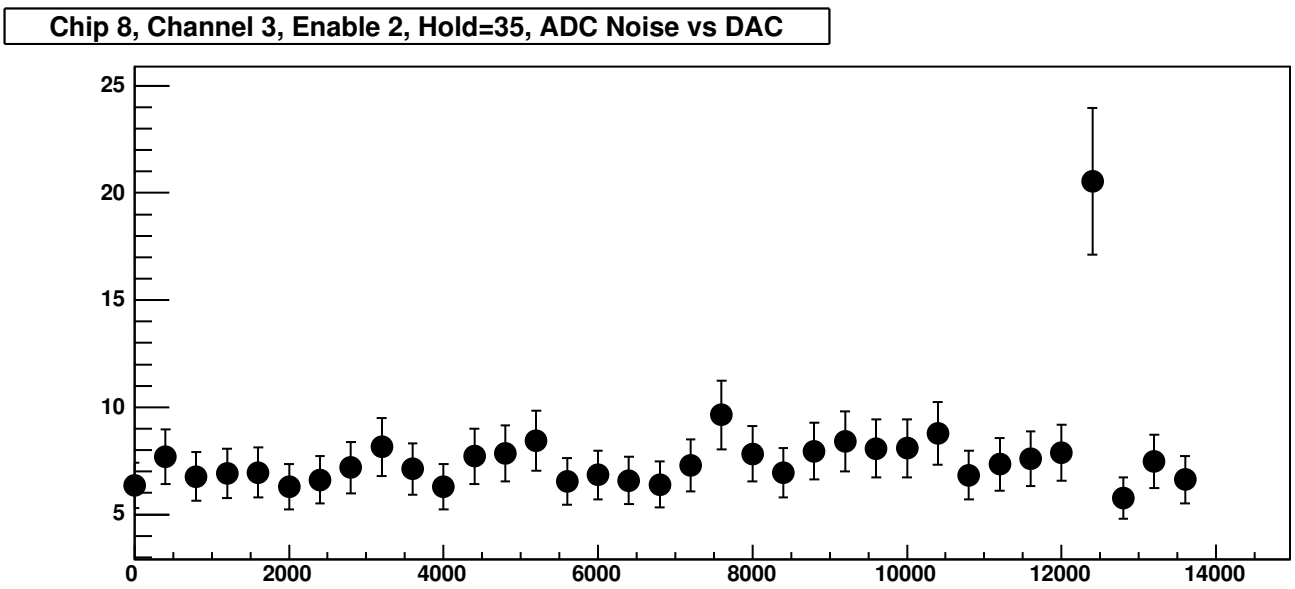

Chip 8, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

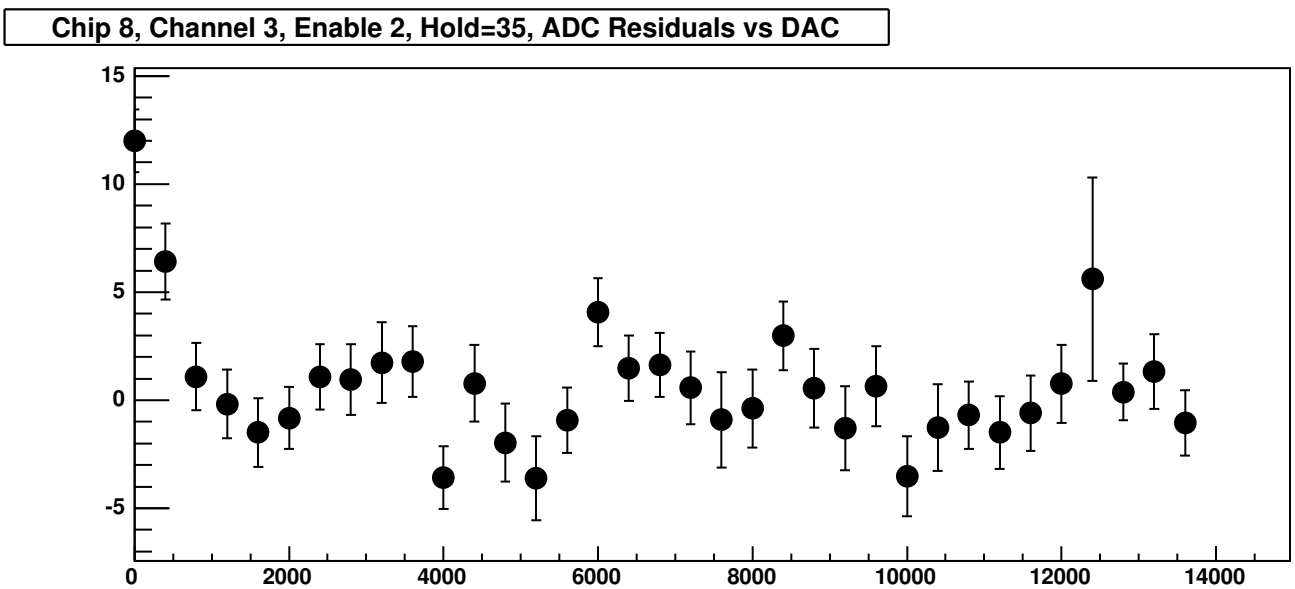

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 661.3 / 23
p0 98.61 ± 2.049
p1 2.383 ± 0.00096
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

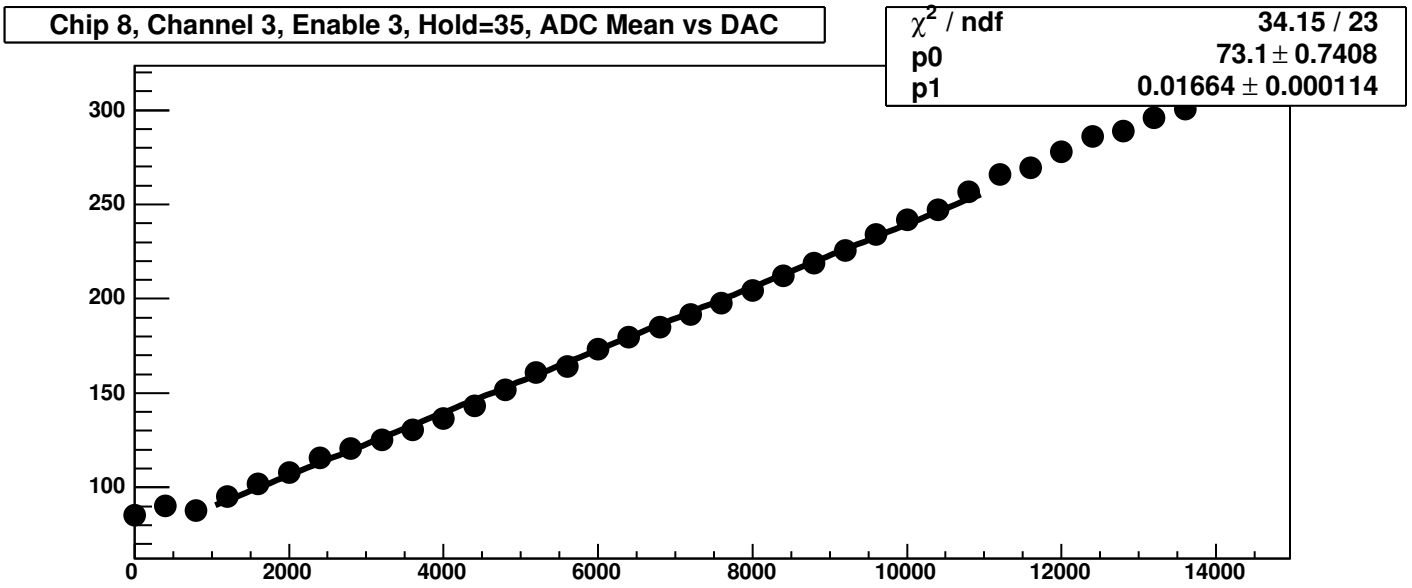

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

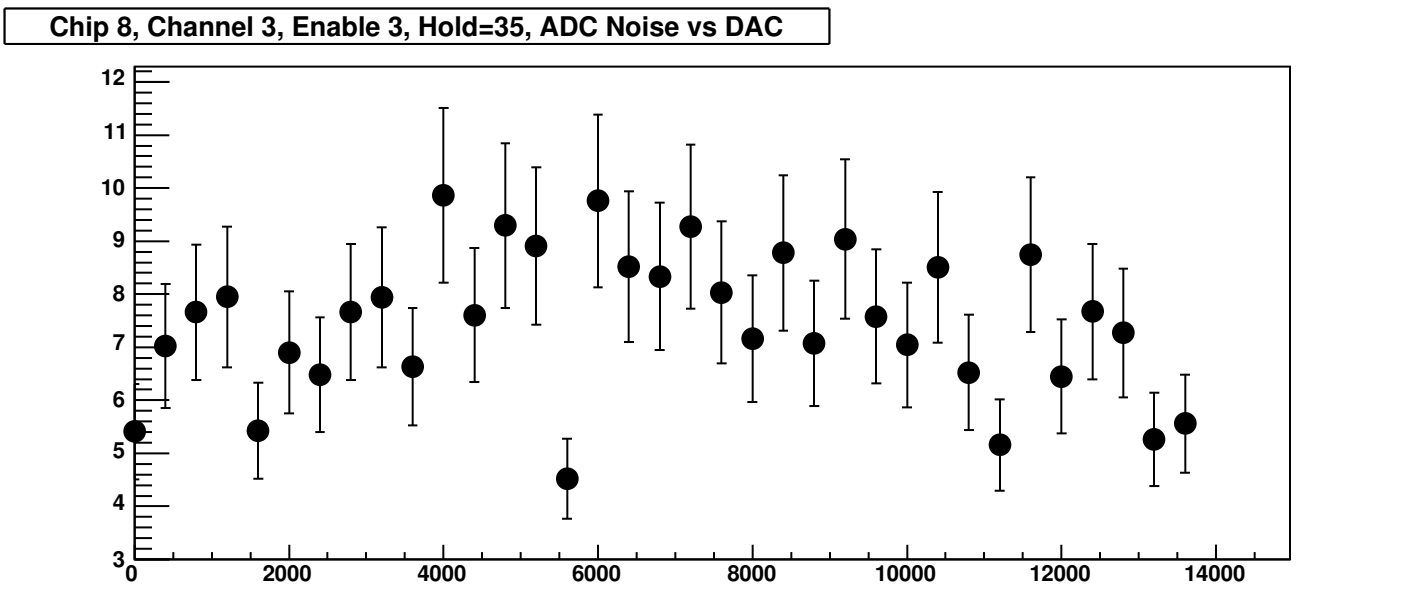

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

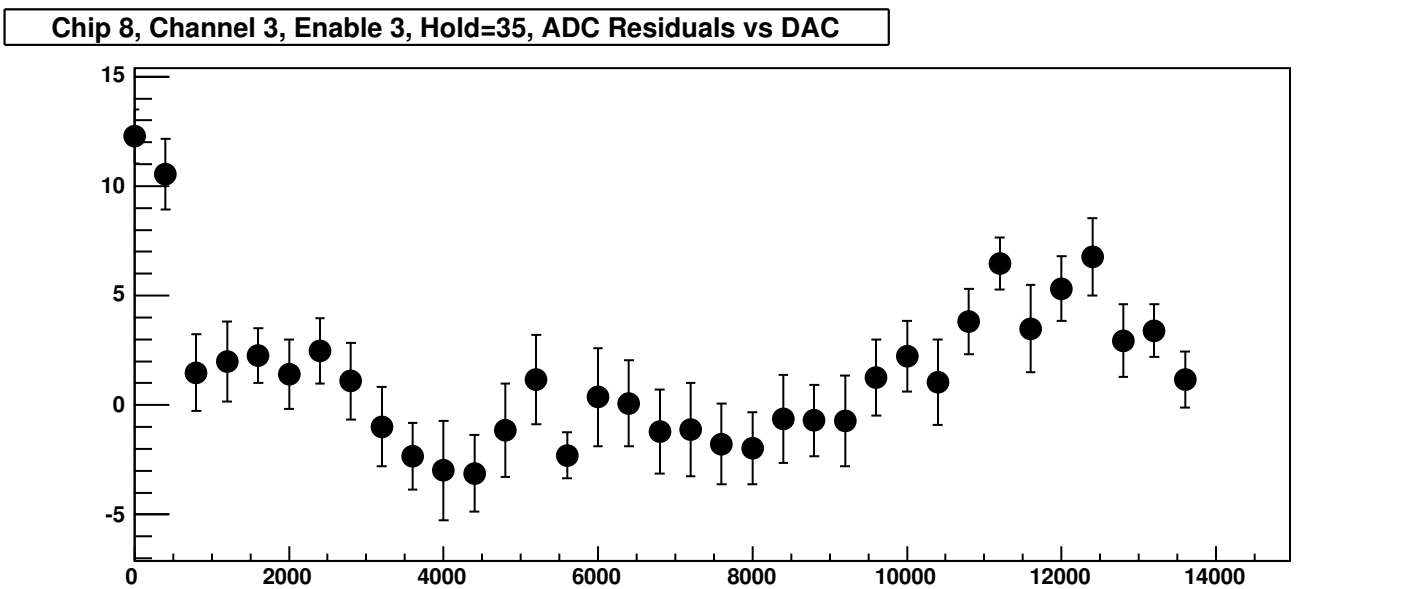

Chip 8, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.13 / 23
p0 69.68 ± 0.747
p1 0.01475 ± 0.0001166
280
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000

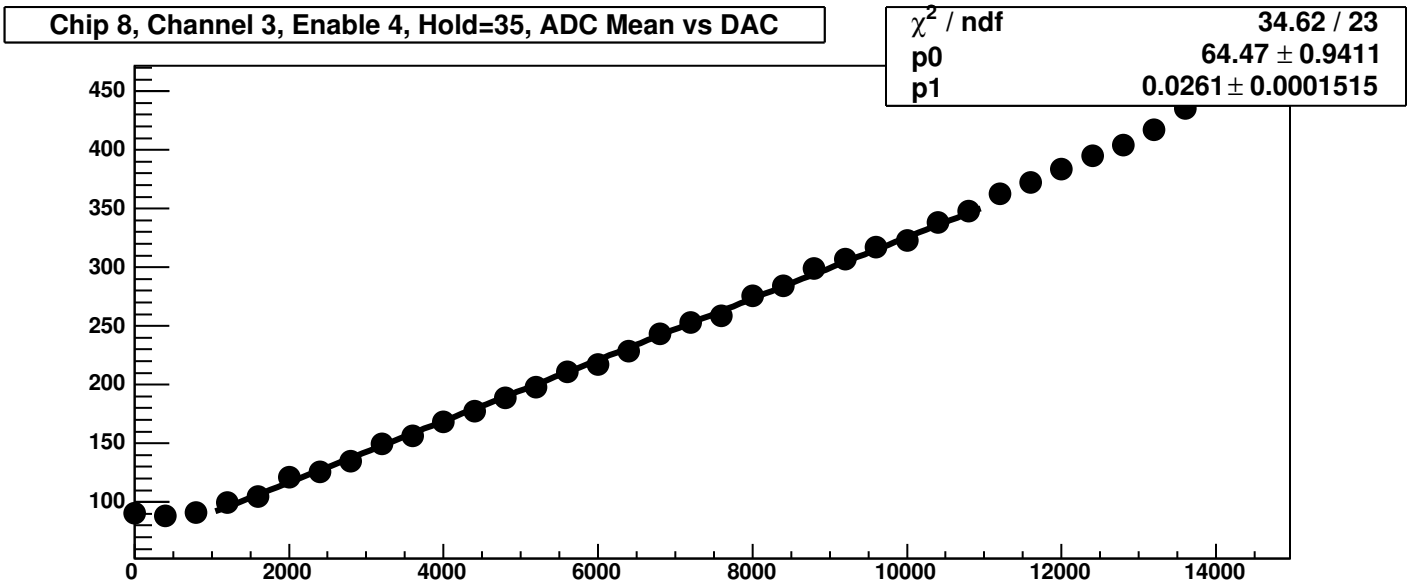

Chip 8, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

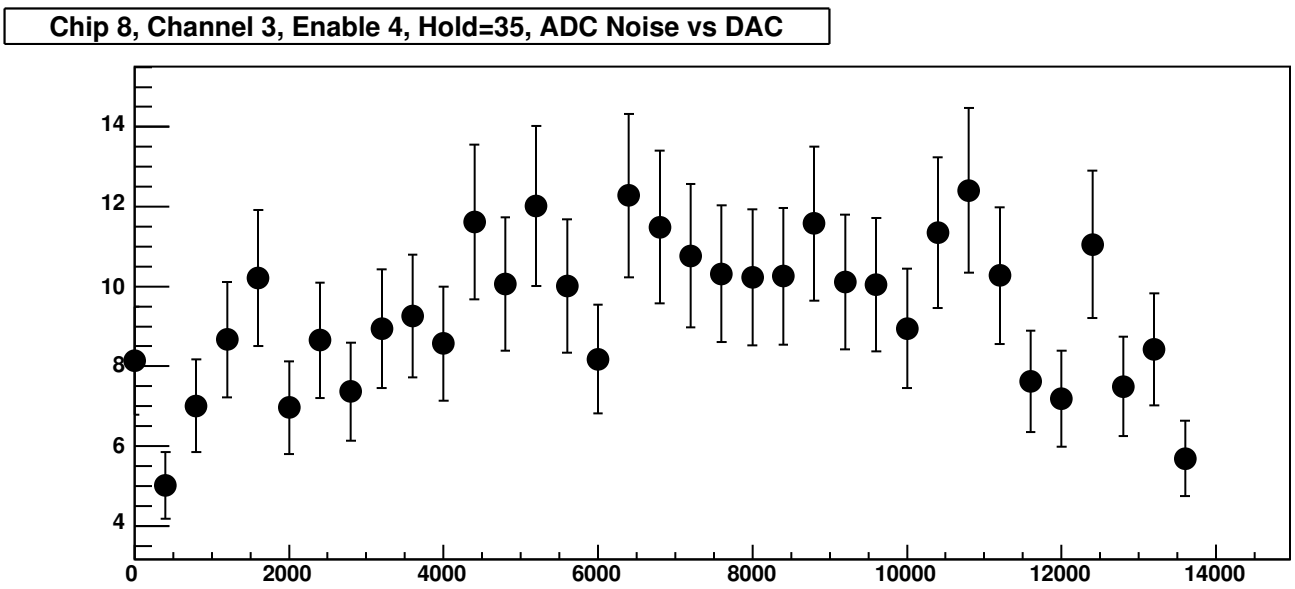

Chip 8, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

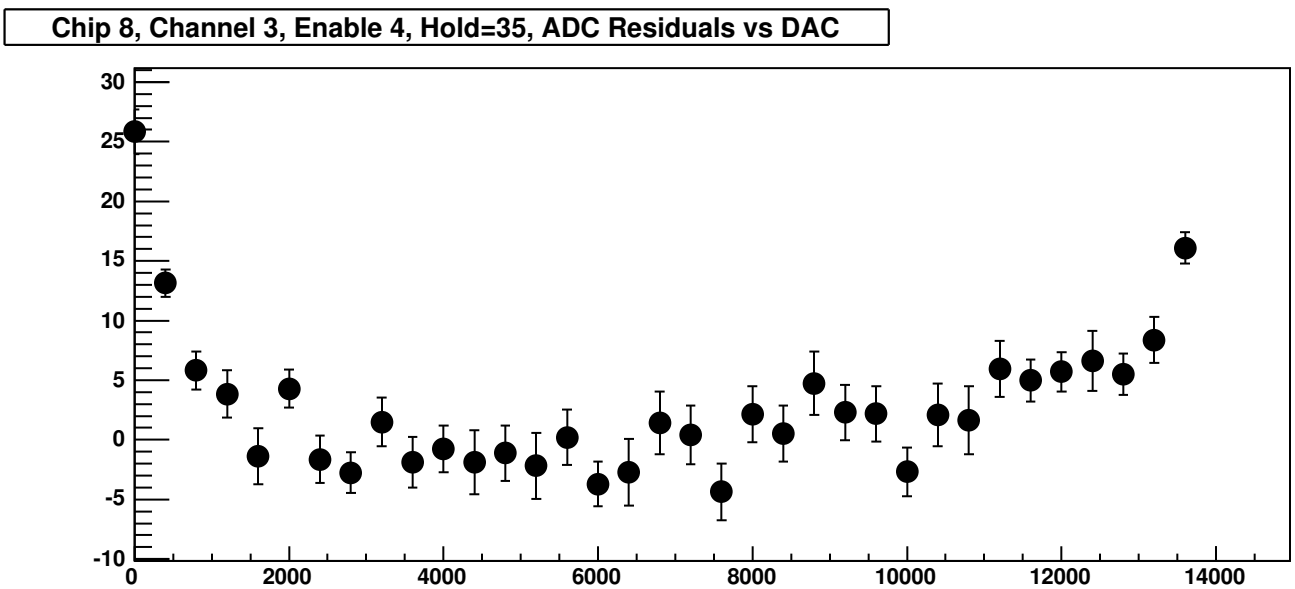

Chip 8, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.15 / 23
p0 73.1 ± 0.7408
p1 0.01664 ± 0.000114
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

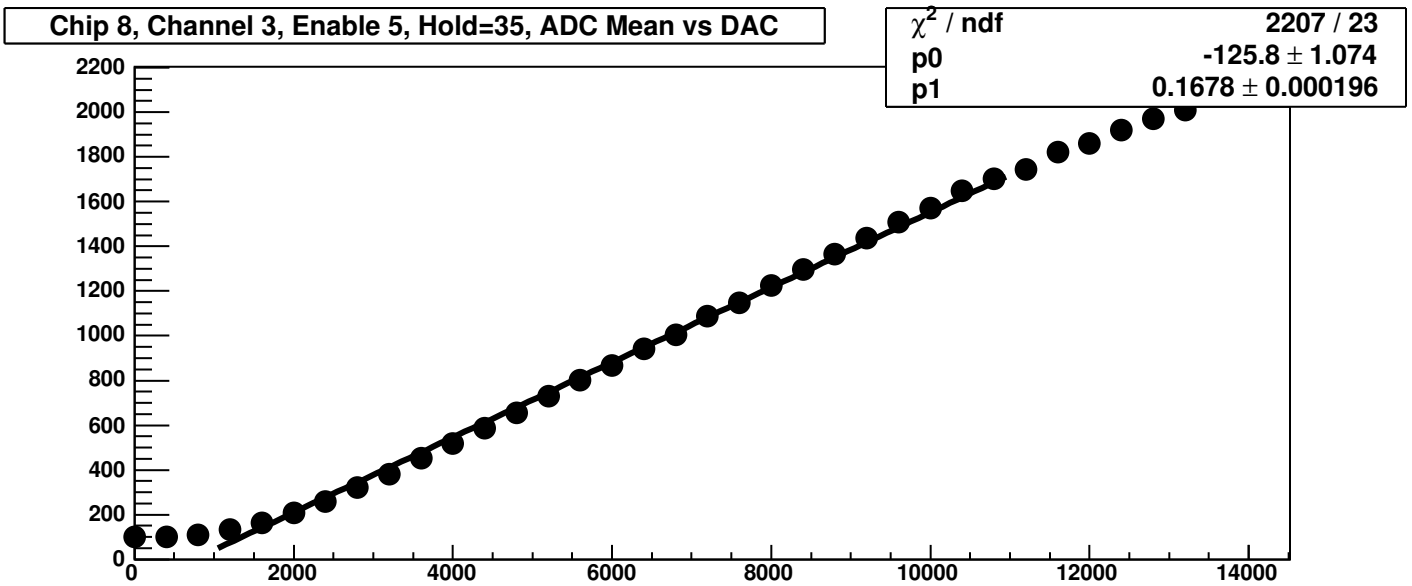

Chip 8, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

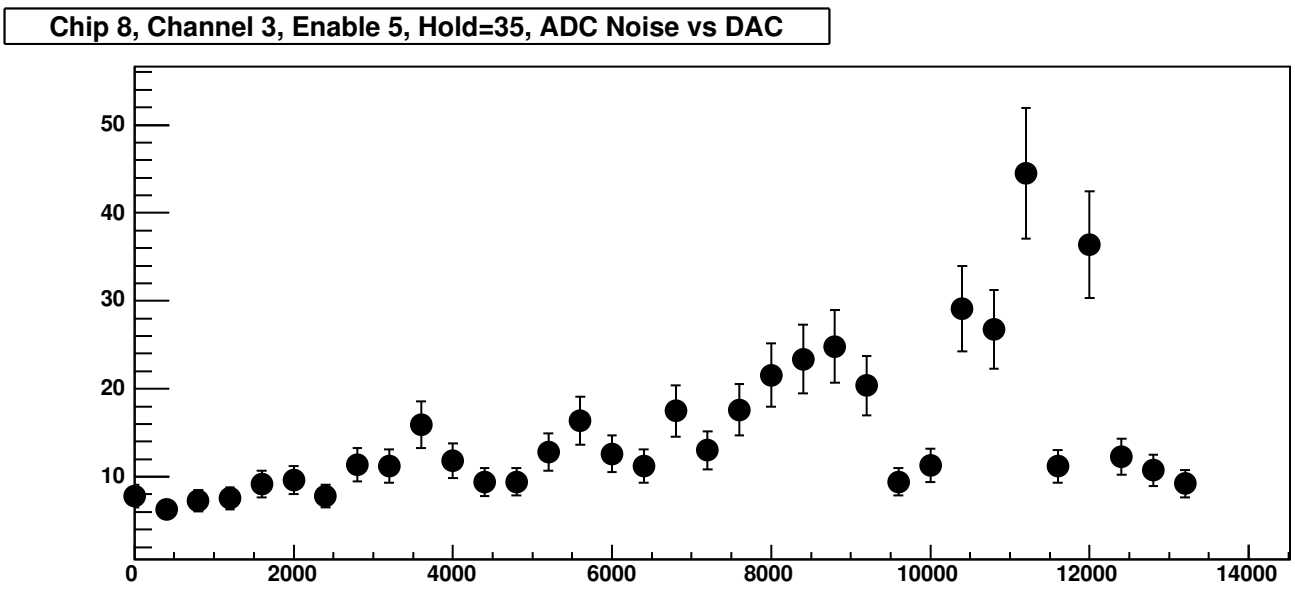

Chip 8, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

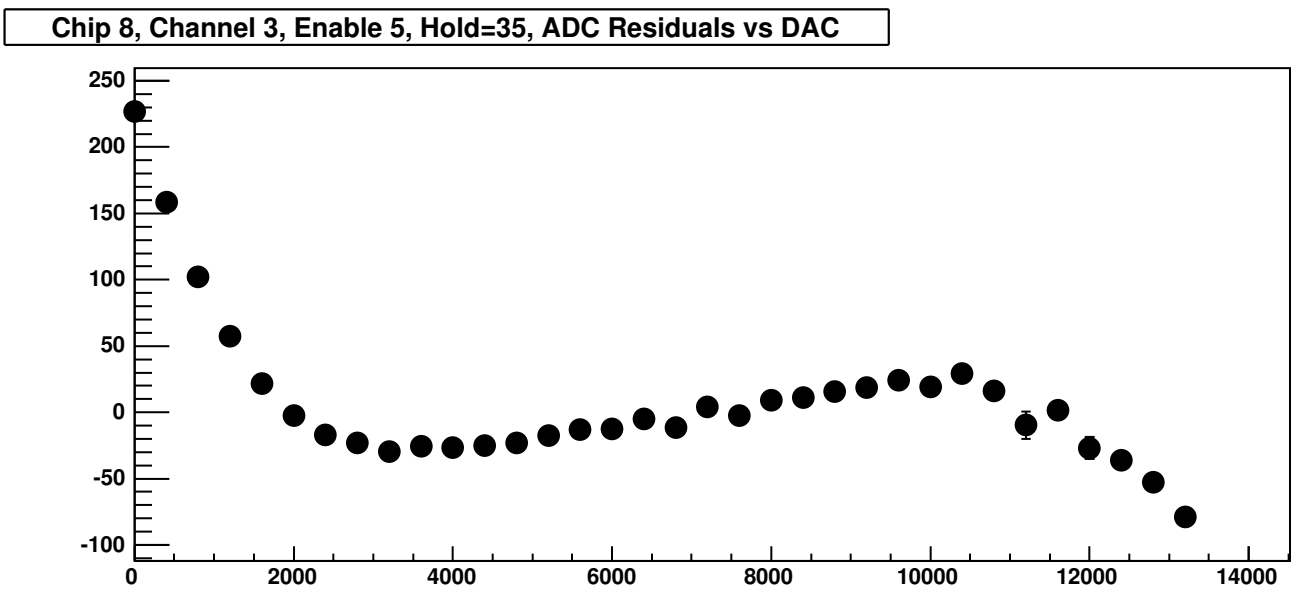

Chip 8, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.62 / 23
p0 64.47 ± 0.9411
p1 0.0261 ± 0.0001515
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

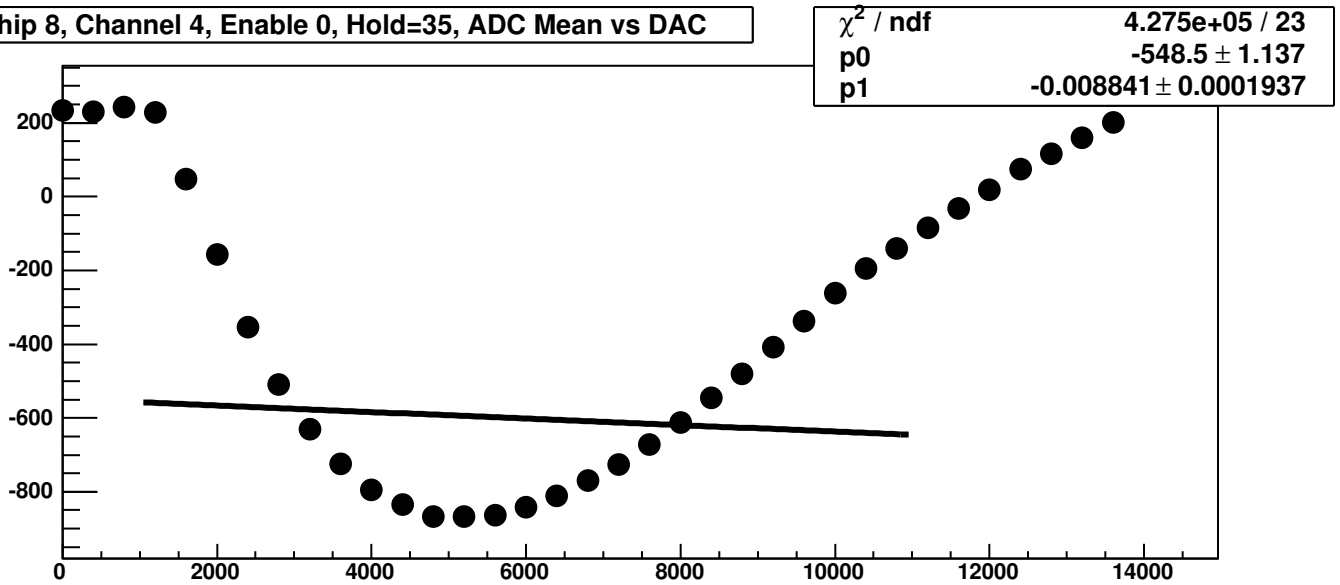

Chip 8, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2207 / 23
p0 -125.8 ± 1.074
p1 0.1678 ± 0.000196
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

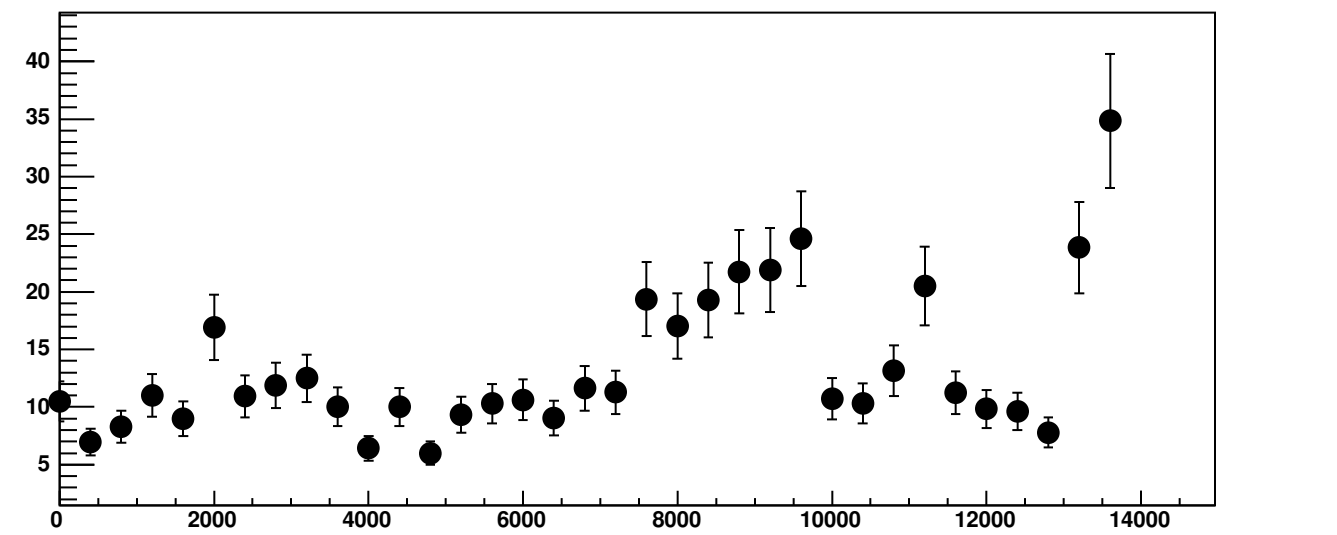

Chip 8, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

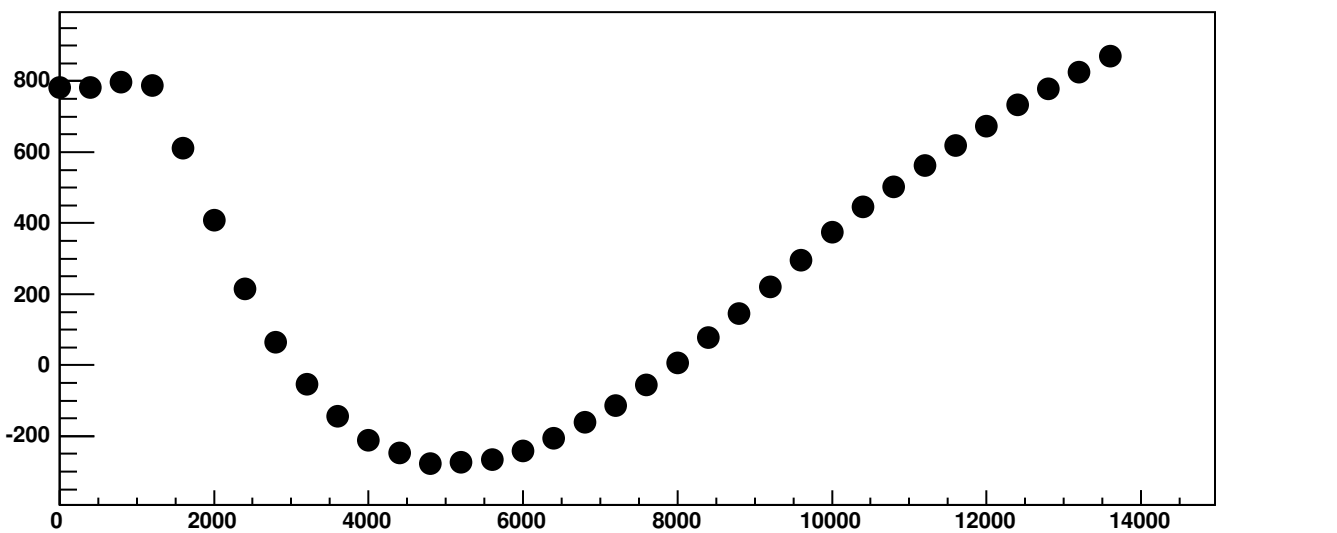
Chip 8, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

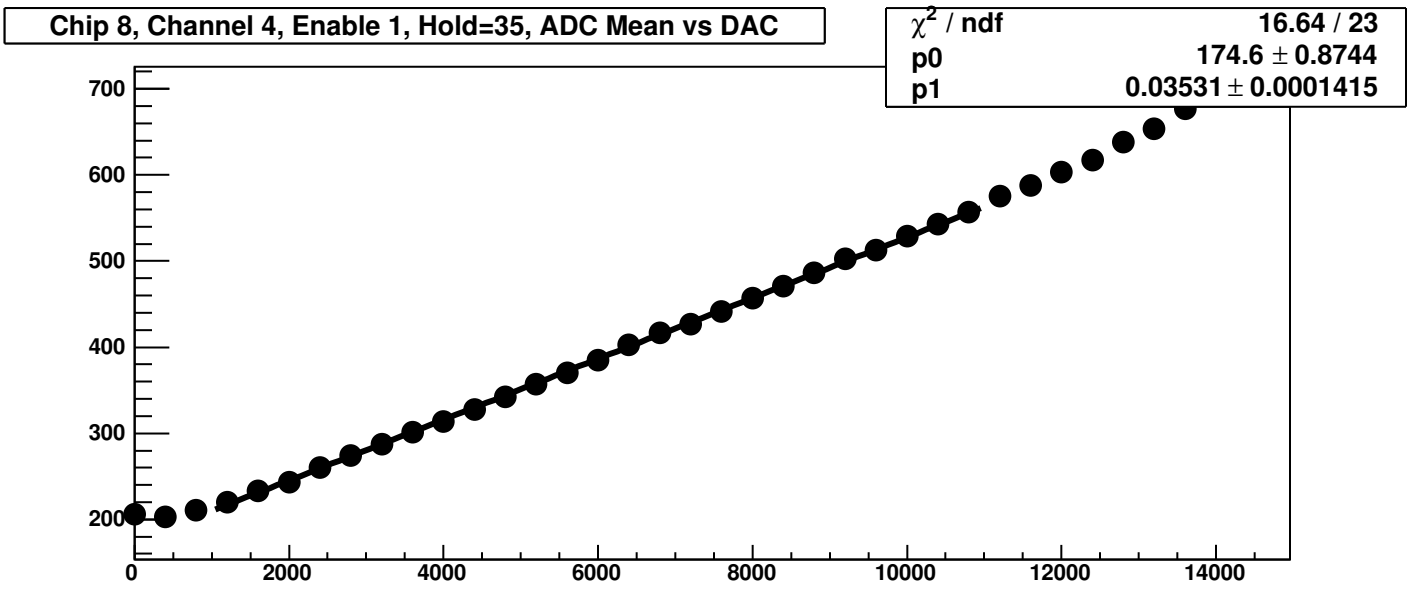


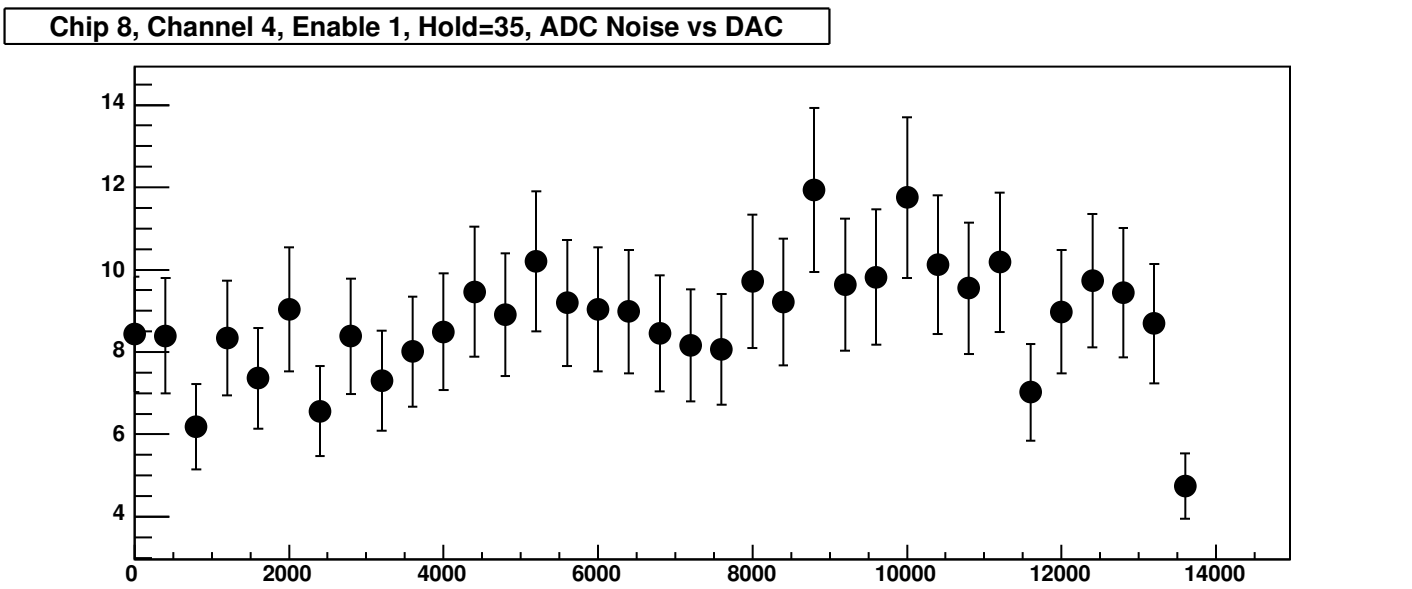
Chip 8, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

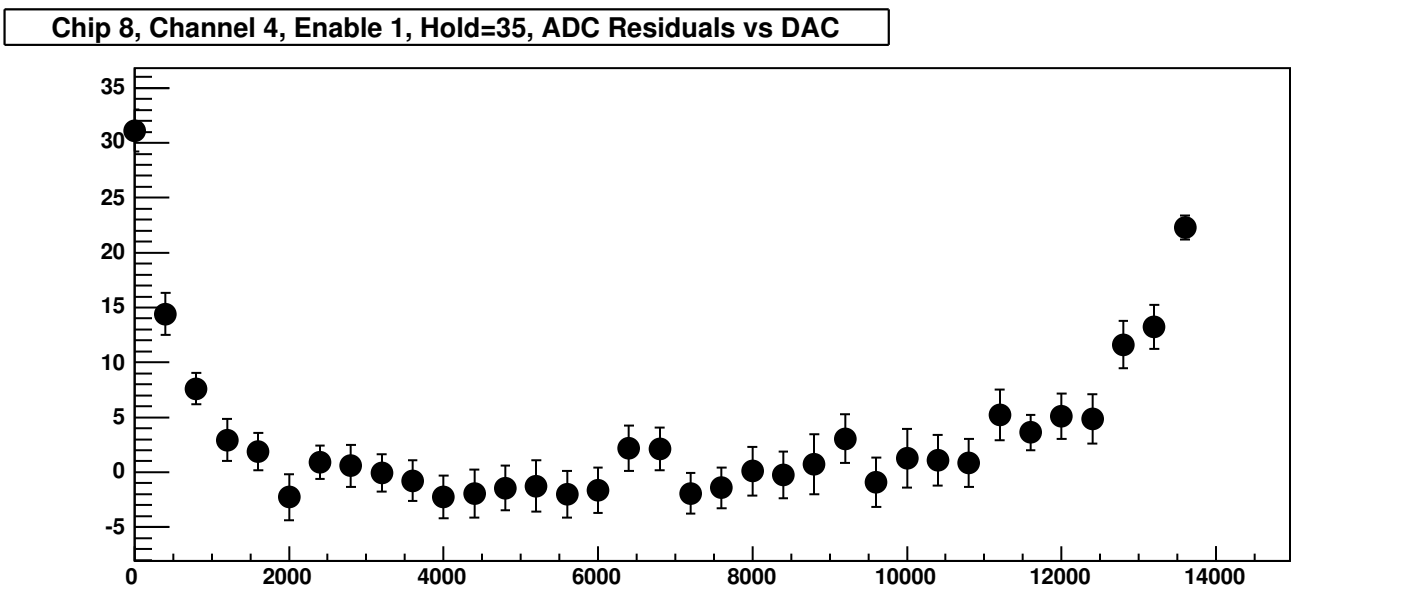


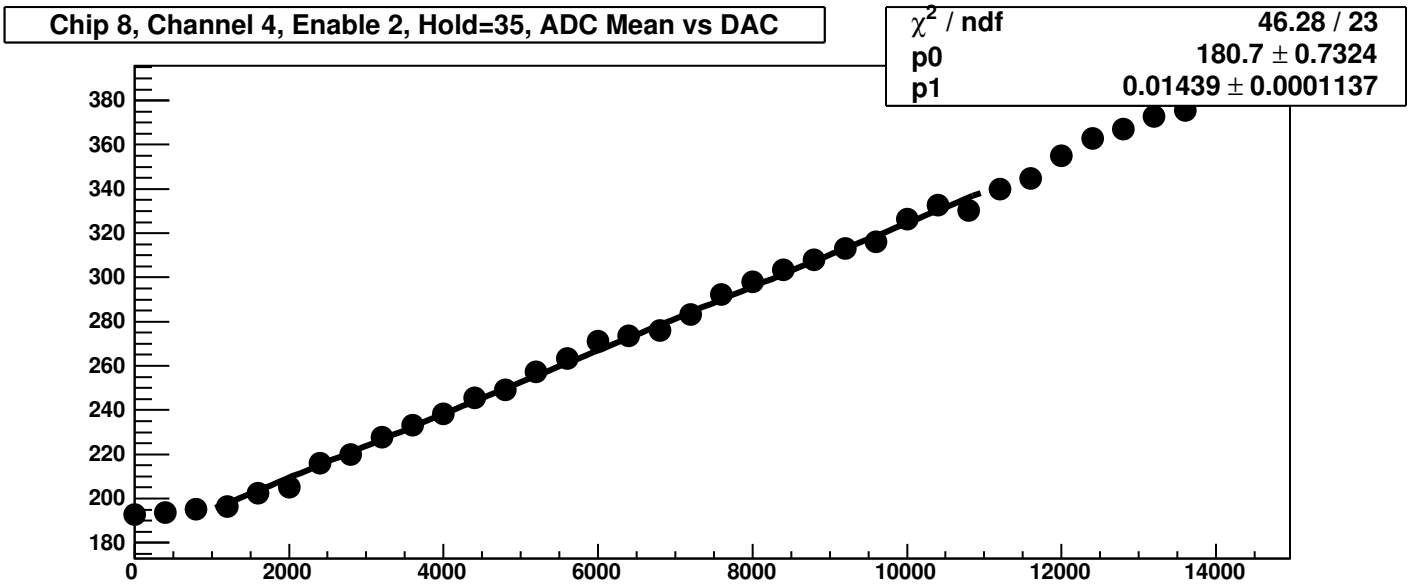
Chip 8, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

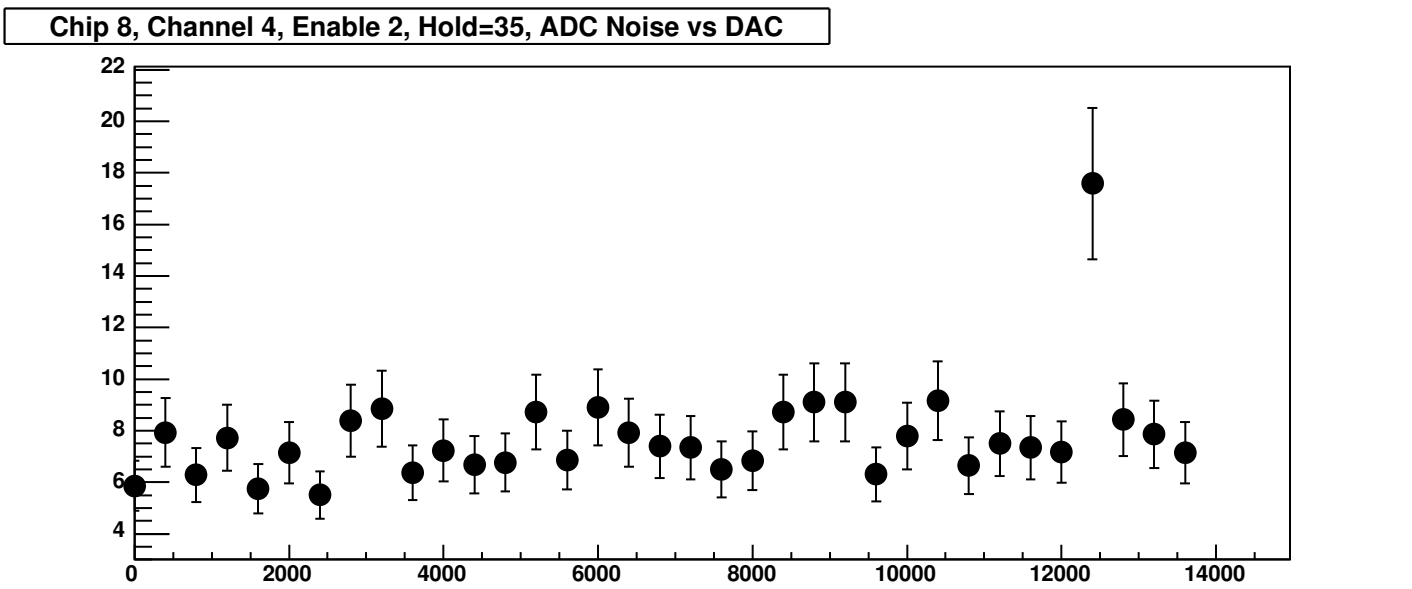


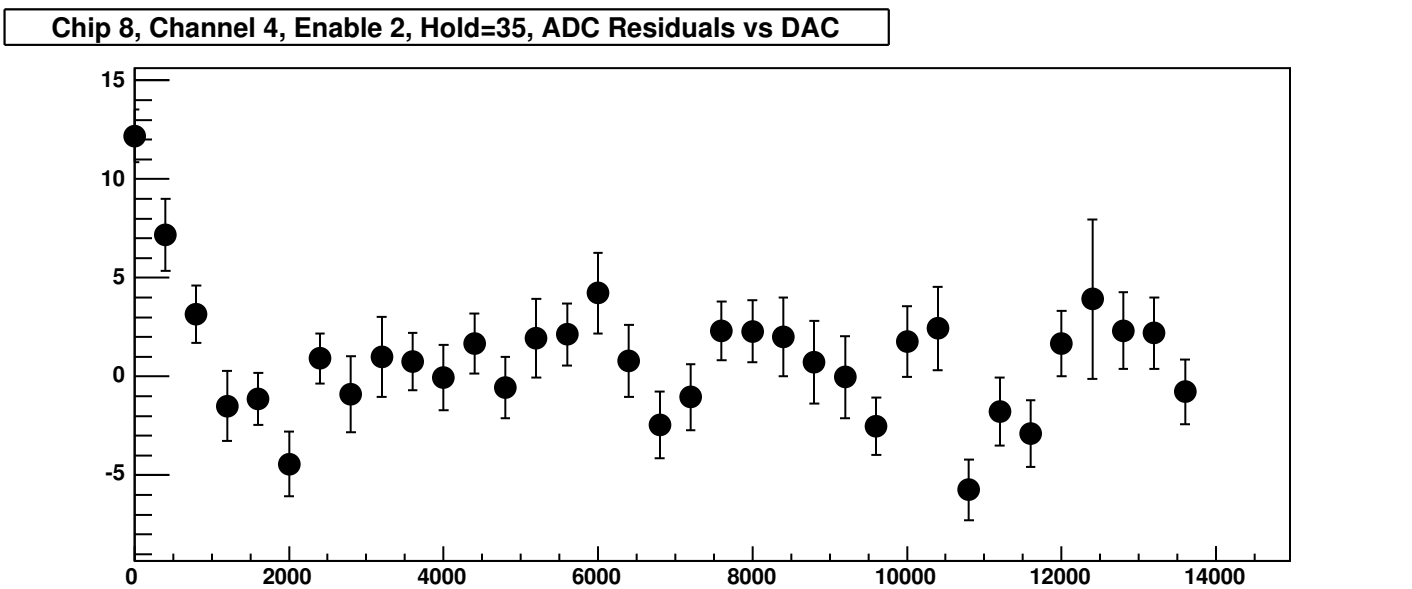

Chip 8, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.64 / 23
p0 174.6 ± 0.8744
p1 0.03531 ± 0.0001415
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

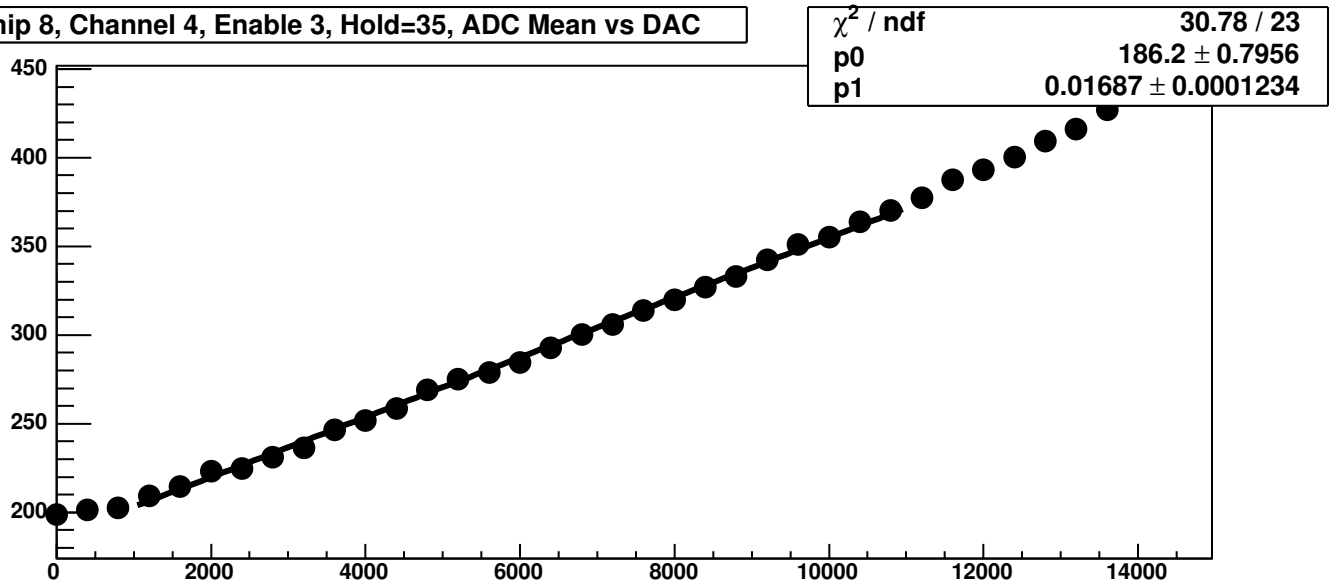

Chip 8, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 46.28 / 23
p0 180.7 ± 0.7324
p1 0.01439 ± 0.0001137
380
360
340
320
300
280
260
240
220
200
180
0
2000
4000
6000
8000
10000
12000
14000

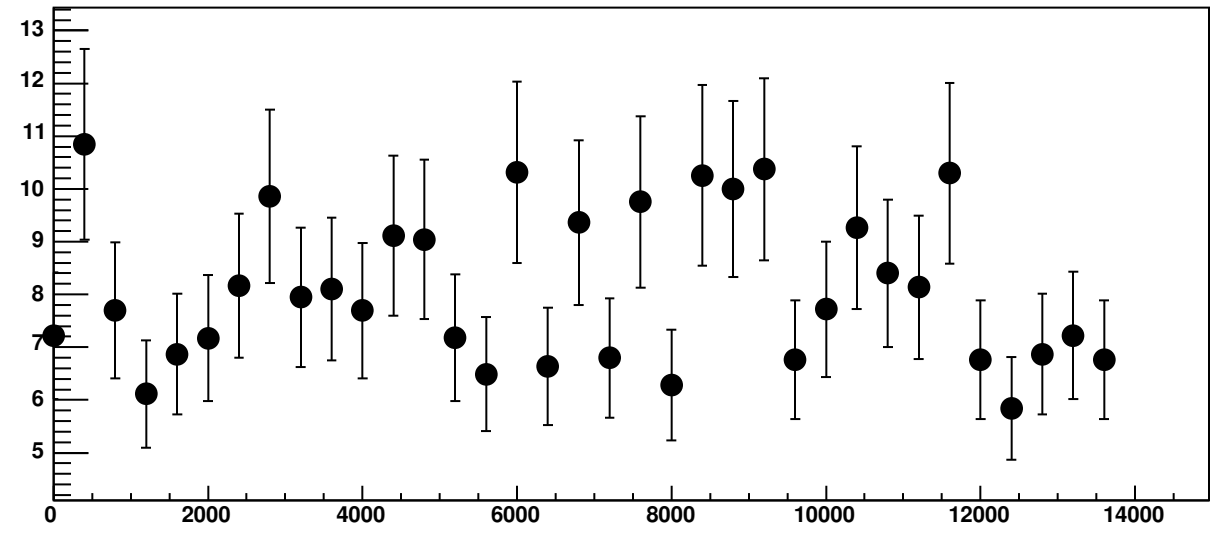

Chip 8, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

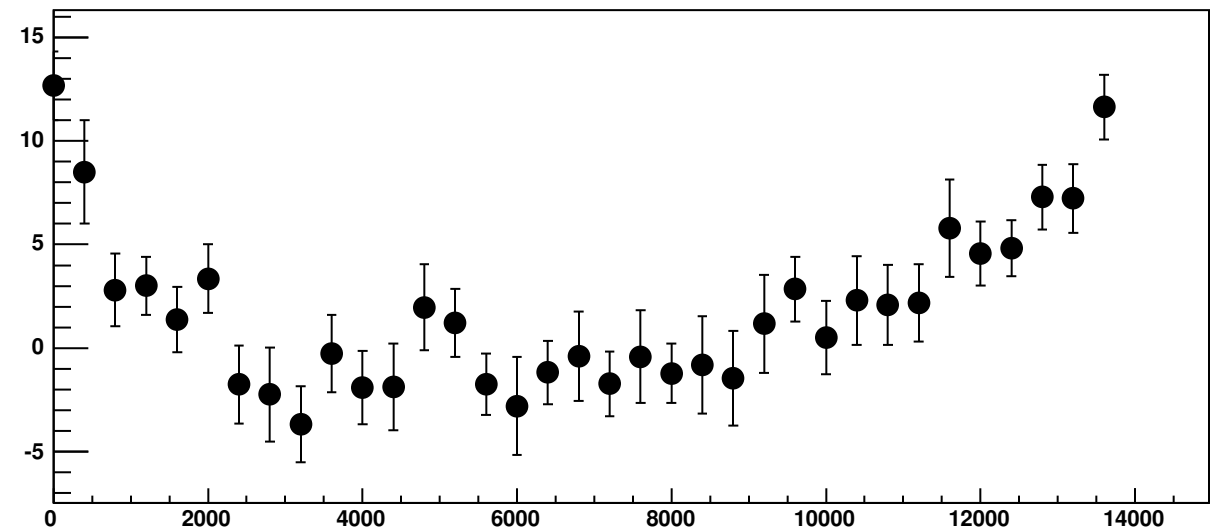
Chip 8, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

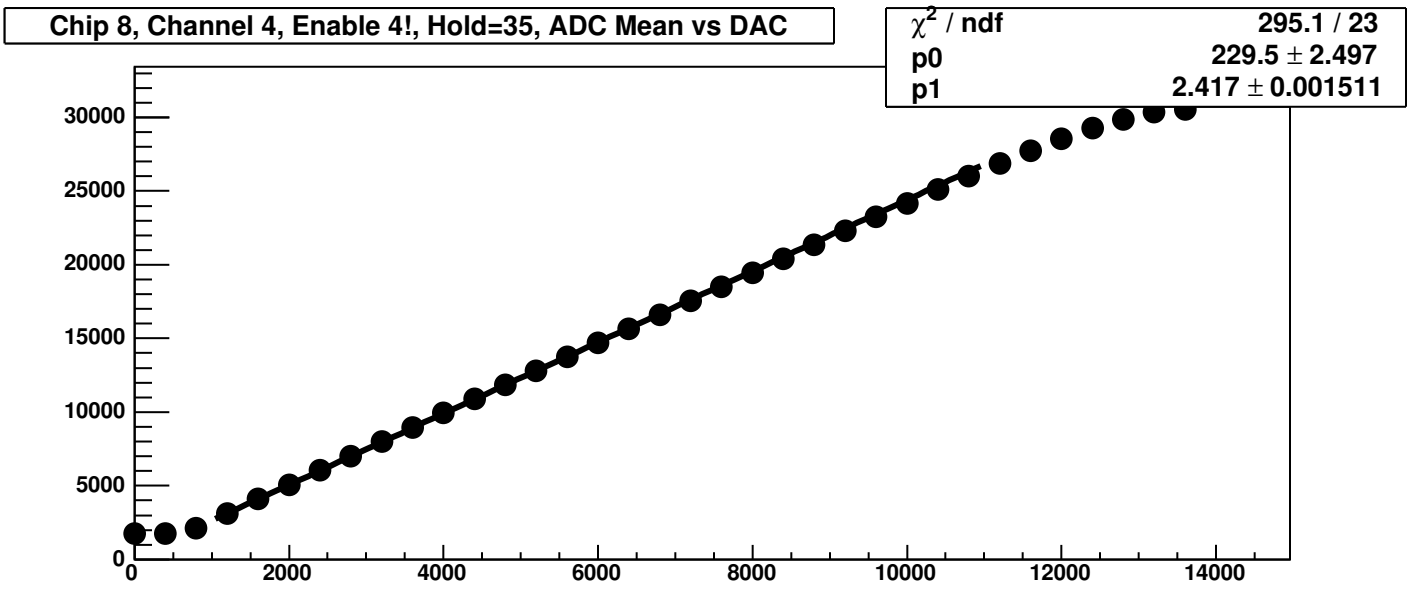


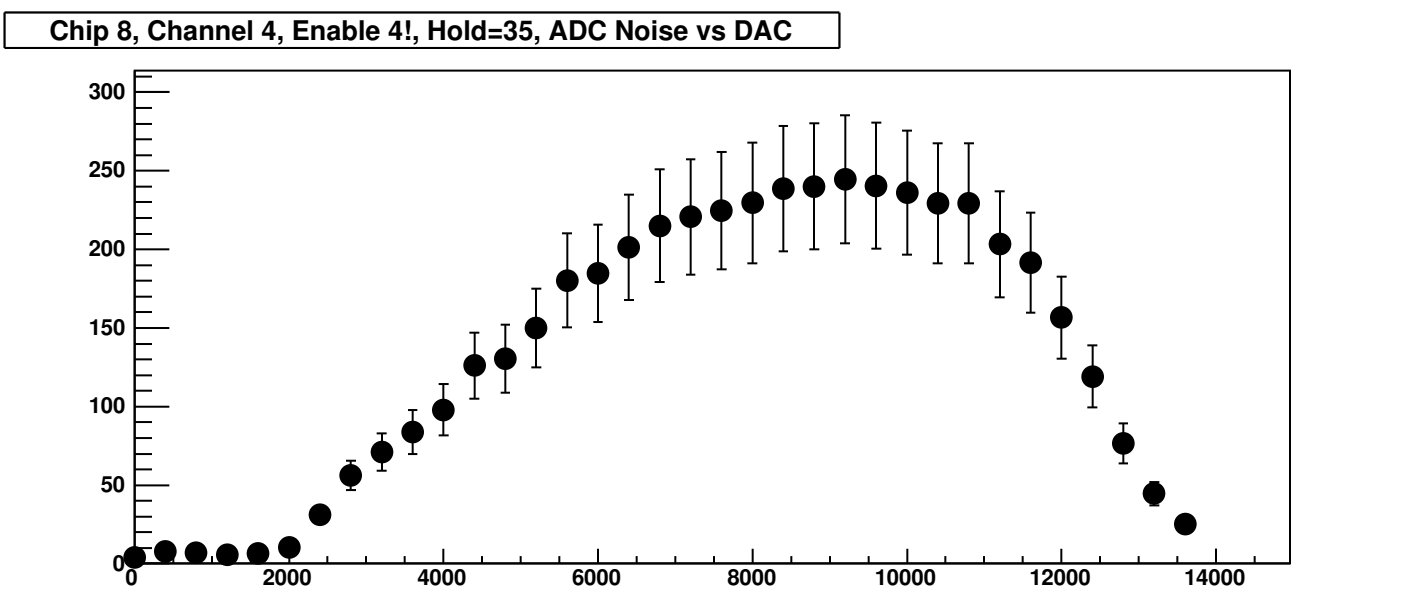
Chip 8, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

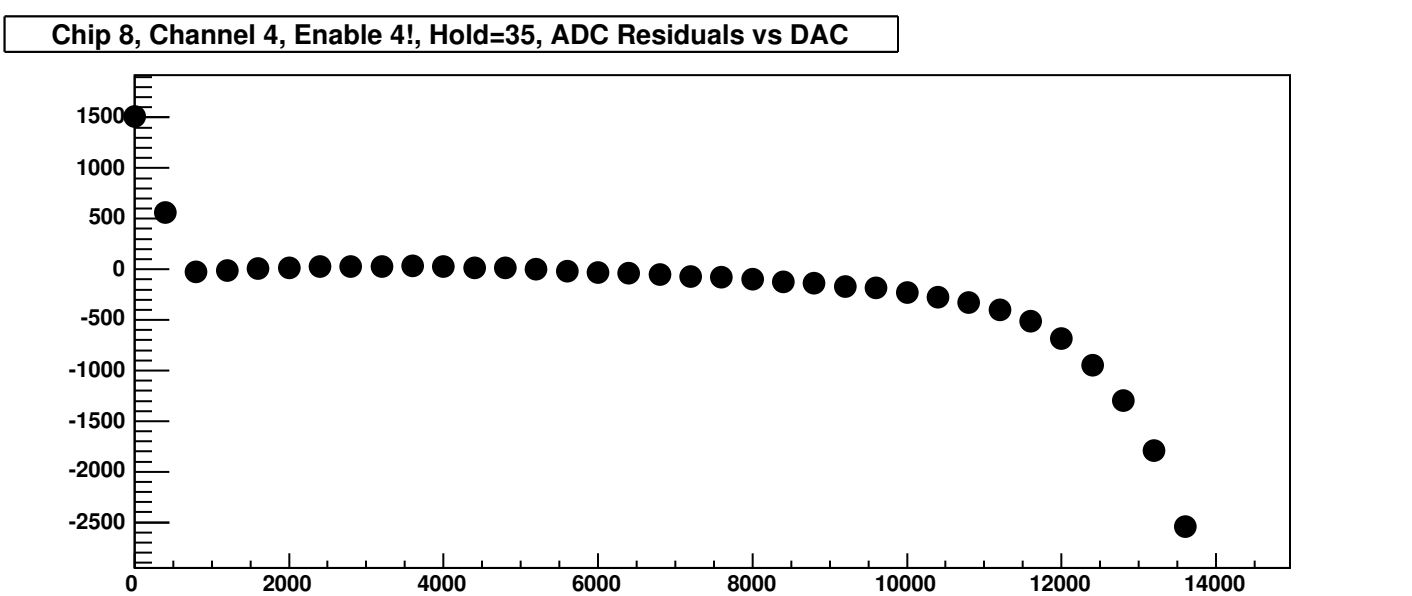


Chip 8, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

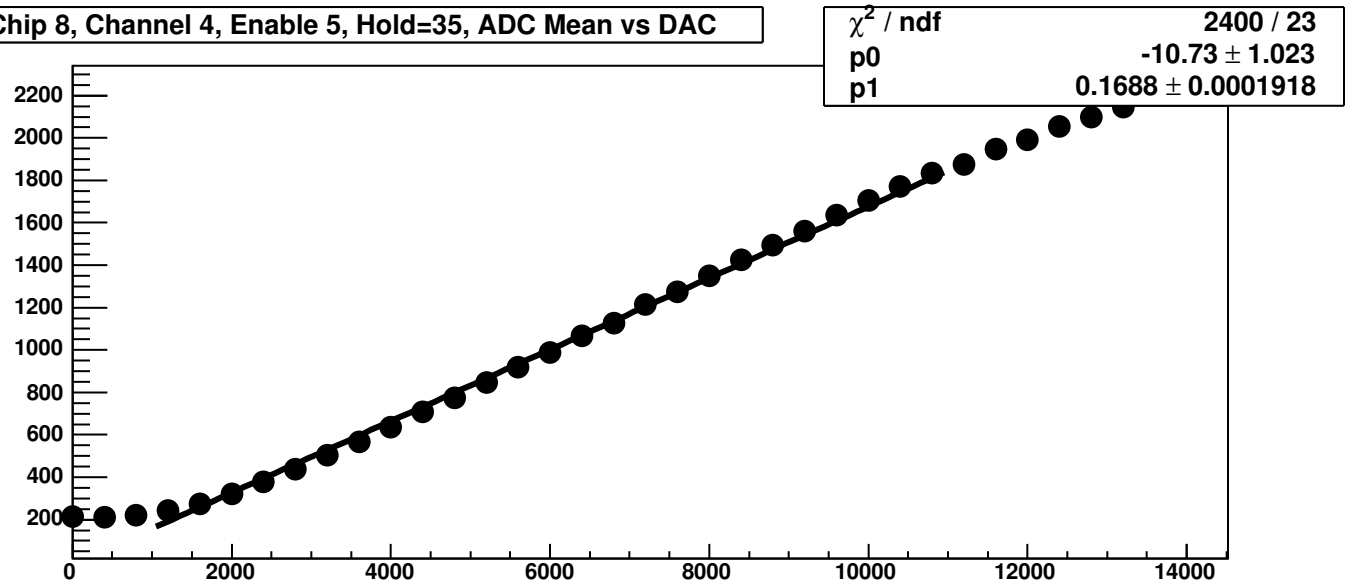



Chip 8, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 295.1 / 23
p0 229.5 ± 2.497
p1 2.417 ± 0.001511
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

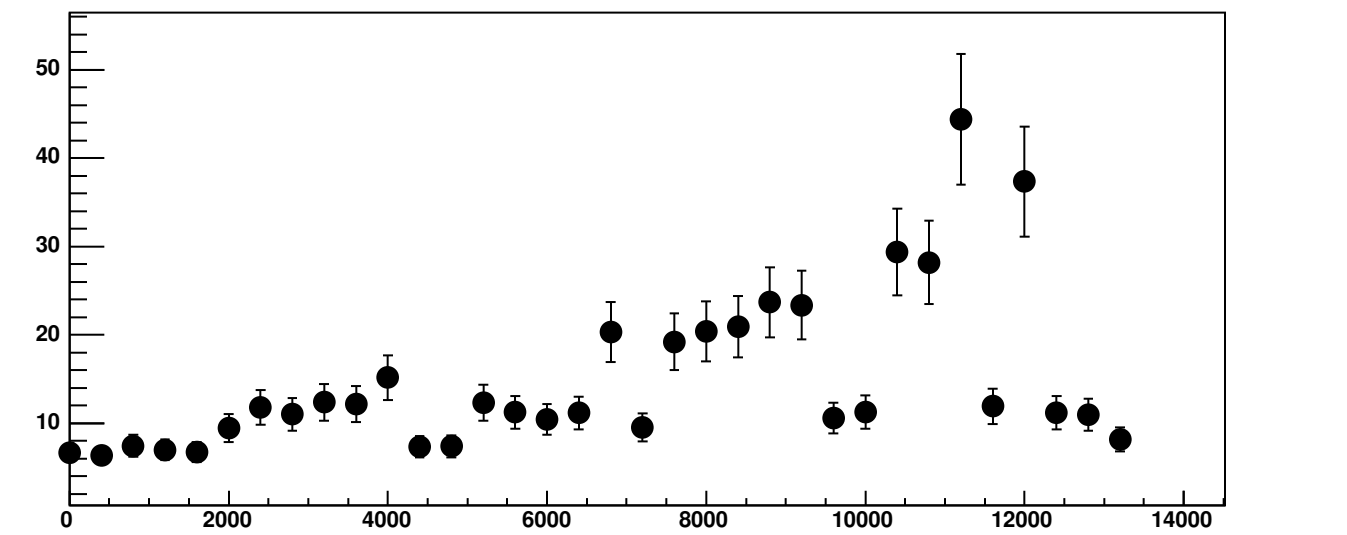

Chip 8, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

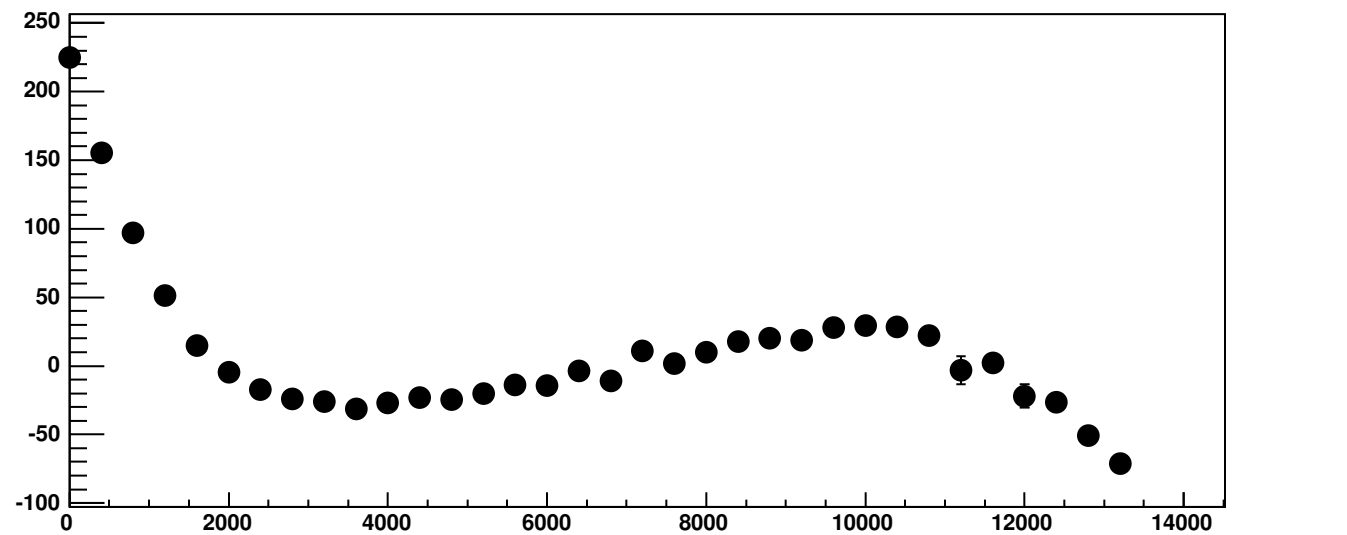
Chip 8, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

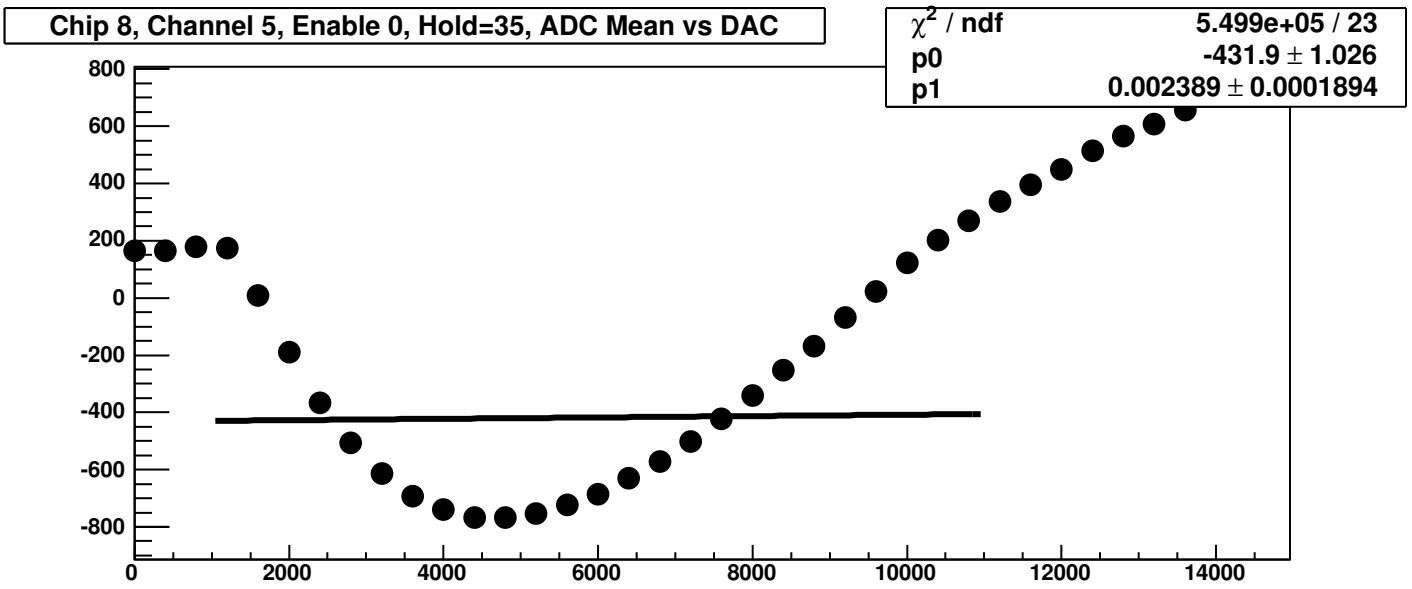


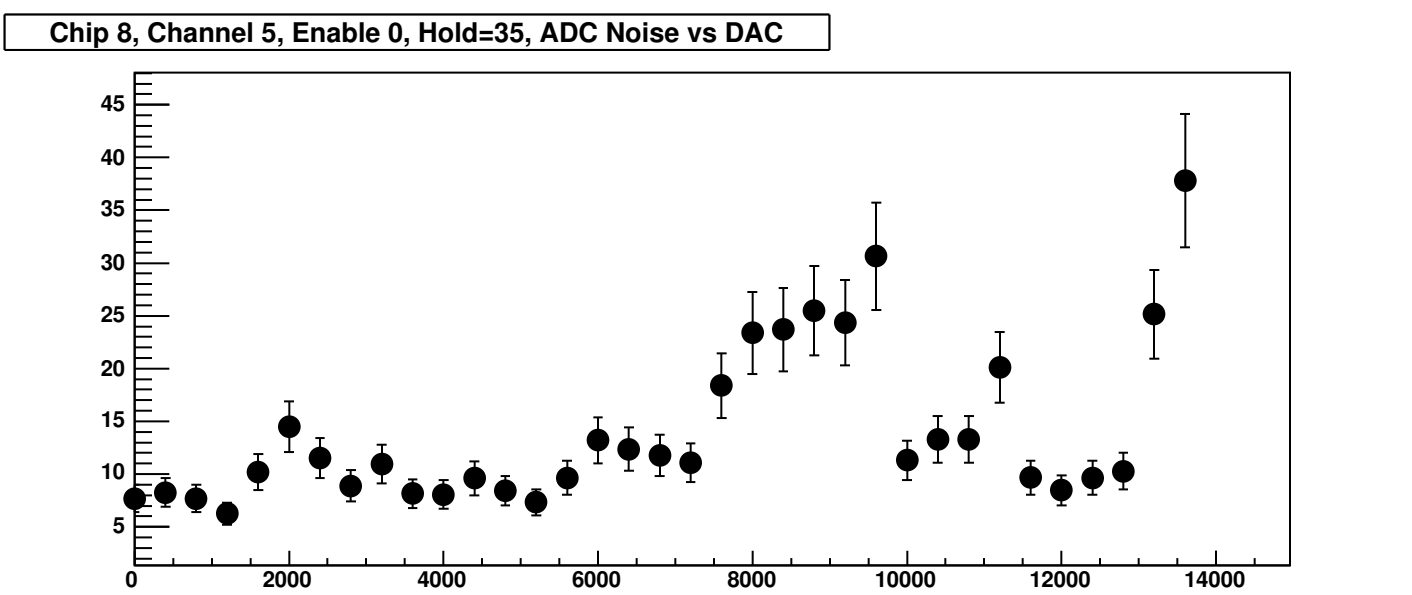
Chip 8, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

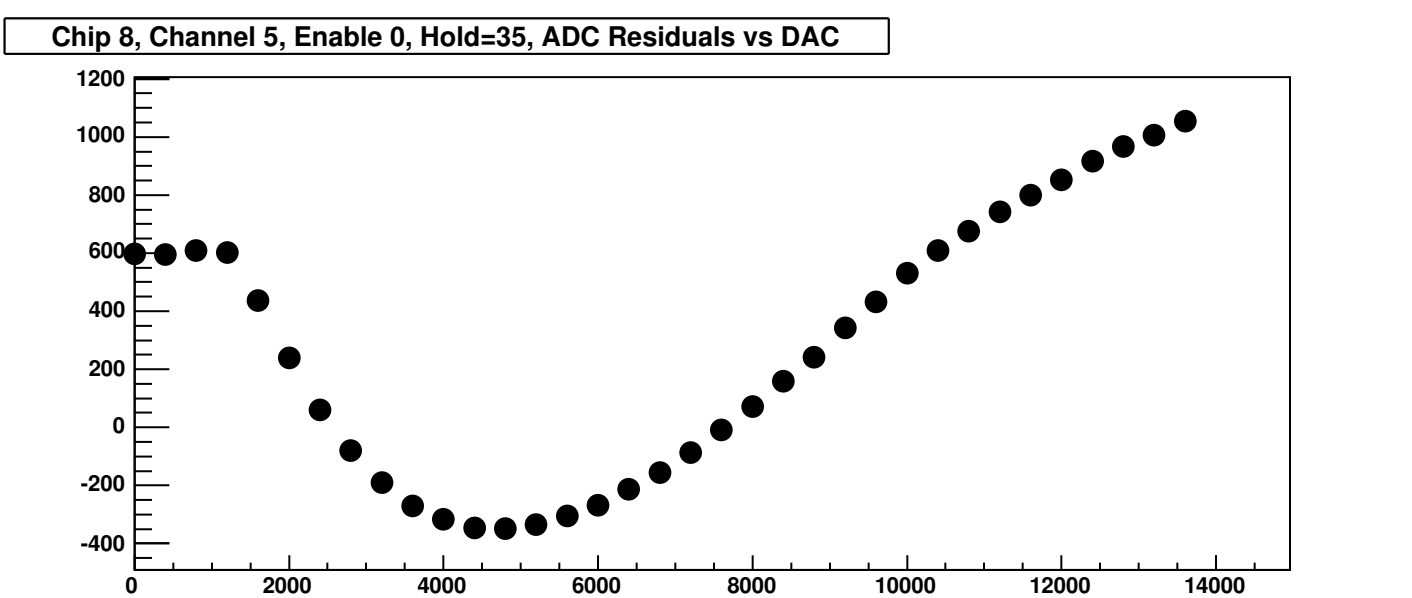


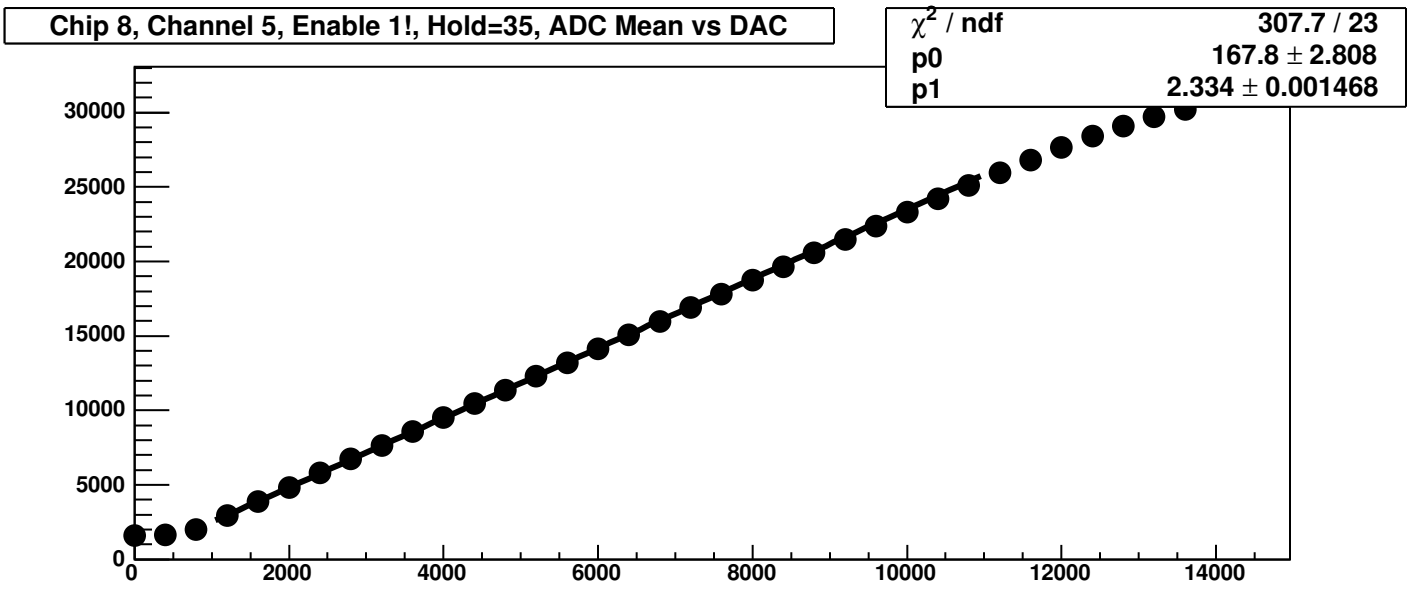
Chip 8, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

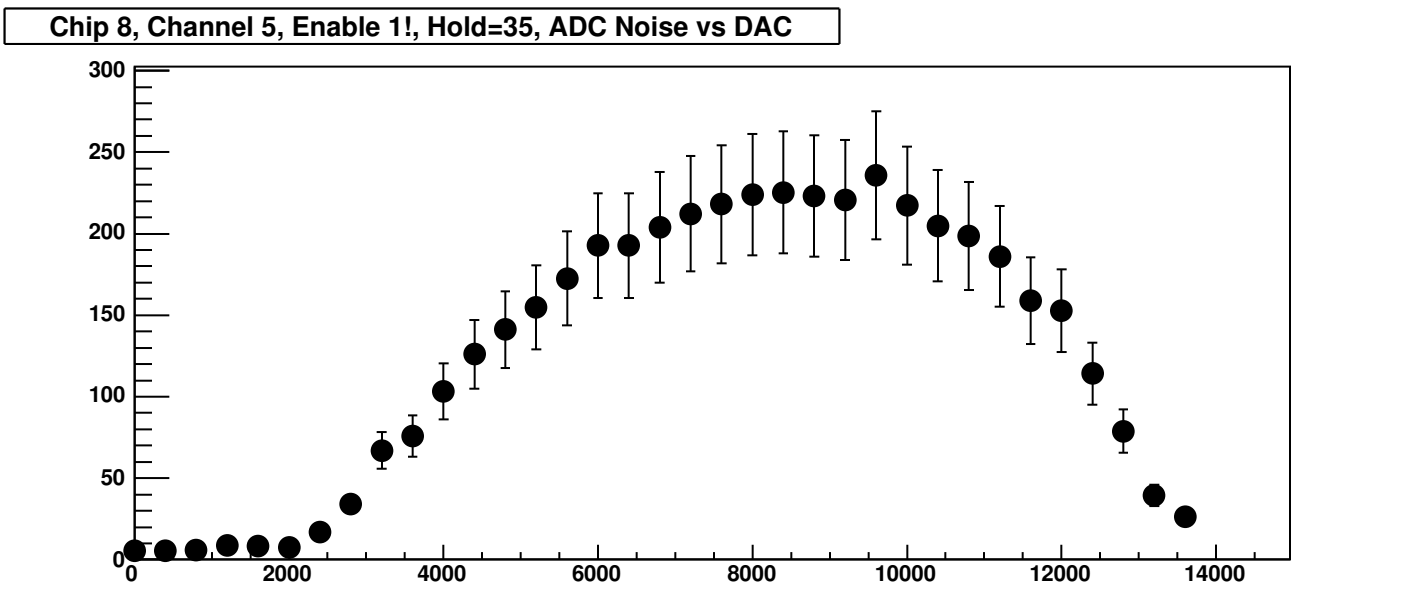


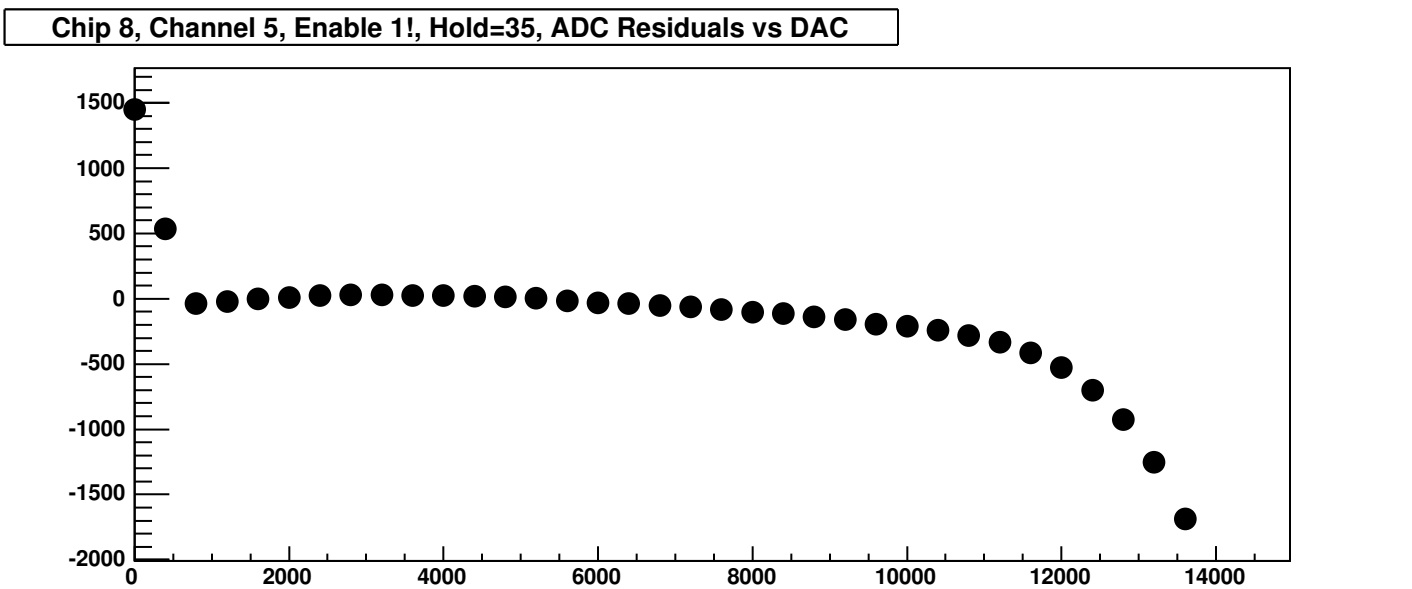

Chip 8, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.499e+05 / 23
p0 -431.9 ± 1.026
p1 0.002389 ± 0.0001894
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

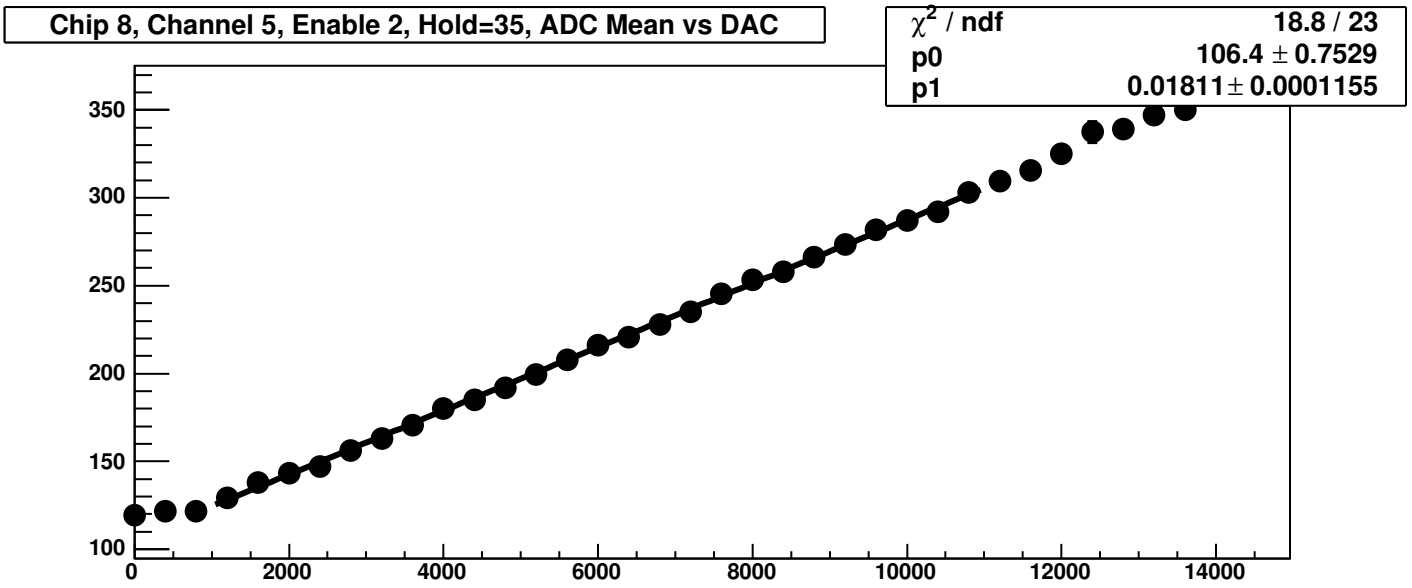

Chip 8, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

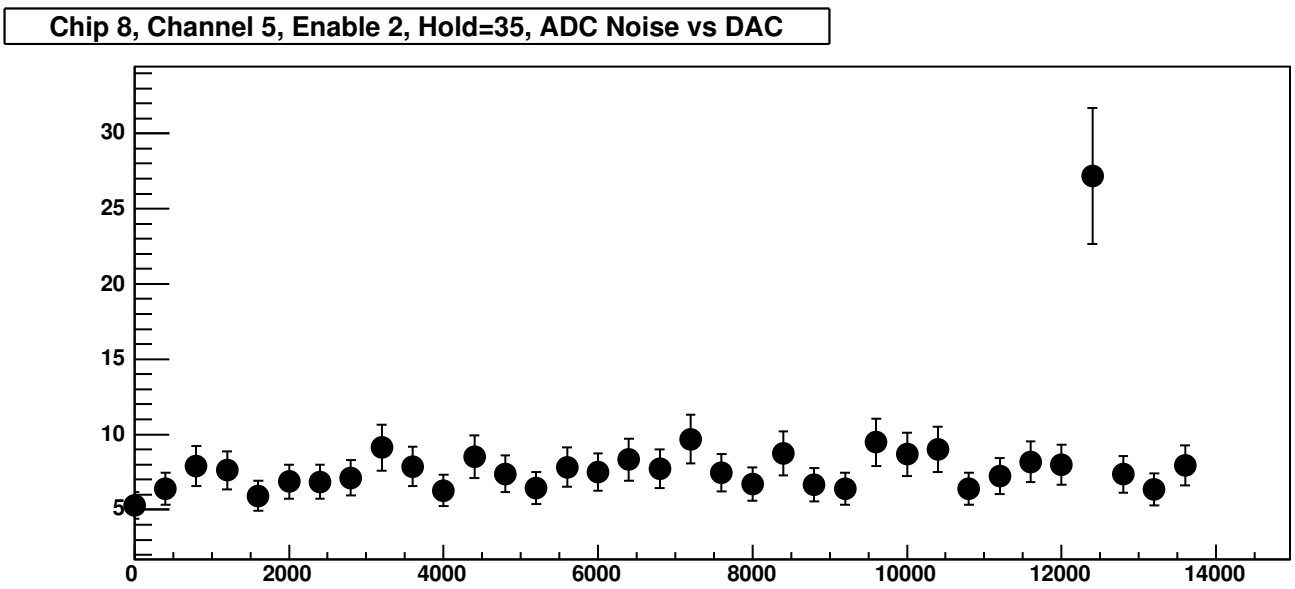

Chip 8, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

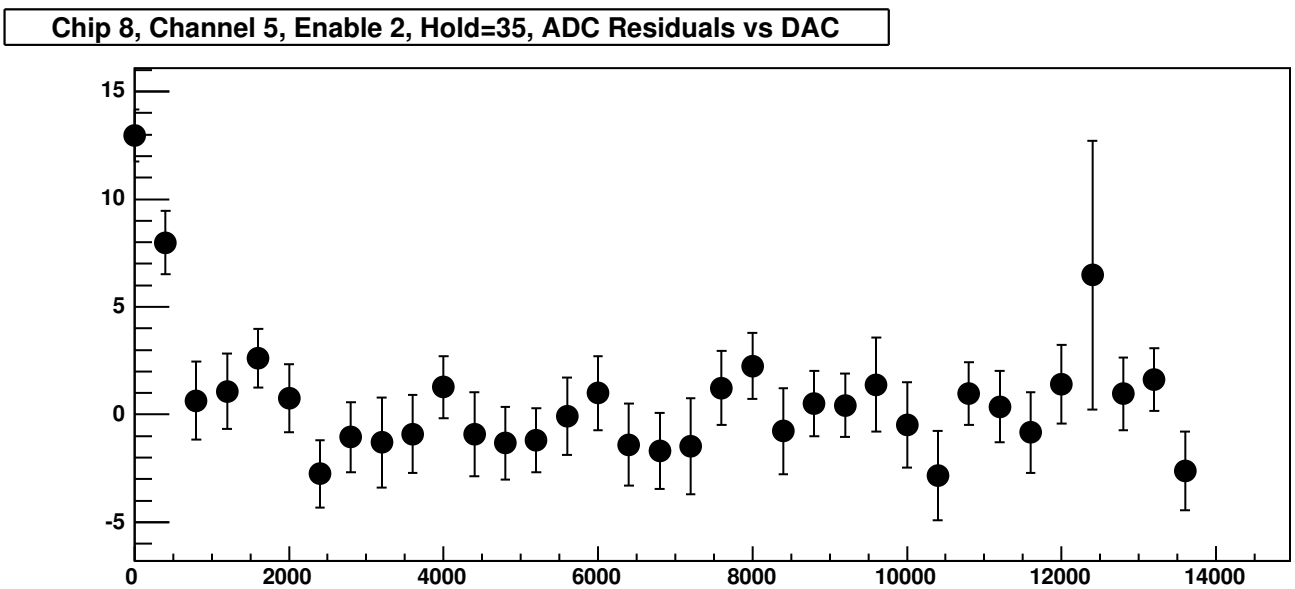

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 307.7 / 23
p0 167.8 ± 2.808
p1 2.334 ± 0.001468
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

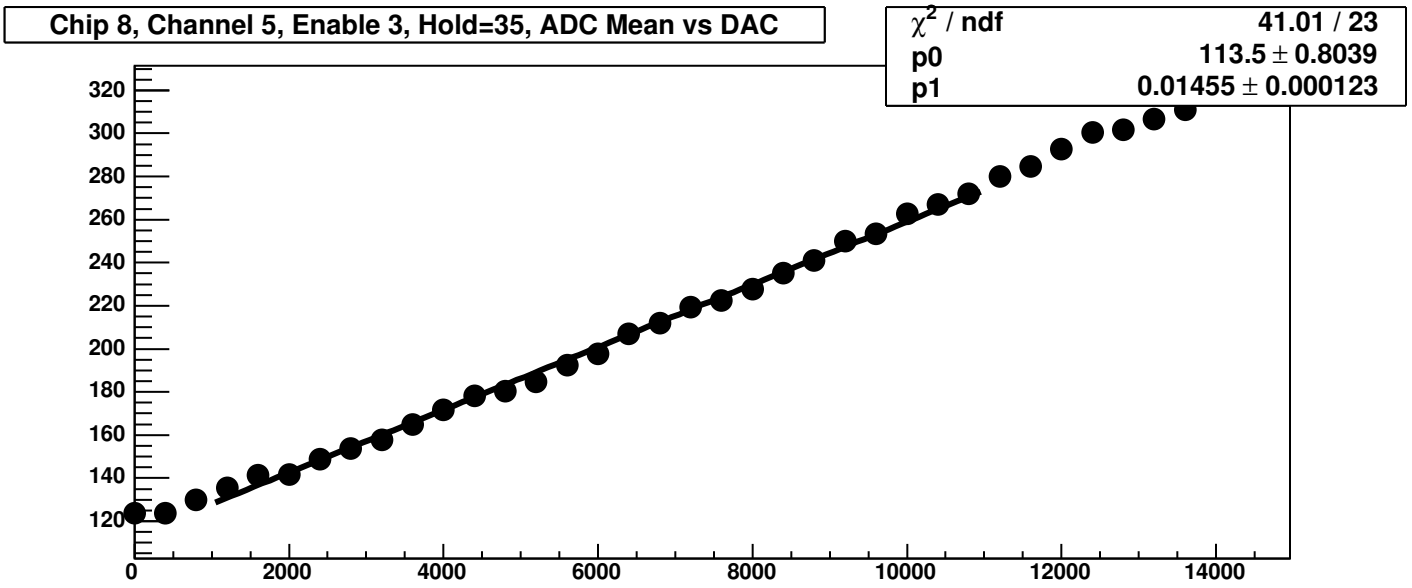

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

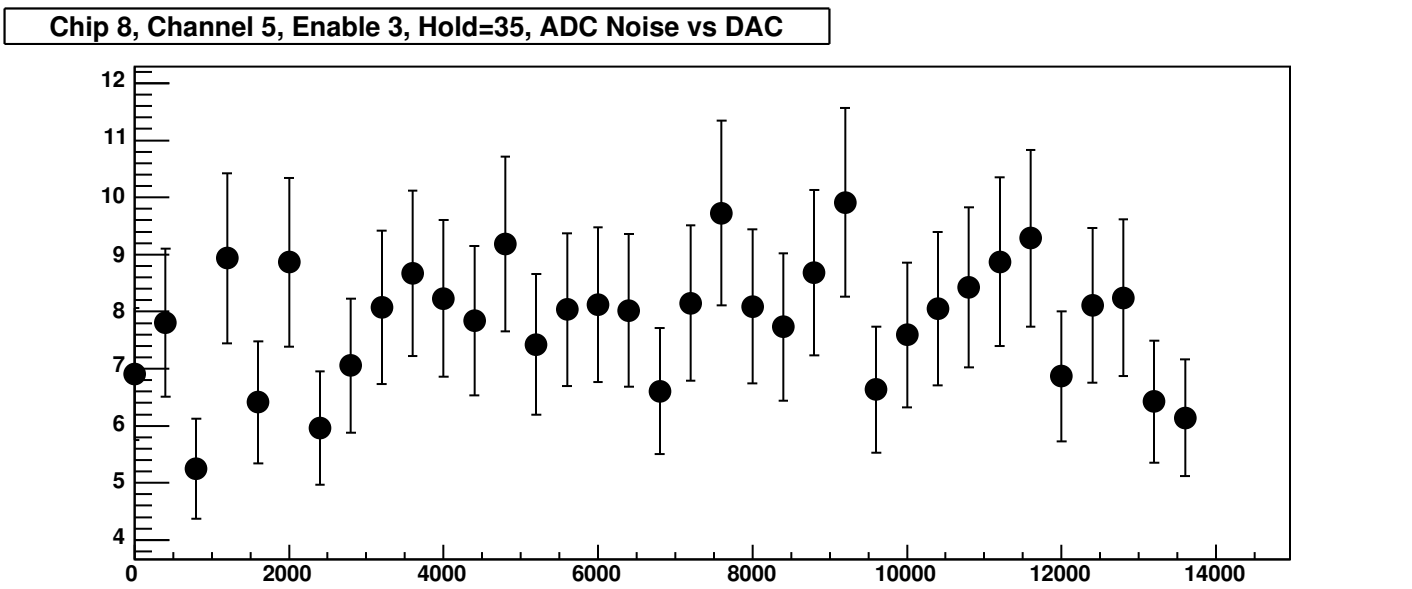

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

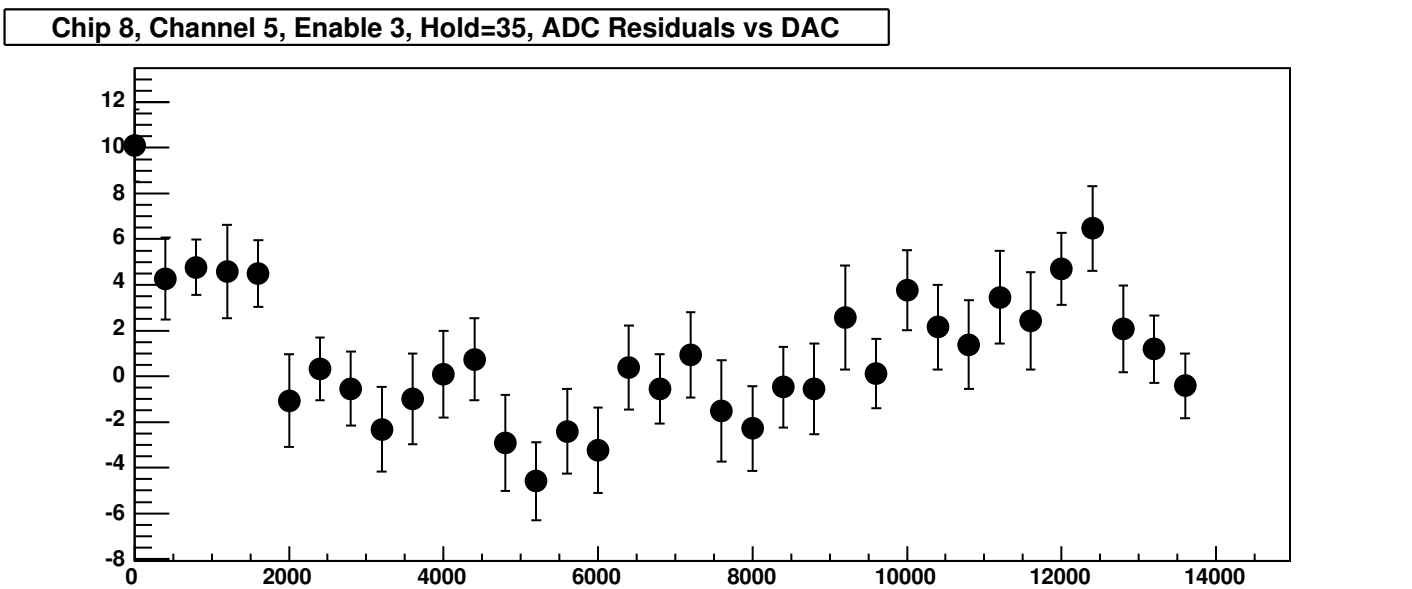

Chip 8, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.8 / 23
p0 106.4 ± 0.7529
p1 0.01811 ± 0.0001155
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

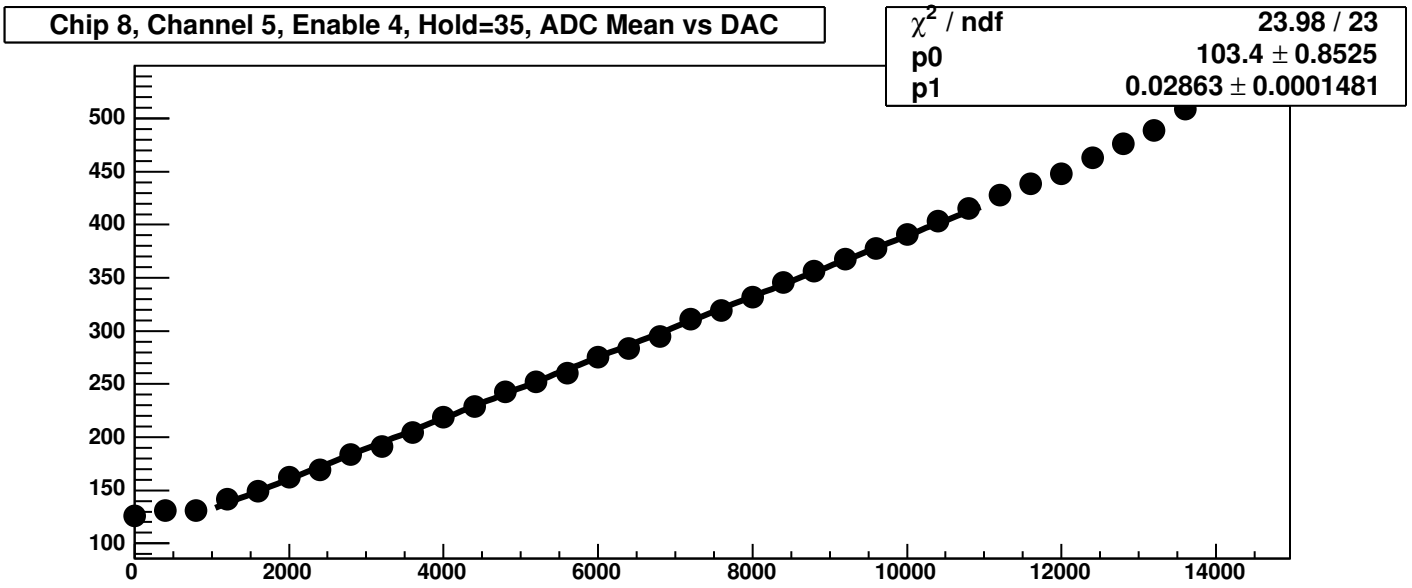

Chip 8, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

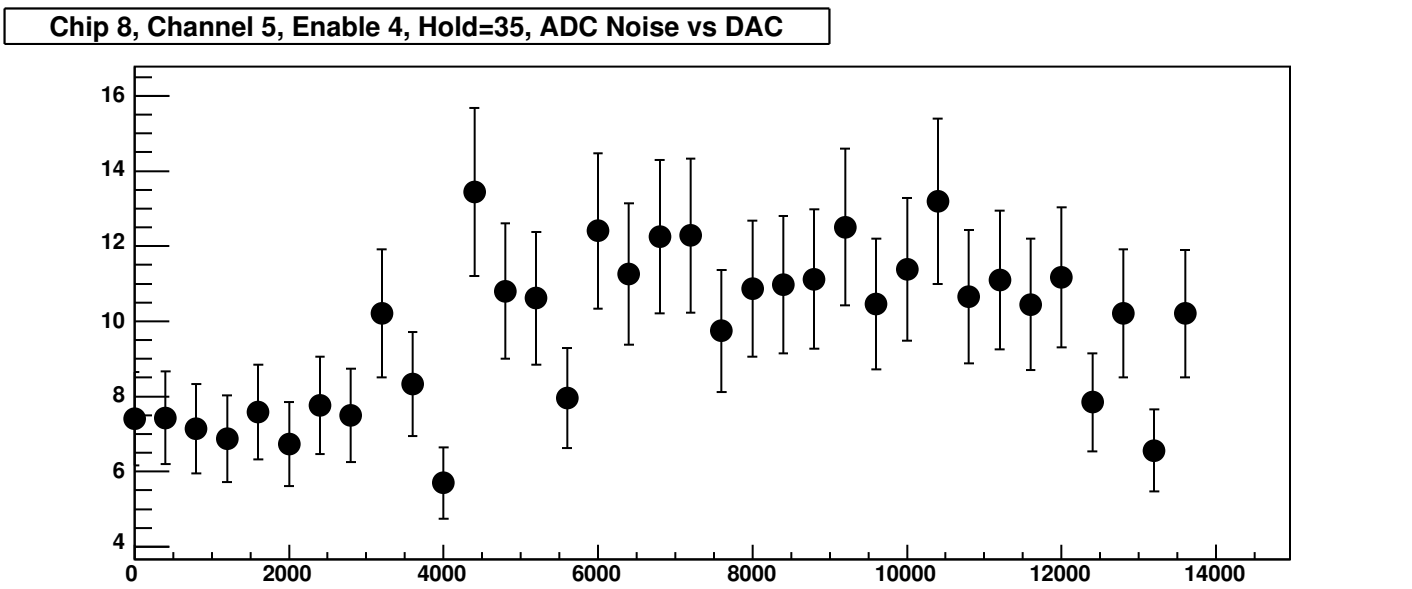

Chip 8, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

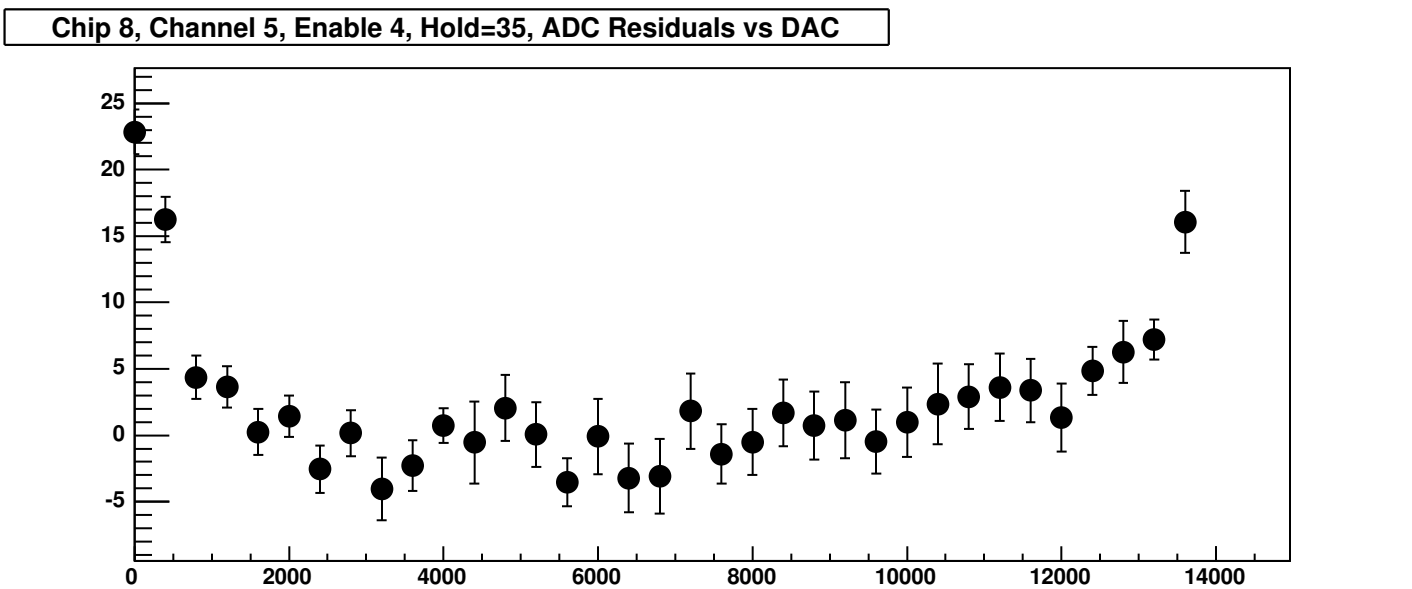

Chip 8, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.01 / 23
p0 113.5 ± 0.8039
p1 0.01455 ± 0.000123
320
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

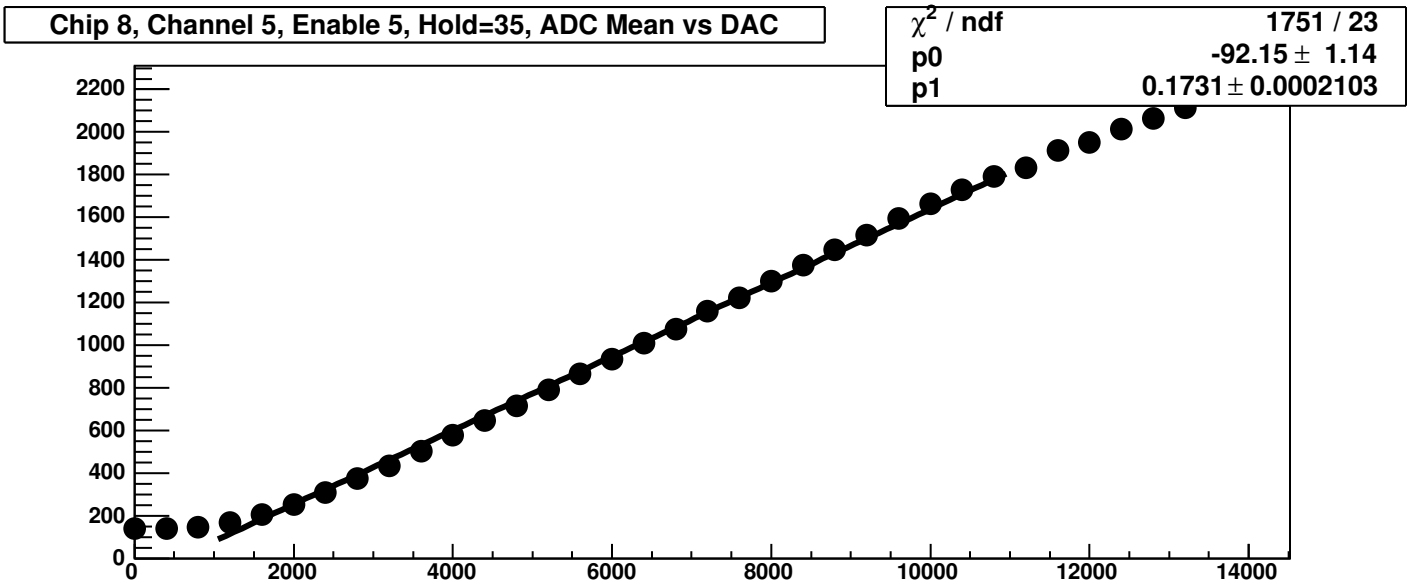

Chip 8, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

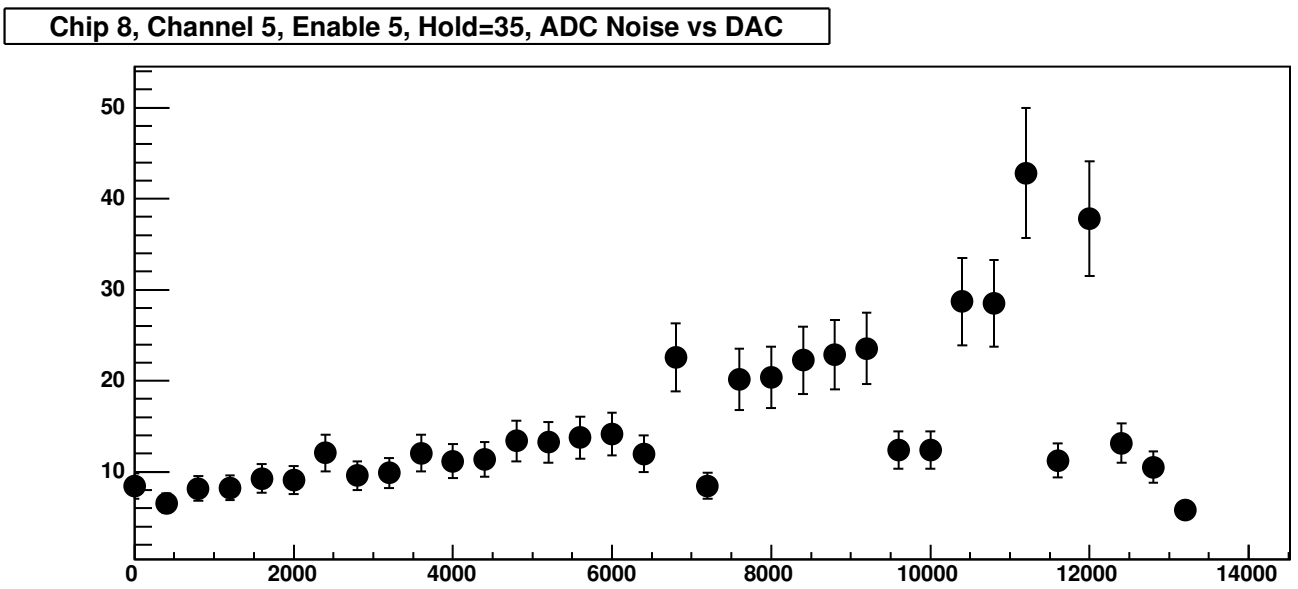

Chip 8, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

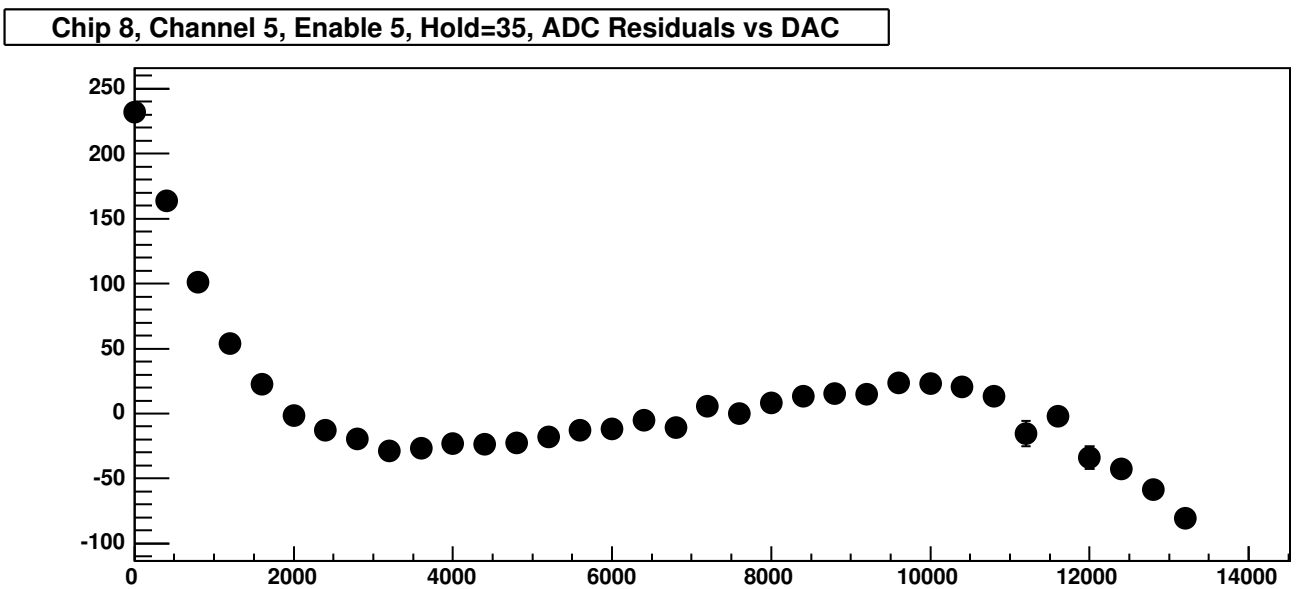

Chip 8, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.98 / 23
p0 103.4 ± 0.8525
p1 0.02863 ± 0.0001481
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

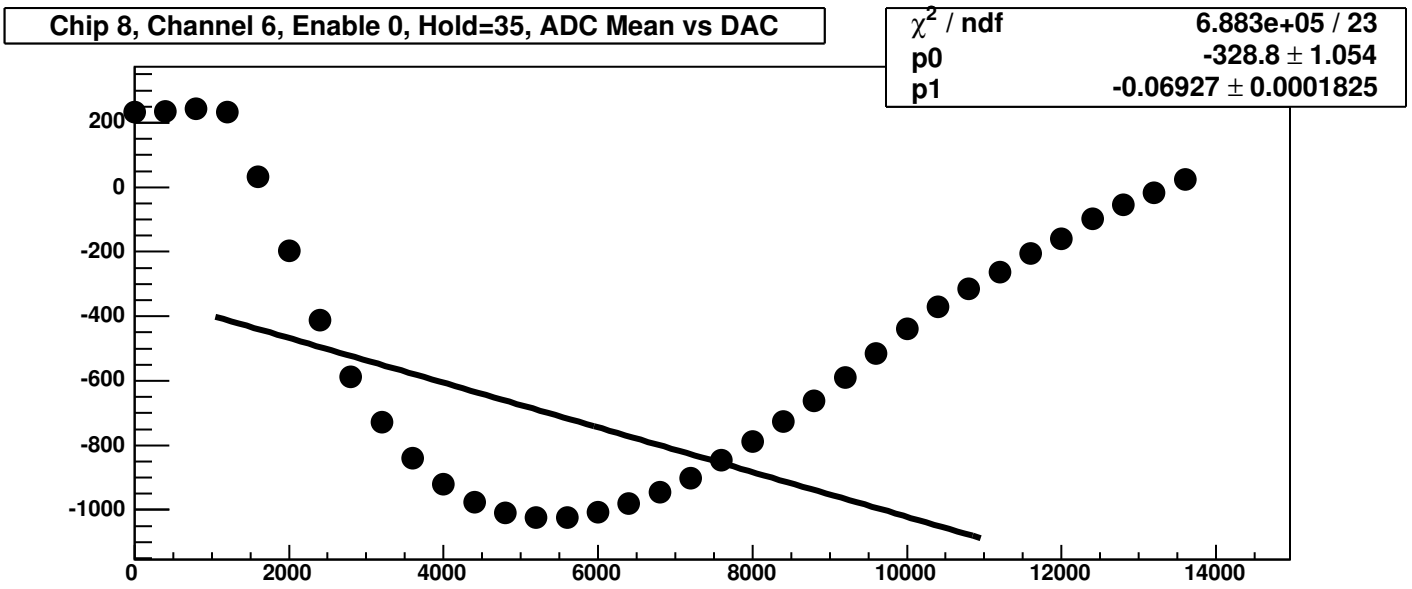

Chip 8, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

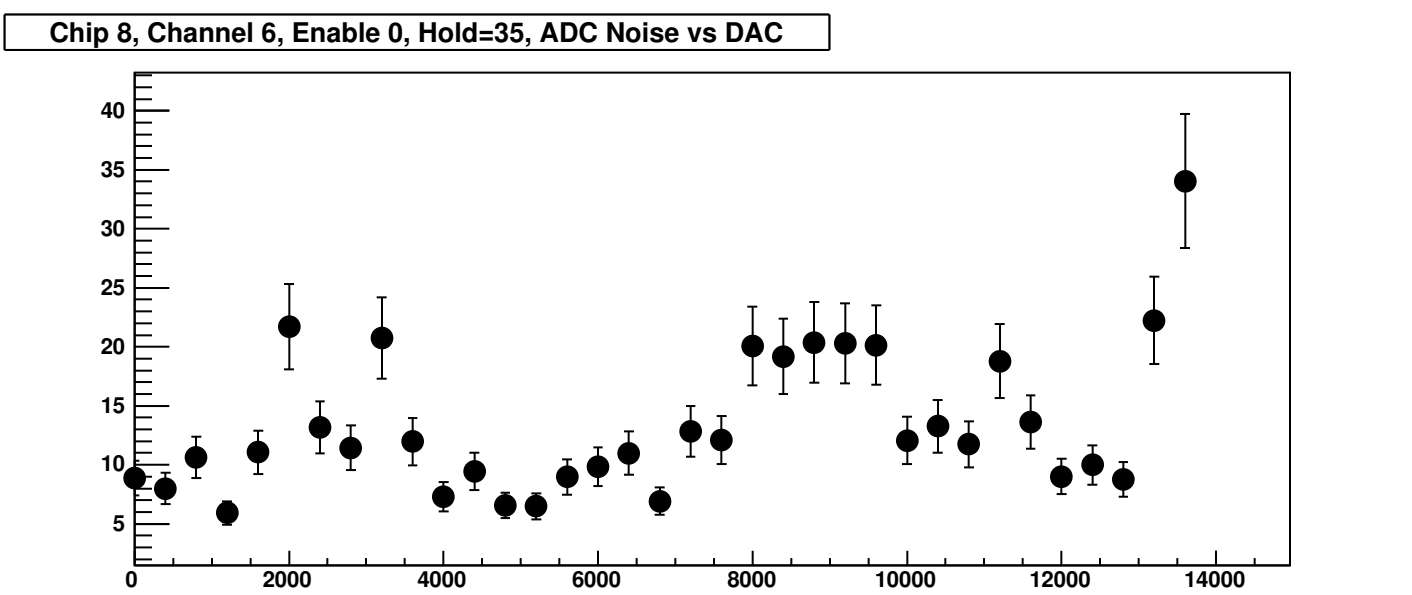

Chip 8, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

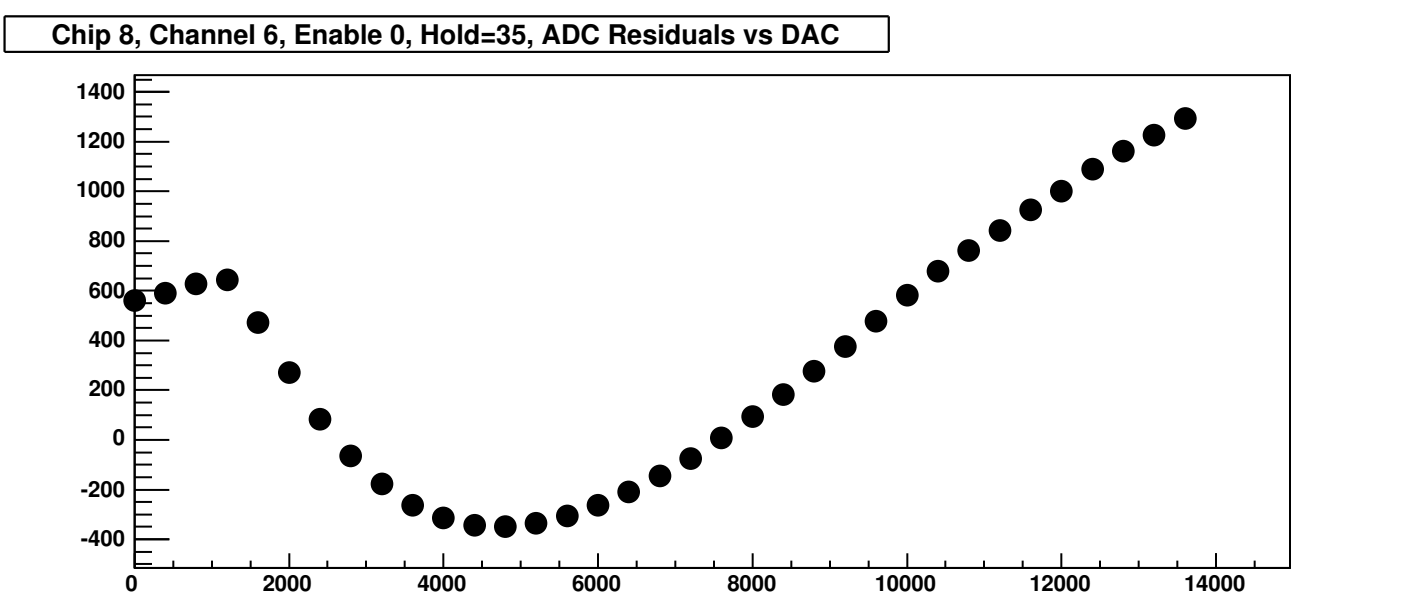

Chip 8, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1751 / 23
p0 -92.15 ± 1.14
p1 0.1731 ± 0.0002103
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

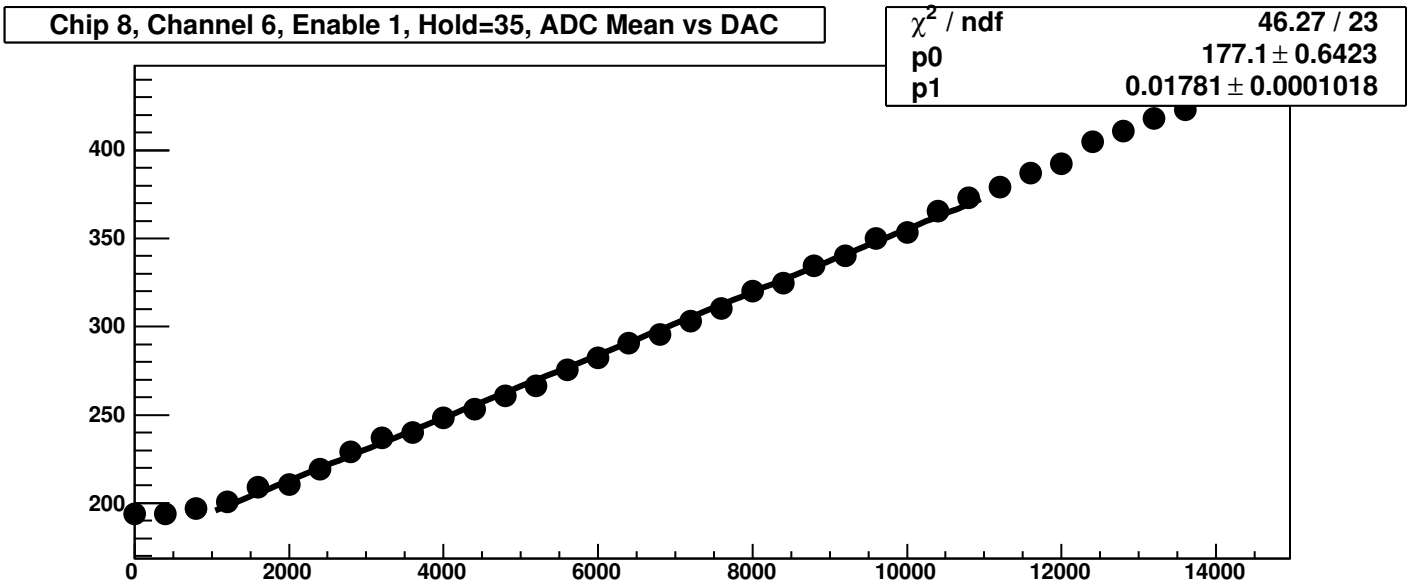

Chip 8, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

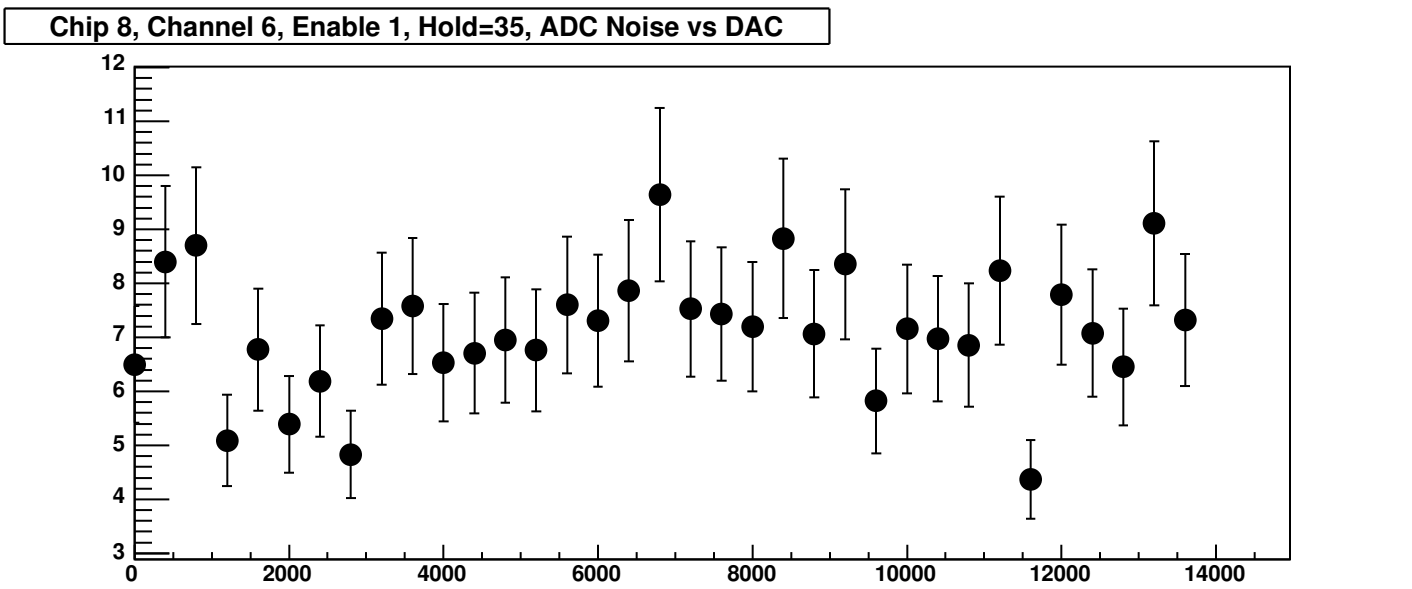

Chip 8, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

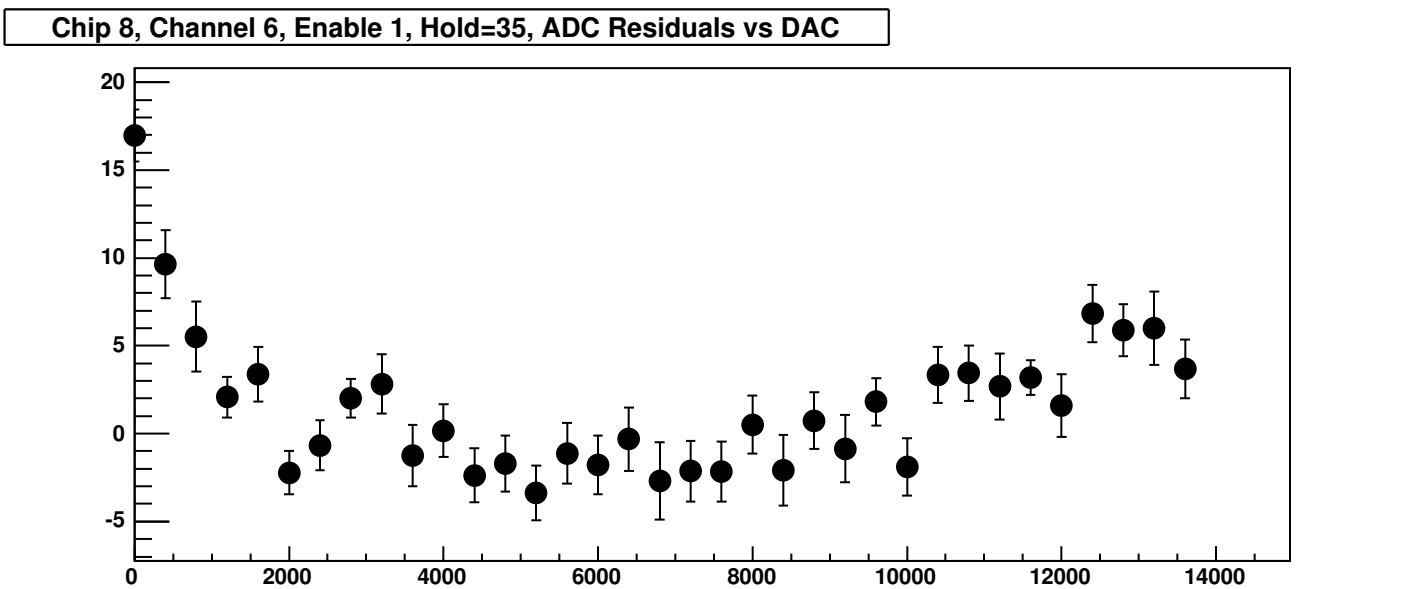

Chip 8, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.883e+05 / 23
p0 -328.8 ± 1.054
p1 -0.06927 ± 0.0001825
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

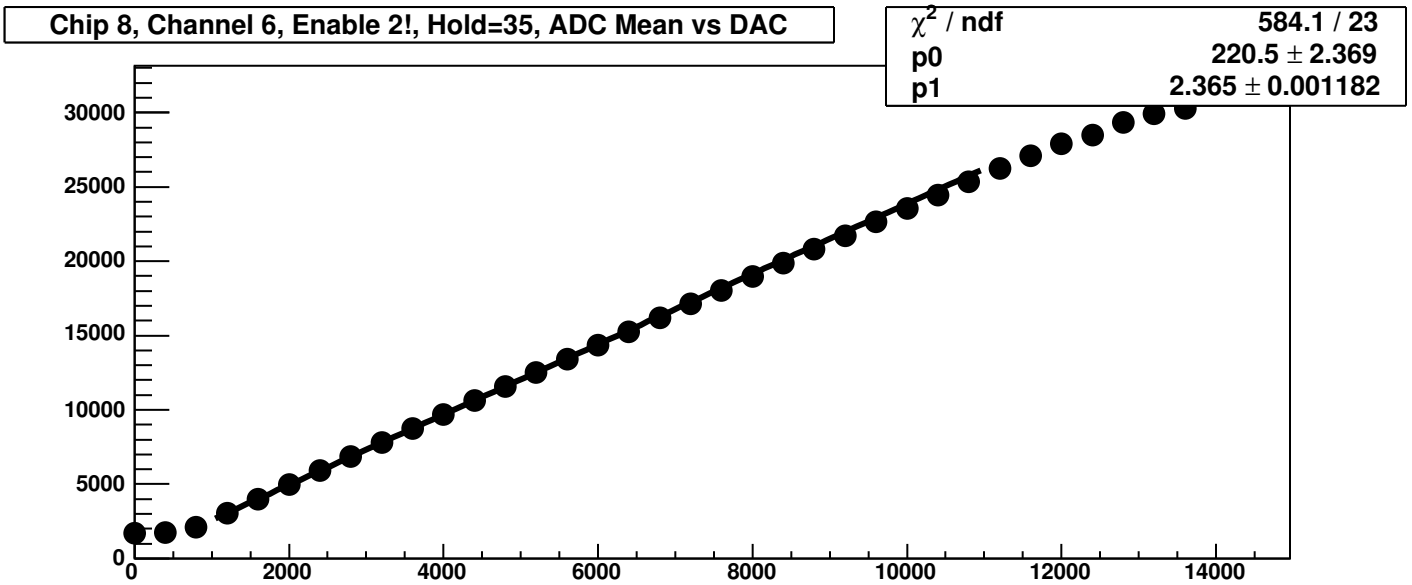

Chip 8, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

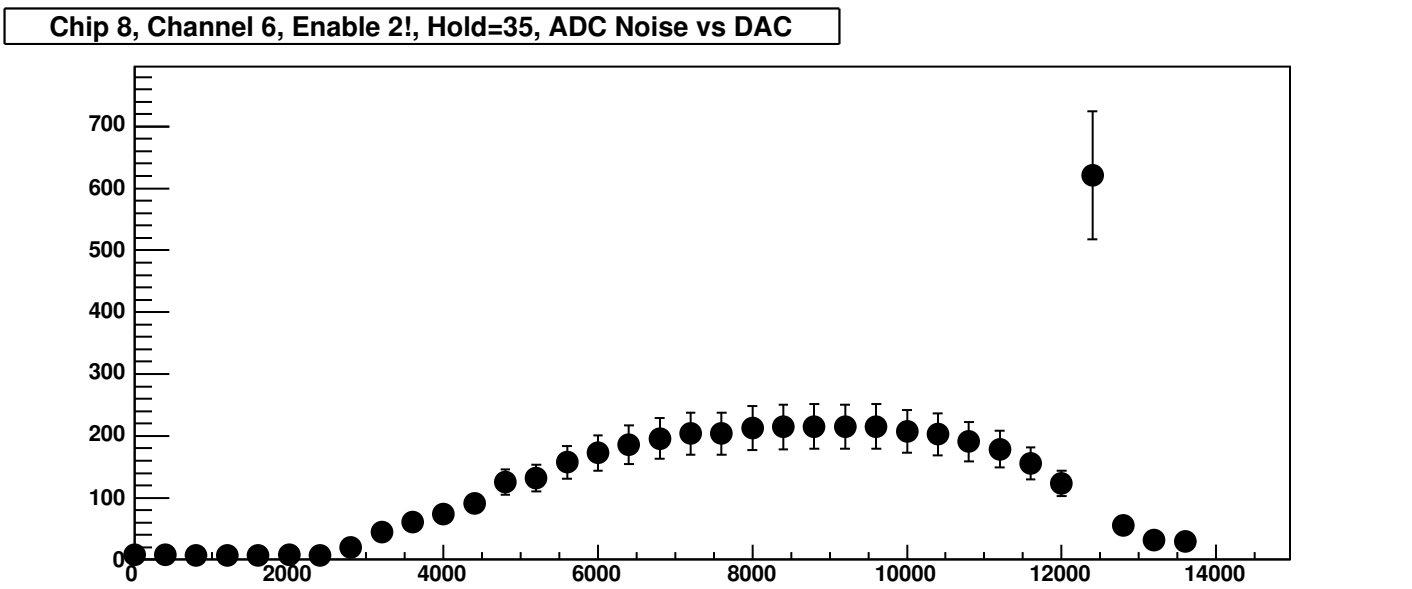

Chip 8, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

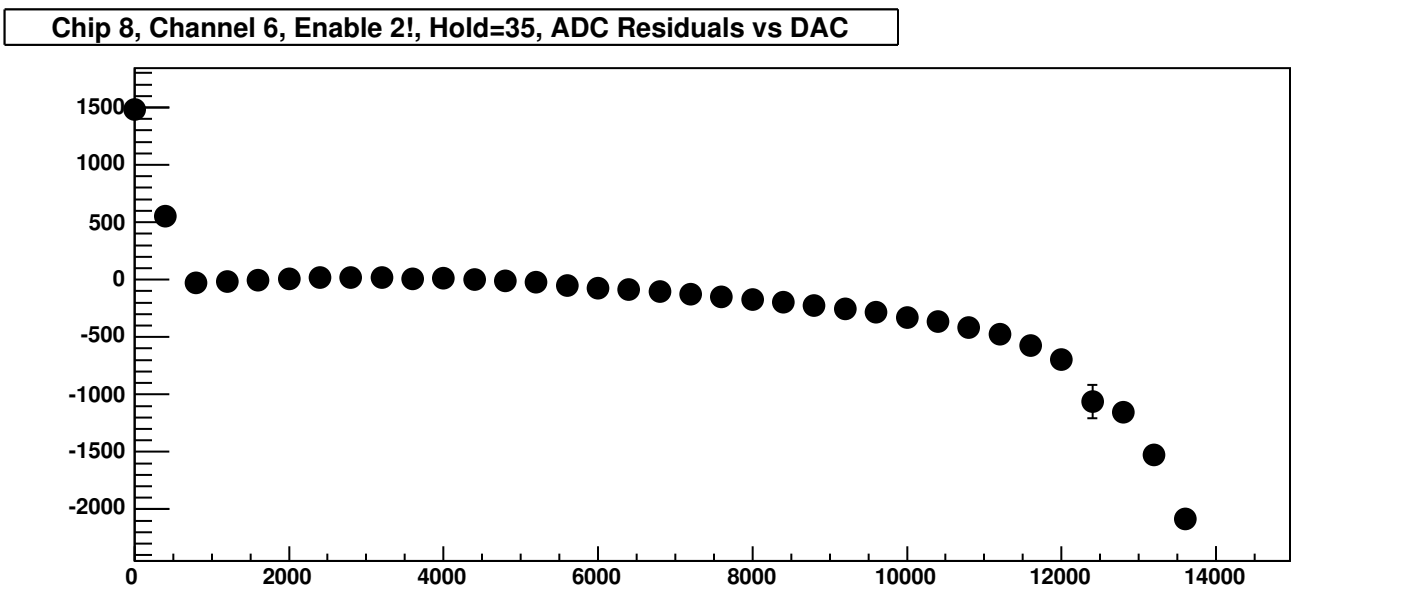

Chip 8, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.27 / 23
p0 177.1 ± 0.6423
p1 0.01781 ± 0.0001018
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

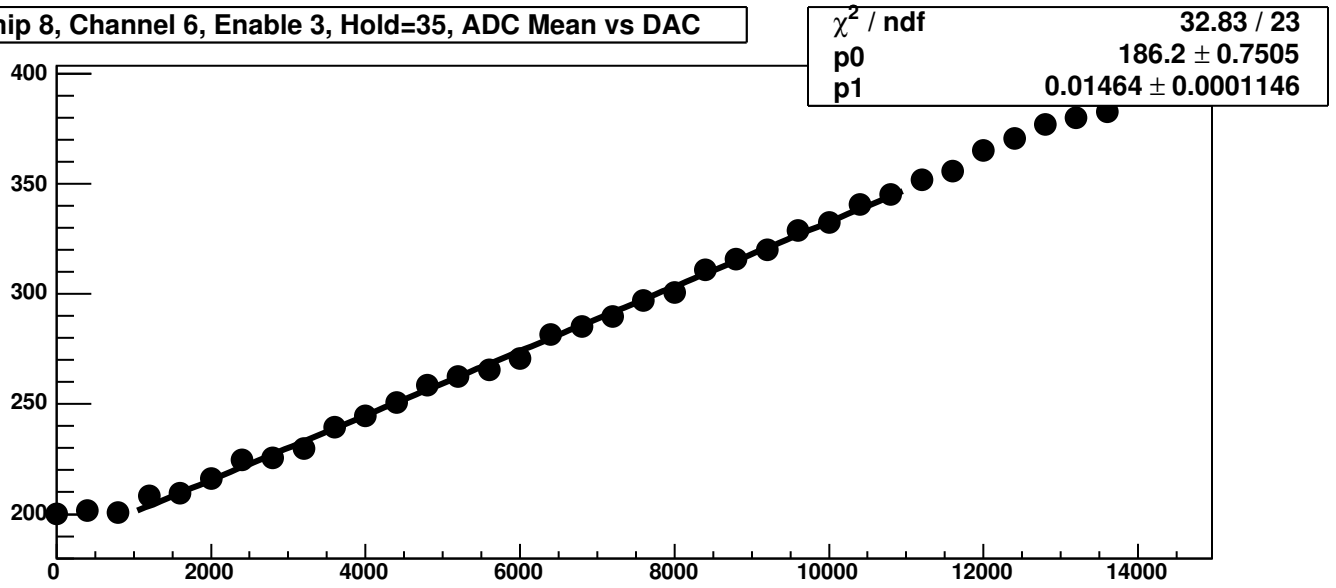

Chip 8, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 584.1 / 23
p0 220.5 ± 2.369
p1 2.365 ± 0.001182
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

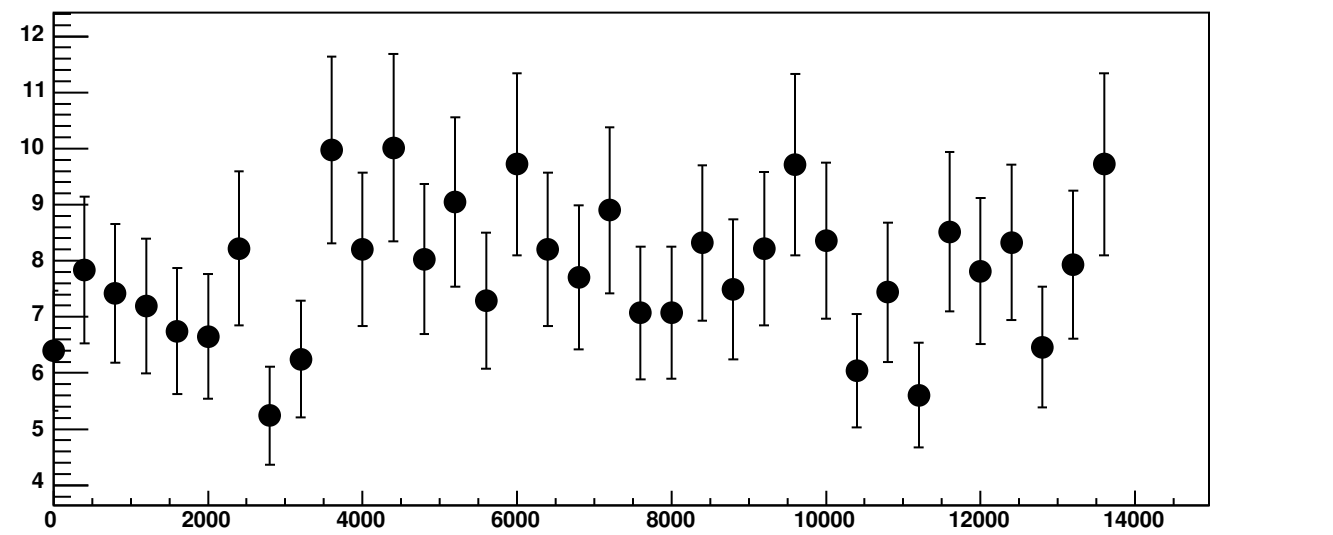

Chip 8, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

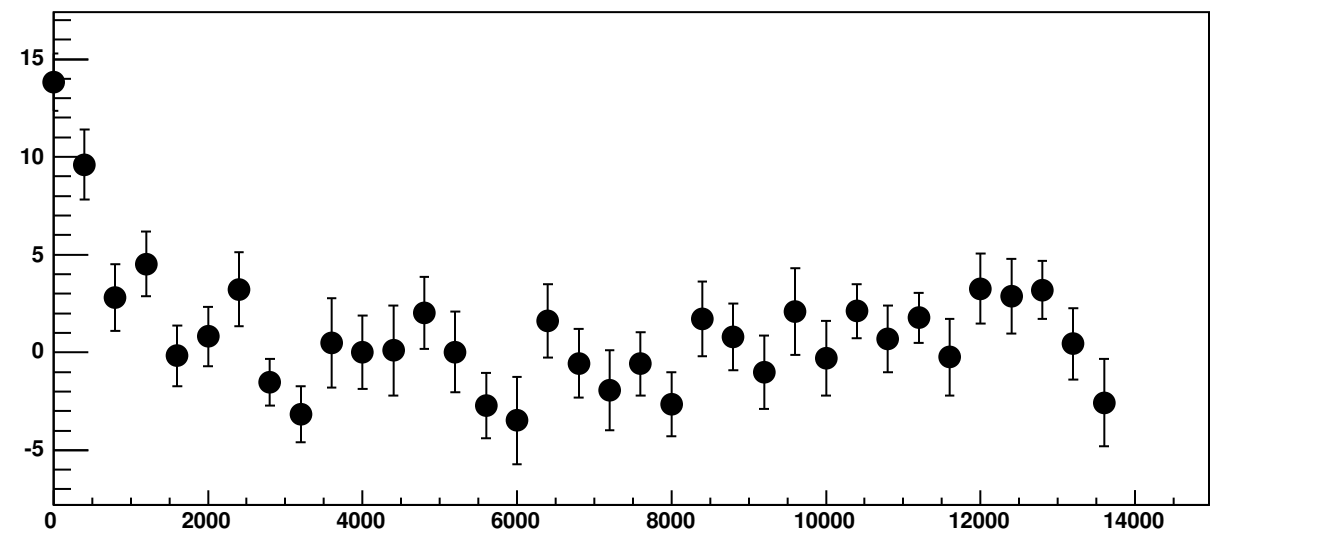
Chip 8, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

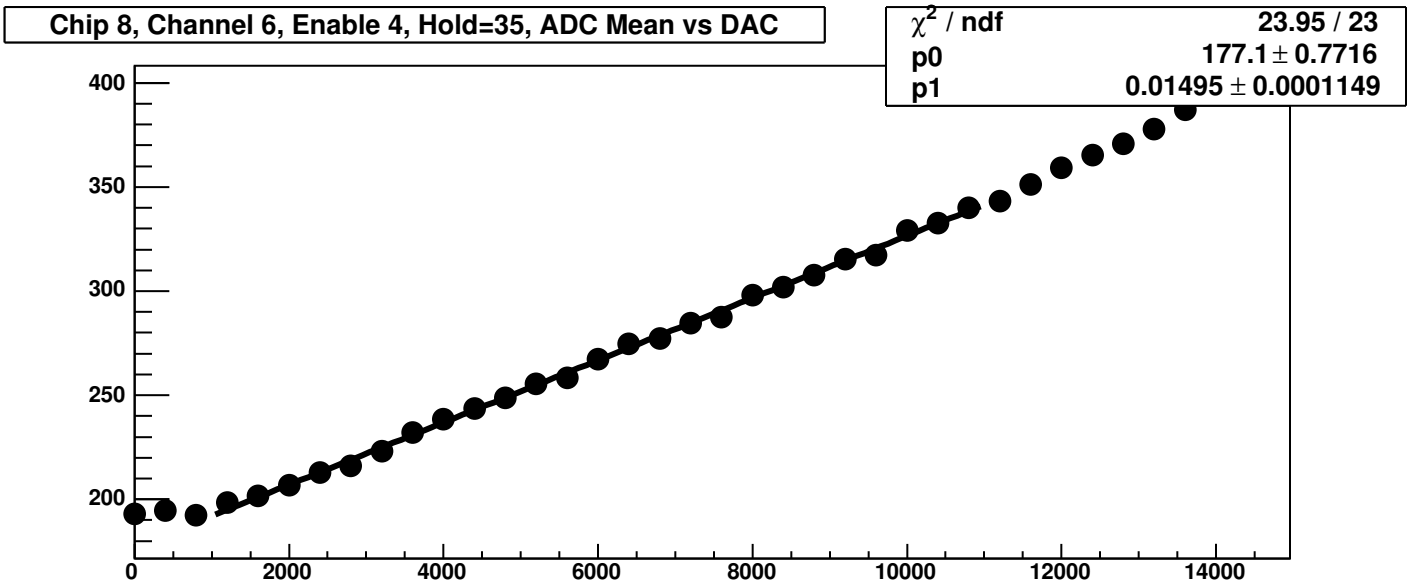


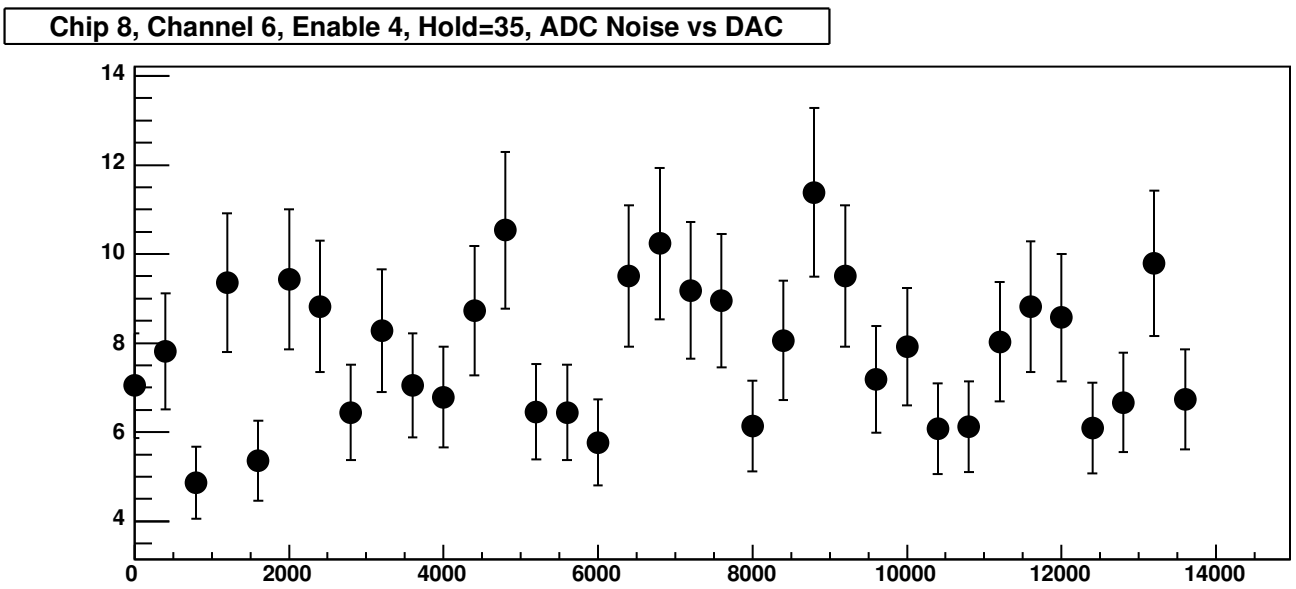
Chip 8, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

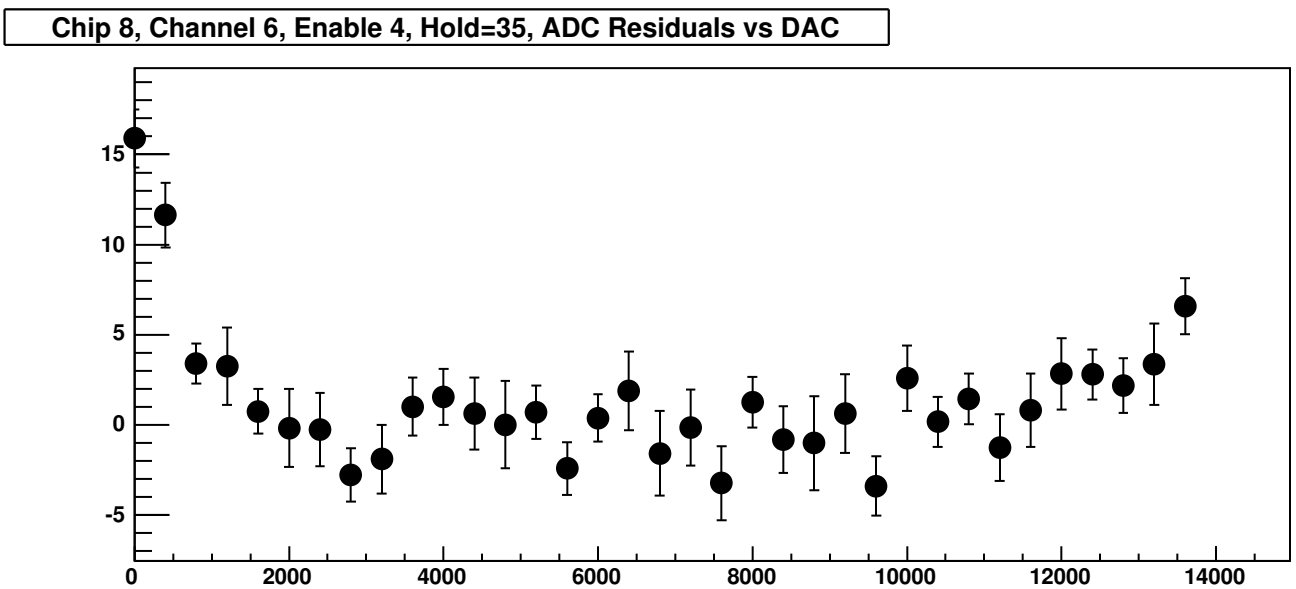


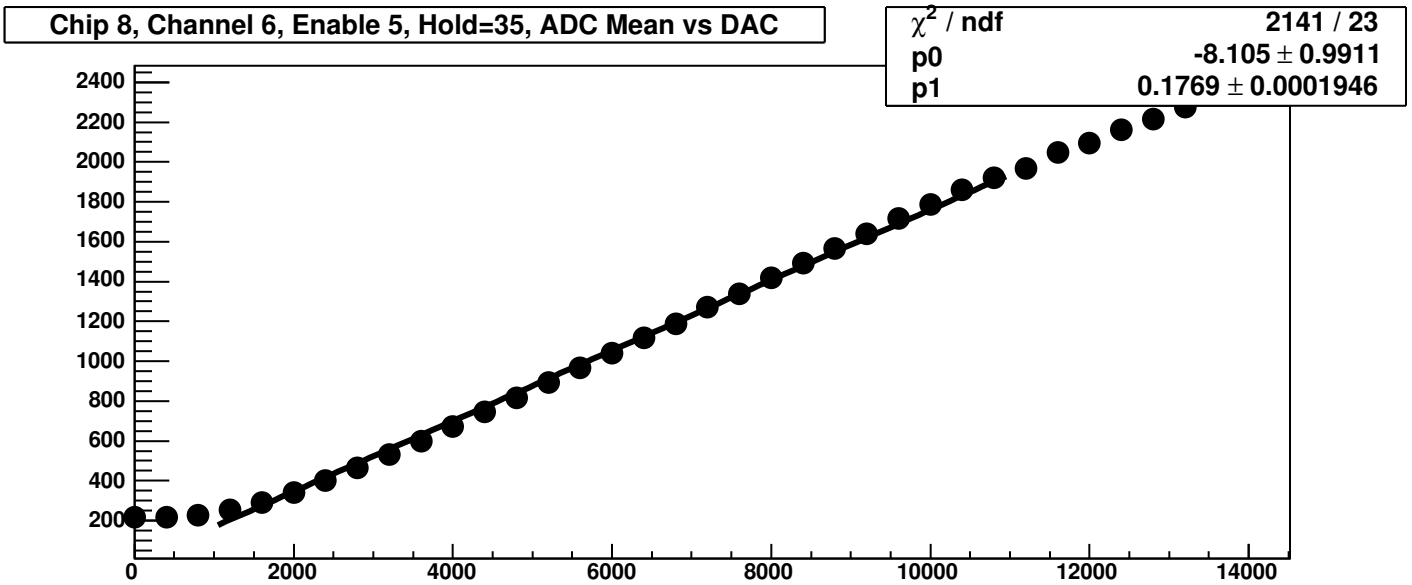
Chip 8, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

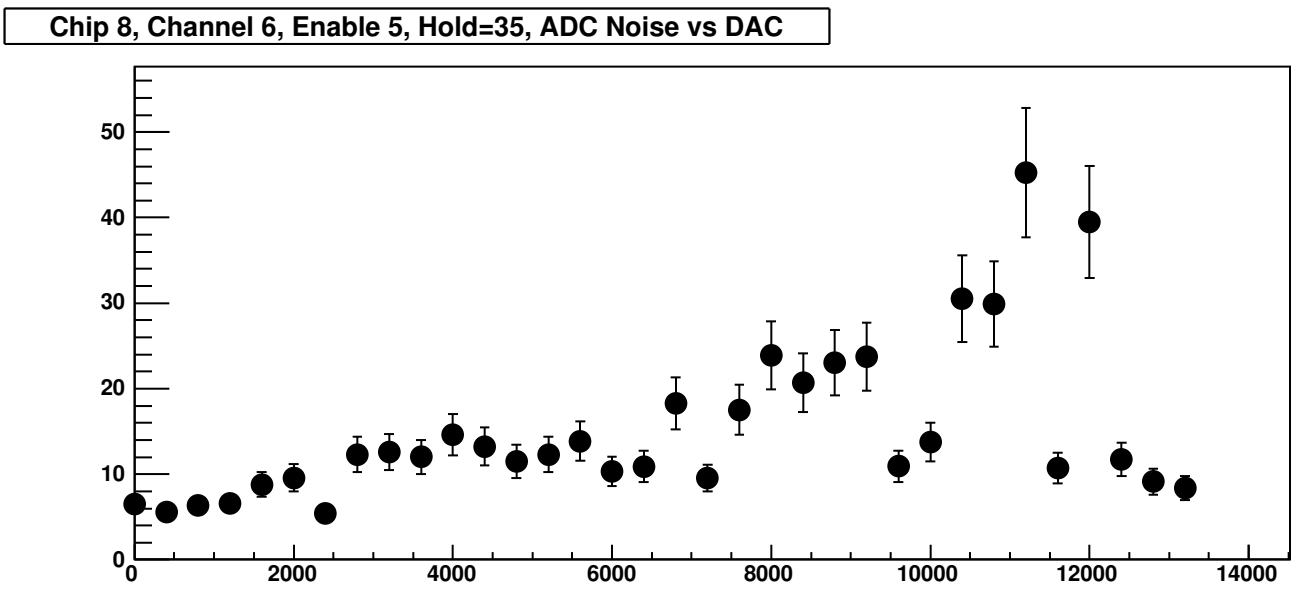


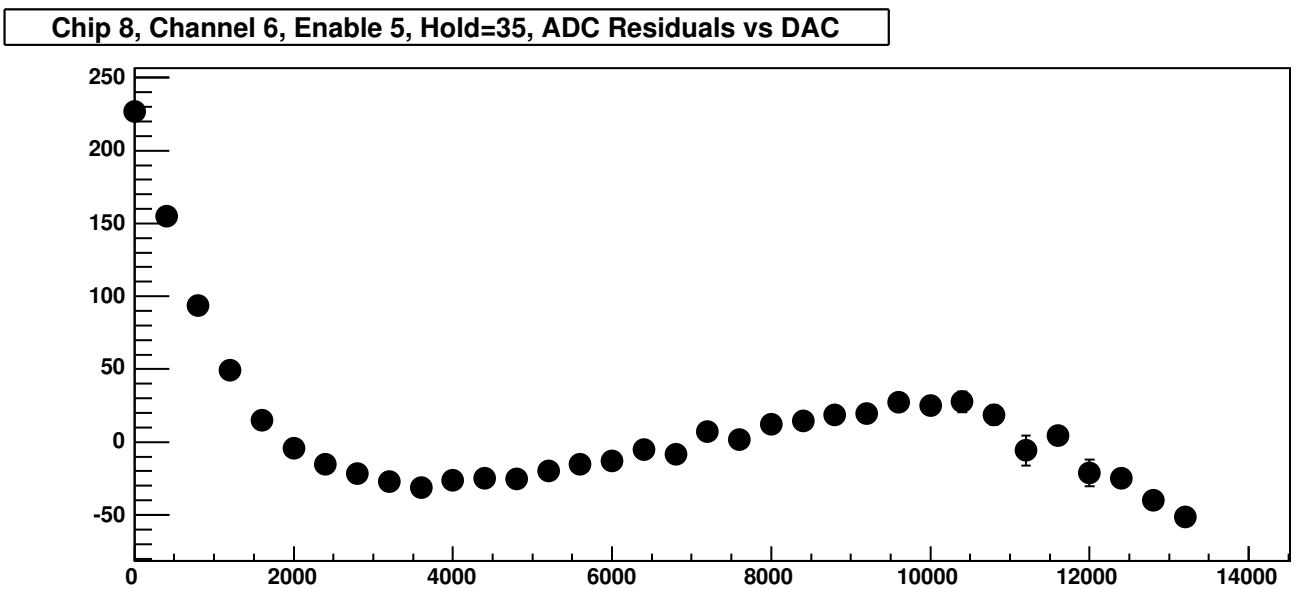

Chip 8, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.95 / 23
p0 177.1 ± 0.7716
p1 0.01495 ± 0.0001149
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

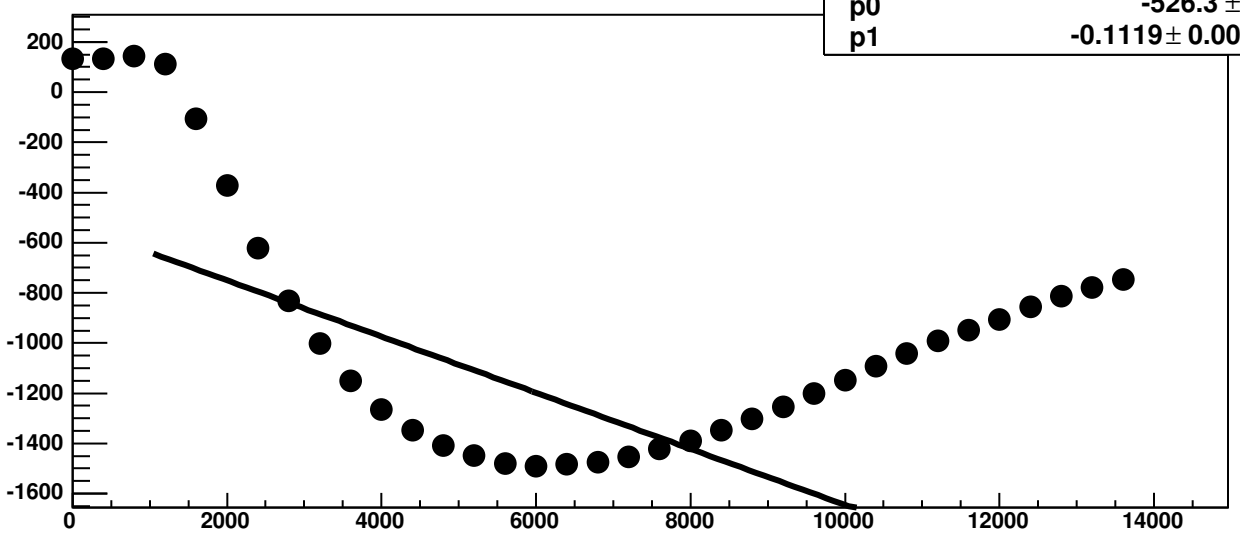

Chip 8, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2141 / 23
p0 -8.105 ± 0.9911
p1 0.1769 ± 0.0001946
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

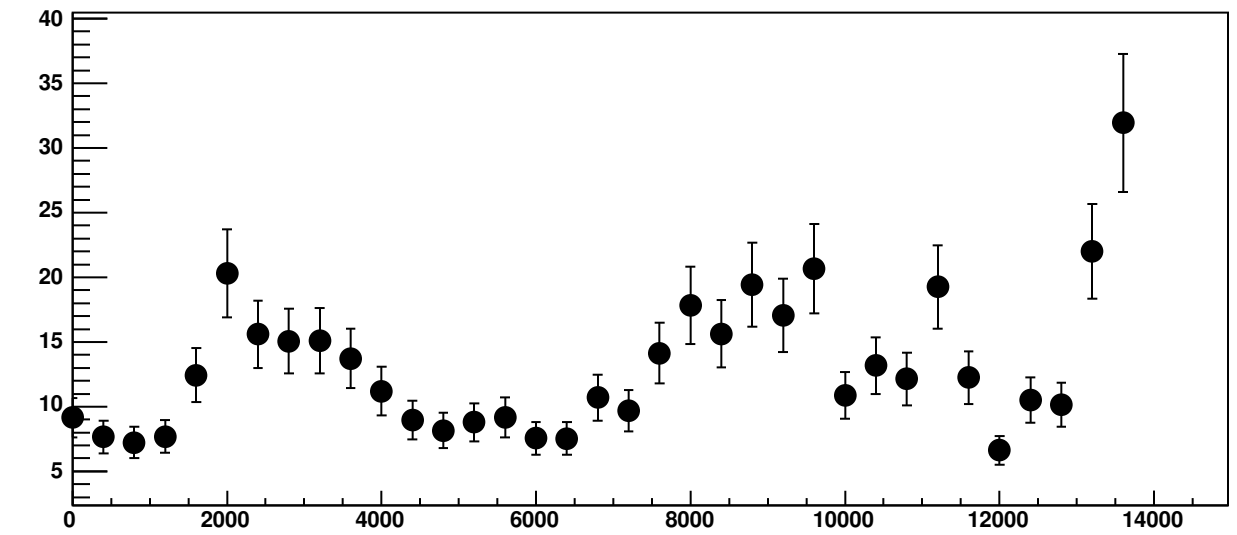

Chip 8, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

Chip 8, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

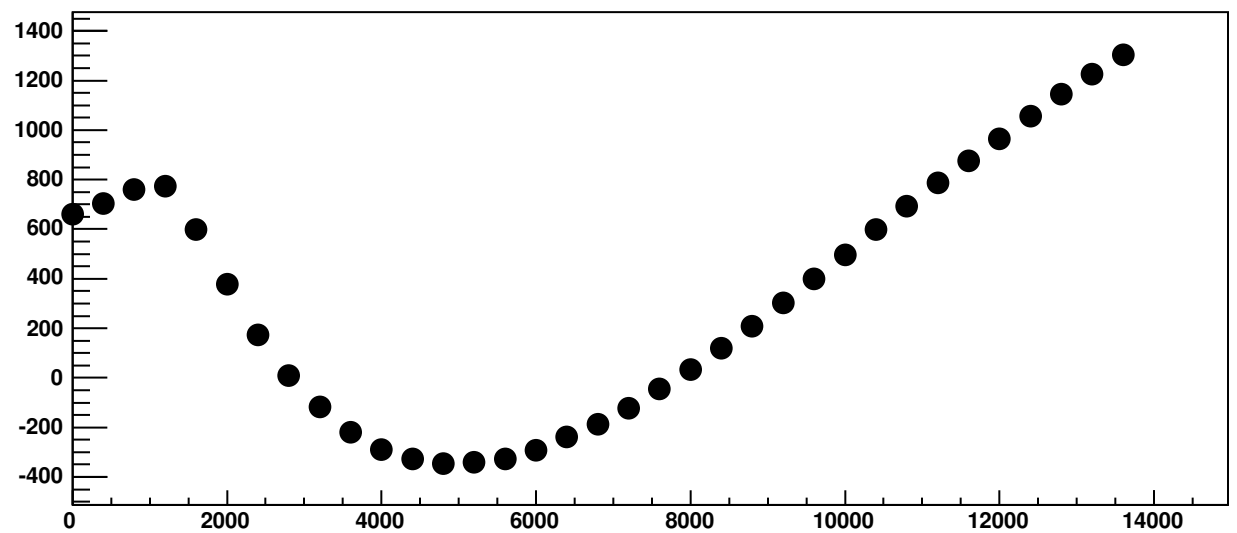
χ^2 / ndf	5.891e+05 / 23
p0	-526.3 ± 1.226
p1	-0.1119 ± 0.0001976



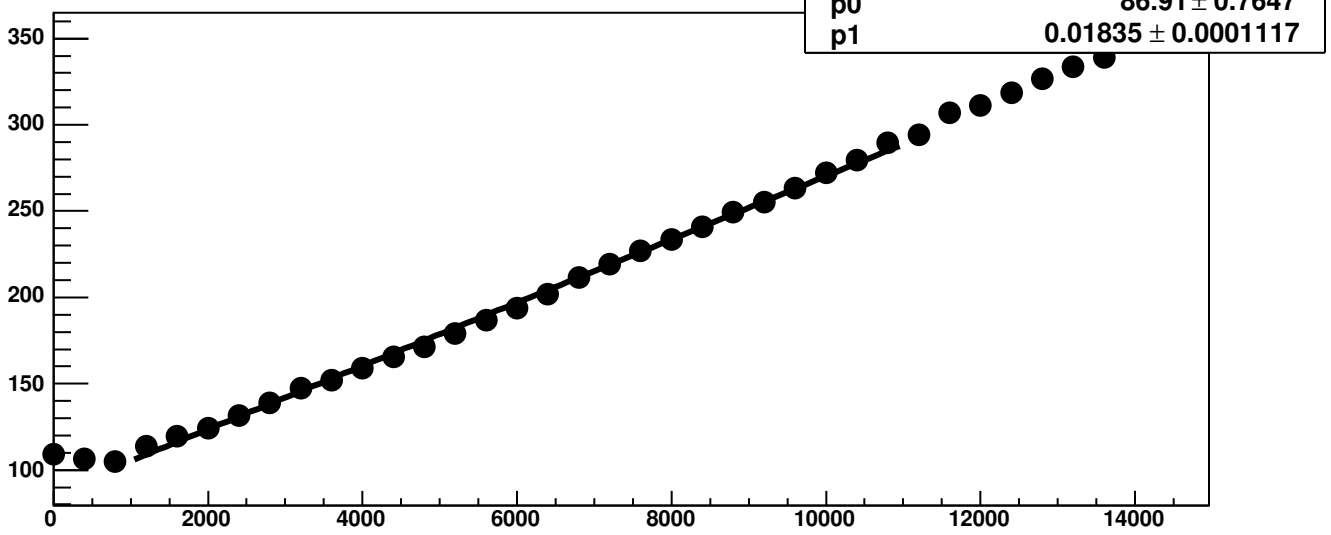
Chip 8, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



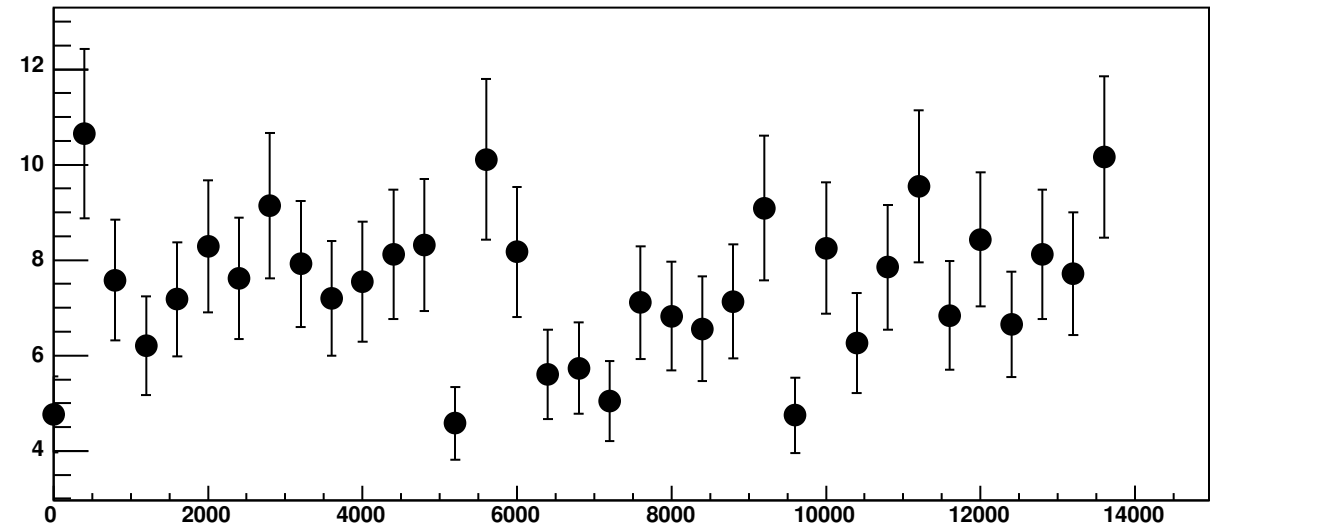
Chip 8, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



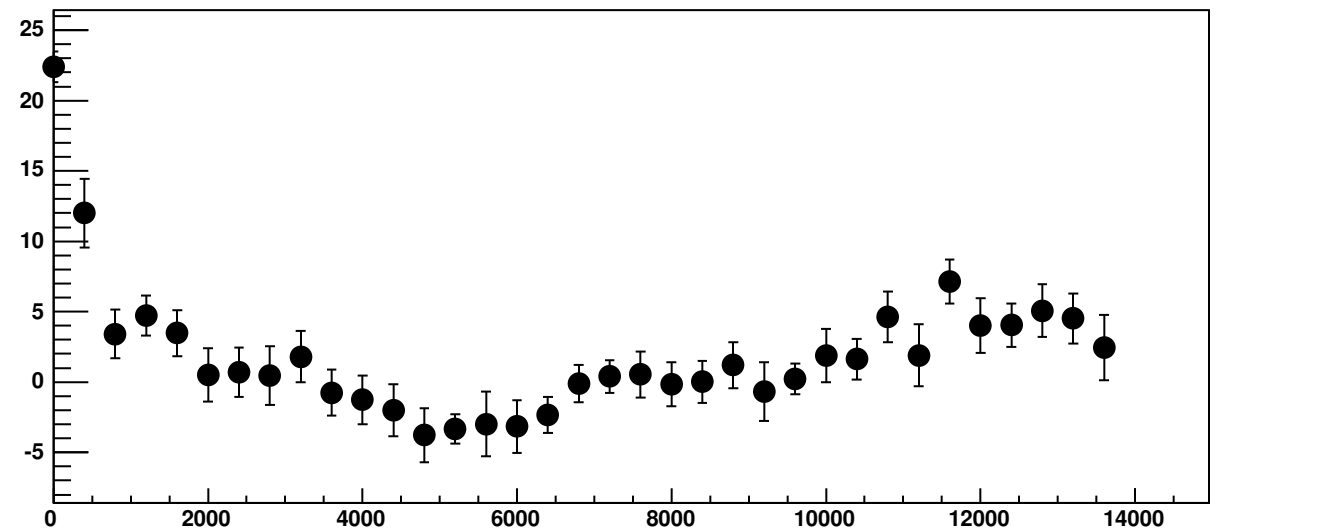
Chip 8, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

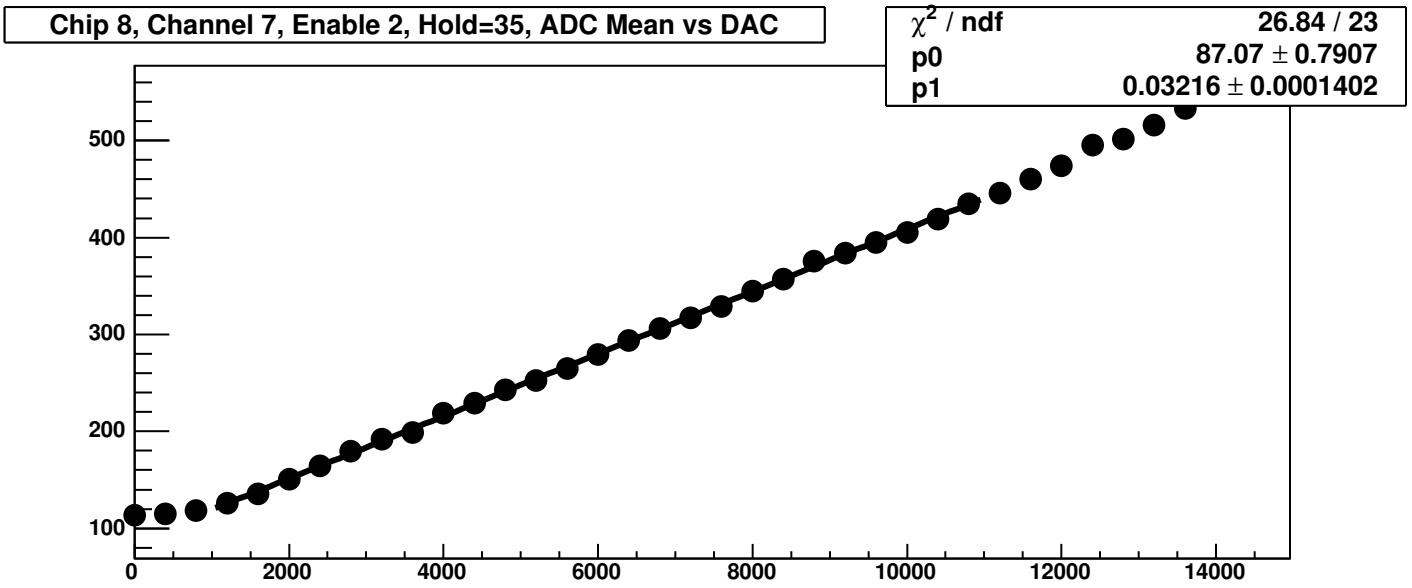


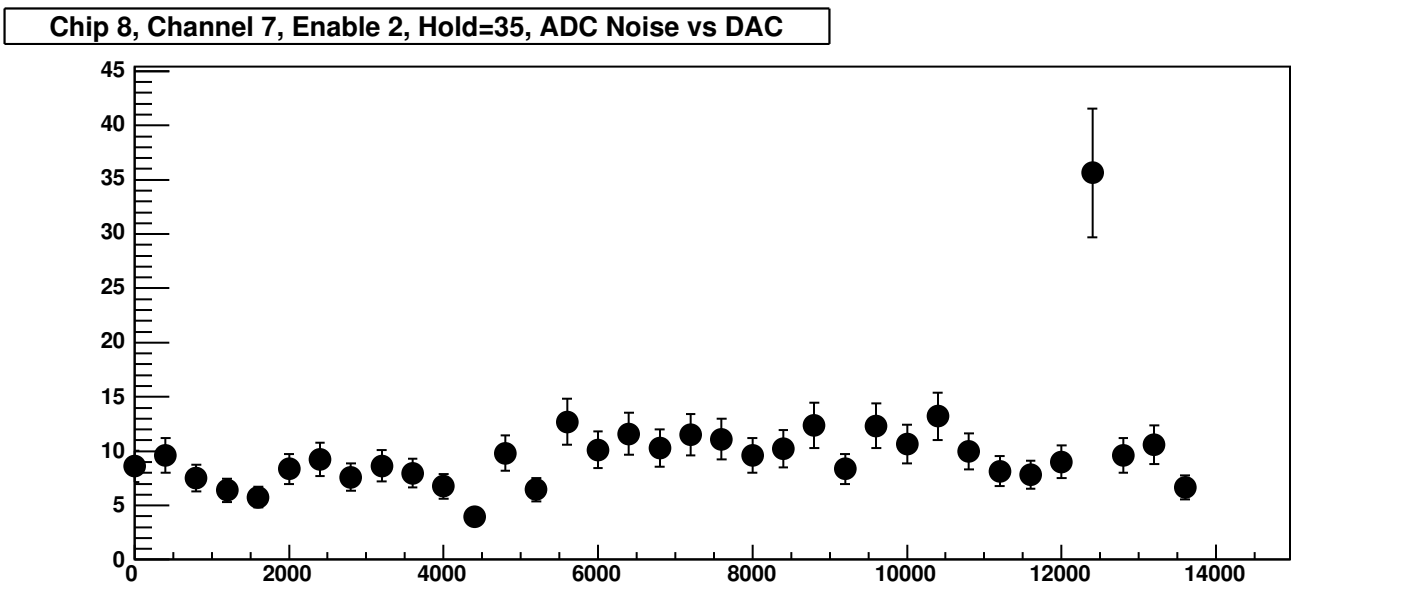
Chip 8, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

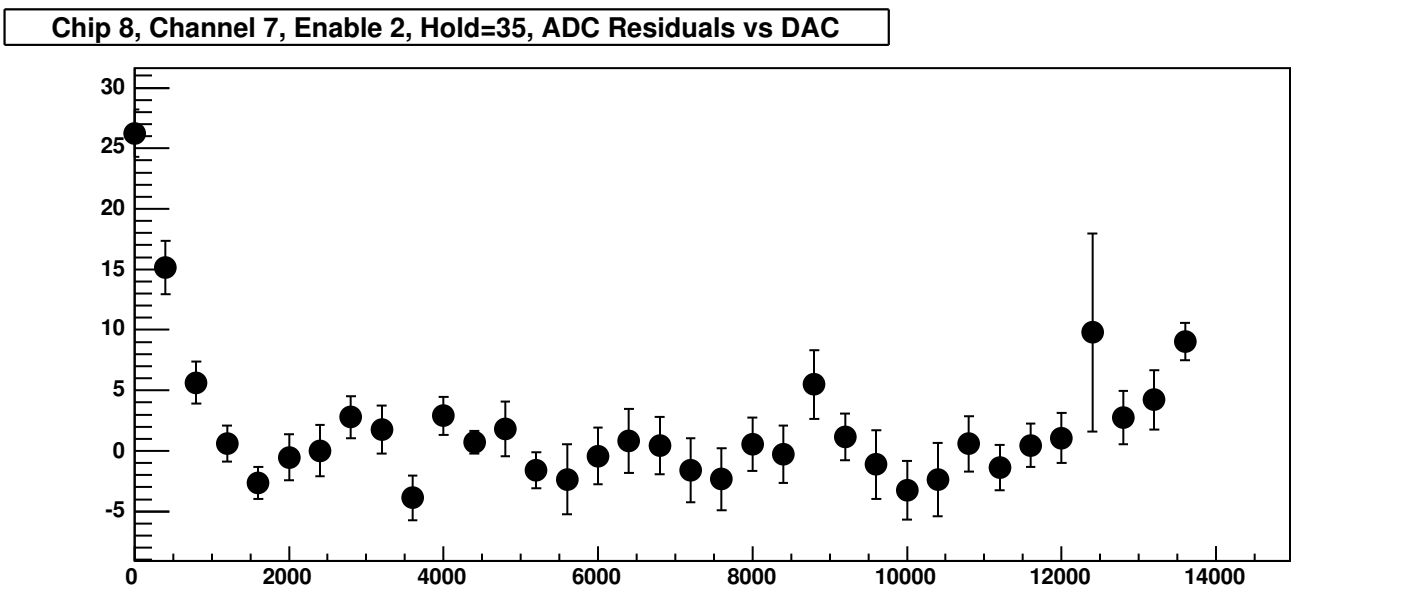


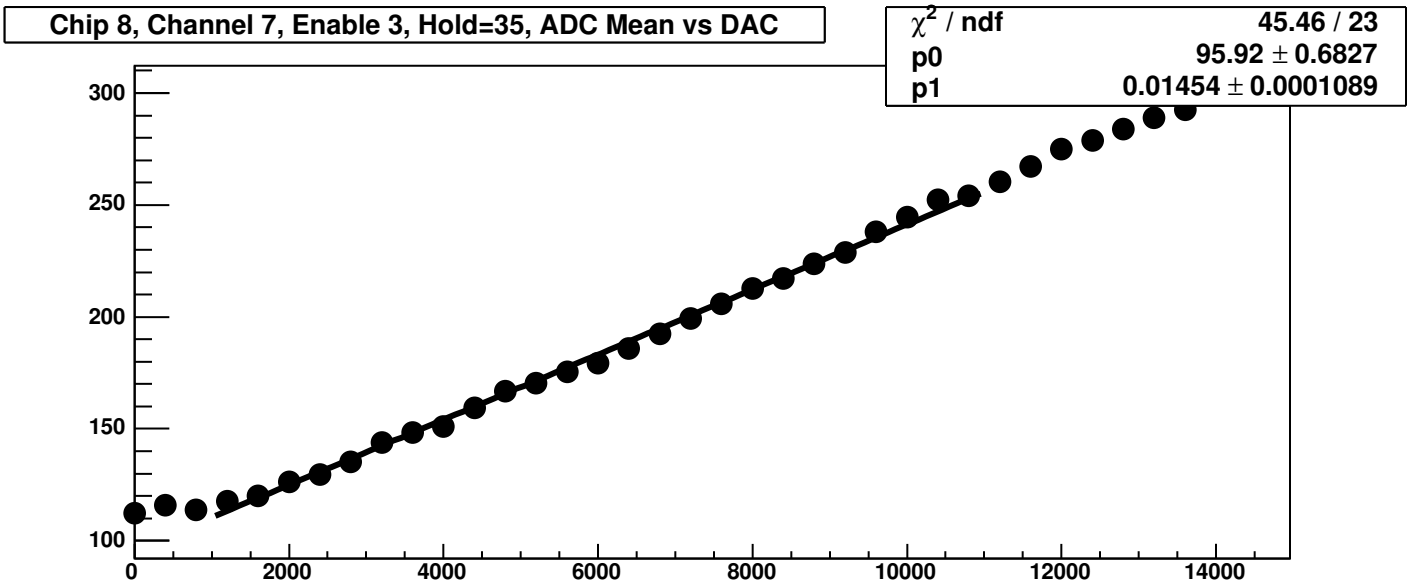
Chip 8, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

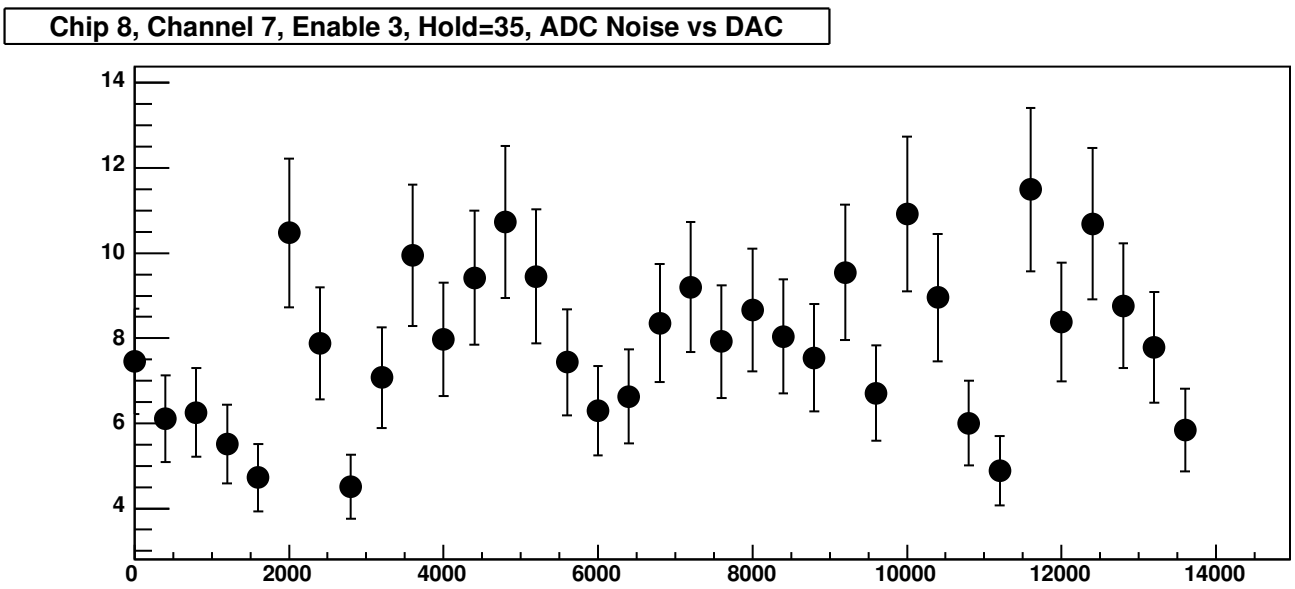


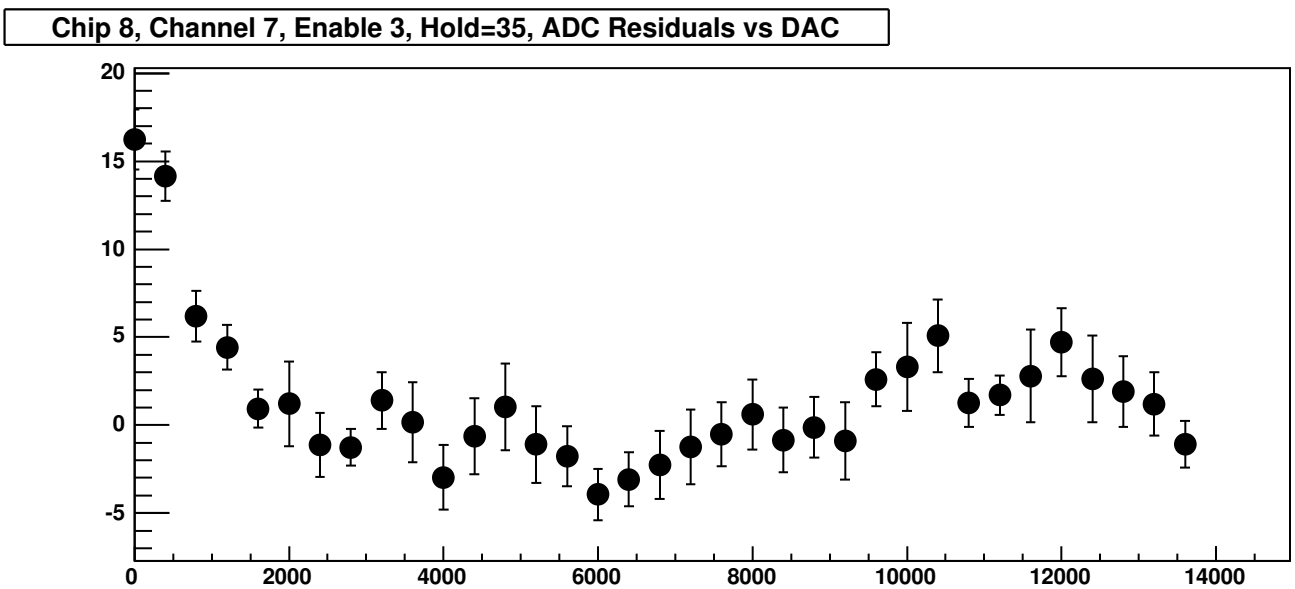

Chip 8, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.84 / 23
p0 87.07 ± 0.7907
p1 0.03216 ± 0.0001402
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

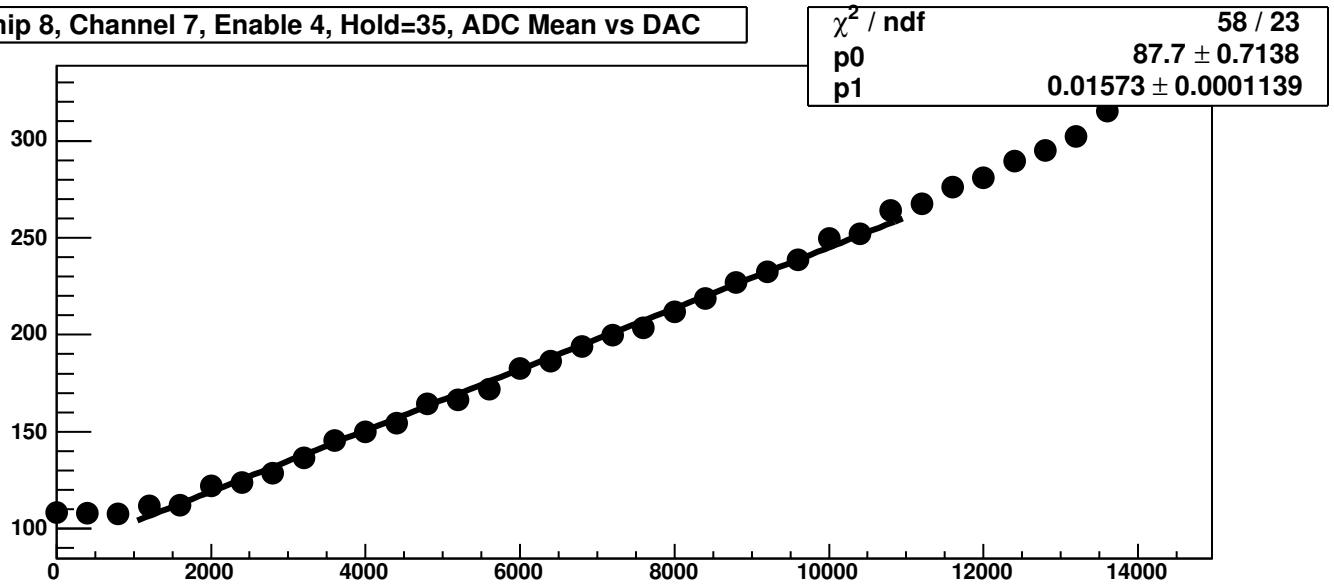

Chip 8, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.46 / 23
p0 95.92 ± 0.6827
p1 0.01454 ± 0.0001089
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

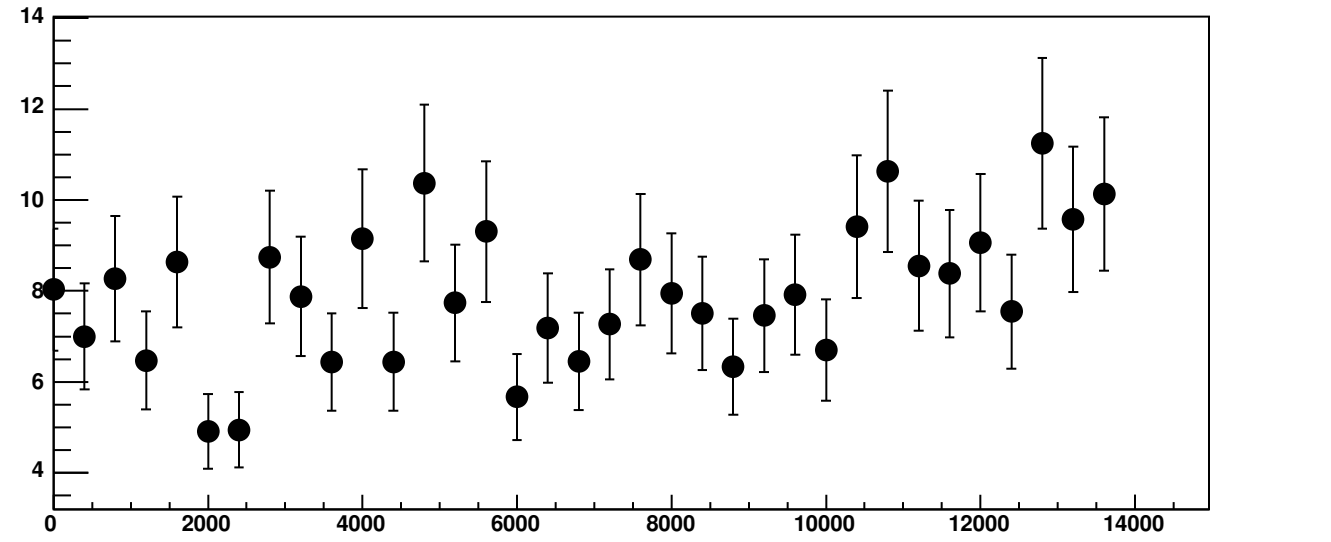

Chip 8, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

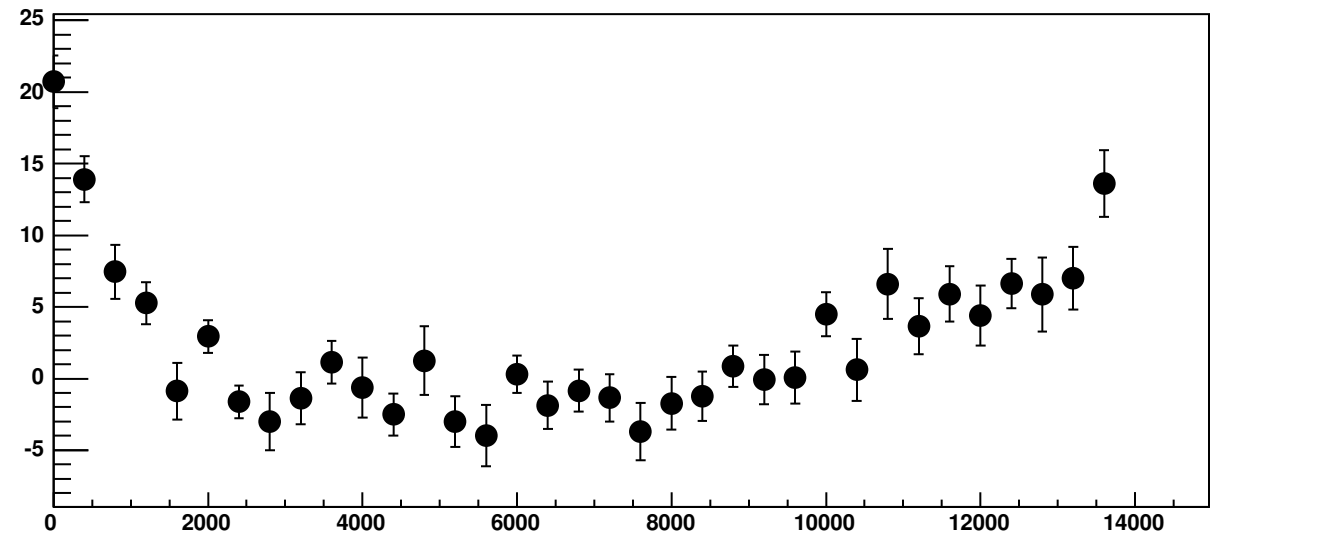
Chip 8, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

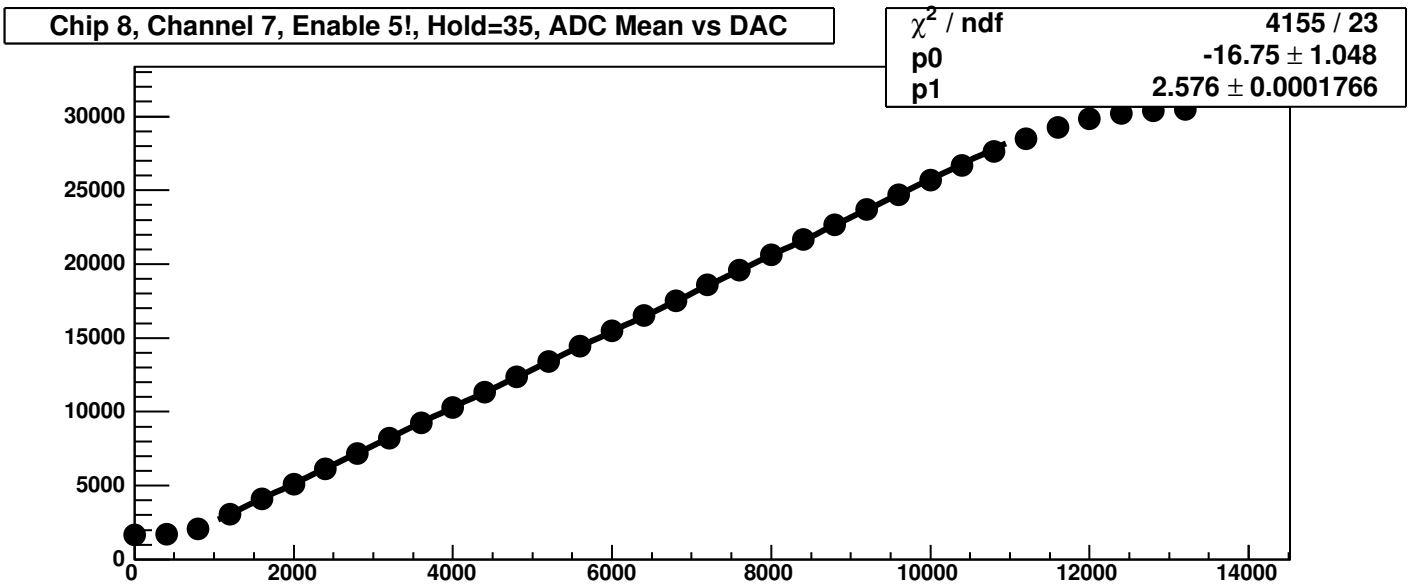


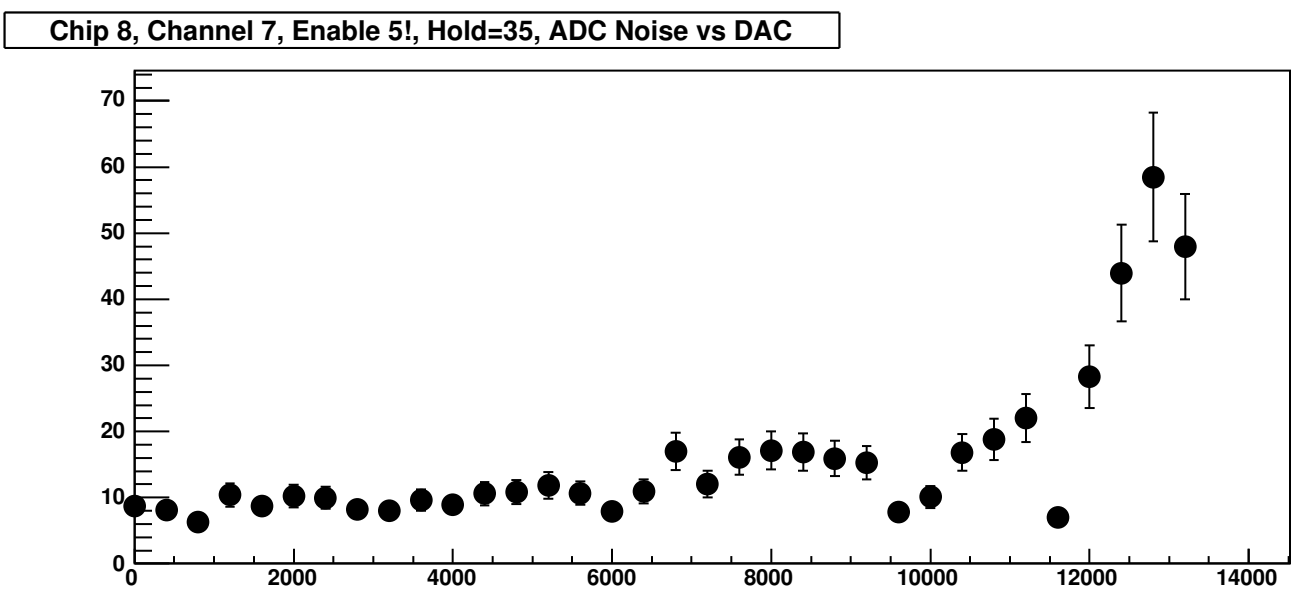
Chip 8, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

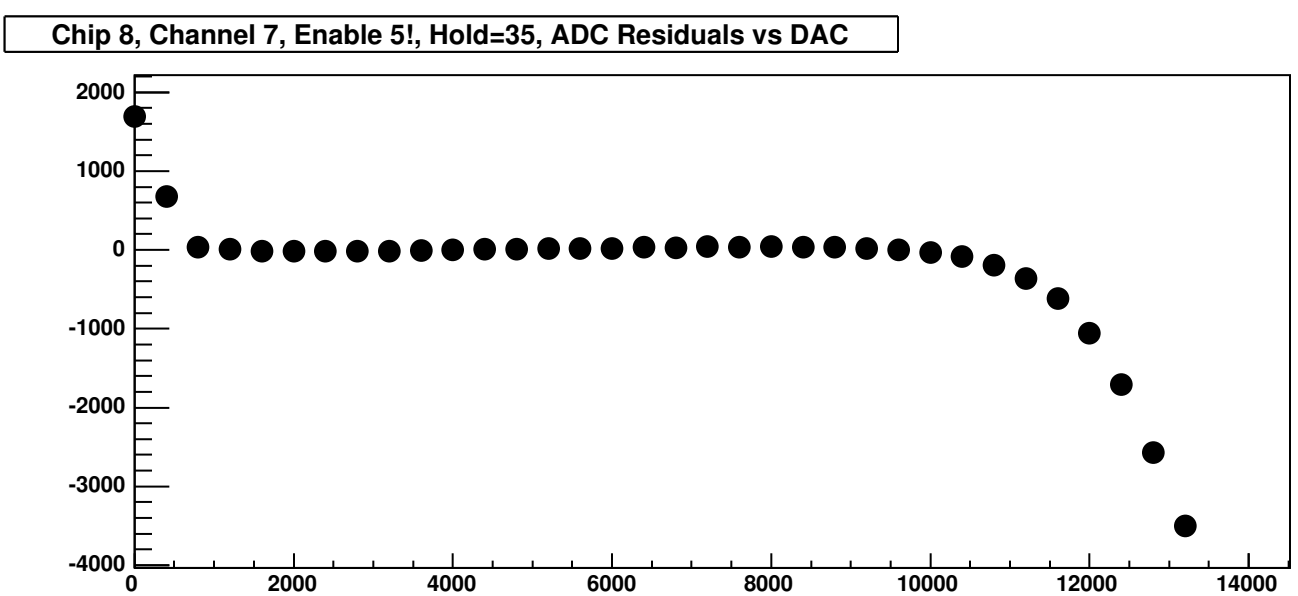


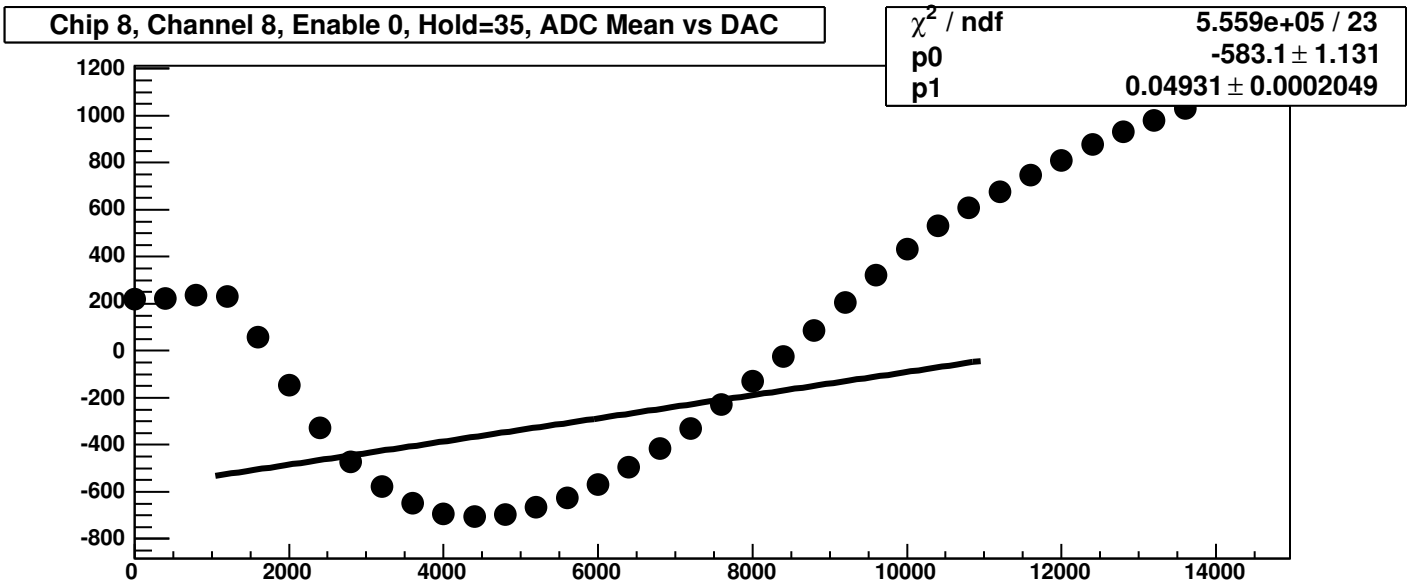
Chip 8, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

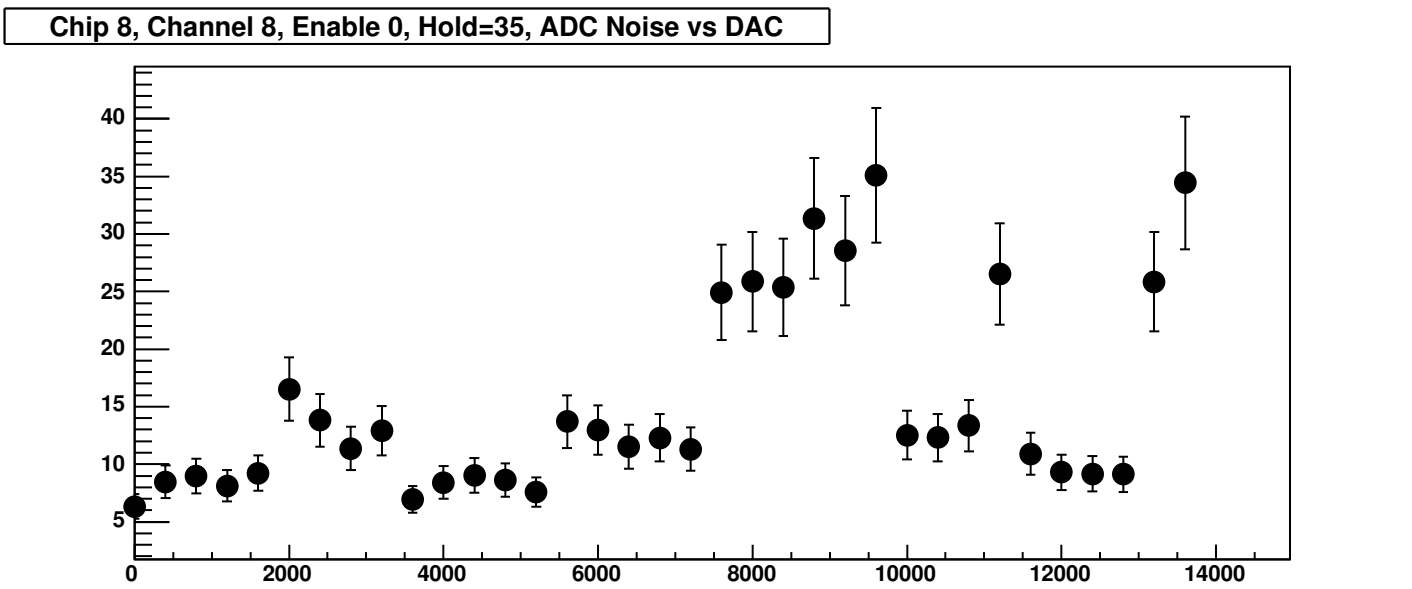


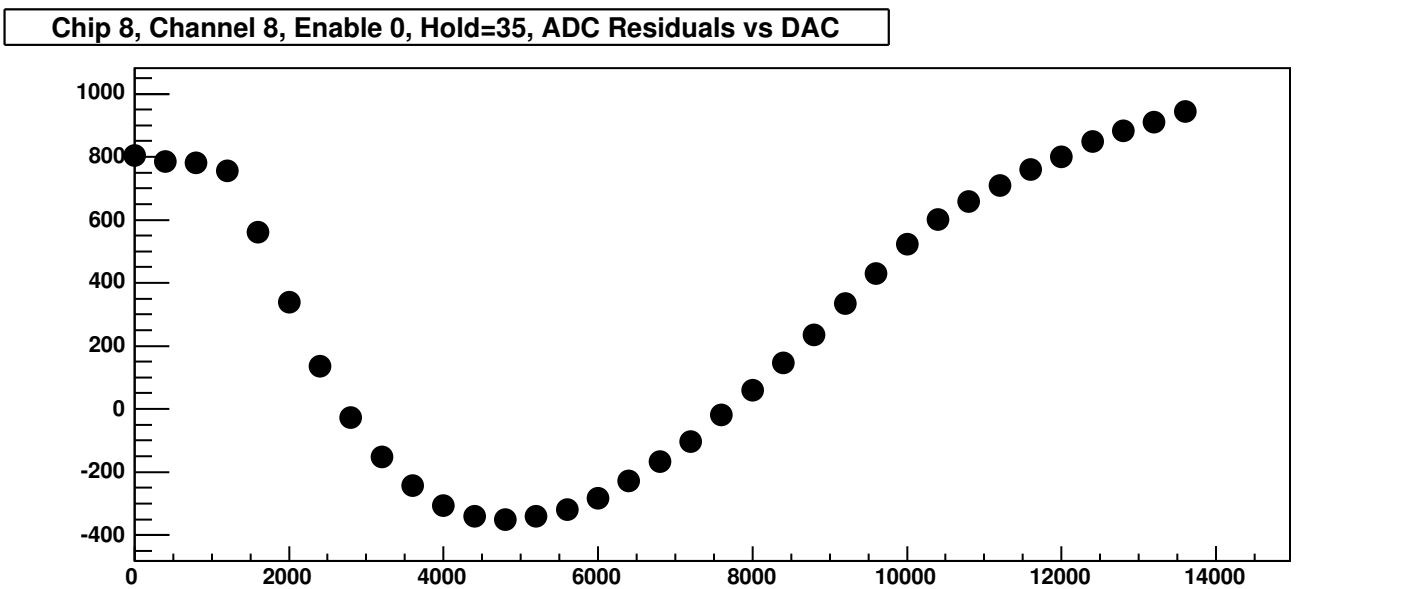

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4155 / 23
p0 -16.75 ± 1.048
p1 2.576 ± 0.0001766
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

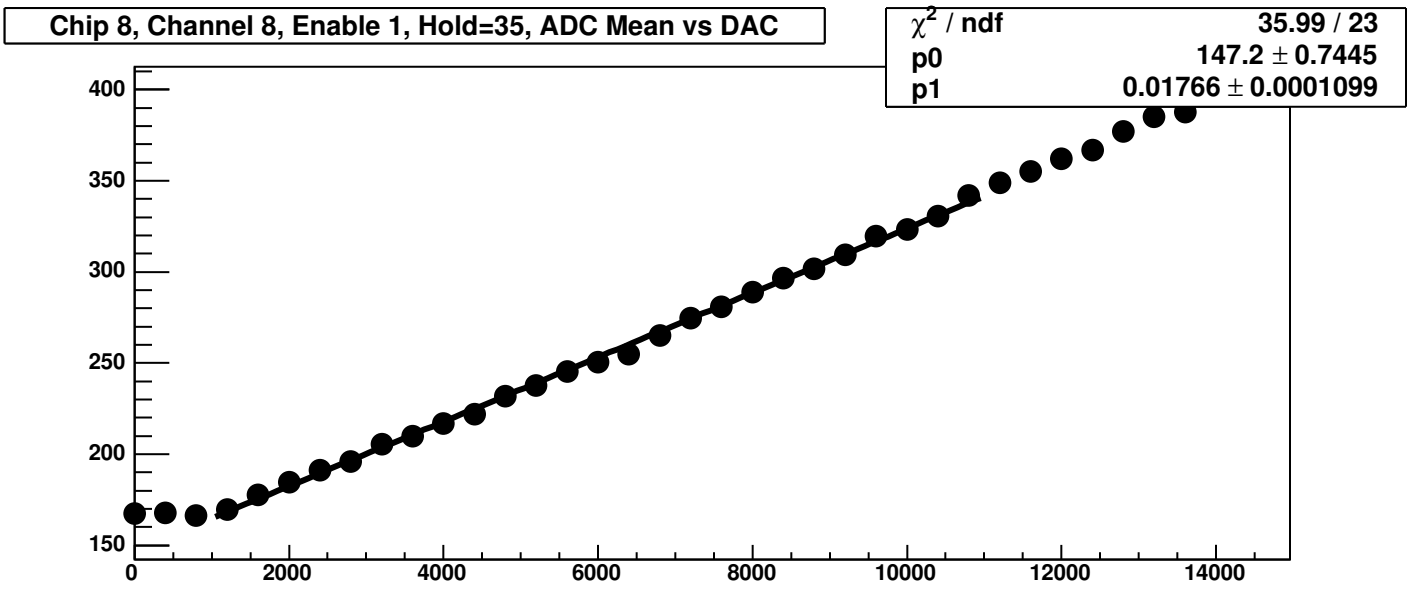

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

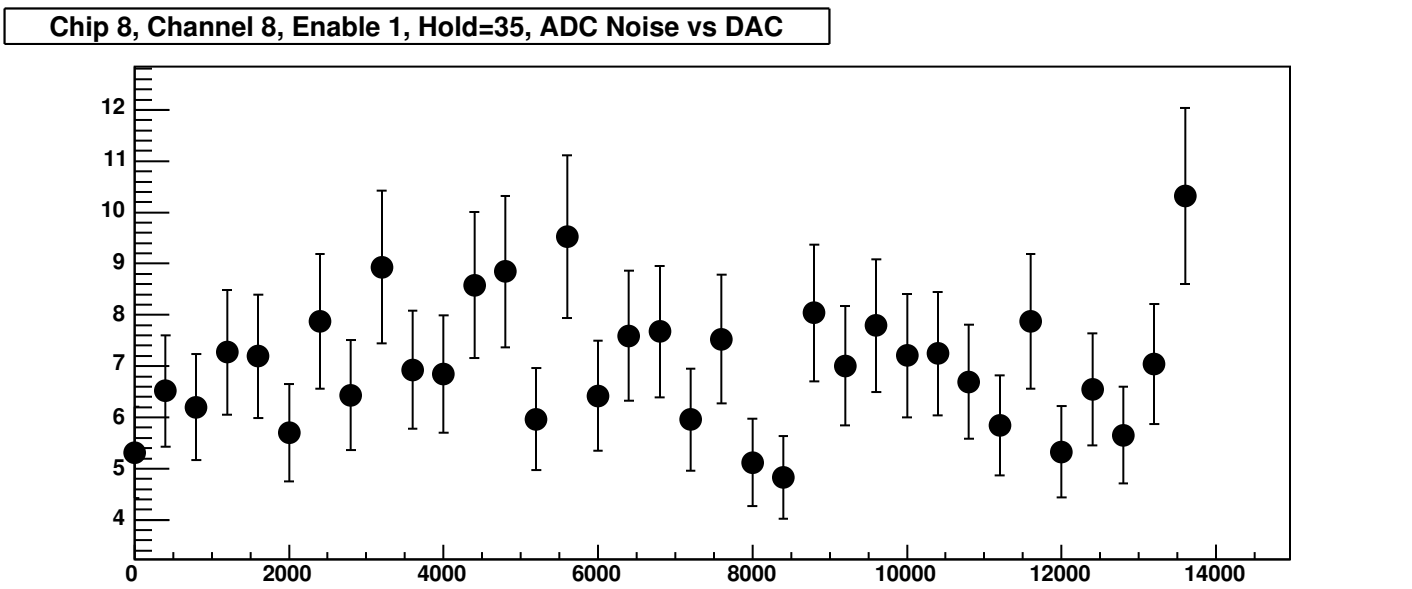

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

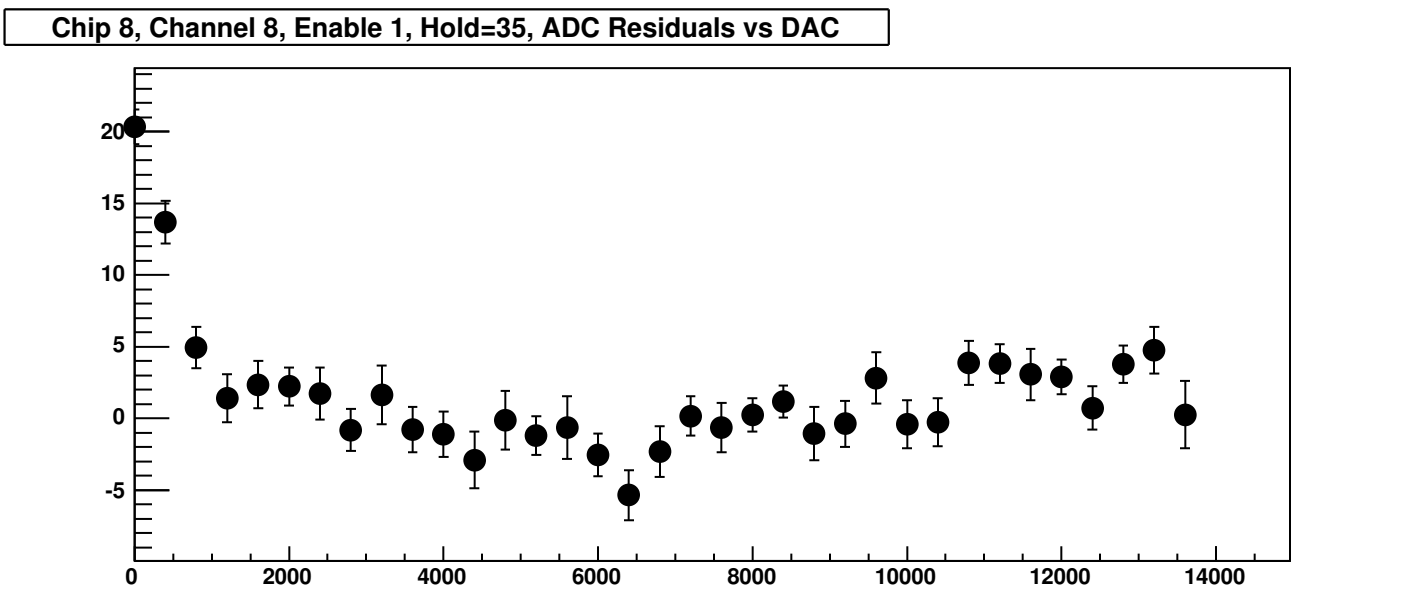

Chip 8, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.559e+05 / 23
p0 -583.1 ± 1.131
p1 0.04931 ± 0.0002049
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

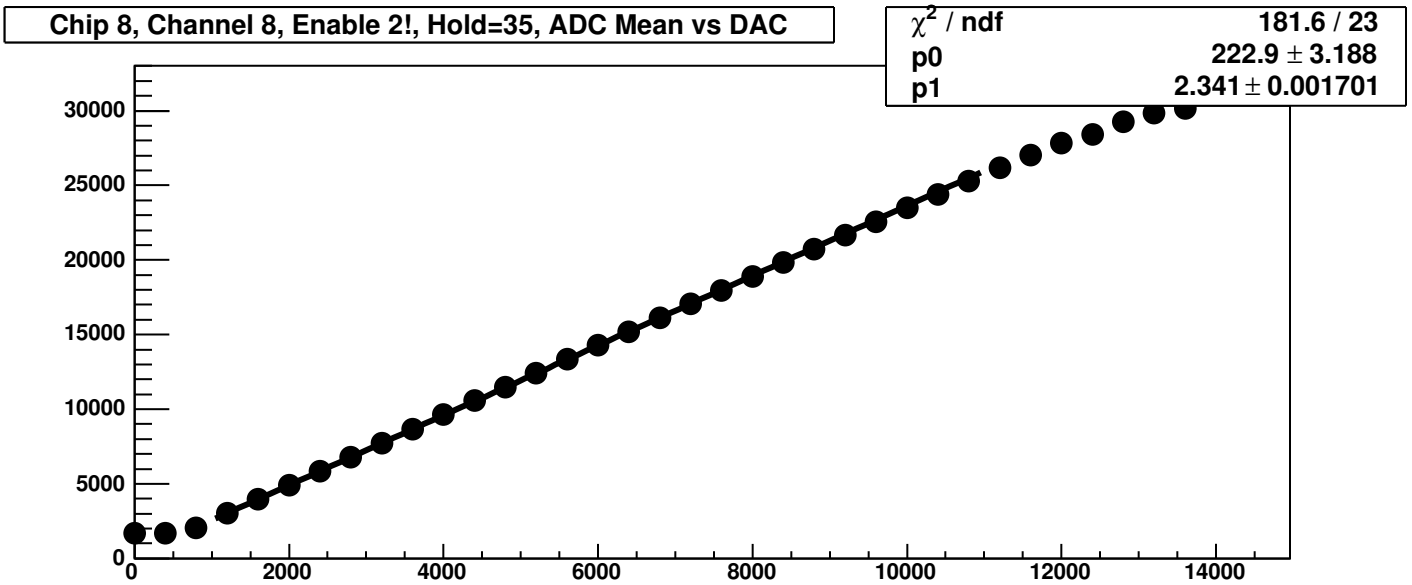

Chip 8, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

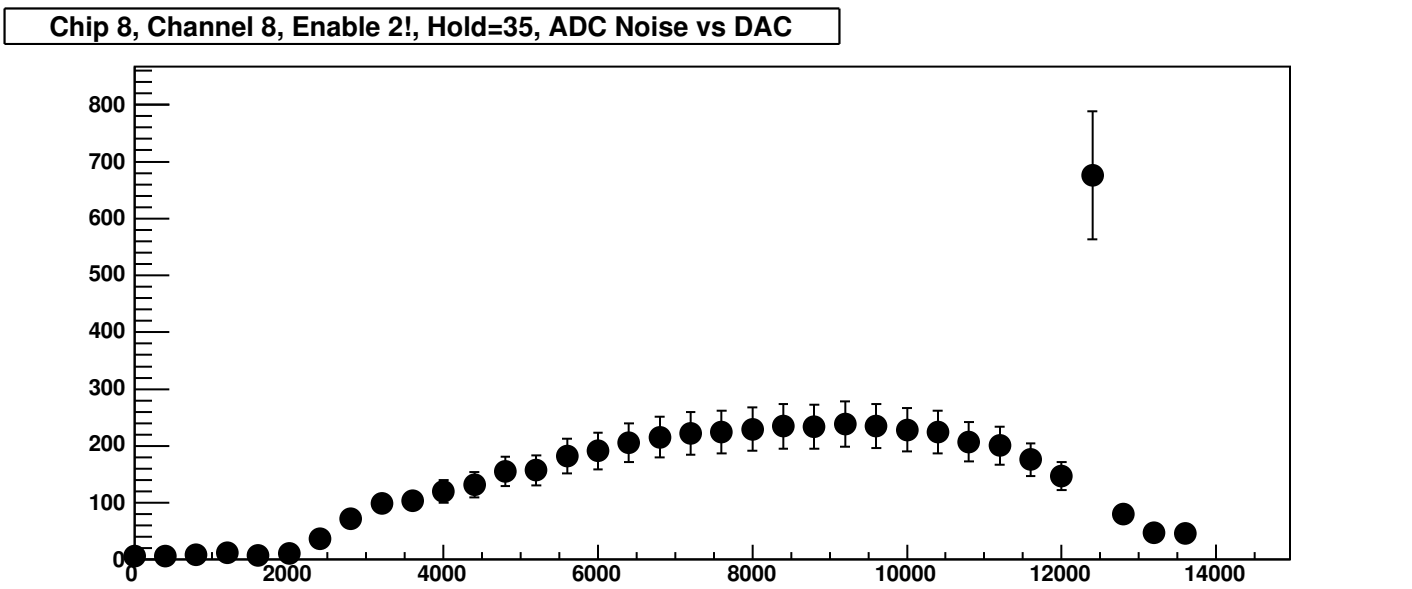

Chip 8, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

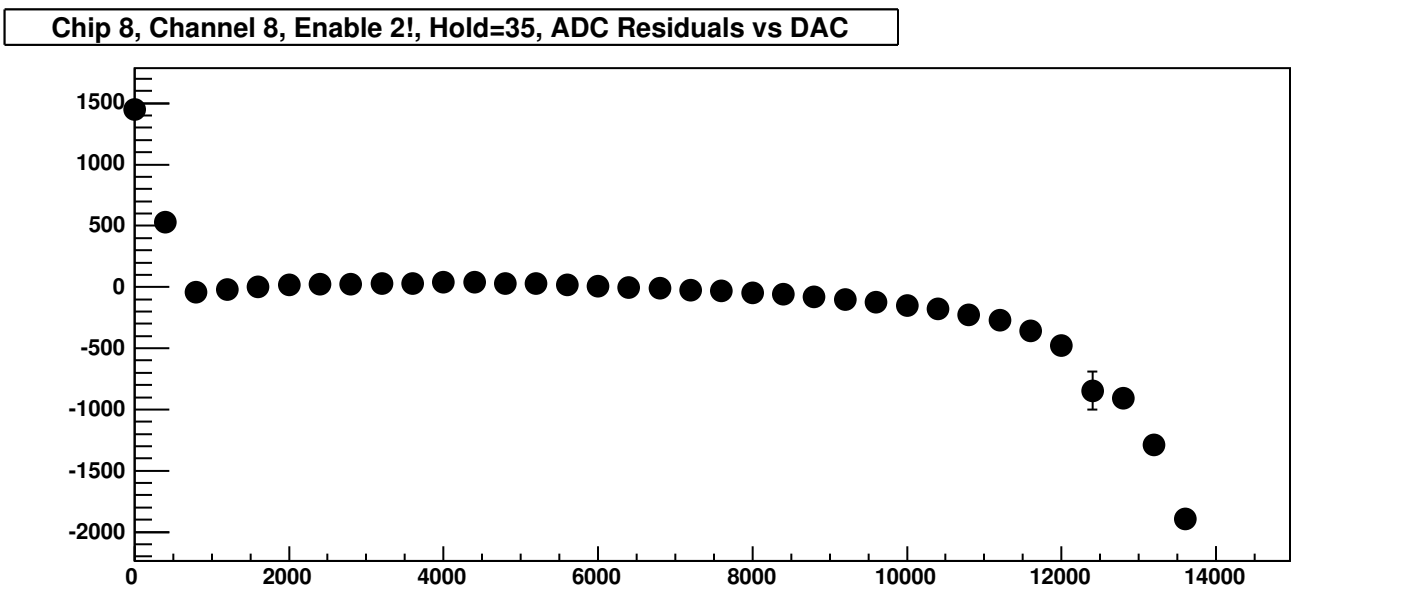

Chip 8, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.99 / 23
p0 147.2 ± 0.7445
p1 0.01766 ± 0.0001099
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

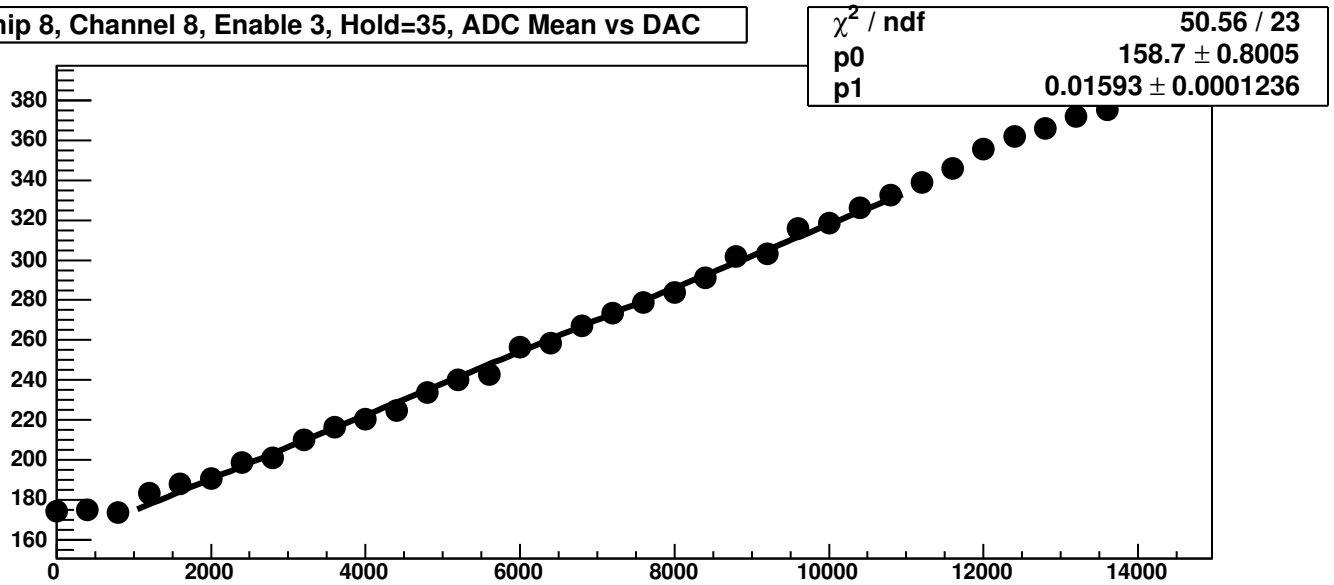

Chip 8, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 181.6 / 23
p0 222.9 ± 3.188
p1 2.341 ± 0.001701
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

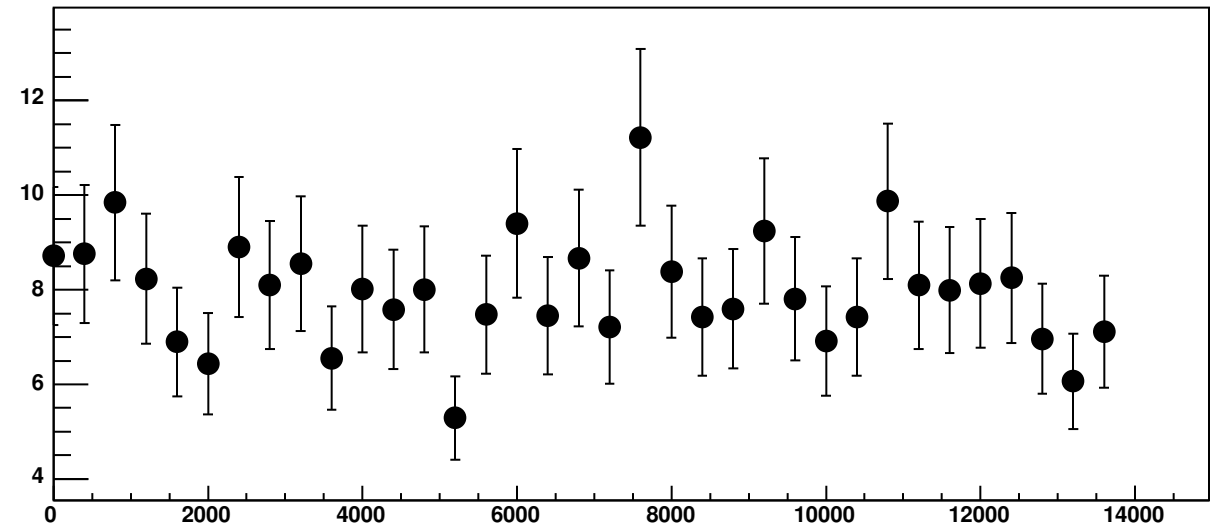

Chip 8, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

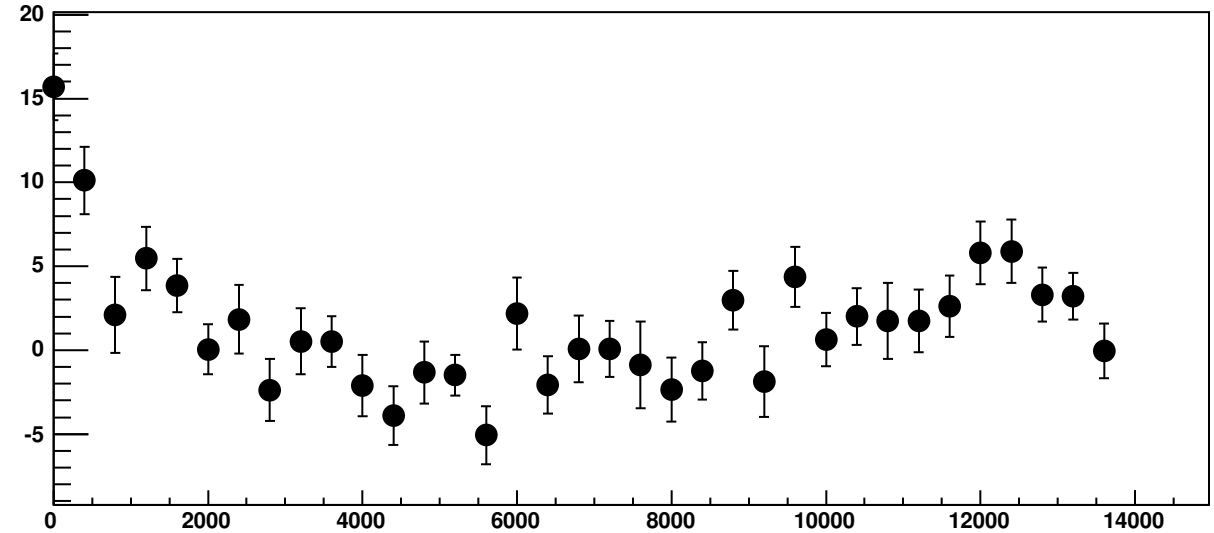
Chip 8, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

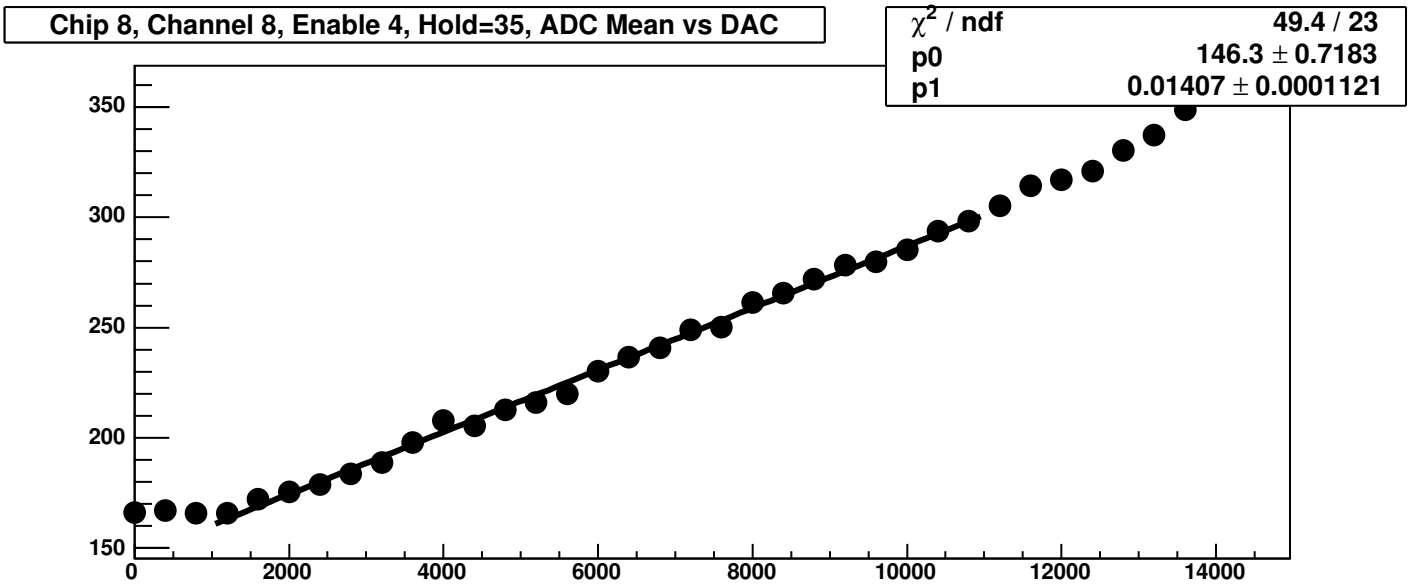


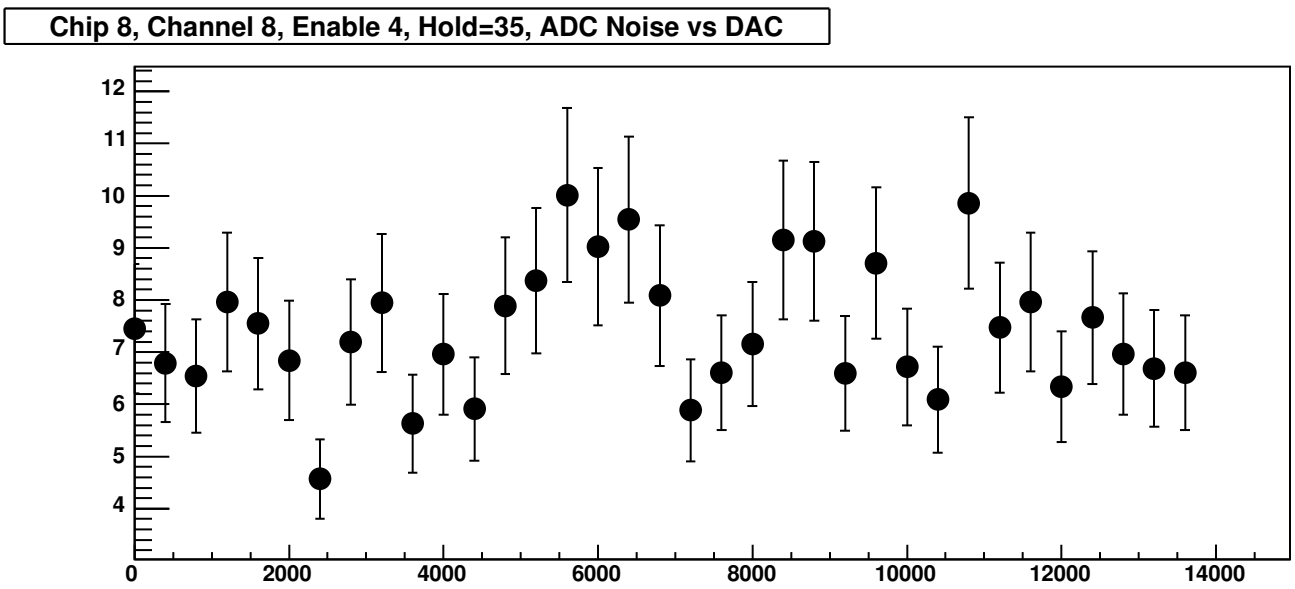
Chip 8, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

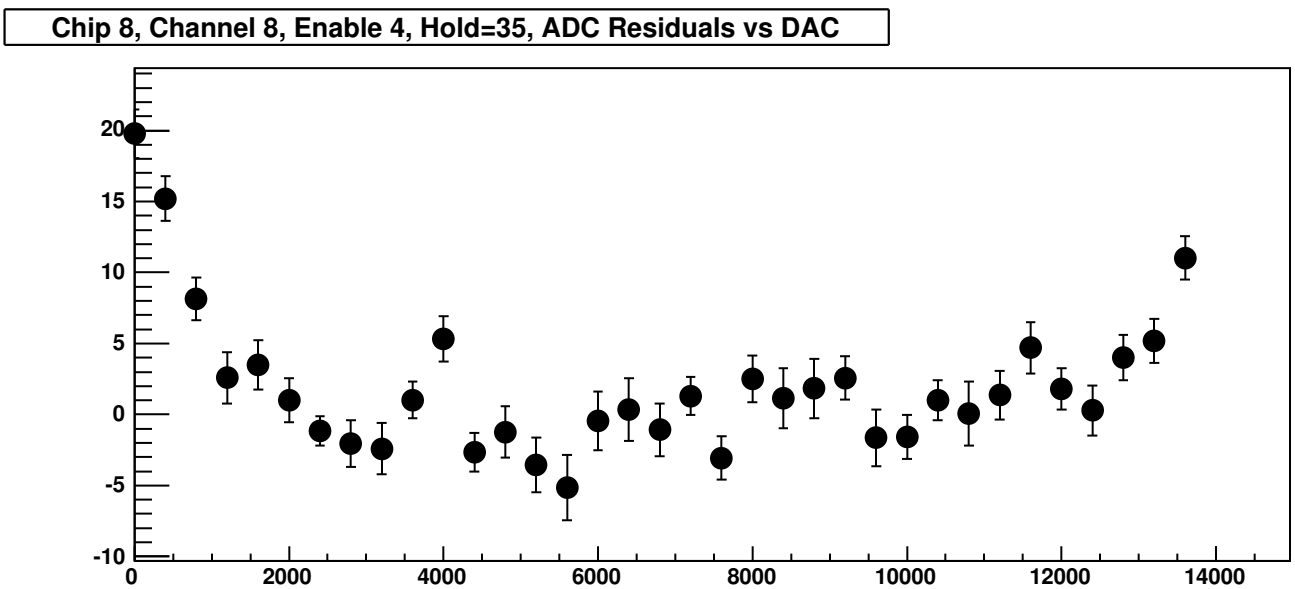


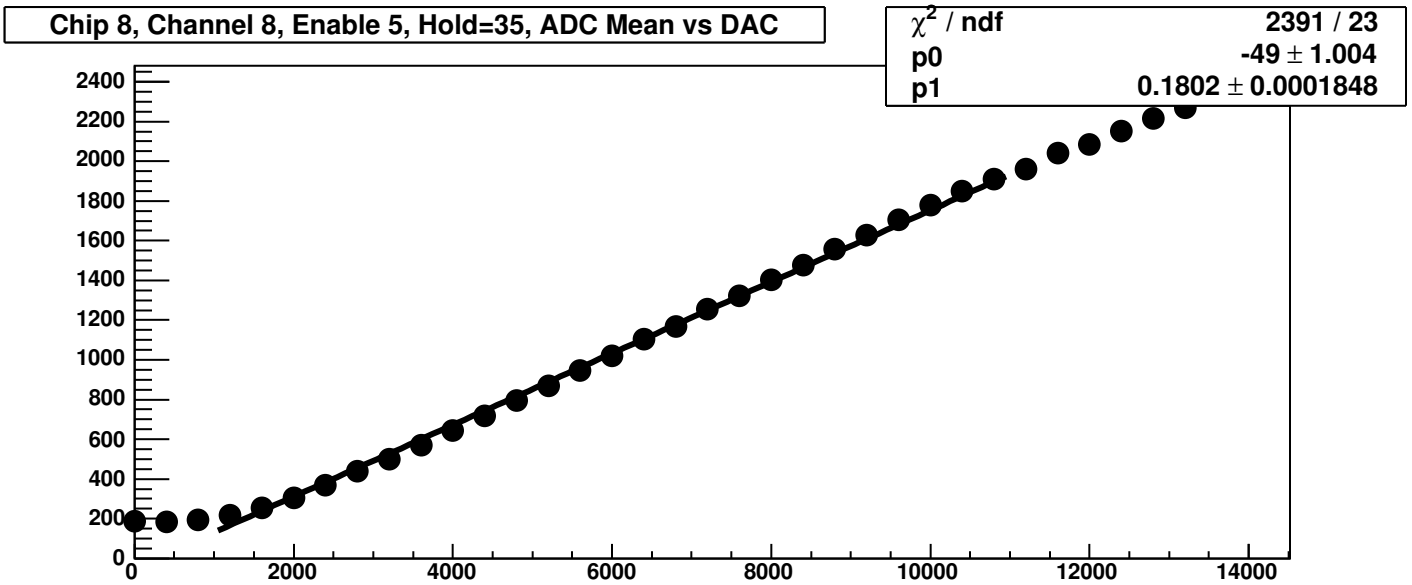
Chip 8, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

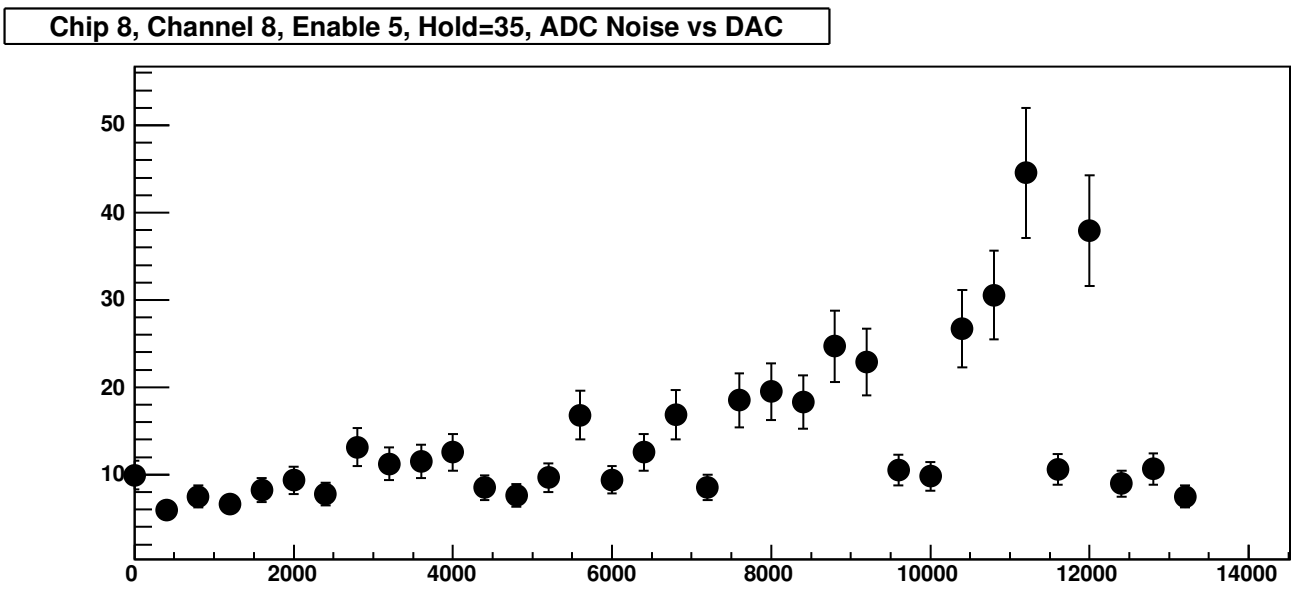


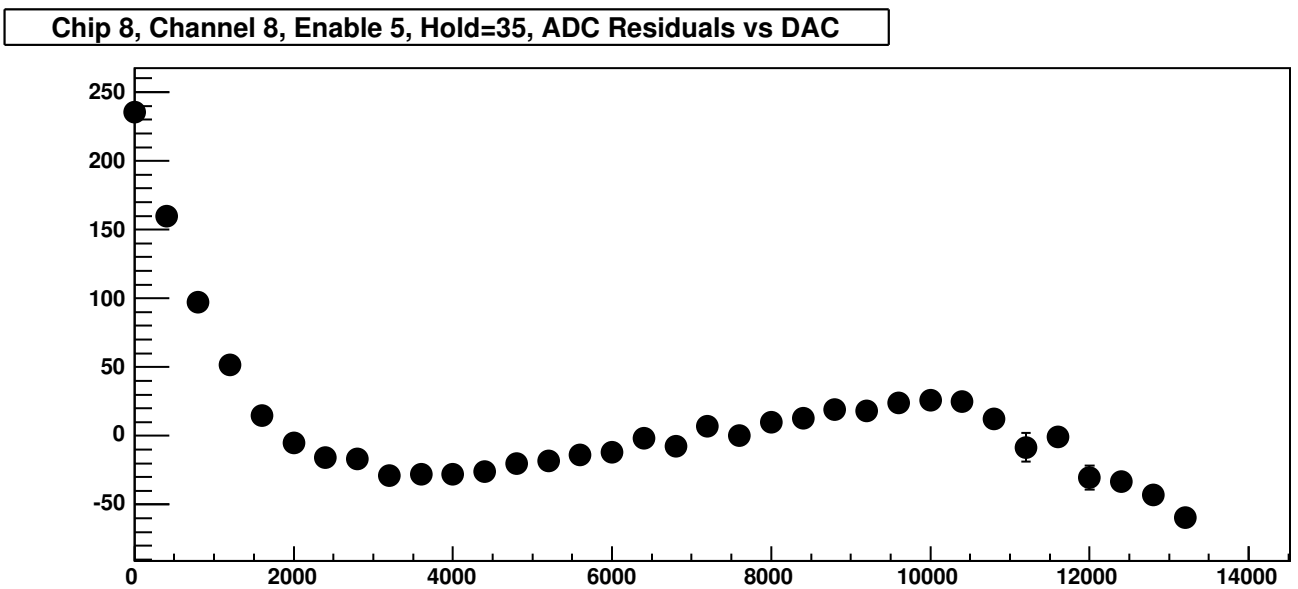

Chip 8, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 49.4 / 23
p0 146.3 ± 0.7183
p1 0.01407 ± 0.0001121
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

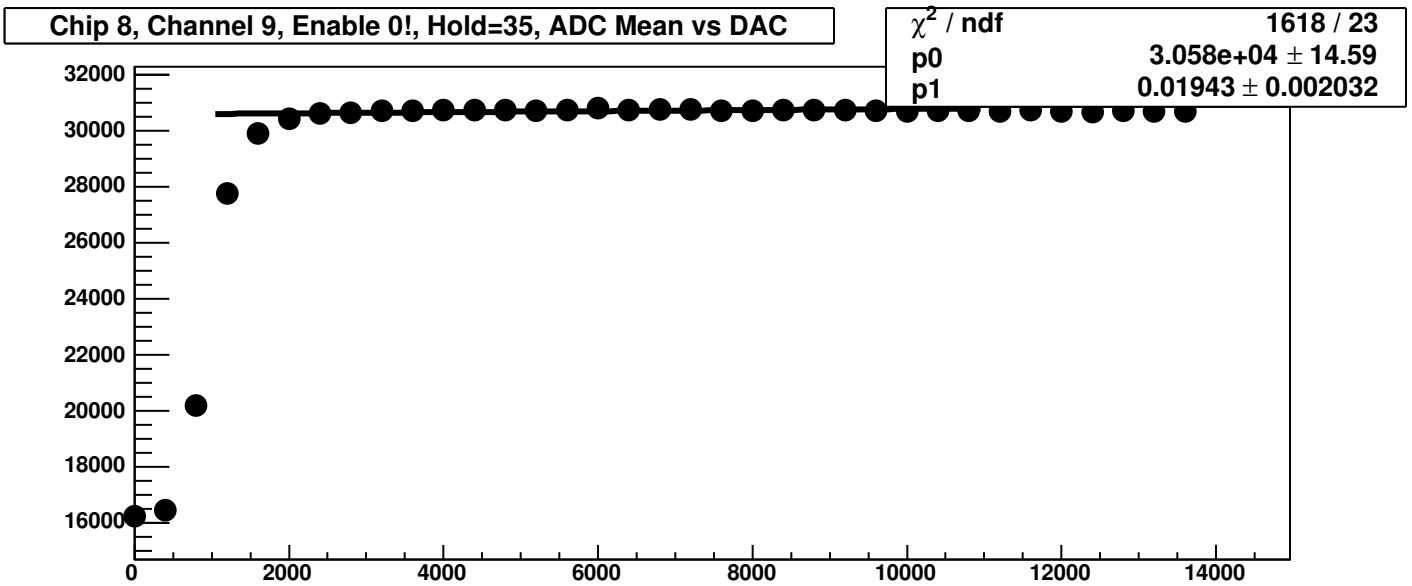

Chip 8, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

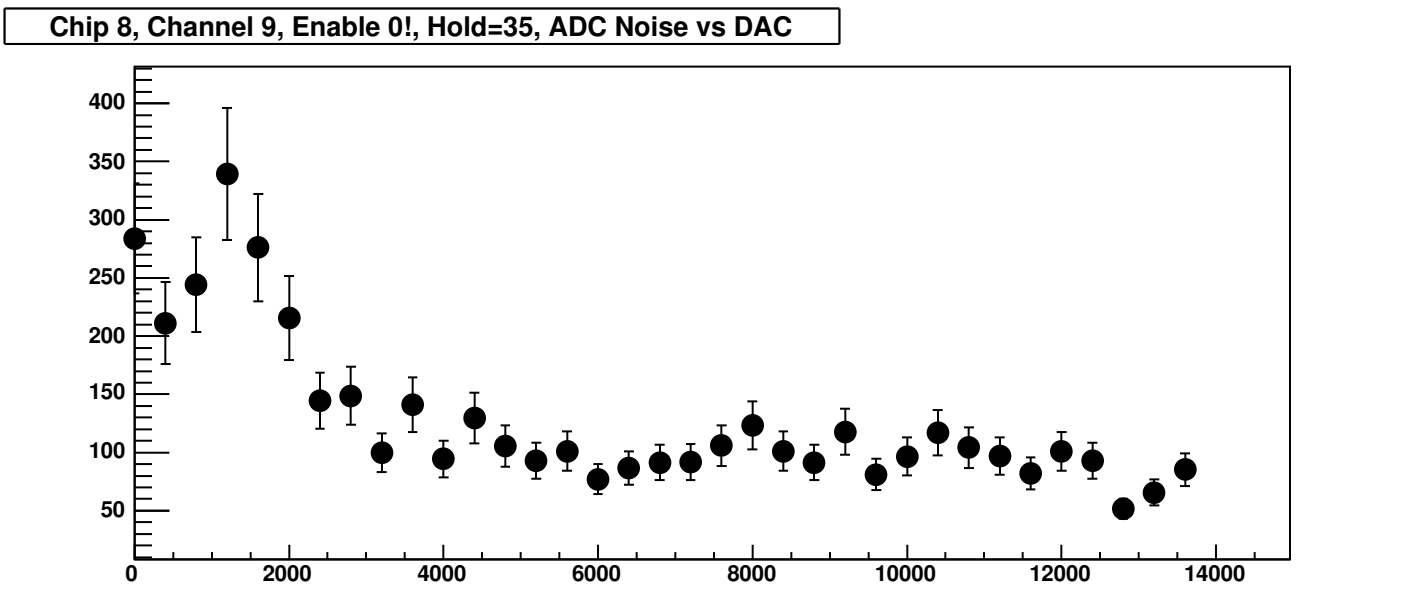

Chip 8, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

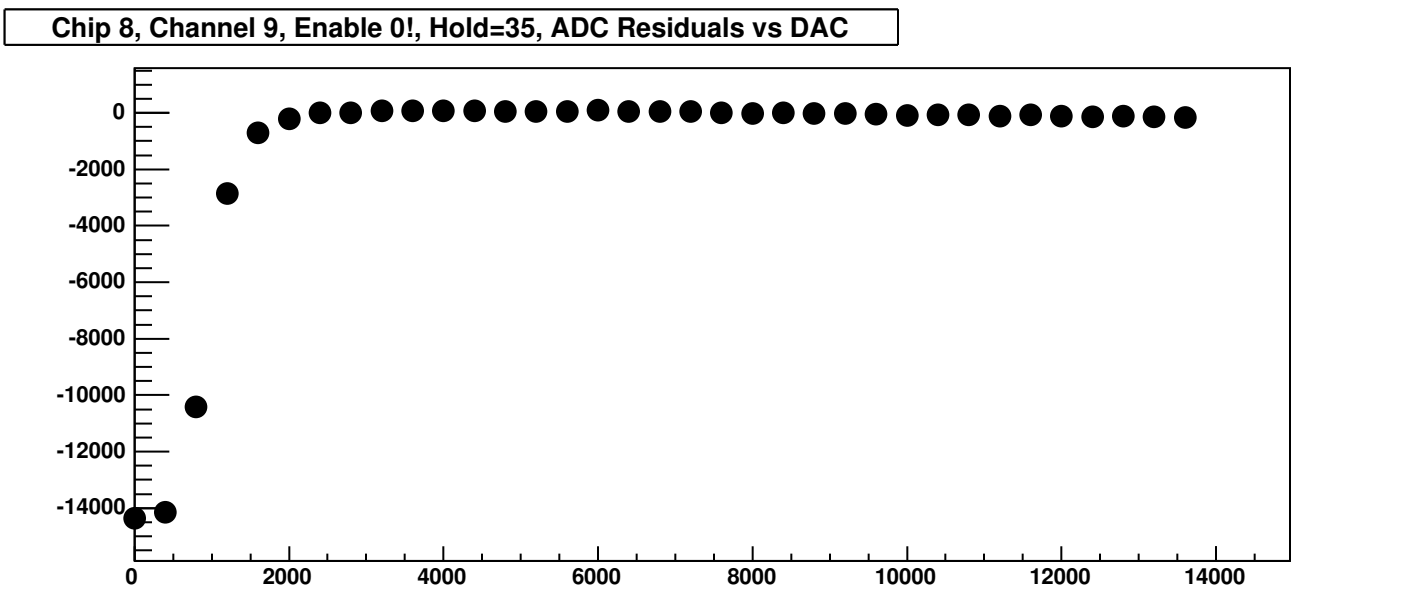

Chip 8, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2391 / 23
p0 -49 ± 1.004
p1 0.1802 ± 0.0001848
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

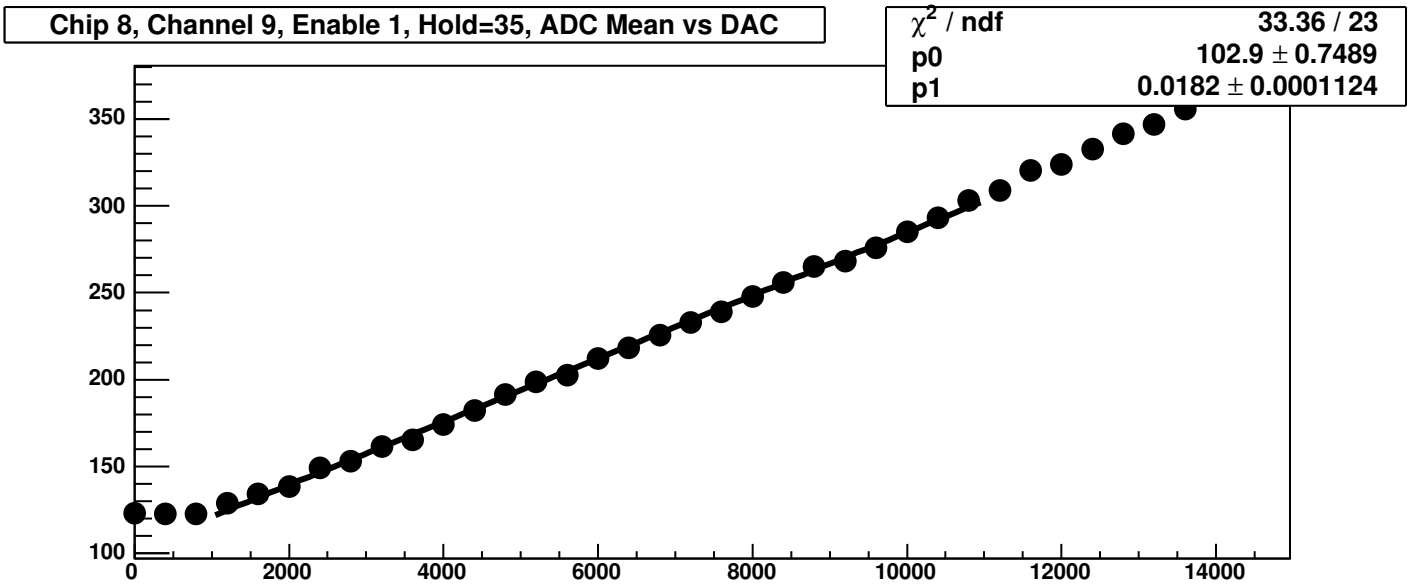

Chip 8, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

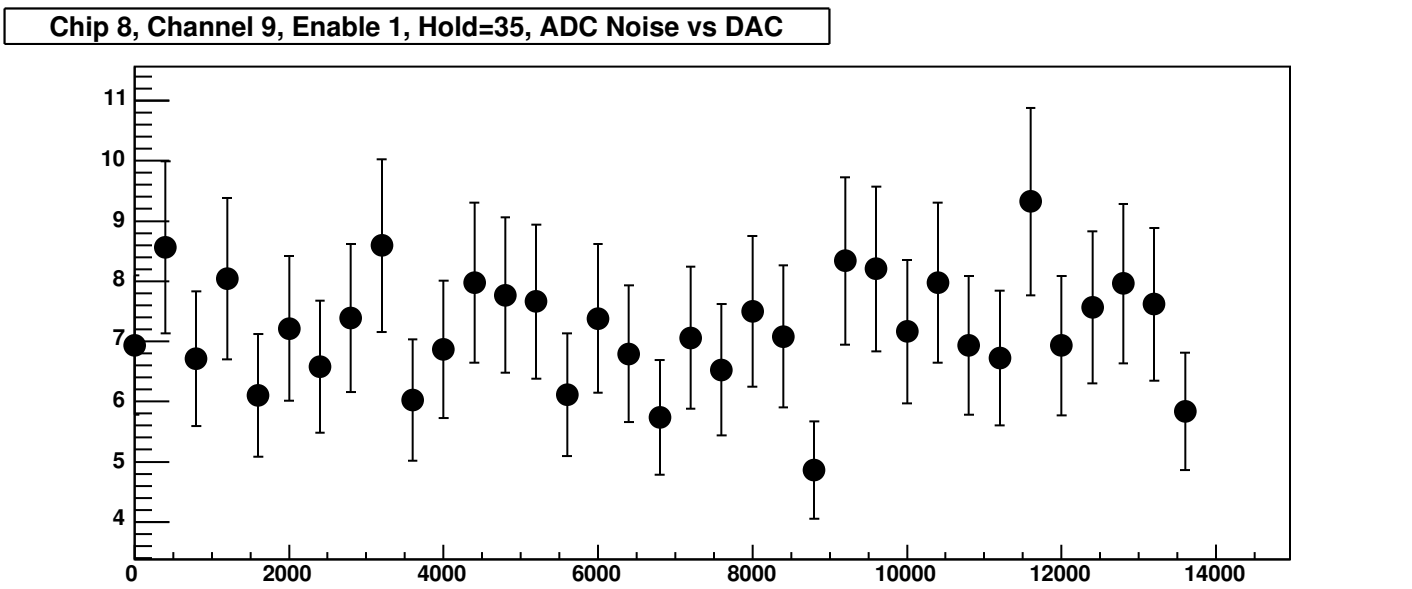

Chip 8, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

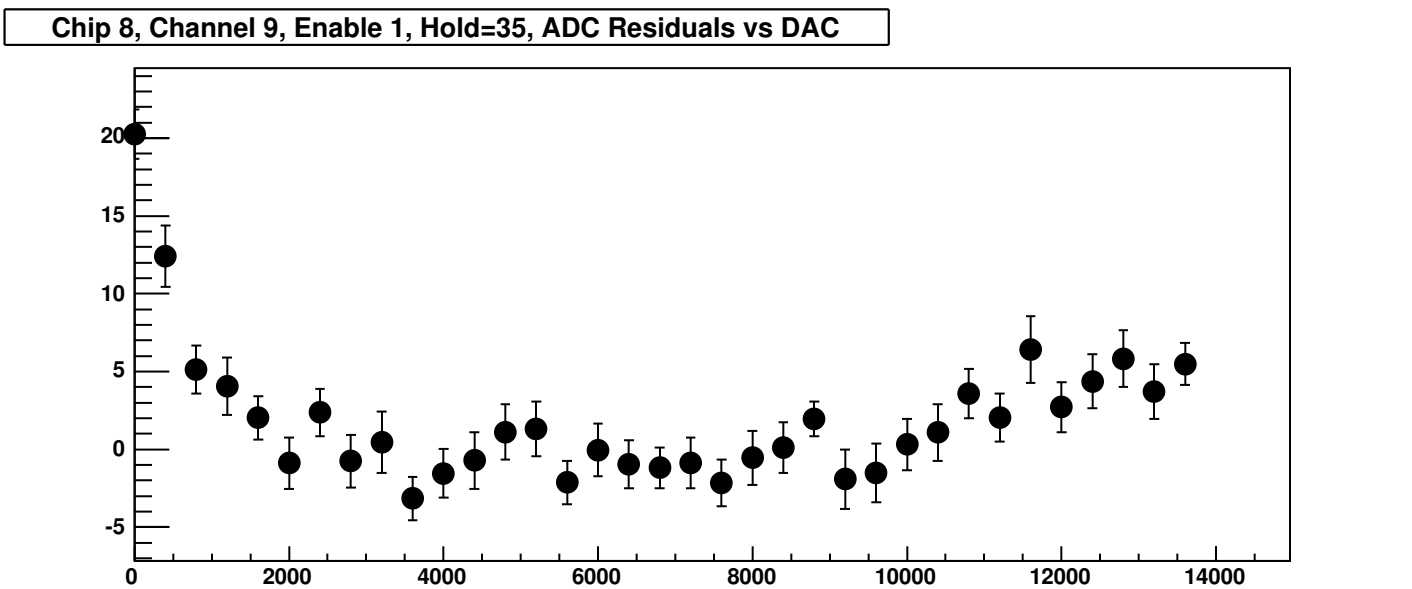

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1618 / 23
p0 3.058e+04 ± 14.59
p1 0.01943 ± 0.002032
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

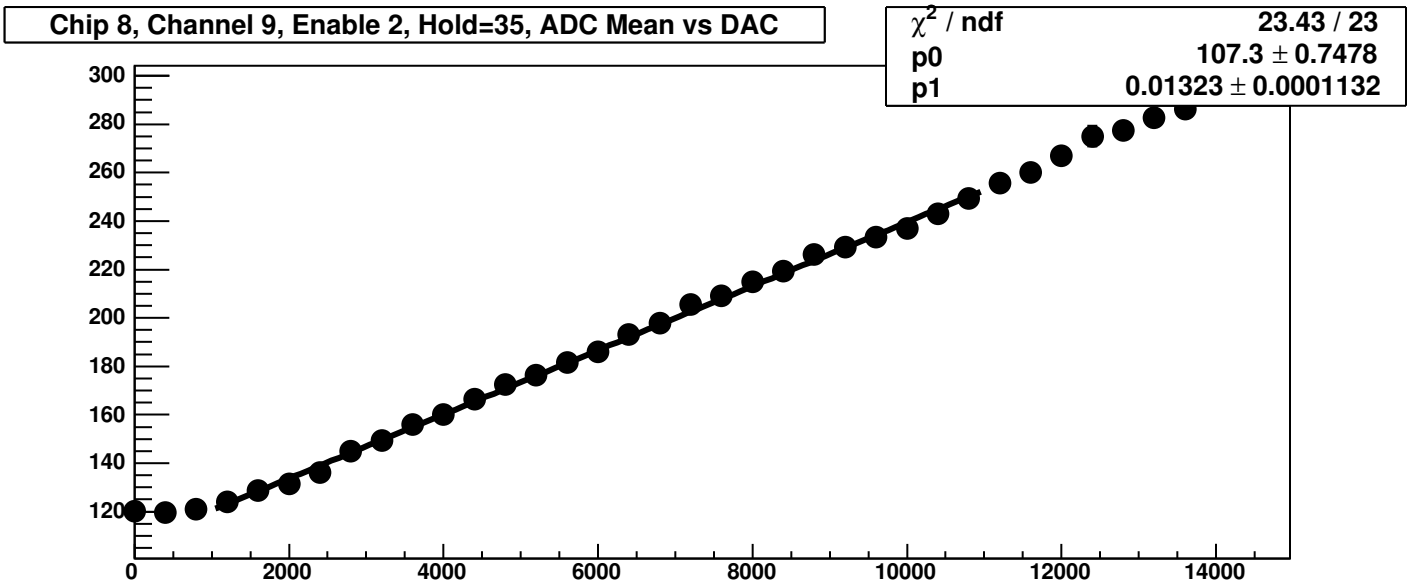

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

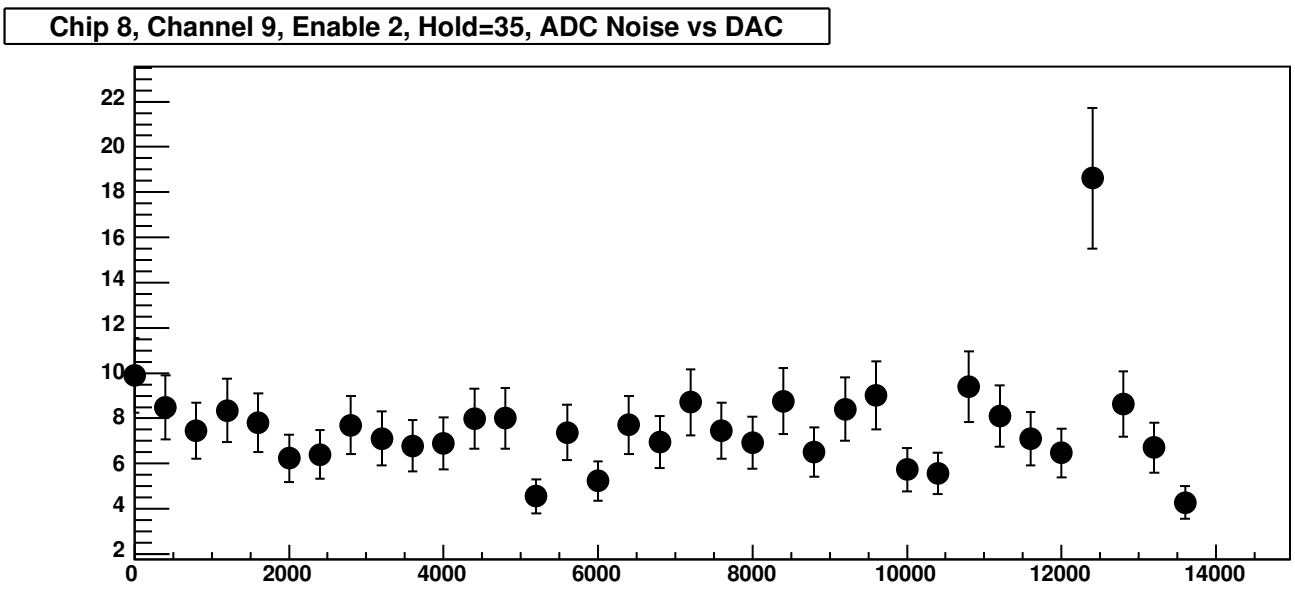

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

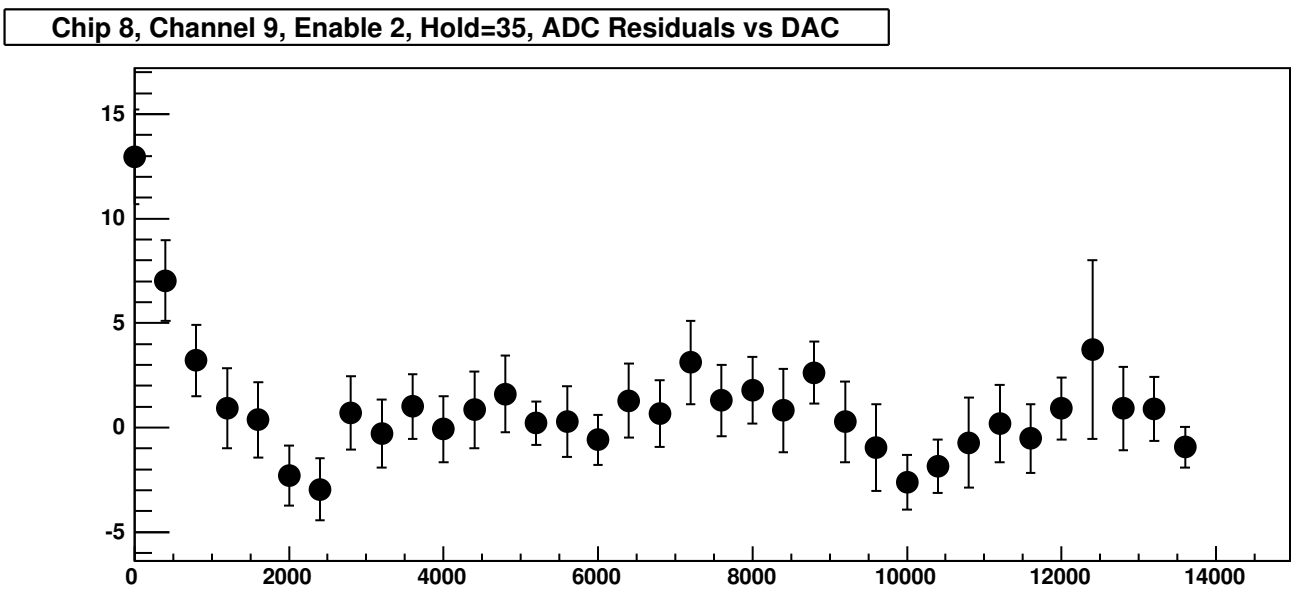

Chip 8, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.36 / 23
p0 102.9 ± 0.7489
p1 0.0182 ± 0.0001124
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

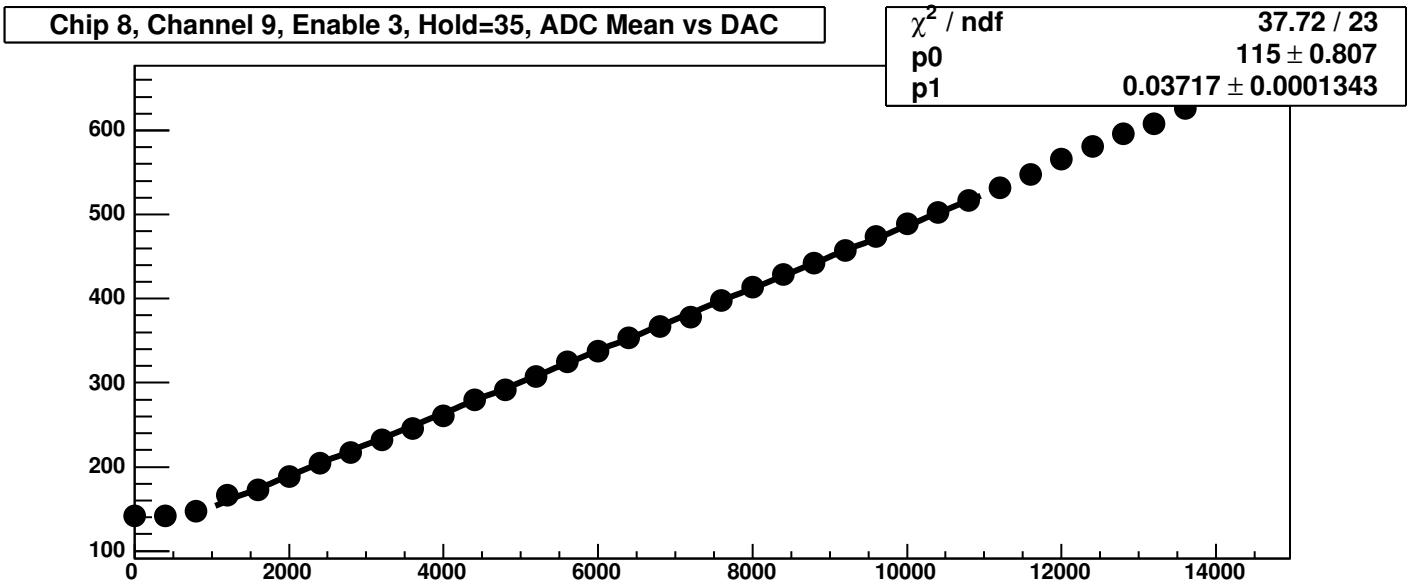

Chip 8, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

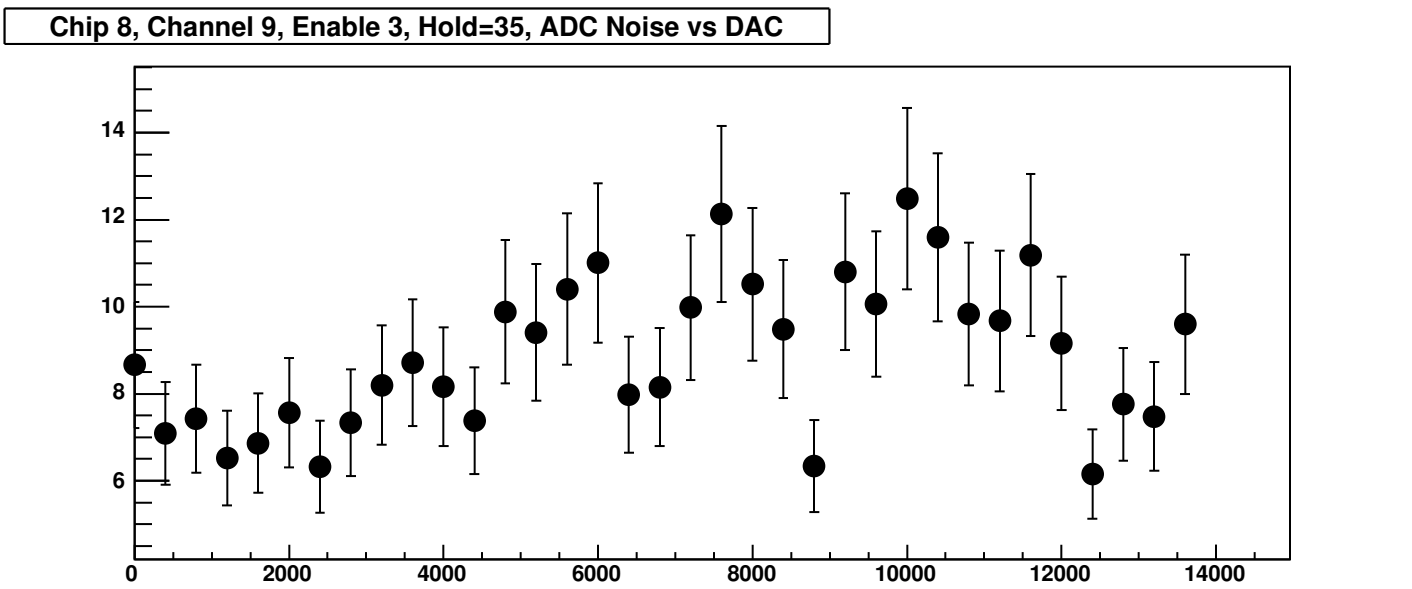

Chip 8, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

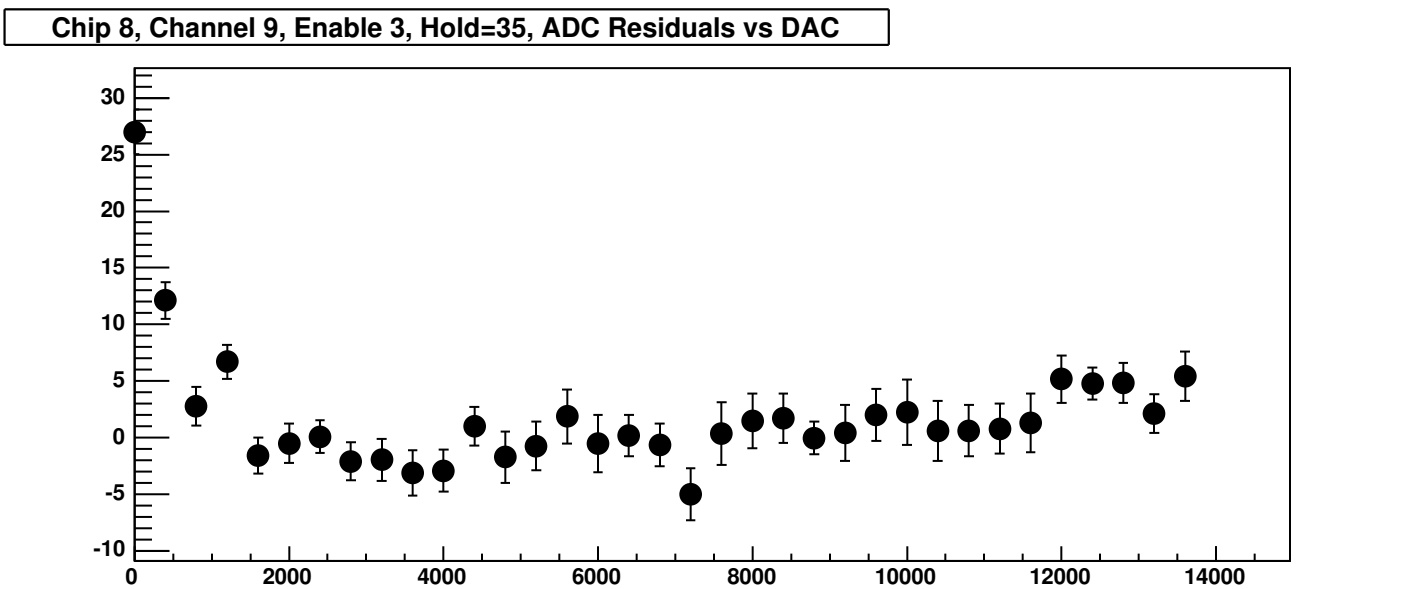

Chip 8, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.43 / 23
p0 107.3 ± 0.7478
p1 0.01323 ± 0.0001132
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

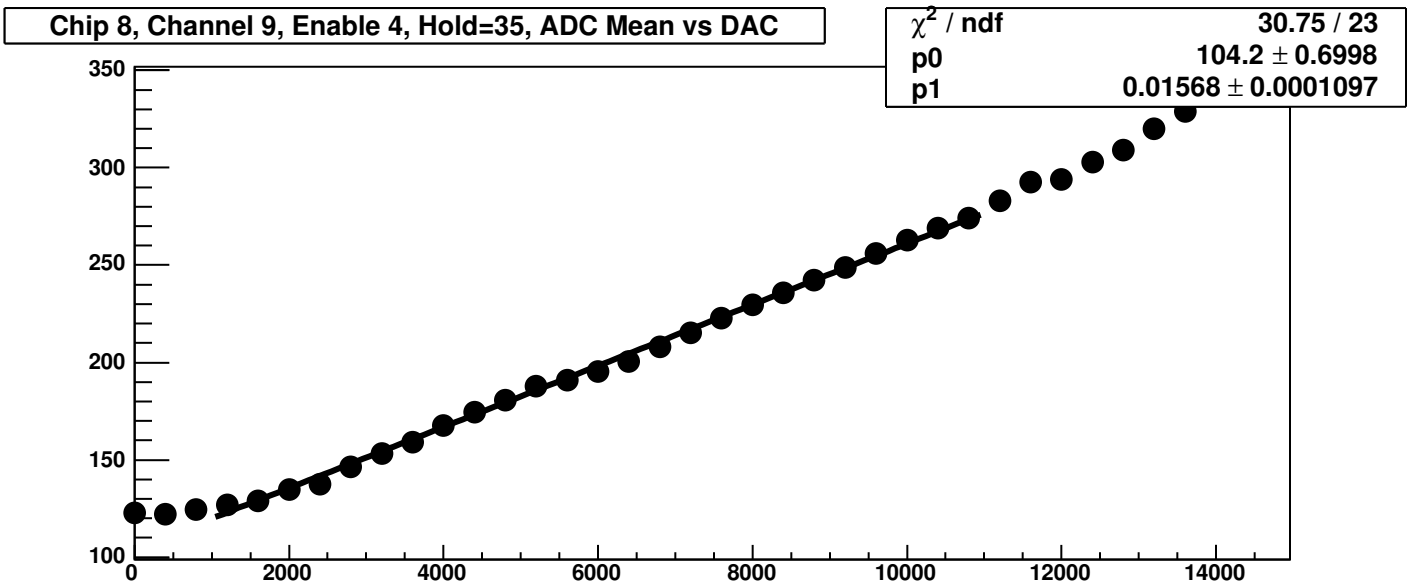

Chip 8, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

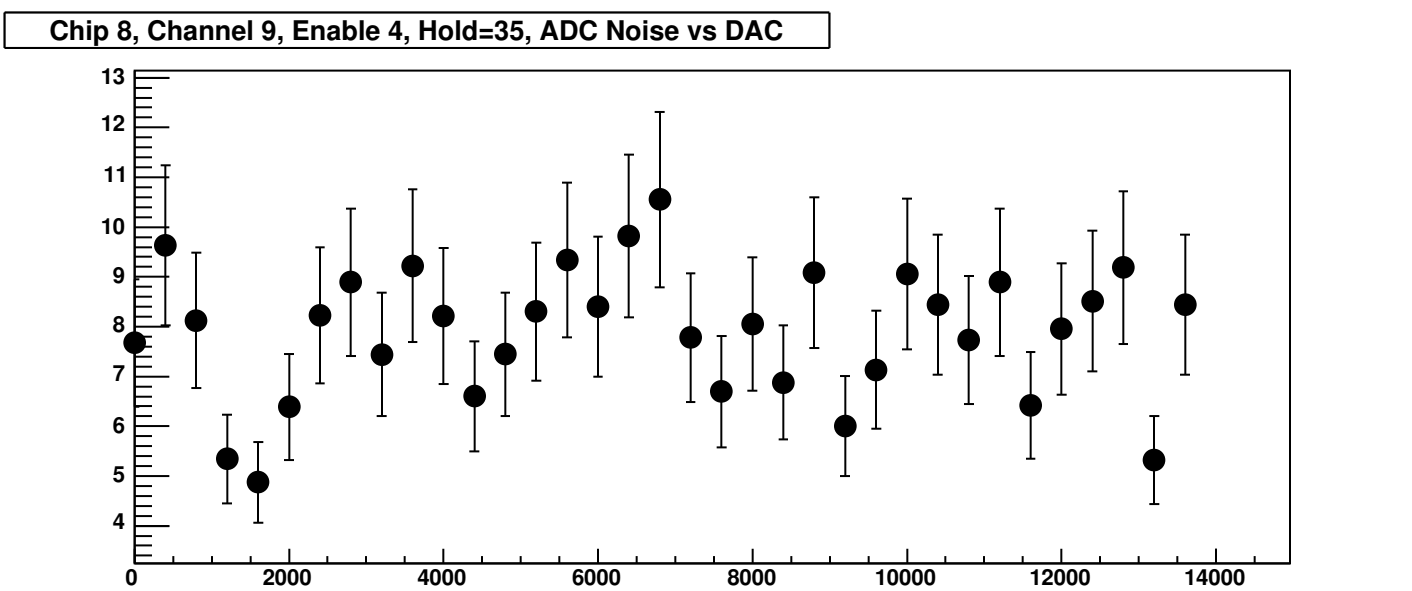

Chip 8, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

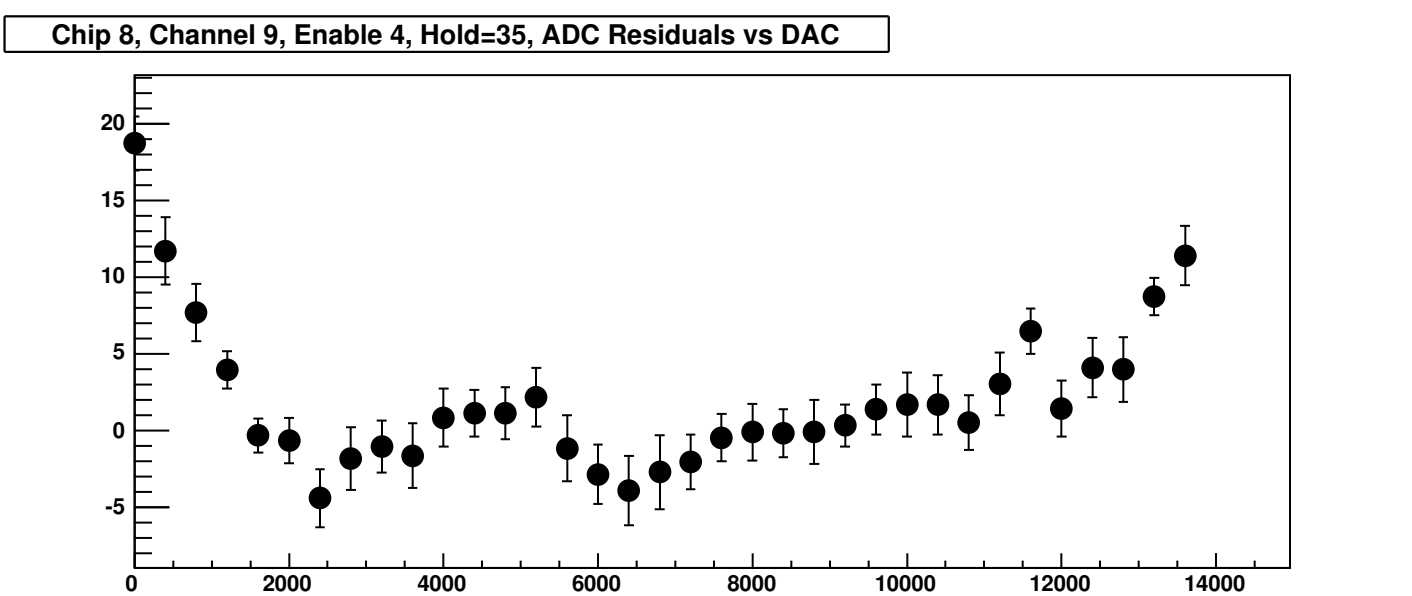

Chip 8, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.72 / 23
p0 115 ± 0.807
p1 0.03717 ± 0.0001343
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

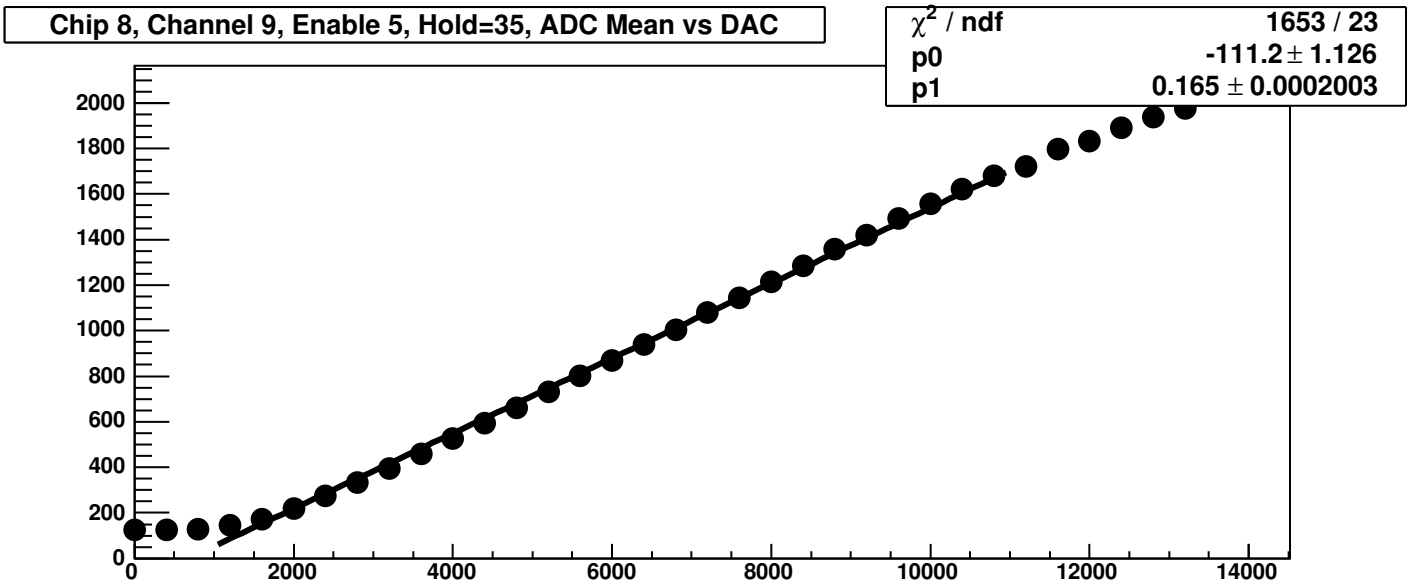

Chip 8, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.75 / 23
p0 104.2 ± 0.6998
p1 0.01568 ± 0.0001097
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

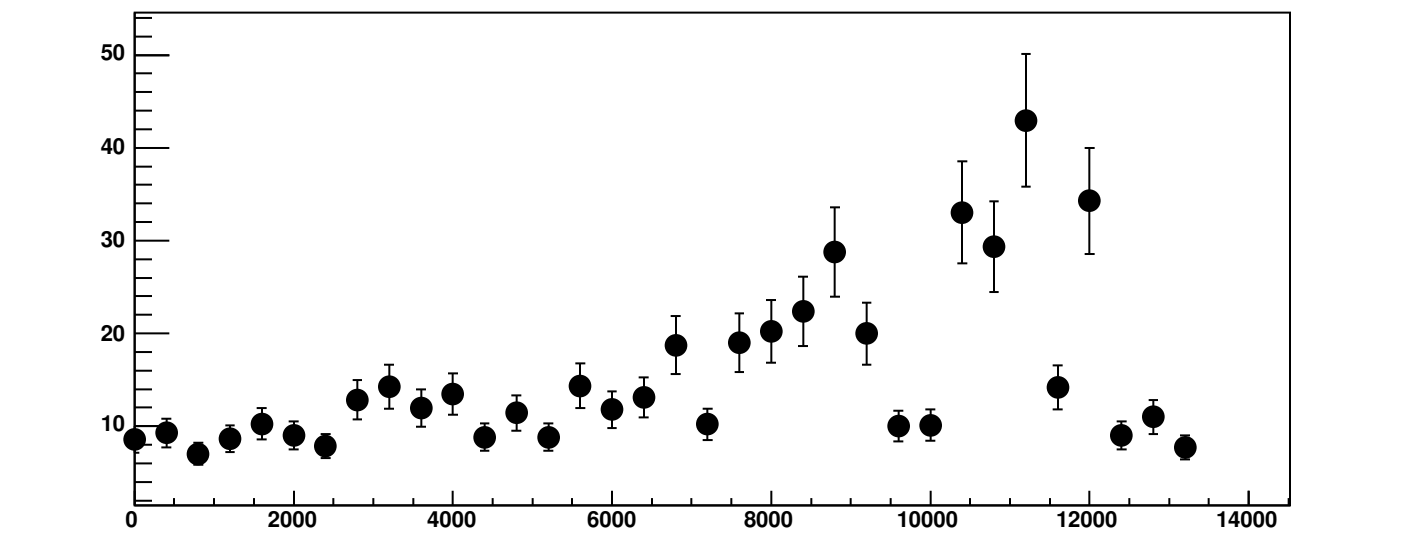

Chip 8, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

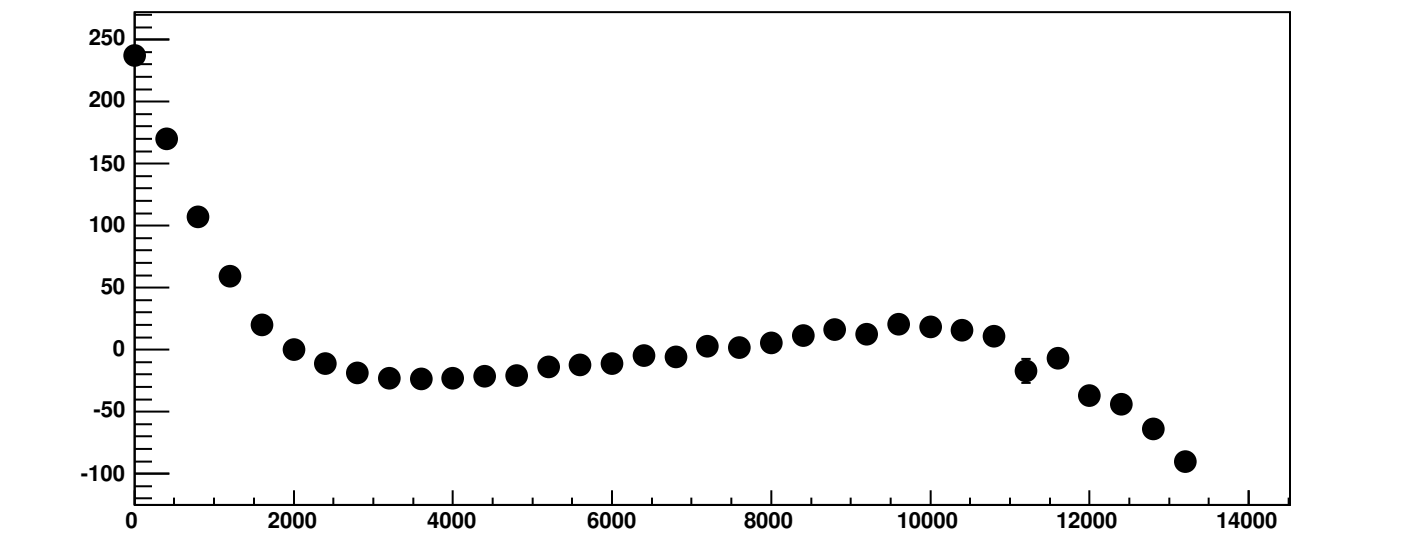
Chip 8, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

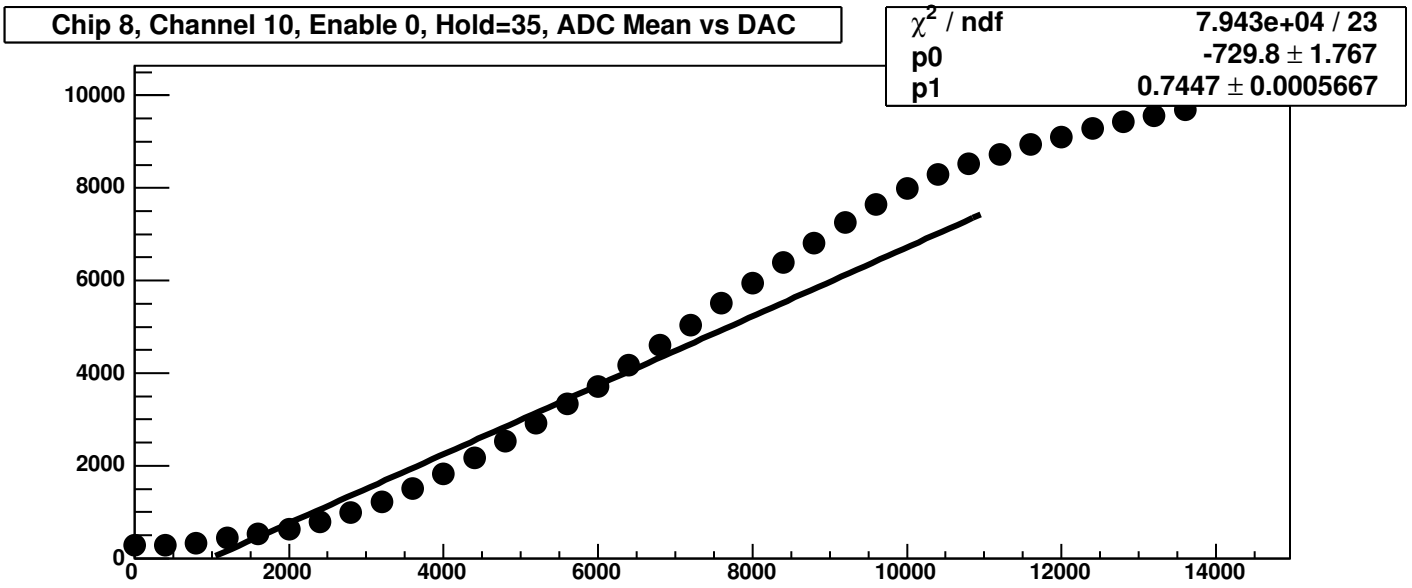


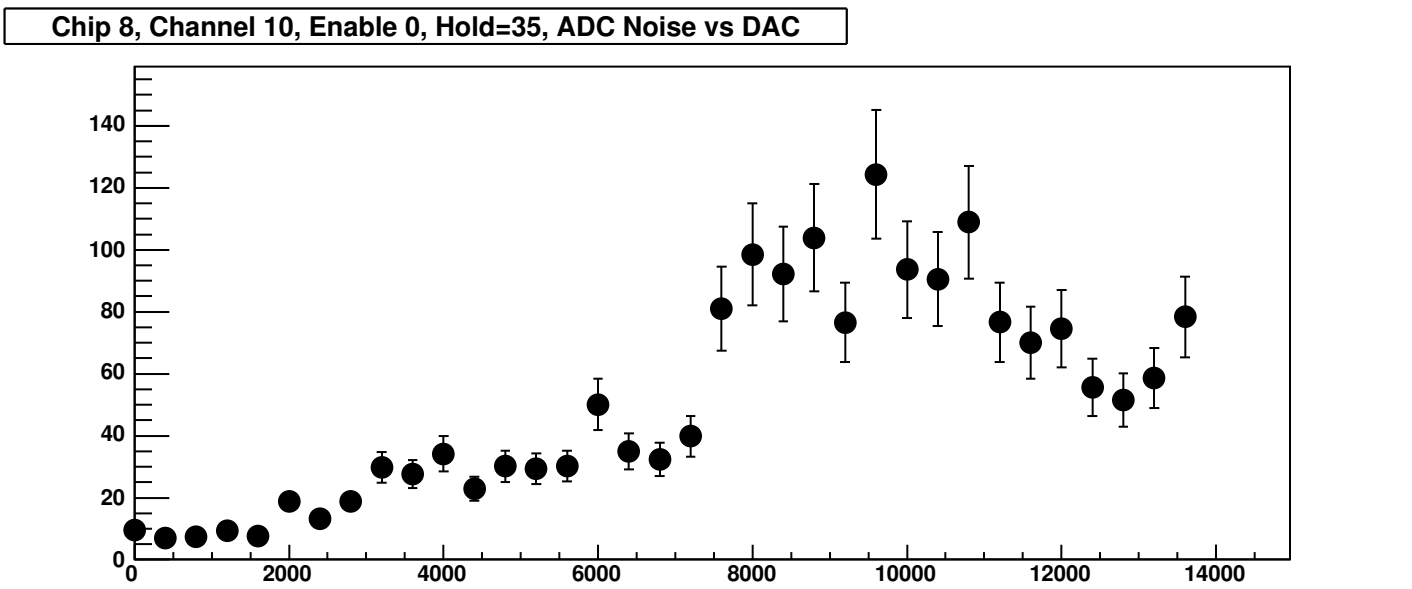
Chip 8, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

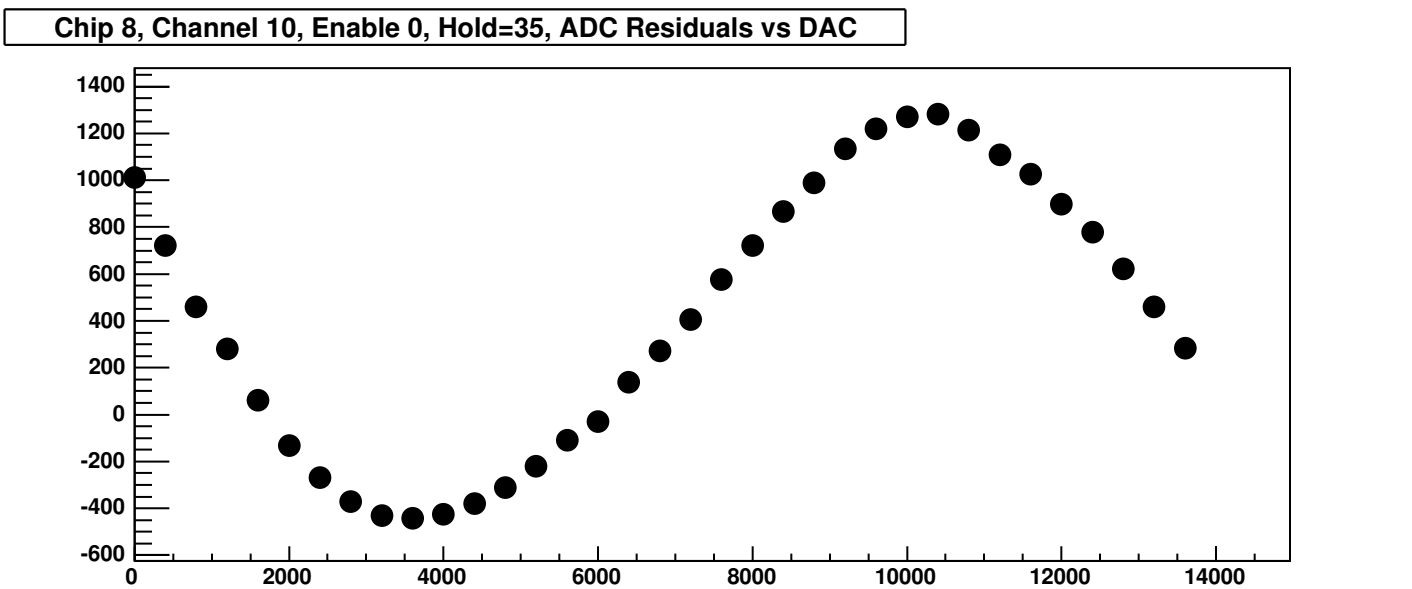


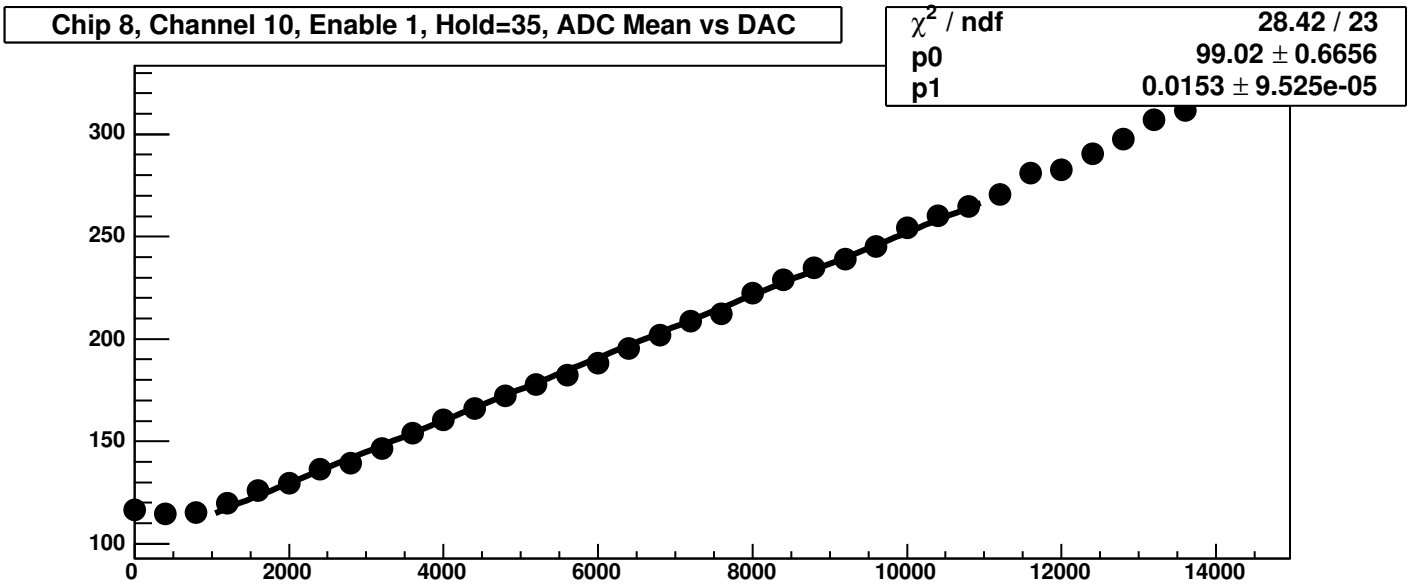
Chip 8, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

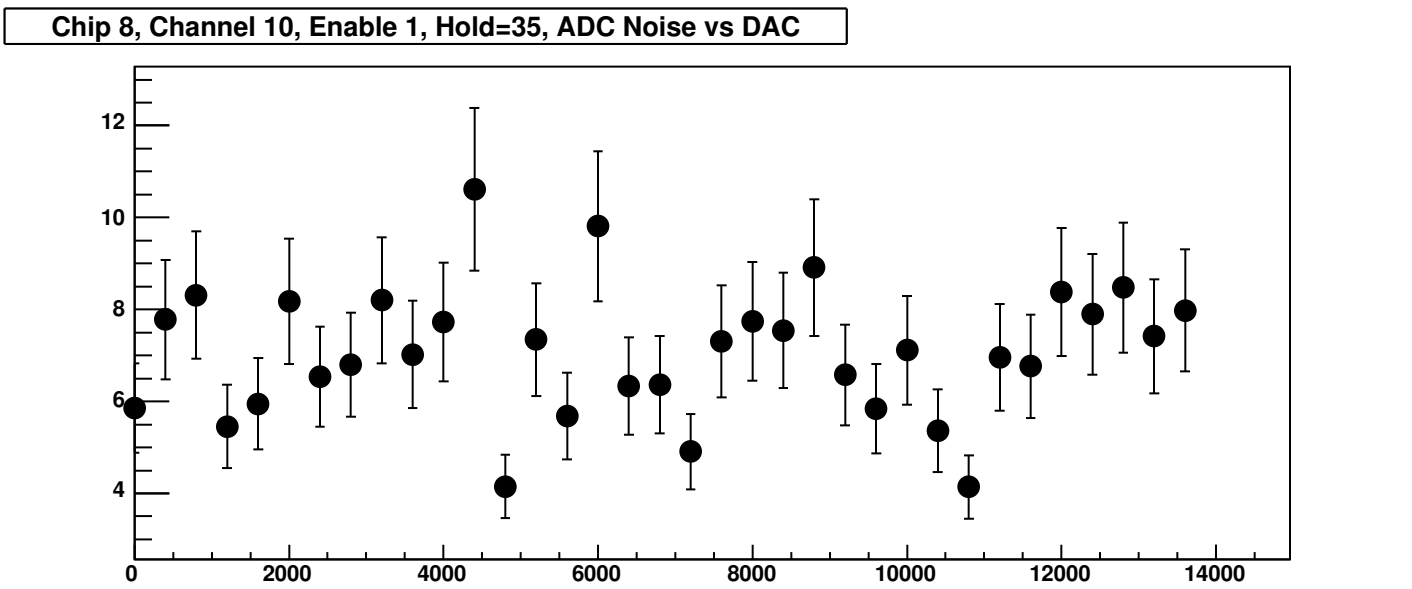


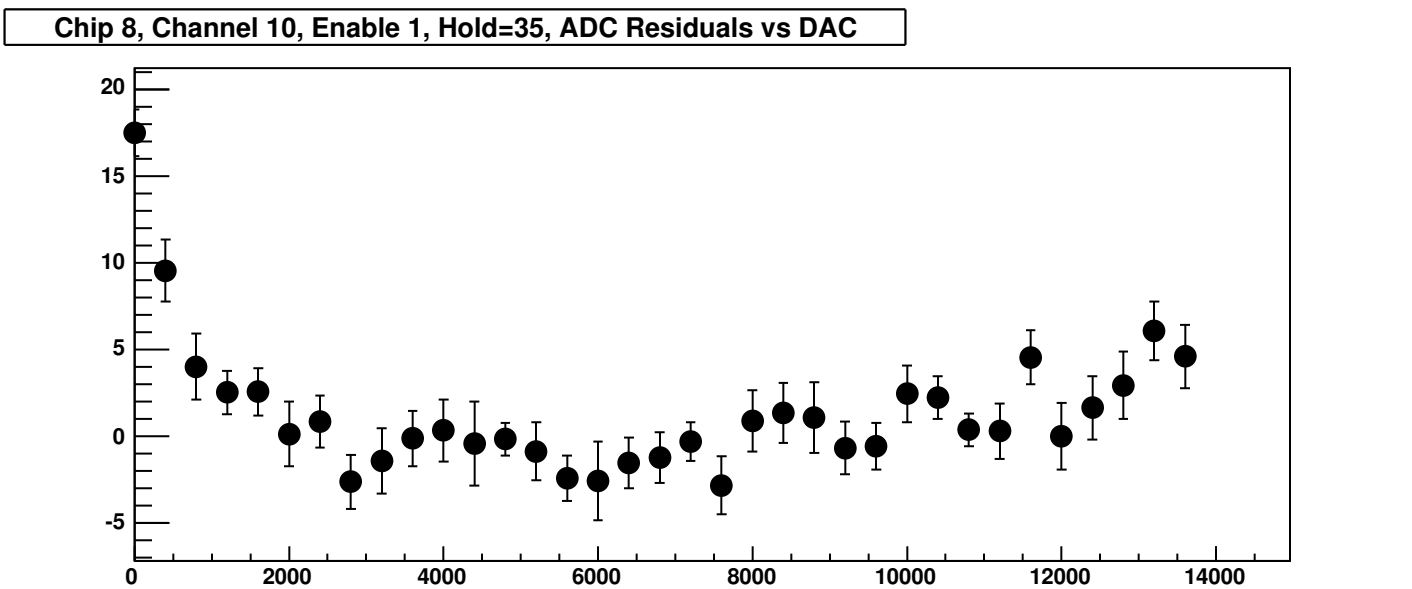

Chip 8, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.943e+04 / 23
p0 -729.8 ± 1.767
p1 0.7447 ± 0.0005667
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

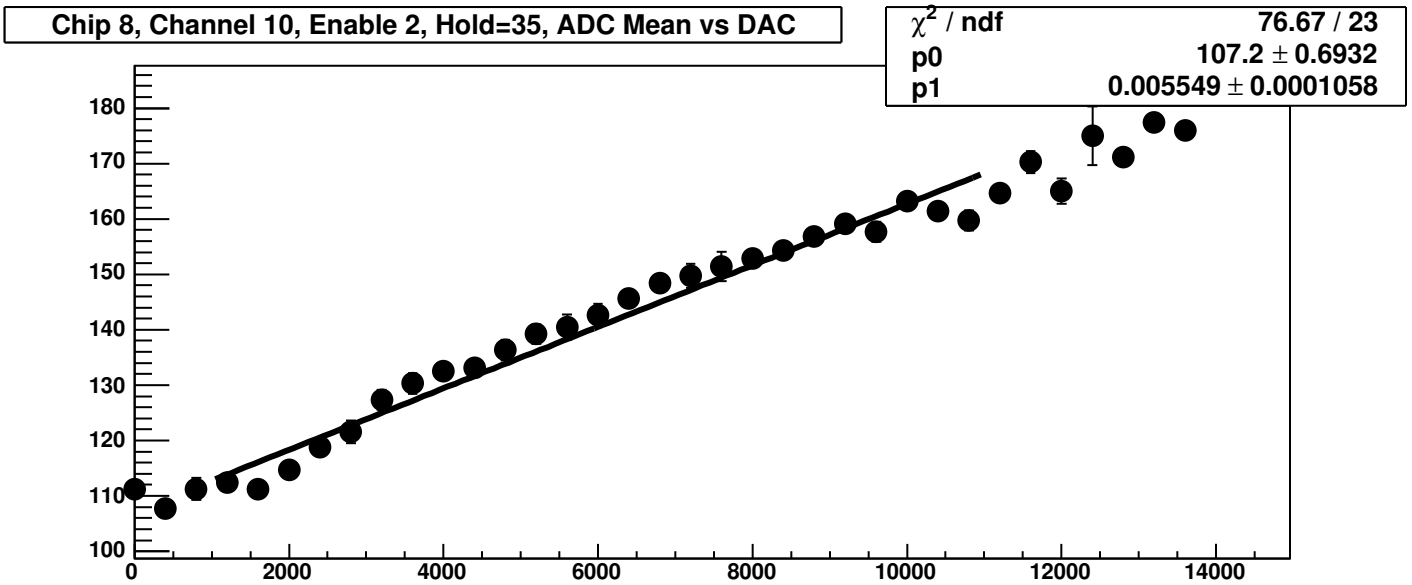

Chip 8, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

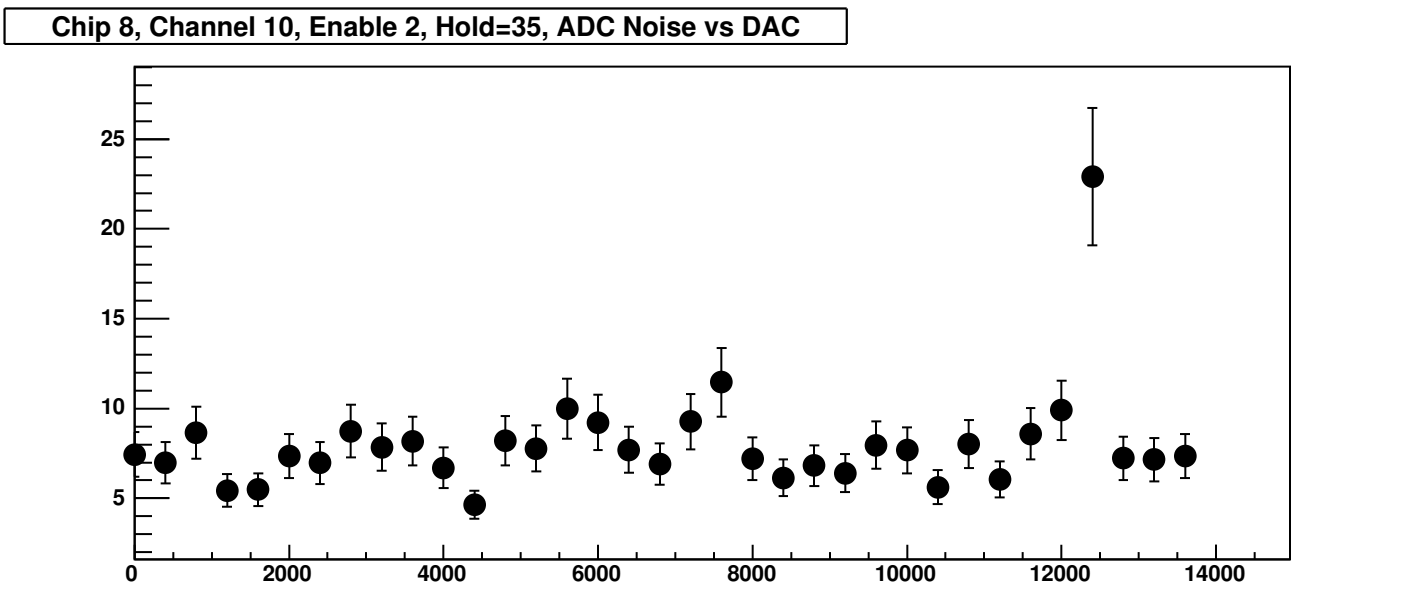

Chip 8, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

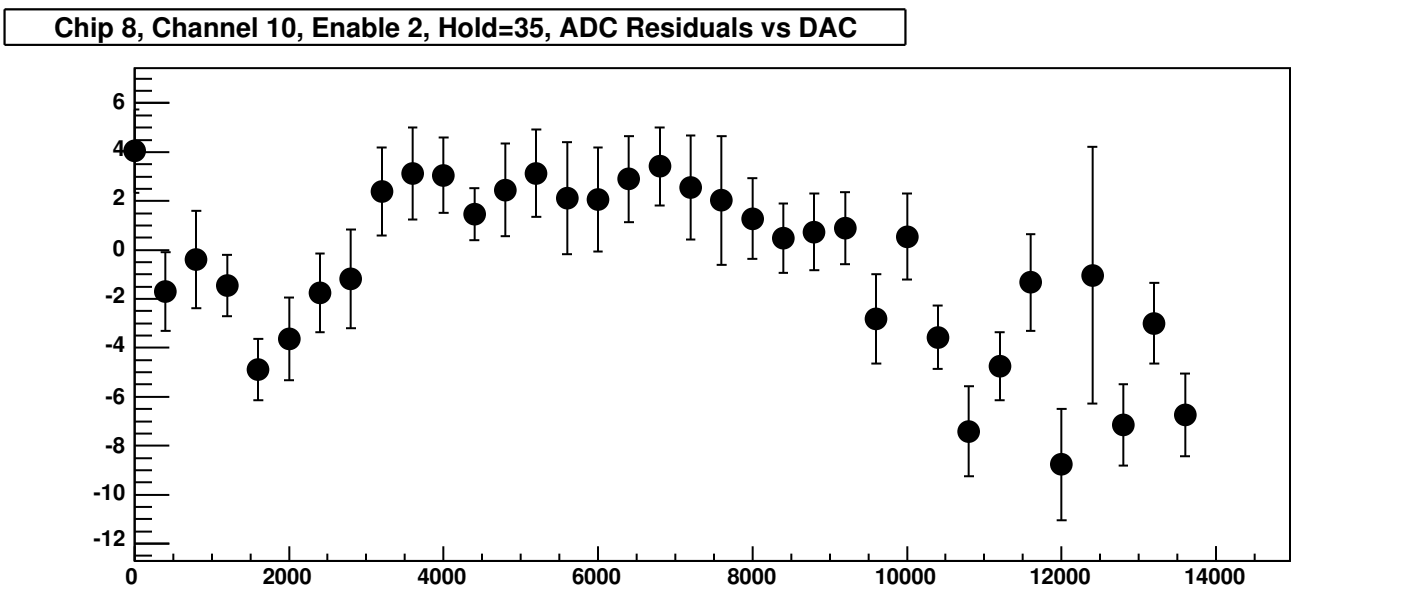

Chip 8, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.42 / 23
p0 99.02 ± 0.6656
p1 0.0153 ± 9.525e-05
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

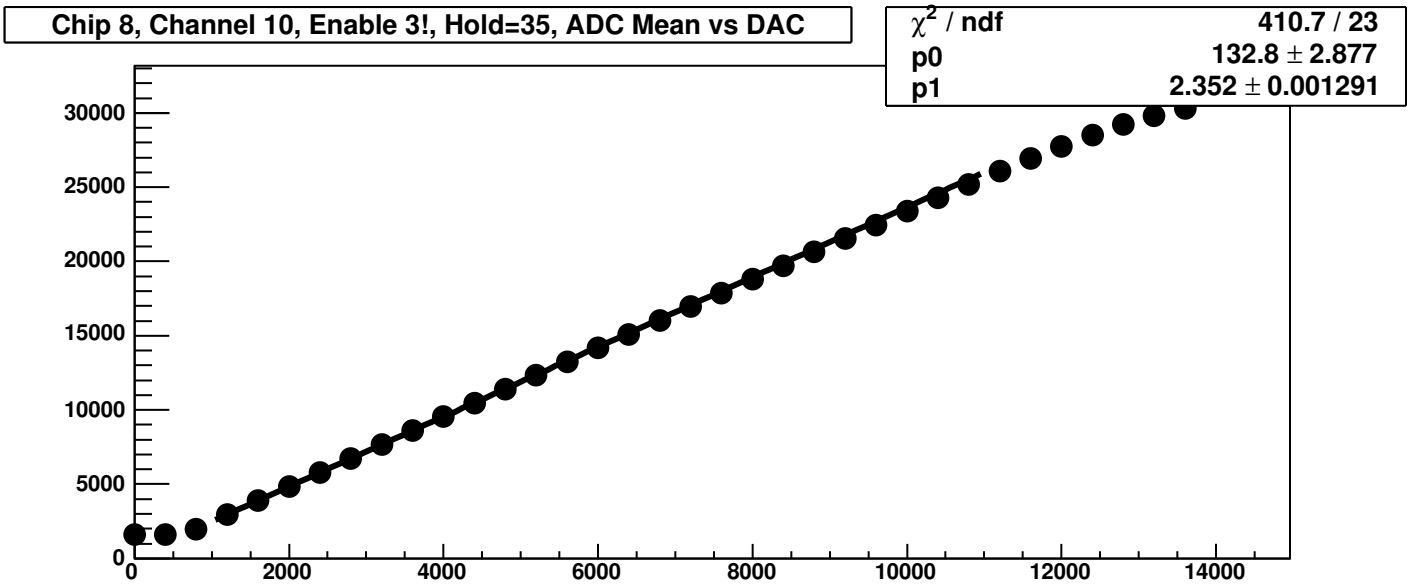

Chip 8, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

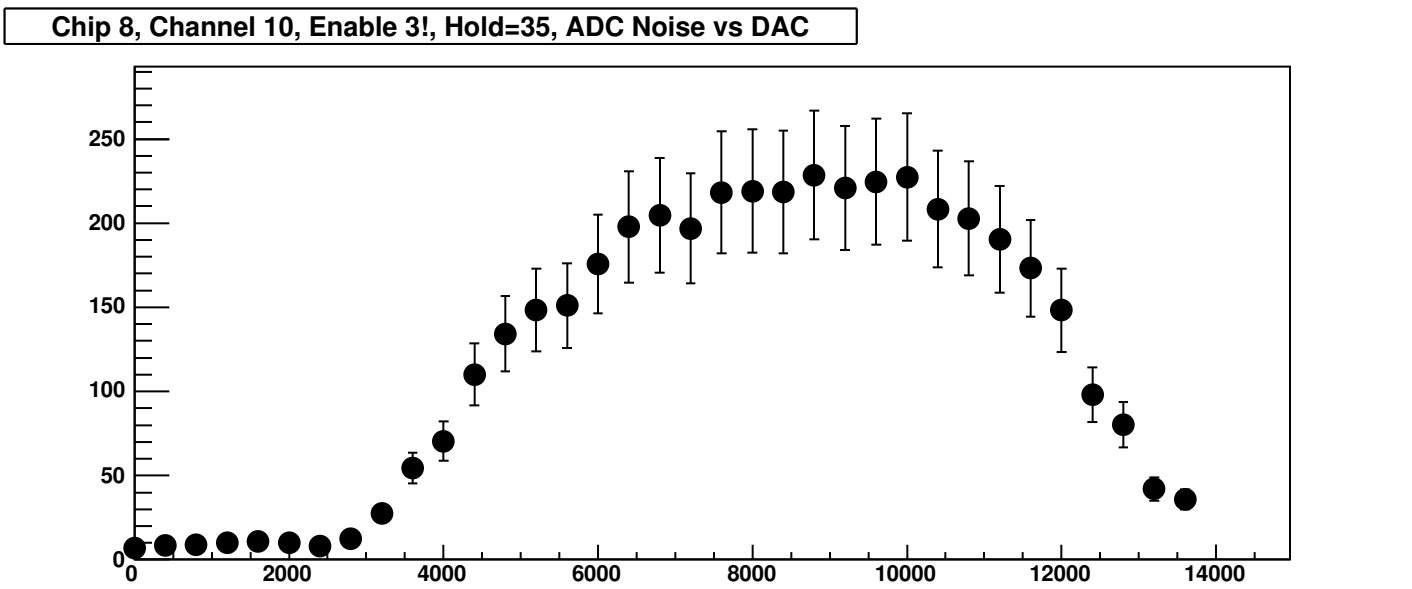

Chip 8, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

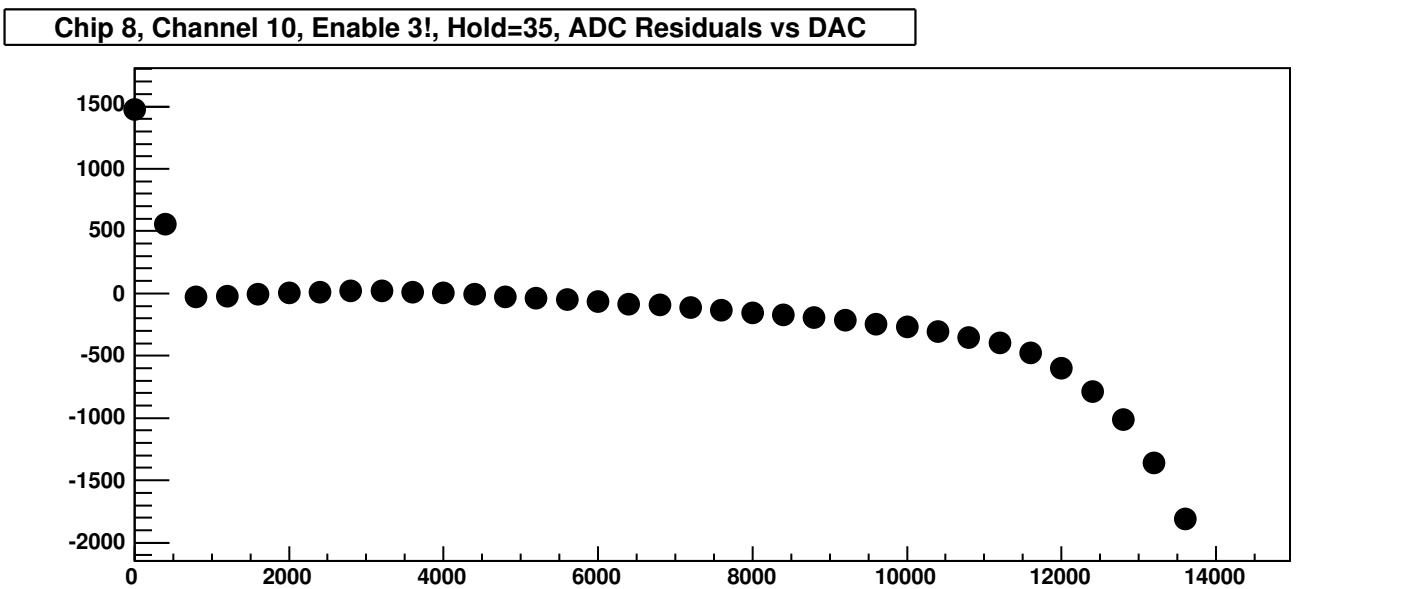

Chip 8, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 76.67 / 23
p0 107.2 ± 0.6932
p1 0.005549 ± 0.0001058
180
170
160
150
140
130
120
110
100
0
2000
4000
6000
8000
10000
12000
14000

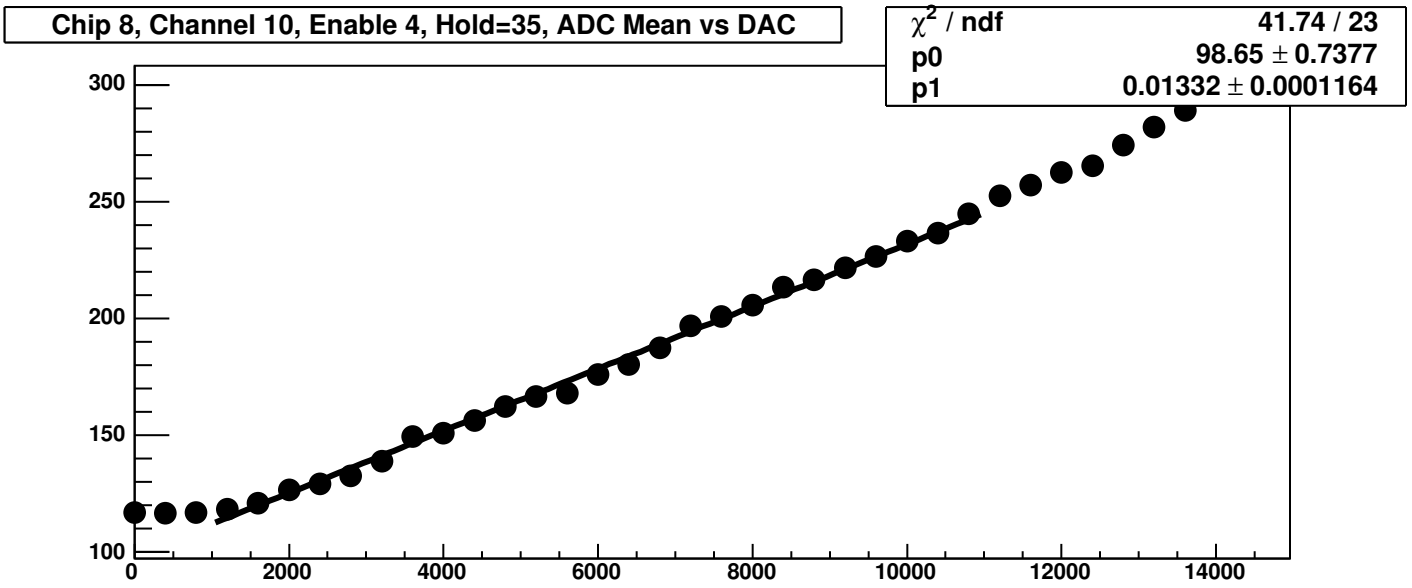

Chip 8, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

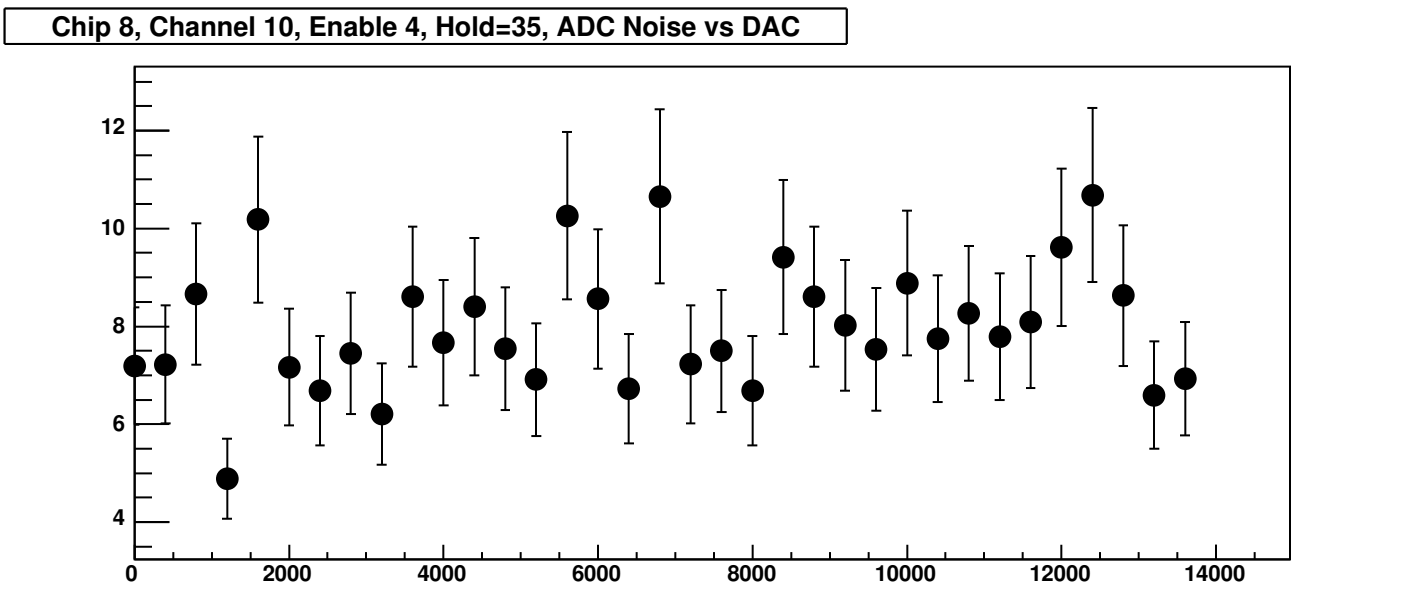

Chip 8, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

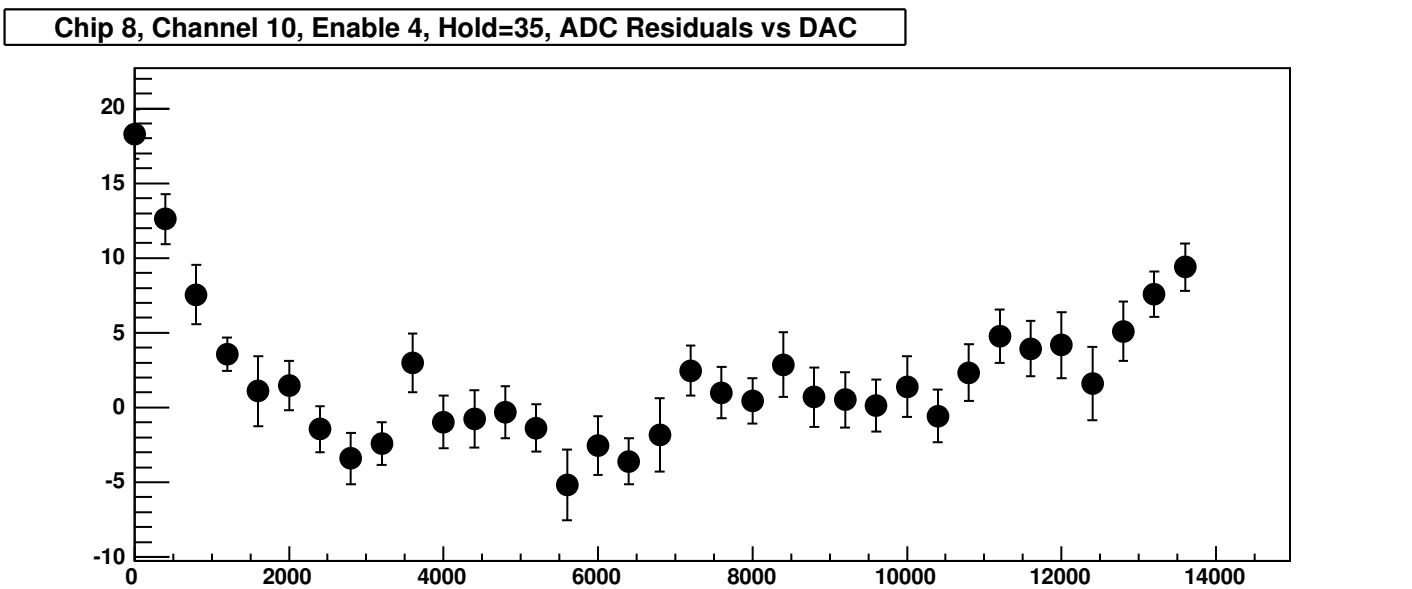

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 410.7 / 23
p0 132.8 ± 2.877
p1 2.352 ± 0.001291
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

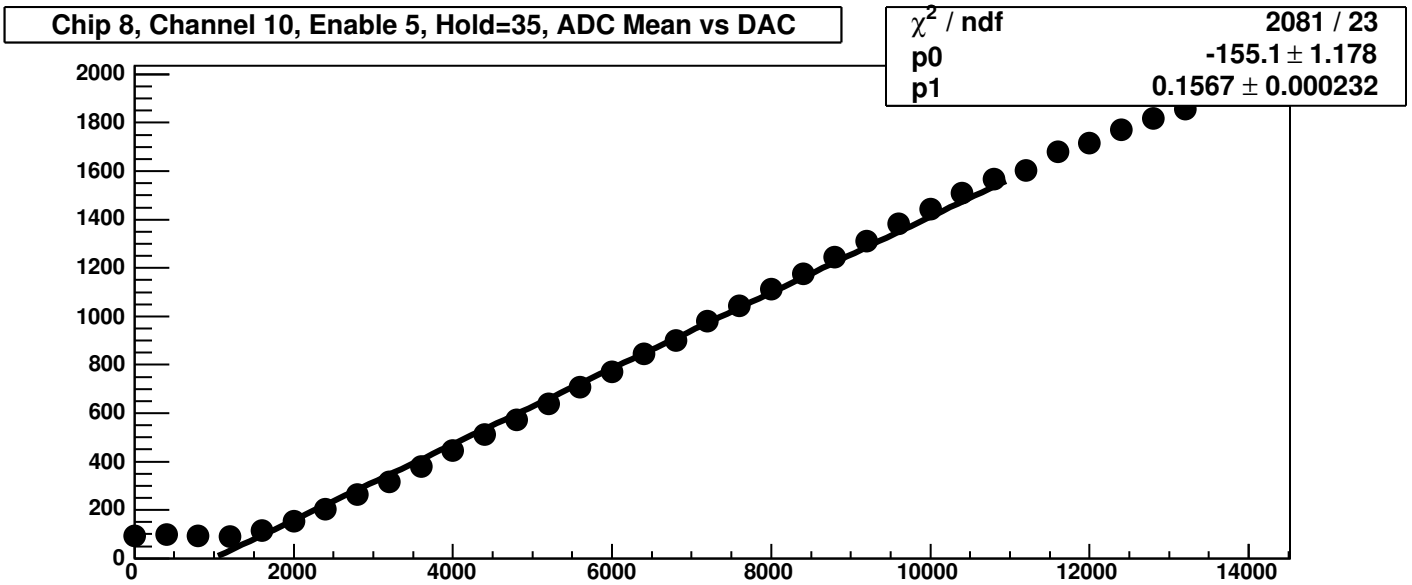

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

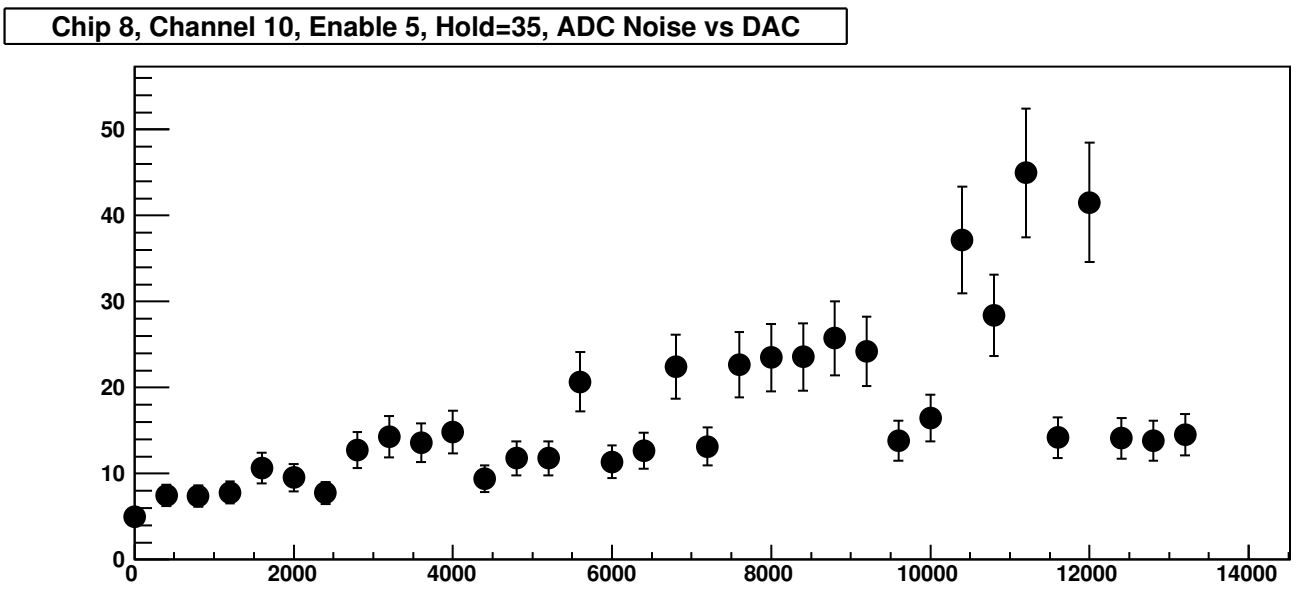

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

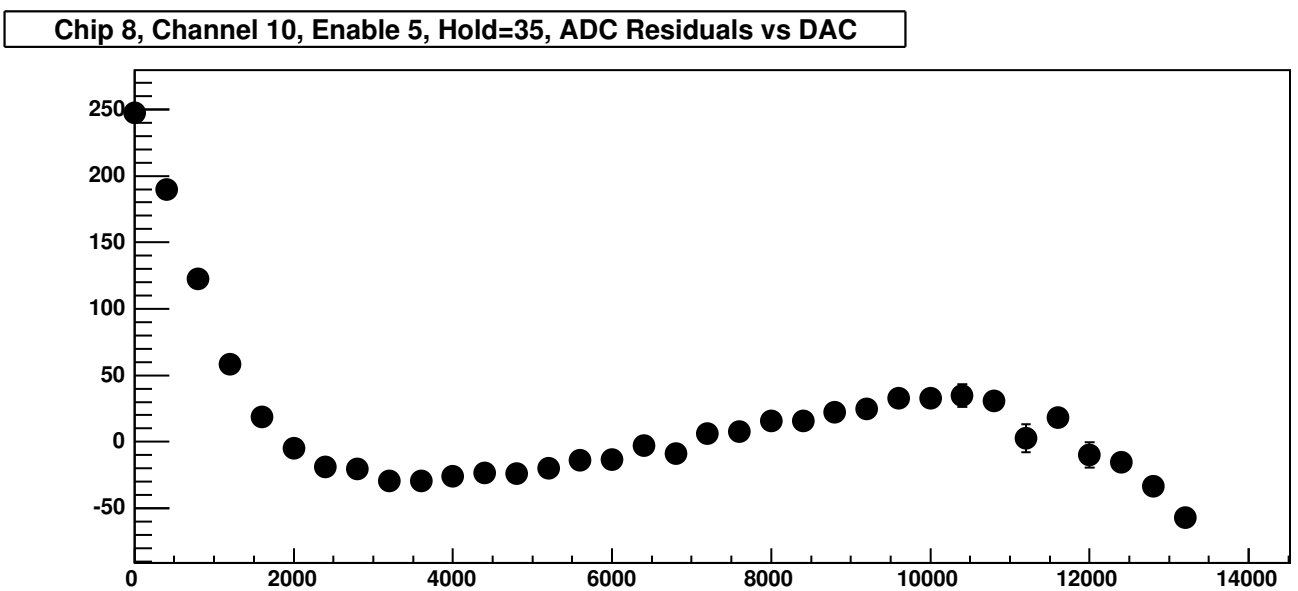

Chip 8, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 41.74 / 23
p0 98.65 ± 0.7377
p1 0.01332 ± 0.0001164
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

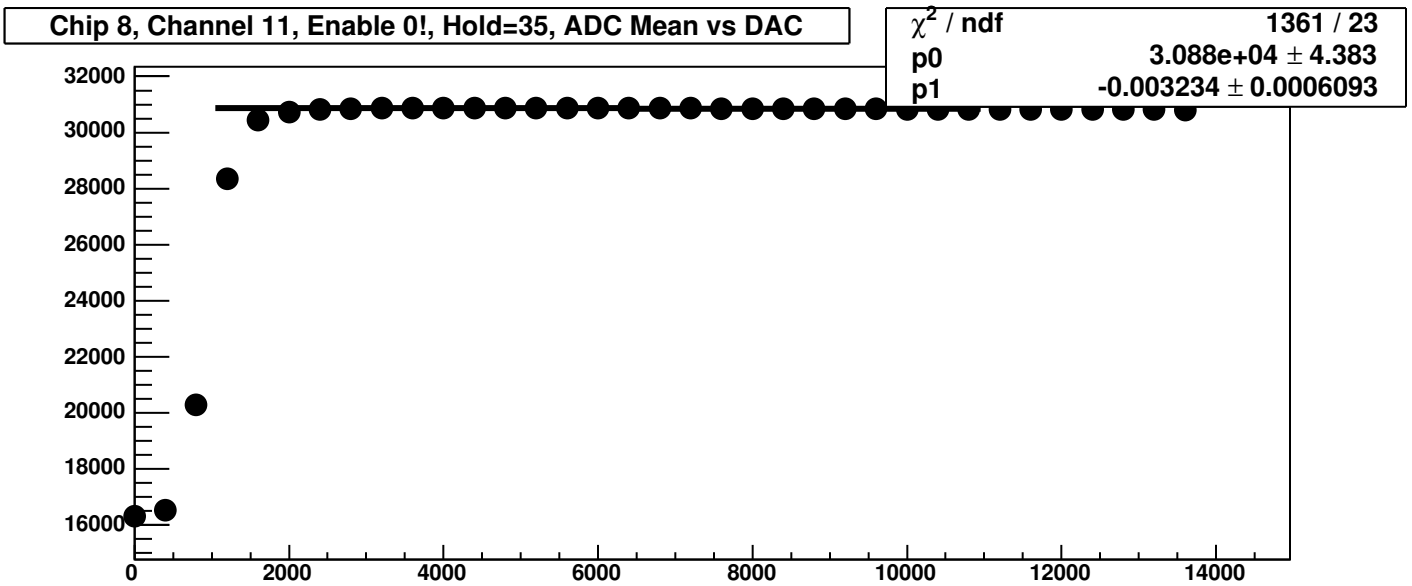

Chip 8, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

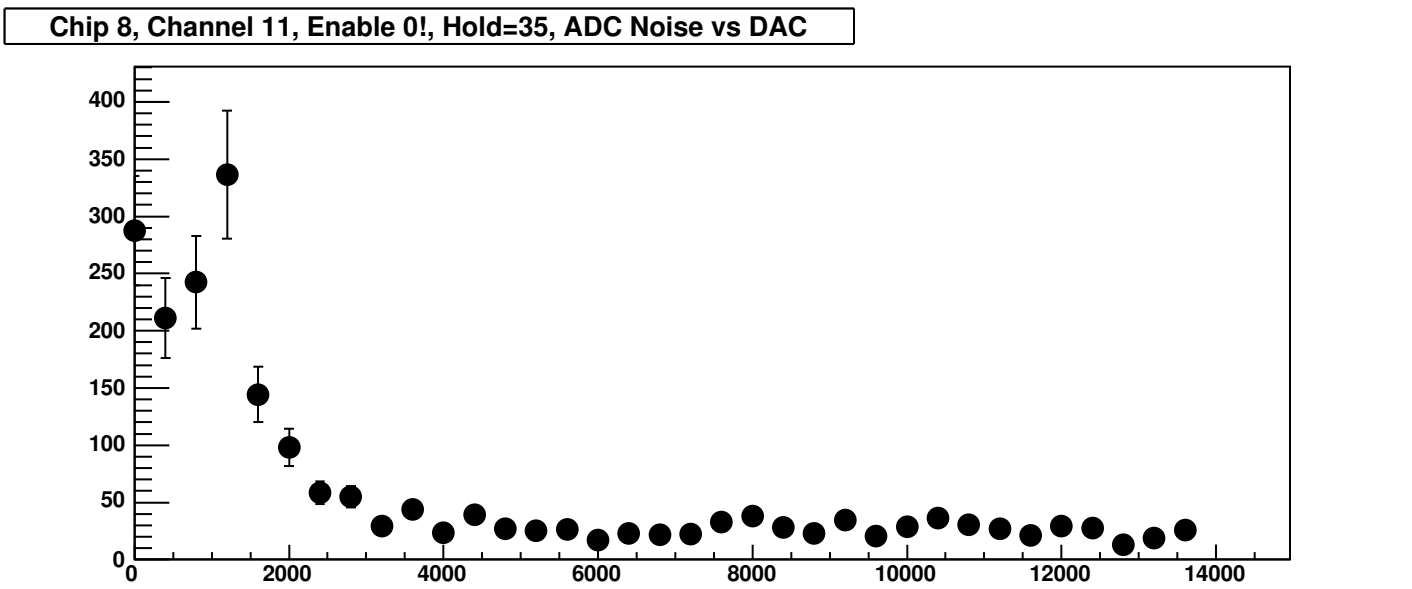

Chip 8, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

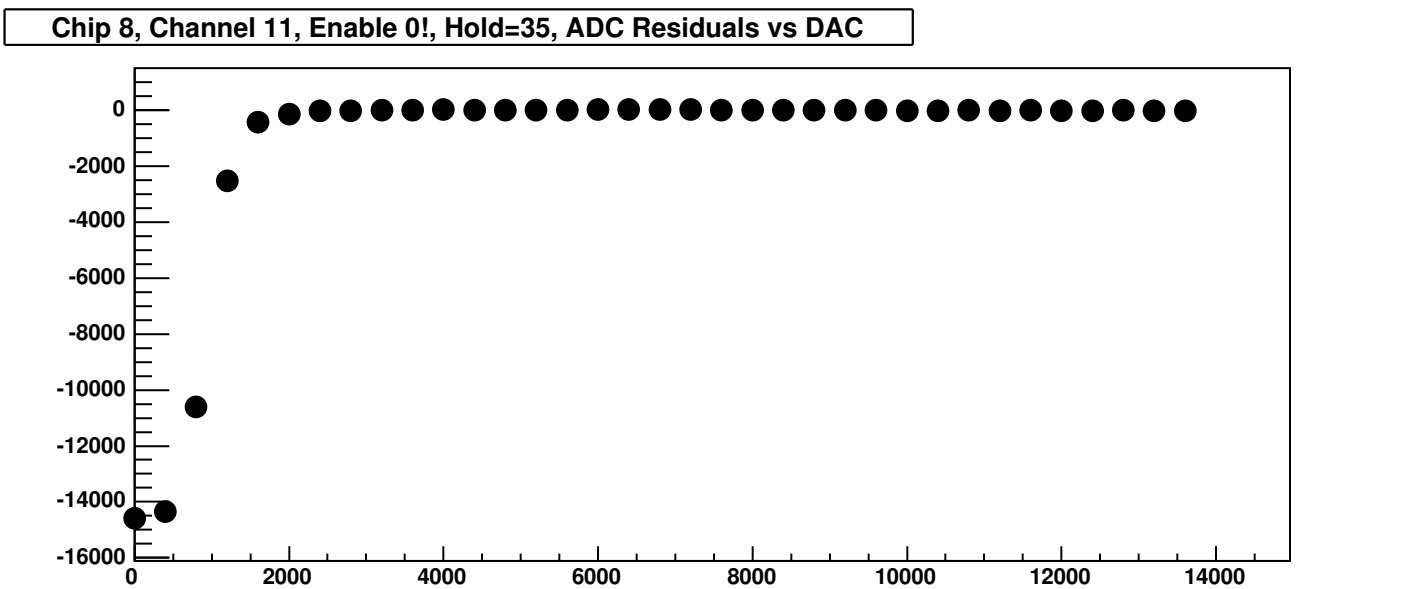

Chip 8, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2081 / 23
p0 -155.1 ± 1.178
p1 0.1567 ± 0.000232
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

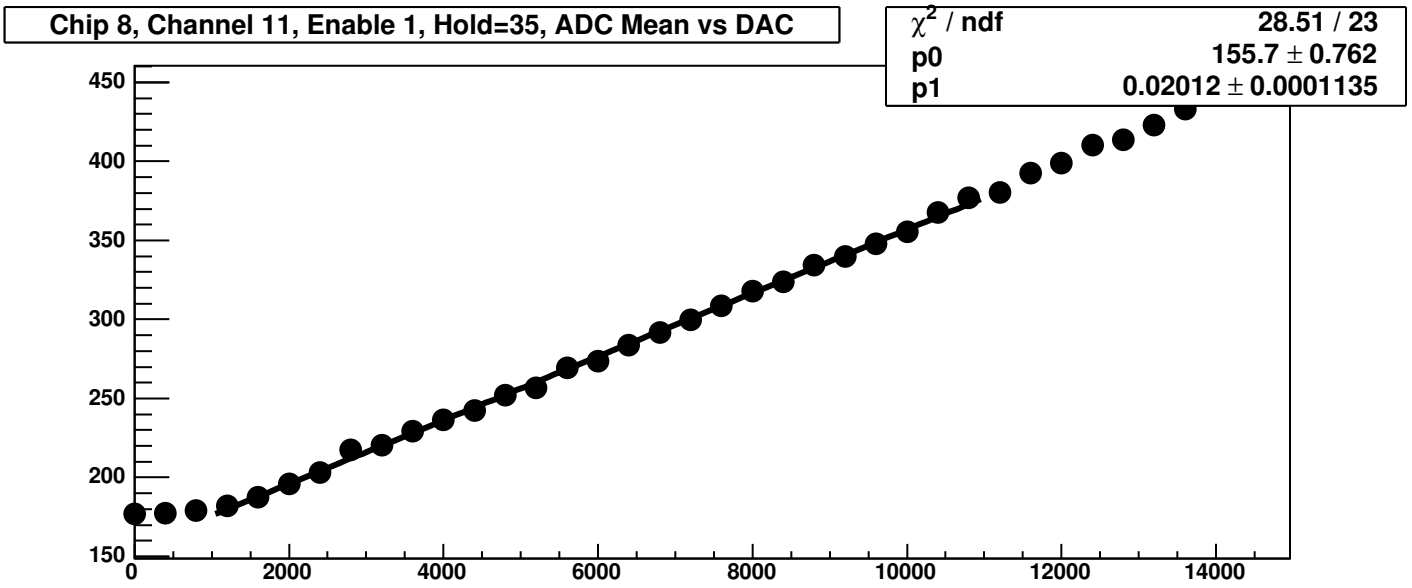

Chip 8, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

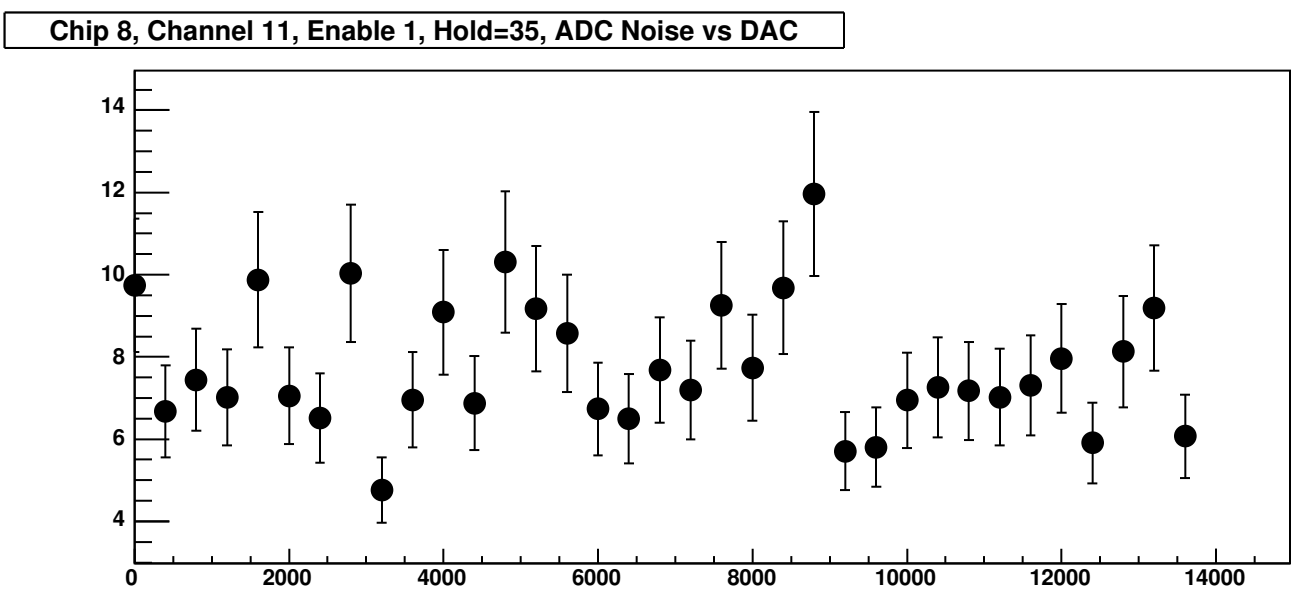

Chip 8, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

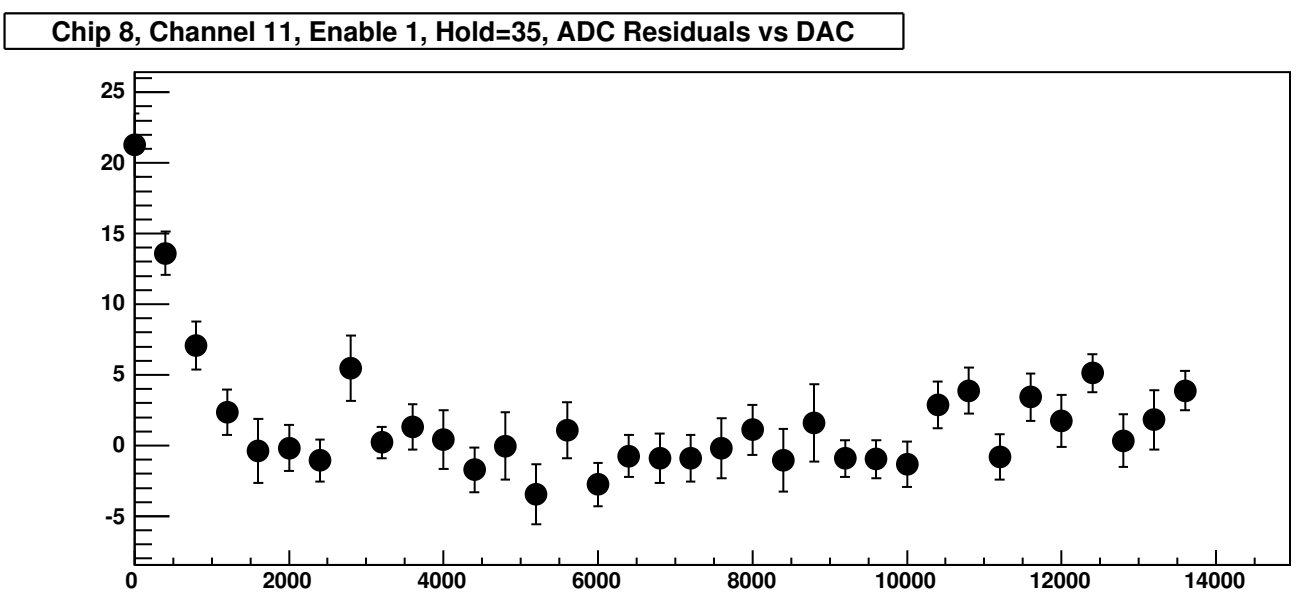

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1361 / 23
p0 3.088e+04 ± 4.383
p1 -0.003234 ± 0.0006093
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

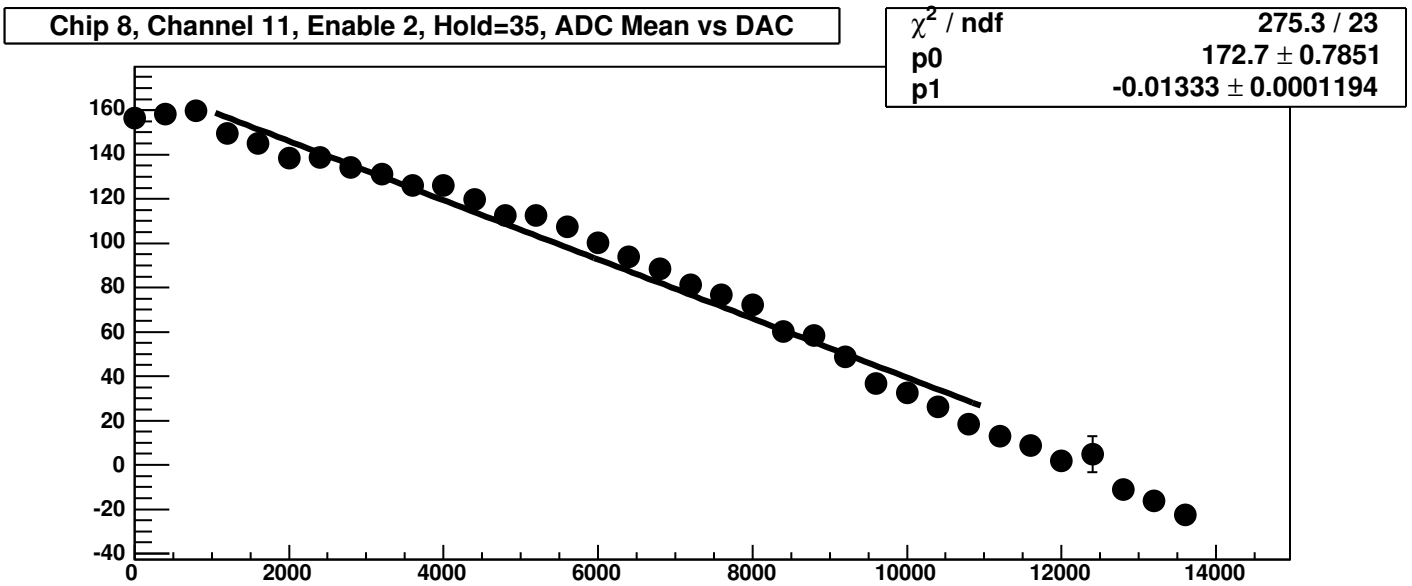

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

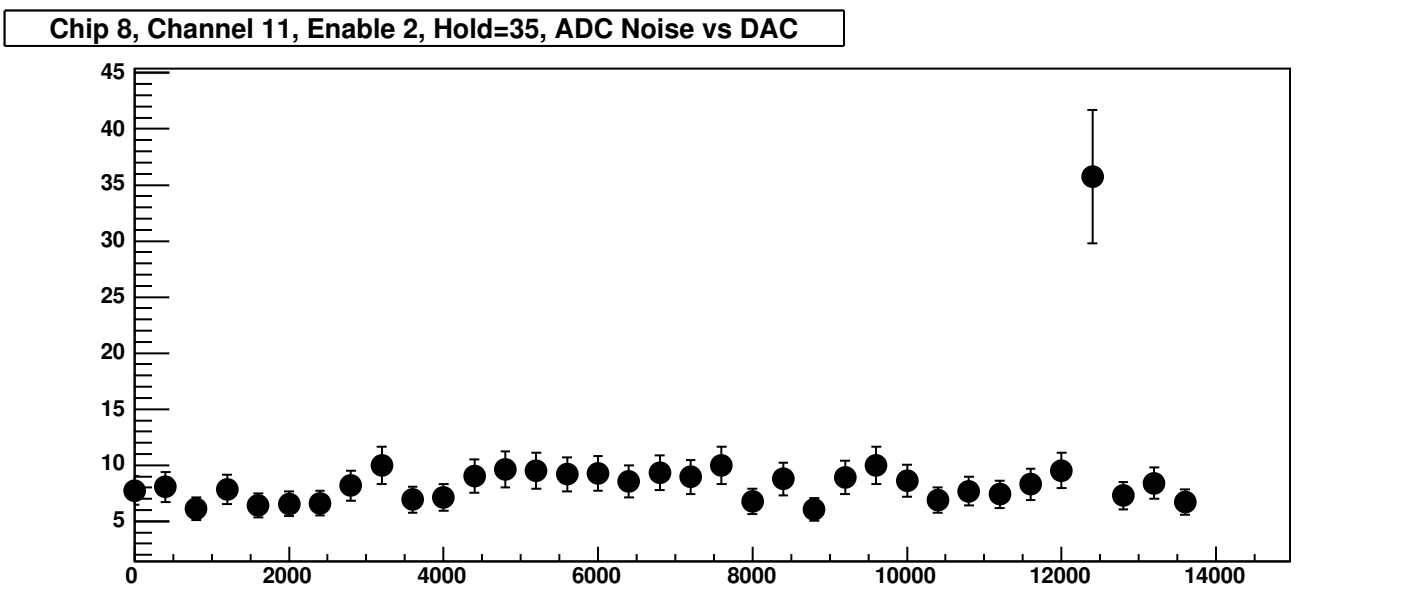

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

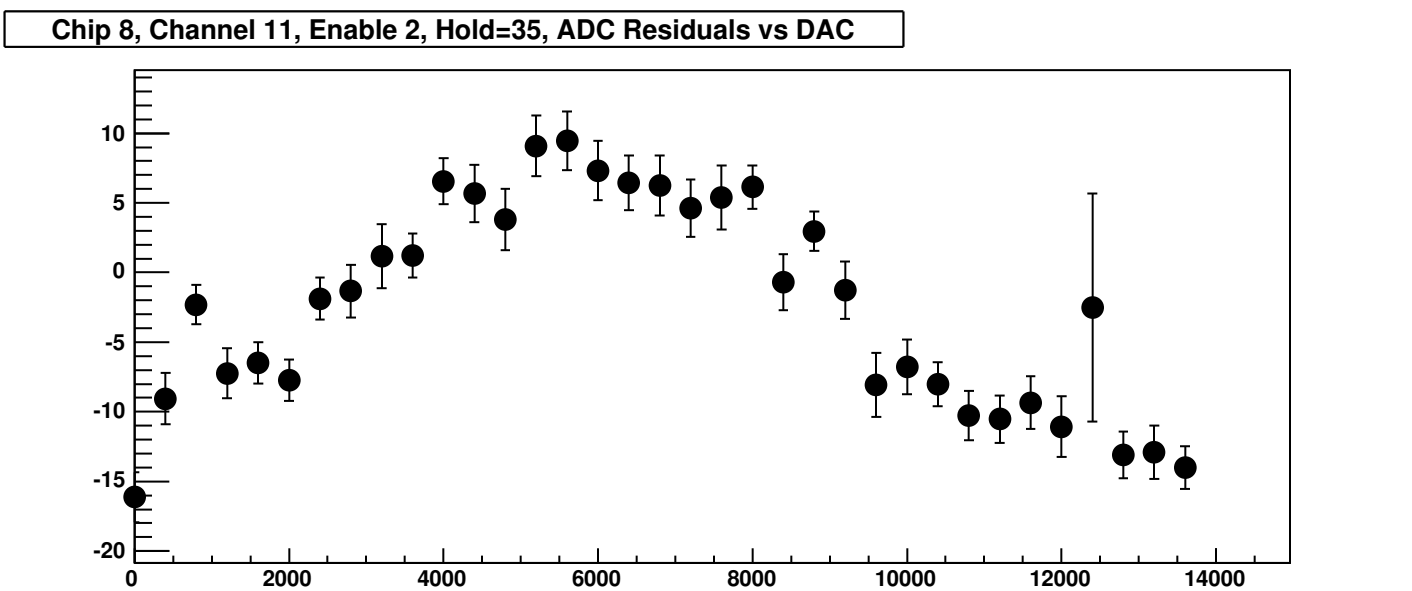

Chip 8, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.51 / 23
p0 155.7 ± 0.762
p1 0.02012 ± 0.0001135
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

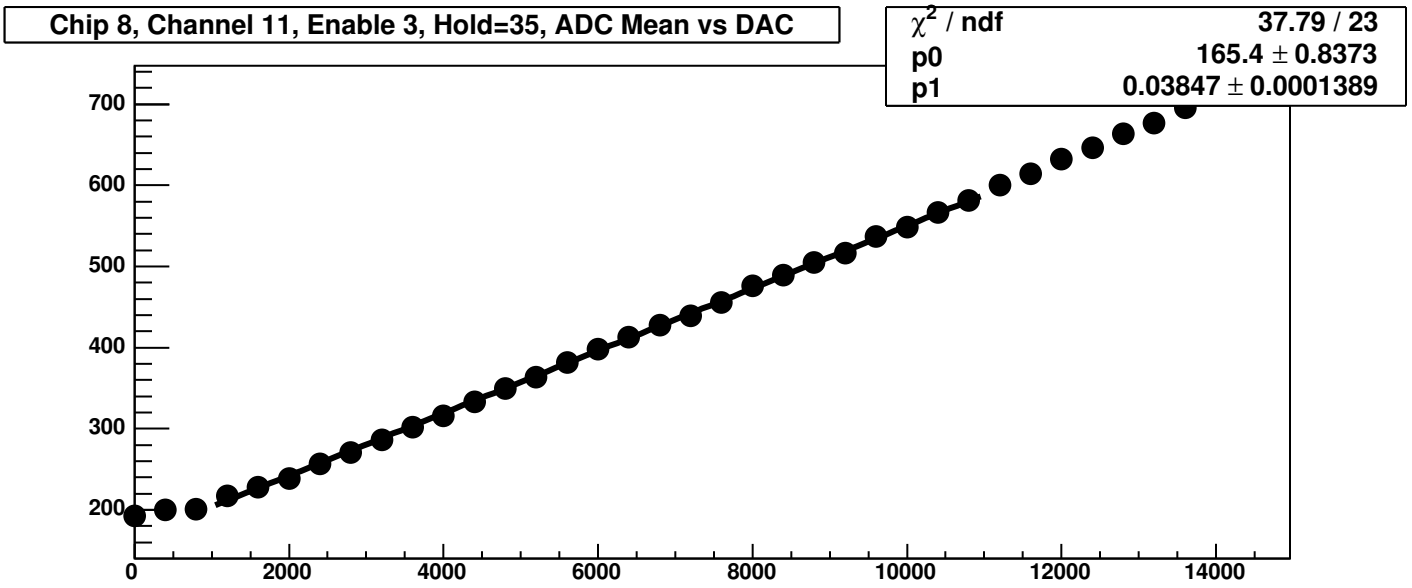

Chip 8, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

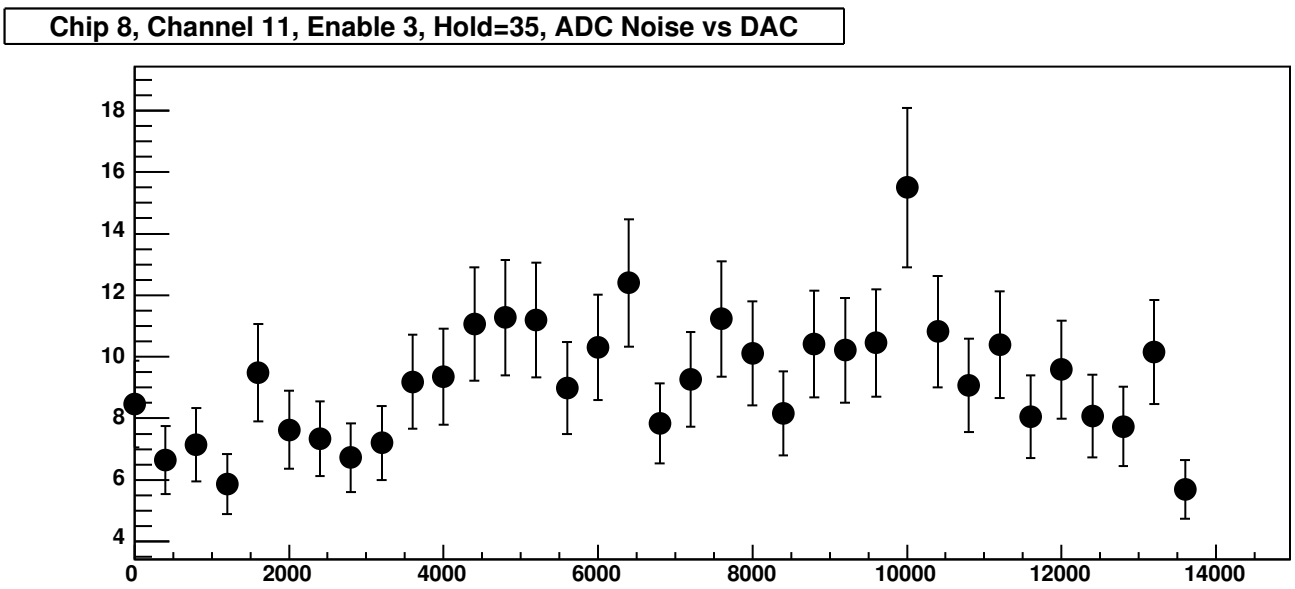

Chip 8, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

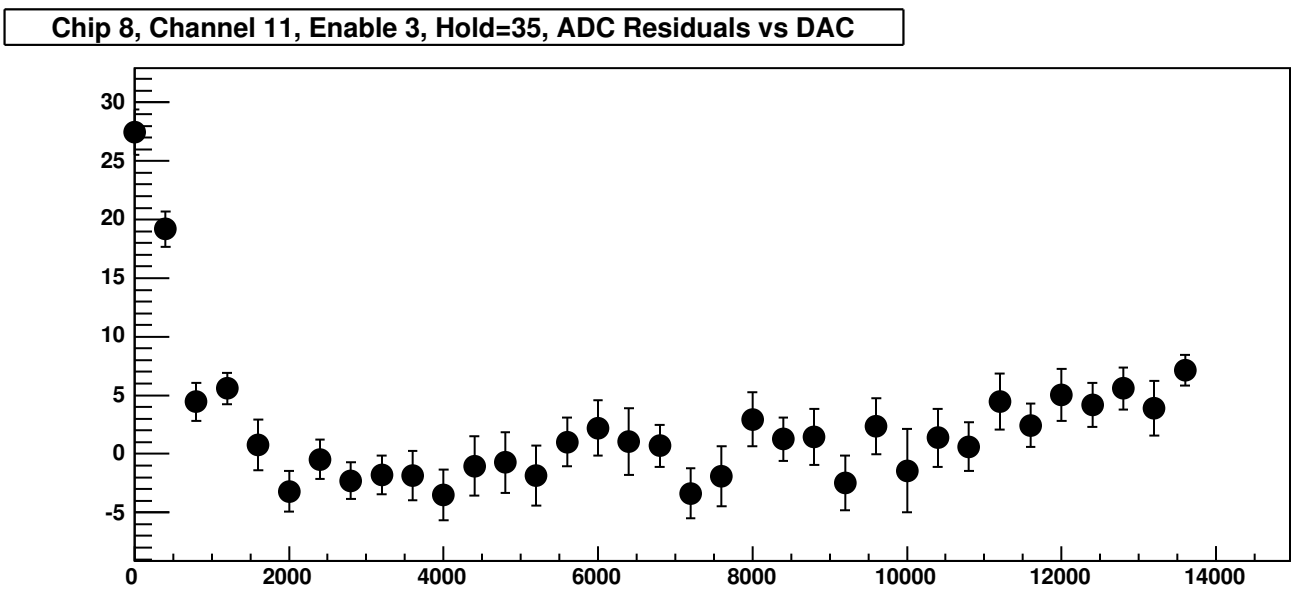

Chip 8, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 275.3 / 23
p0 172.7 ± 0.7851
p1 -0.01333 ± 0.0001194
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

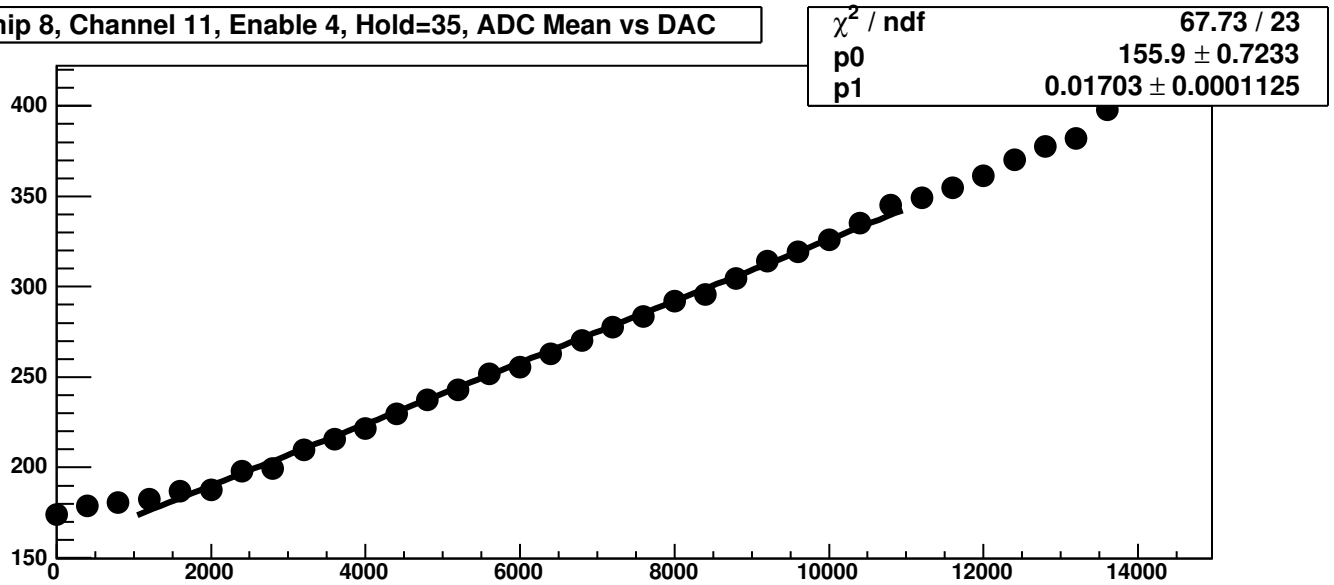

Chip 8, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.79 / 23
p0 165.4 ± 0.8373
p1 0.03847 ± 0.0001389
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

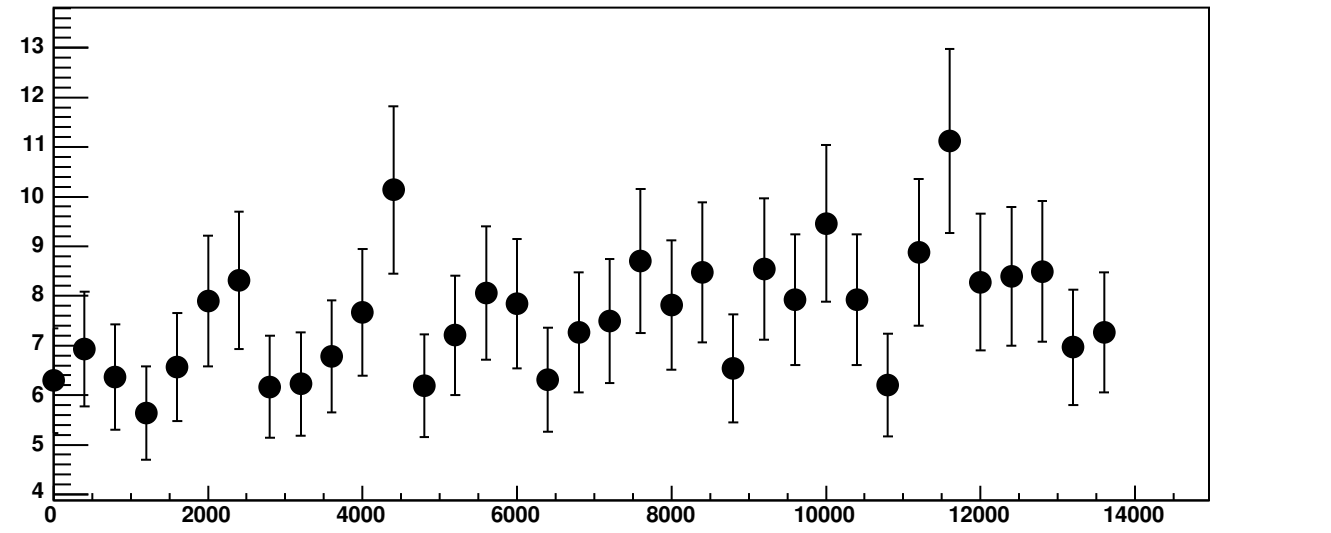

Chip 8, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

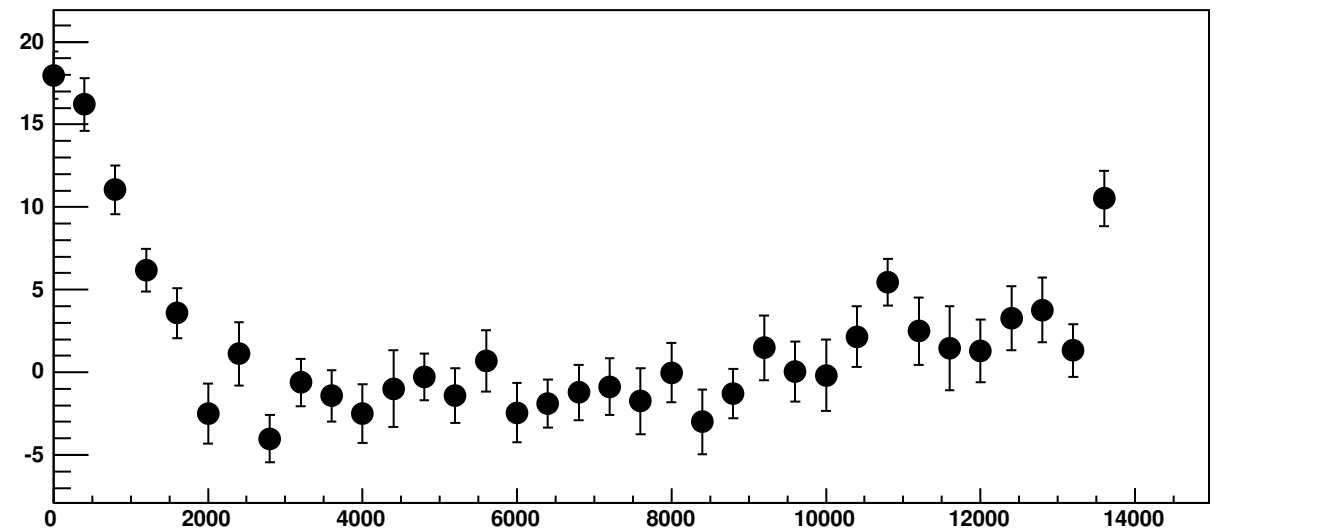
Chip 8, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



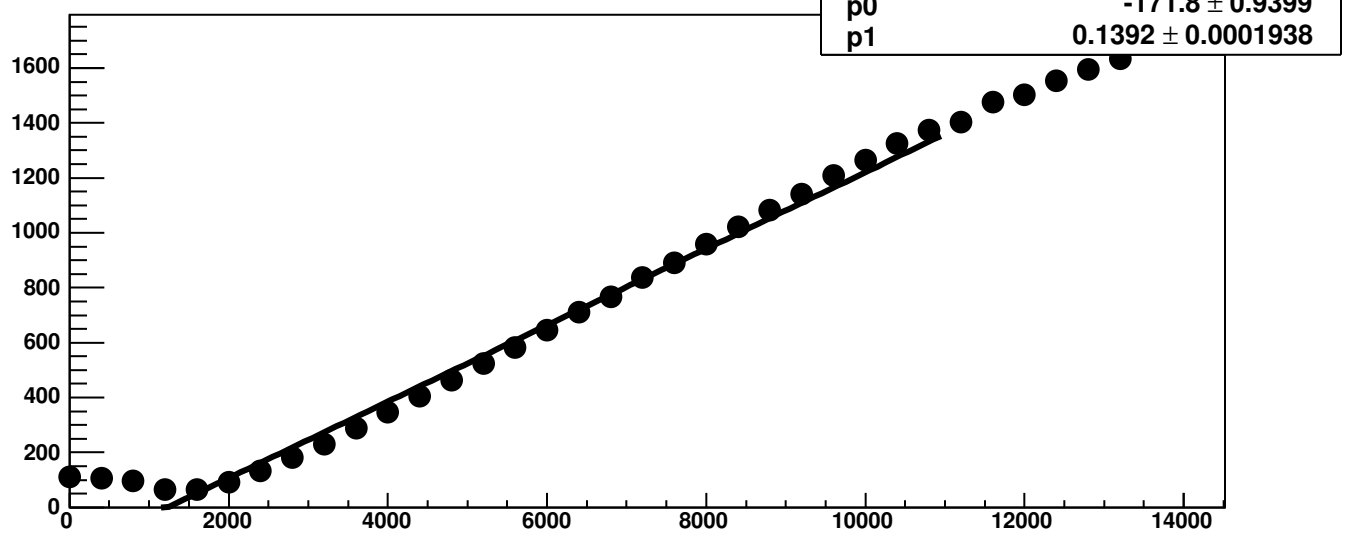
Chip 8, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



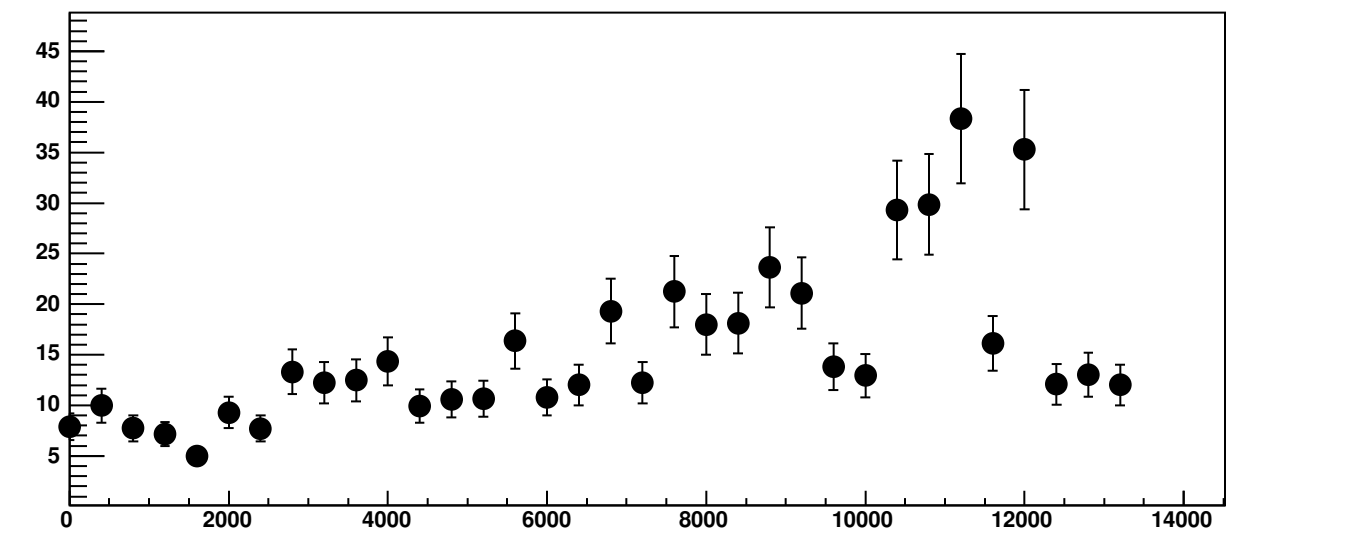
Chip 8, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



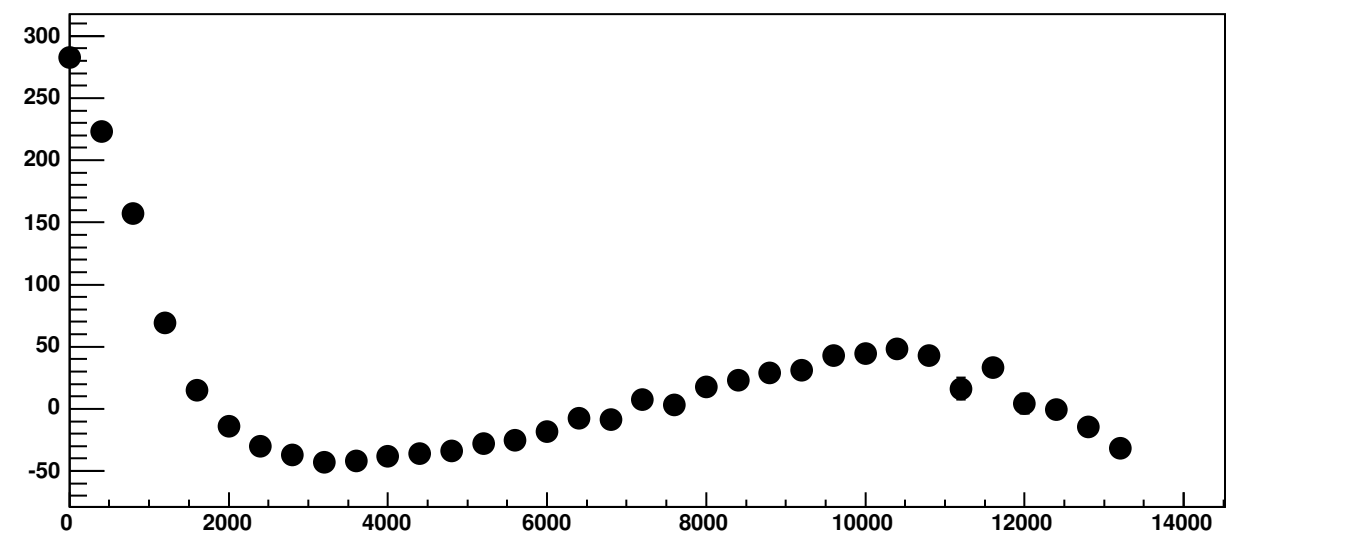
Chip 8, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

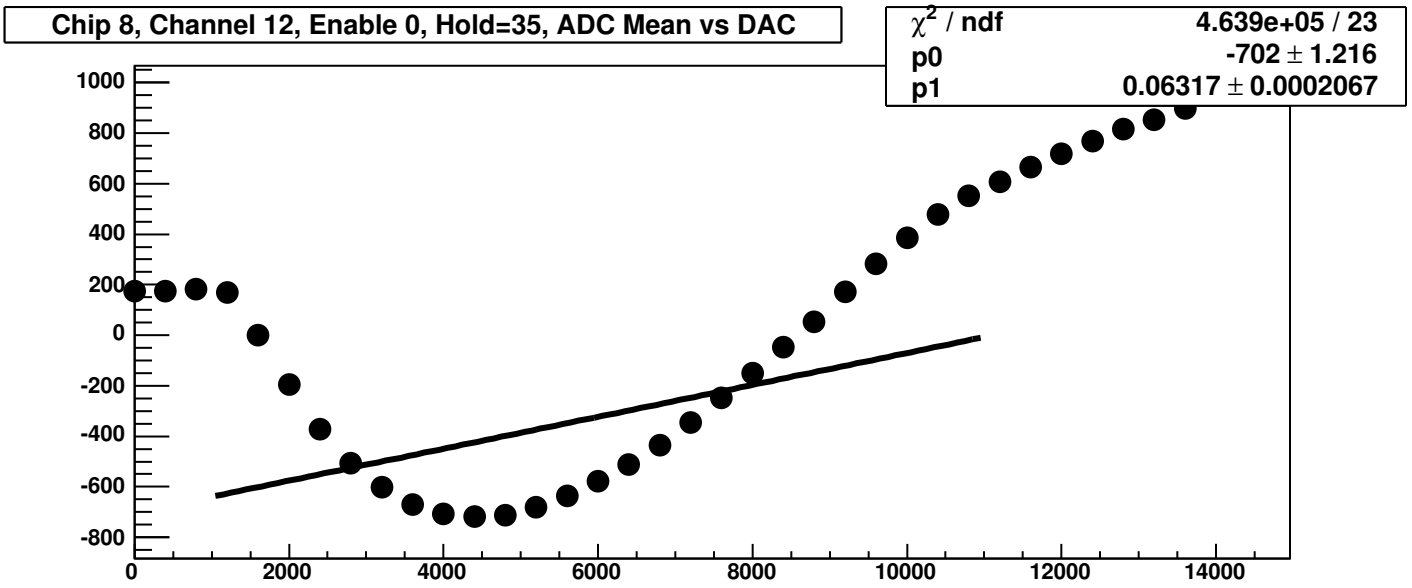


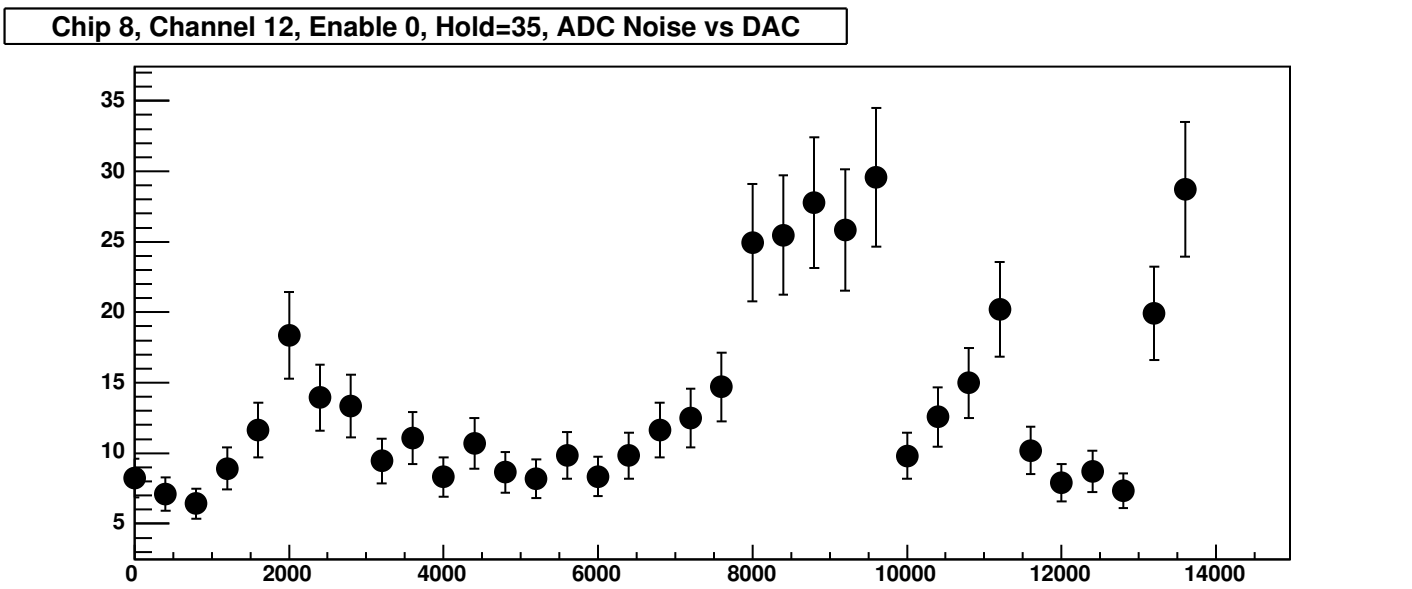
Chip 8, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

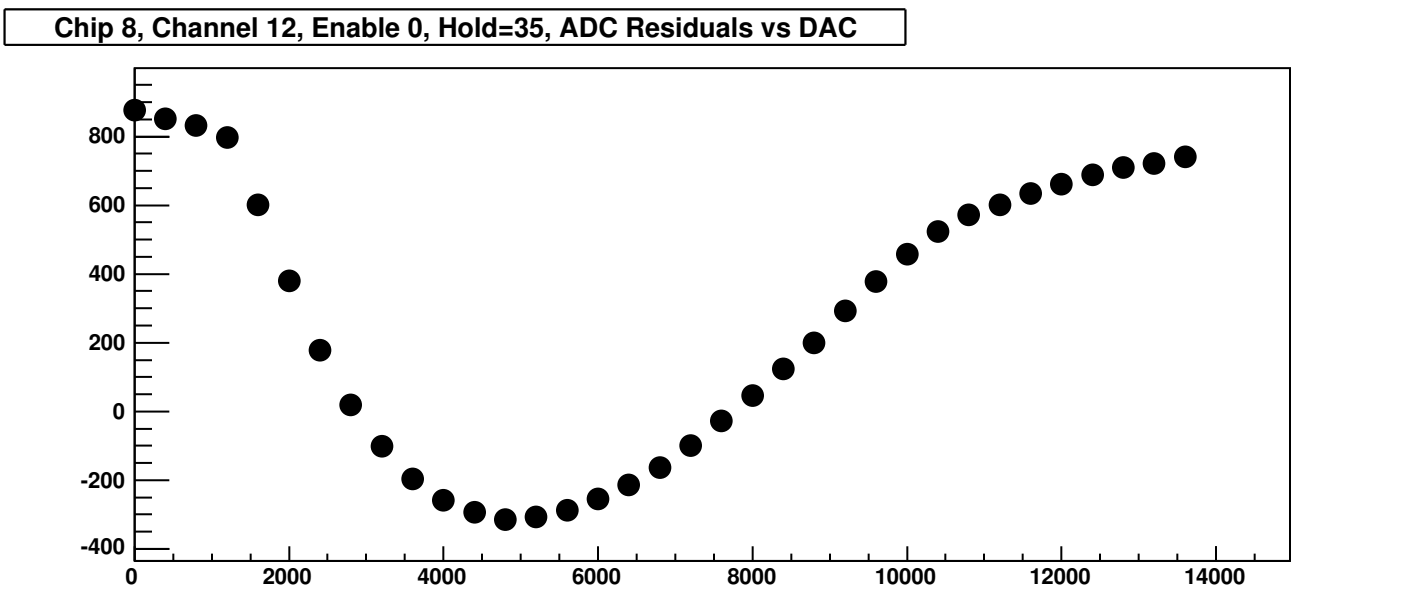


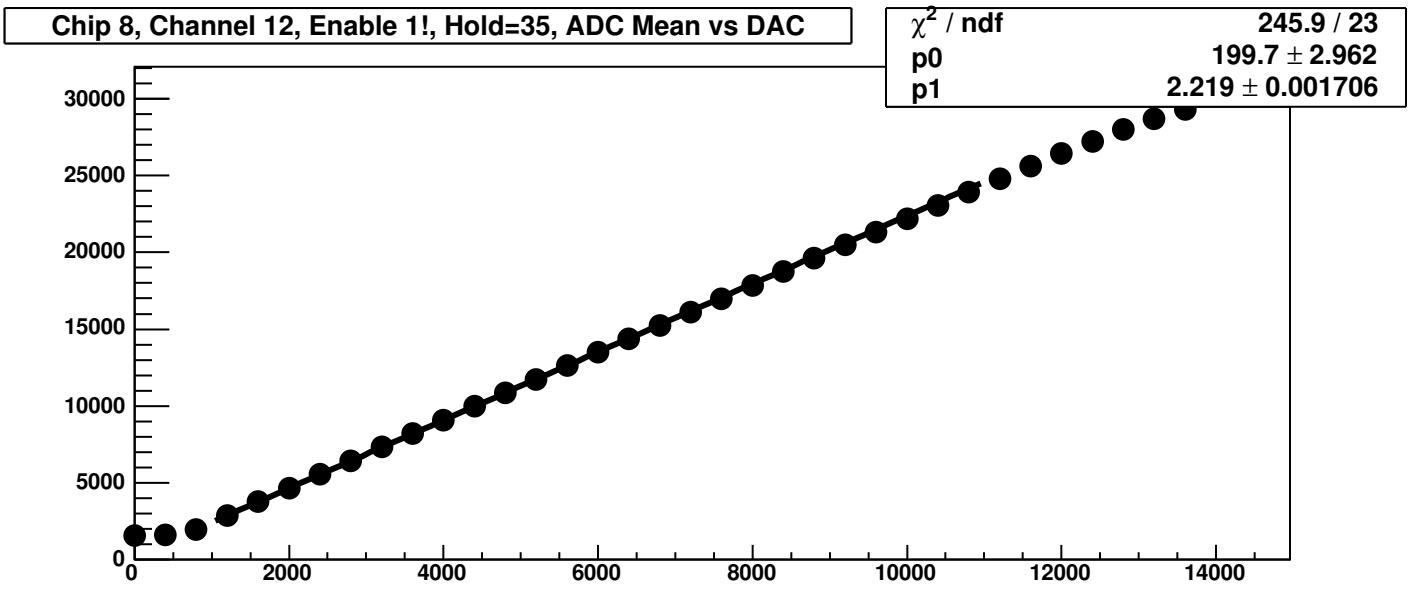
Chip 8, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

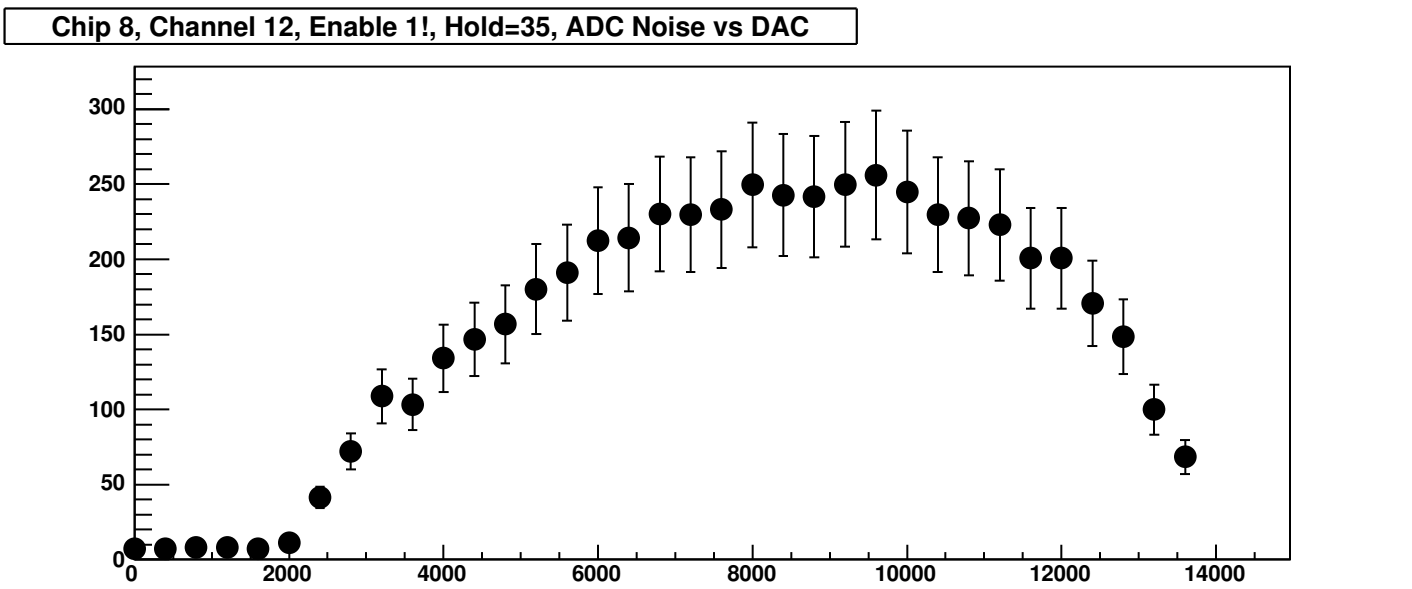


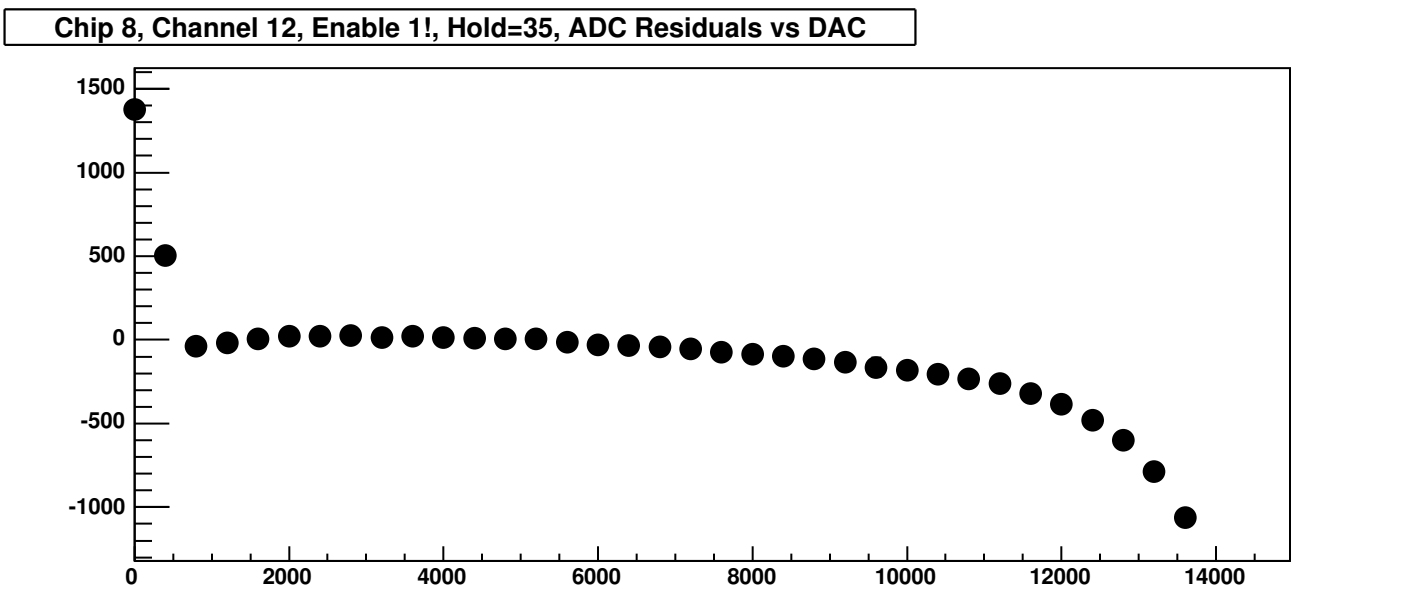

Chip 8, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.639e+05 / 23
p0 -702 ± 1.216
p1 0.06317 ± 0.0002067
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

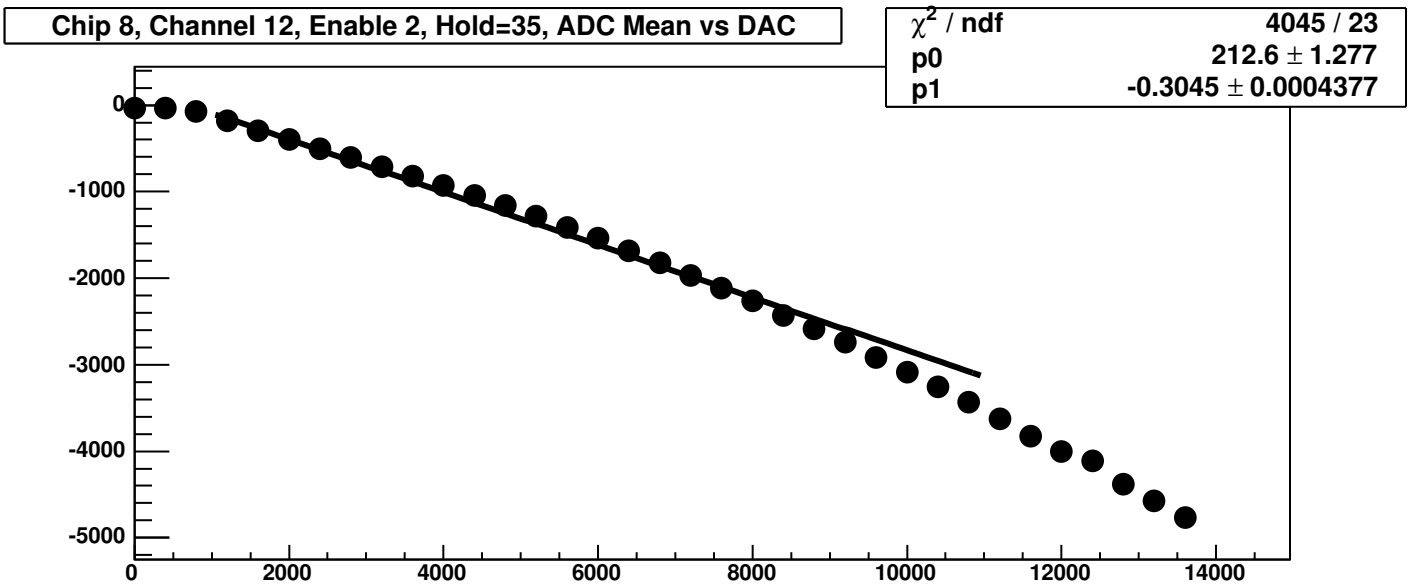

Chip 8, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

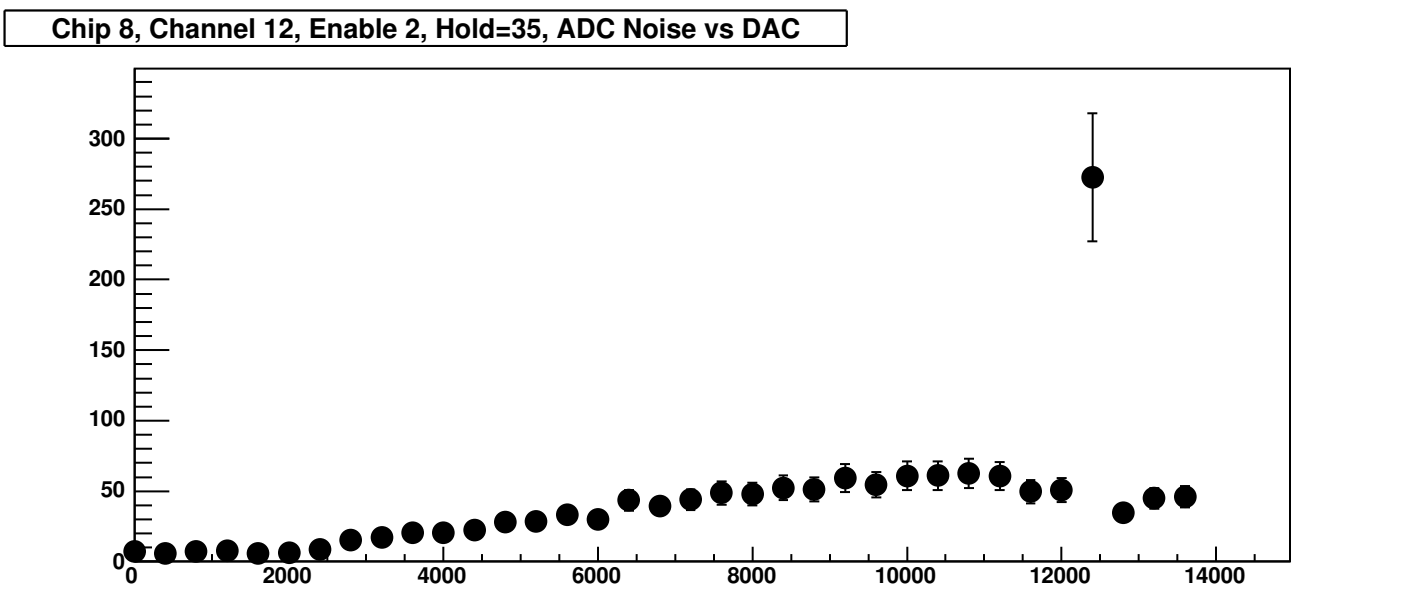

Chip 8, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

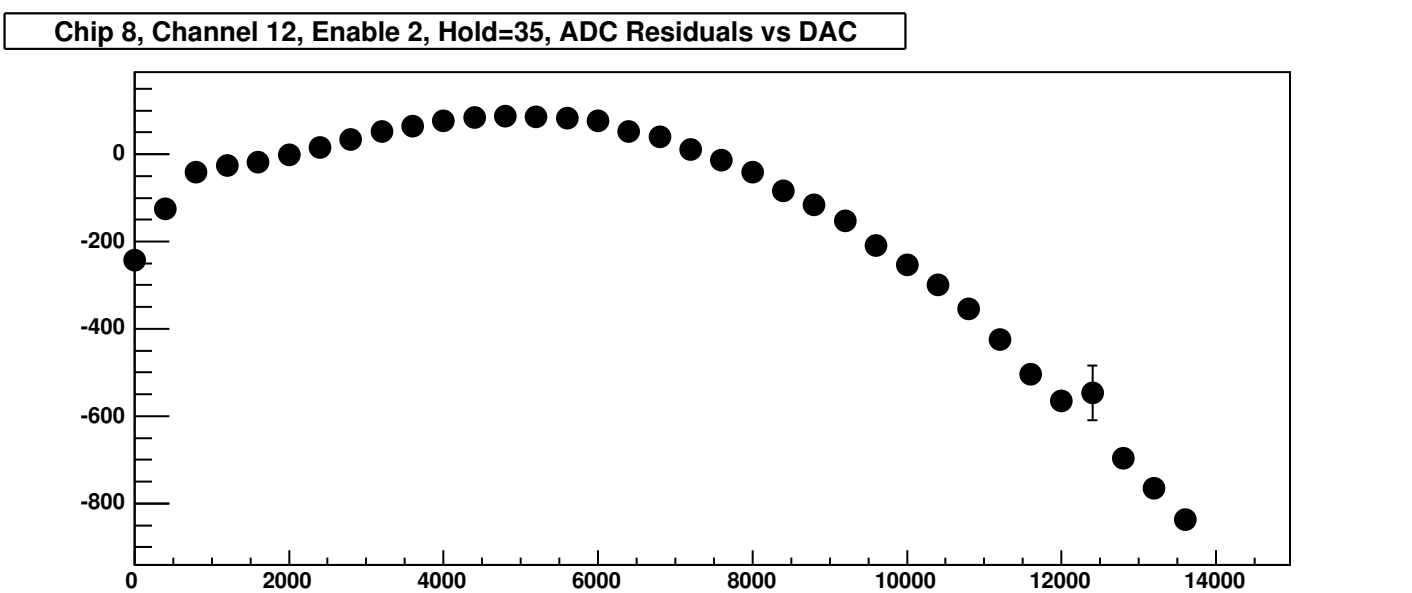

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 245.9 / 23
p0 199.7 ± 2.962
p1 2.219 ± 0.001706
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

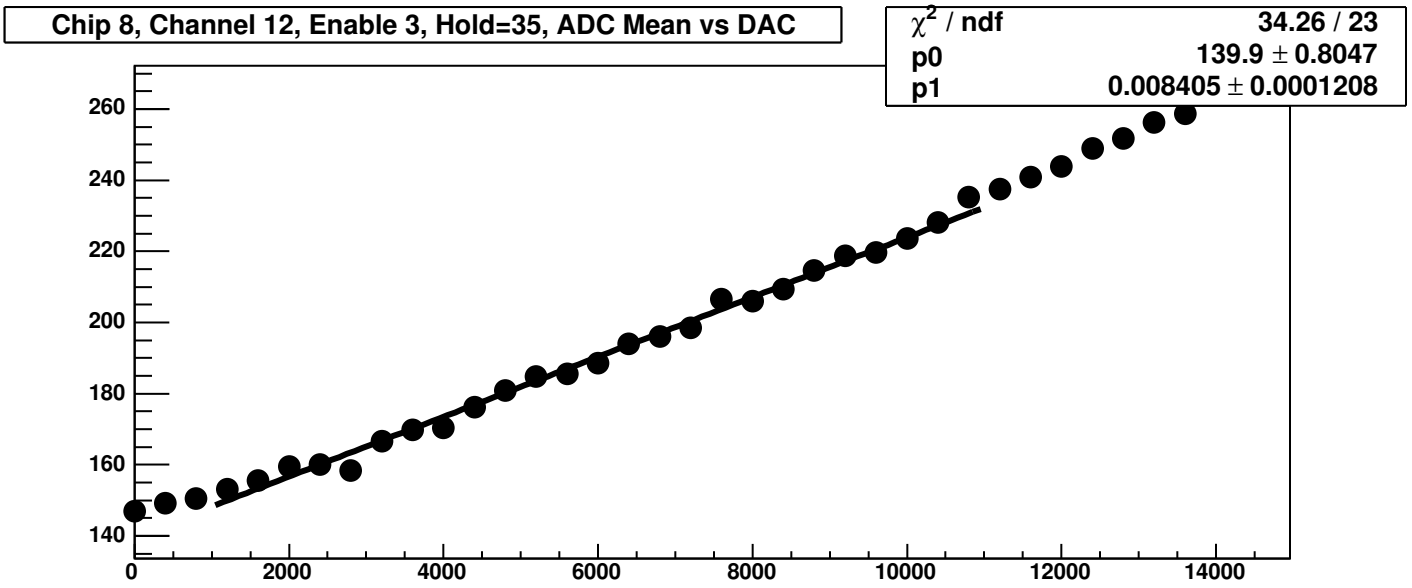

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

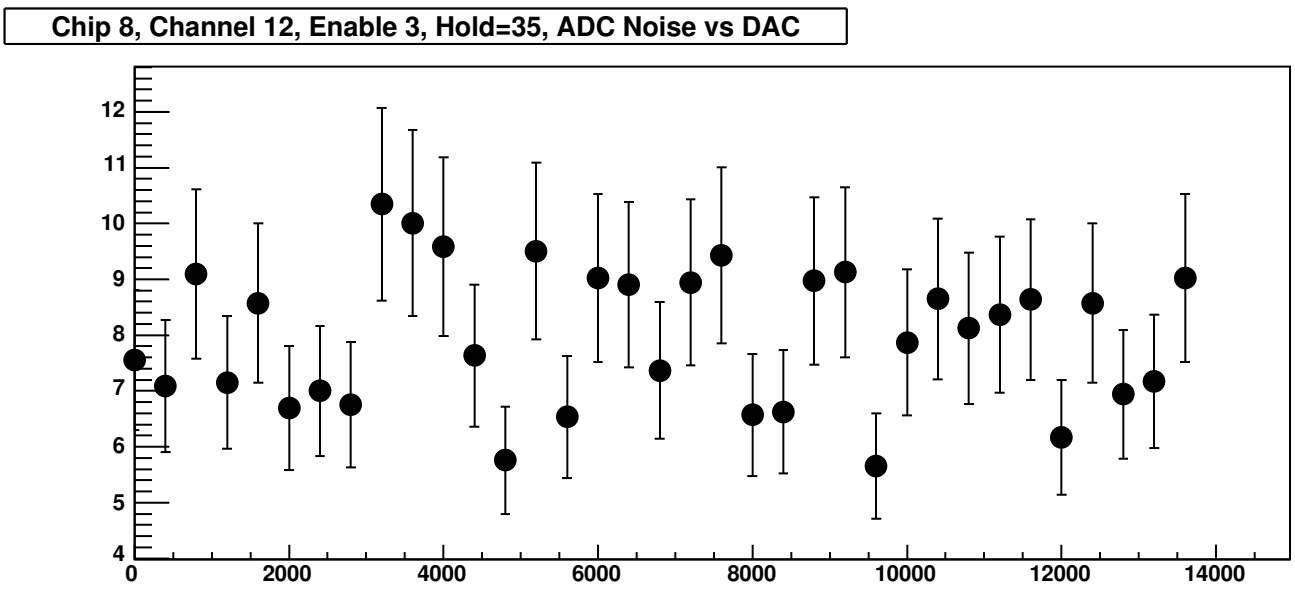

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

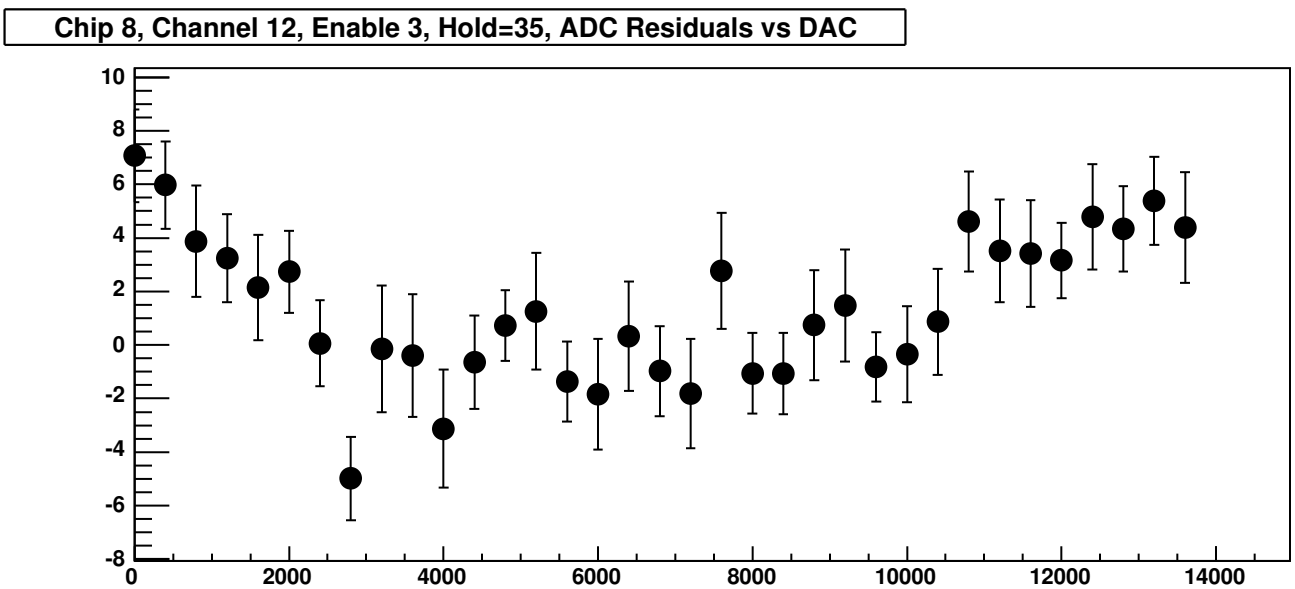

Chip 8, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4045 / 23
p0 212.6 ± 1.277
p1 -0.3045 ± 0.0004377
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

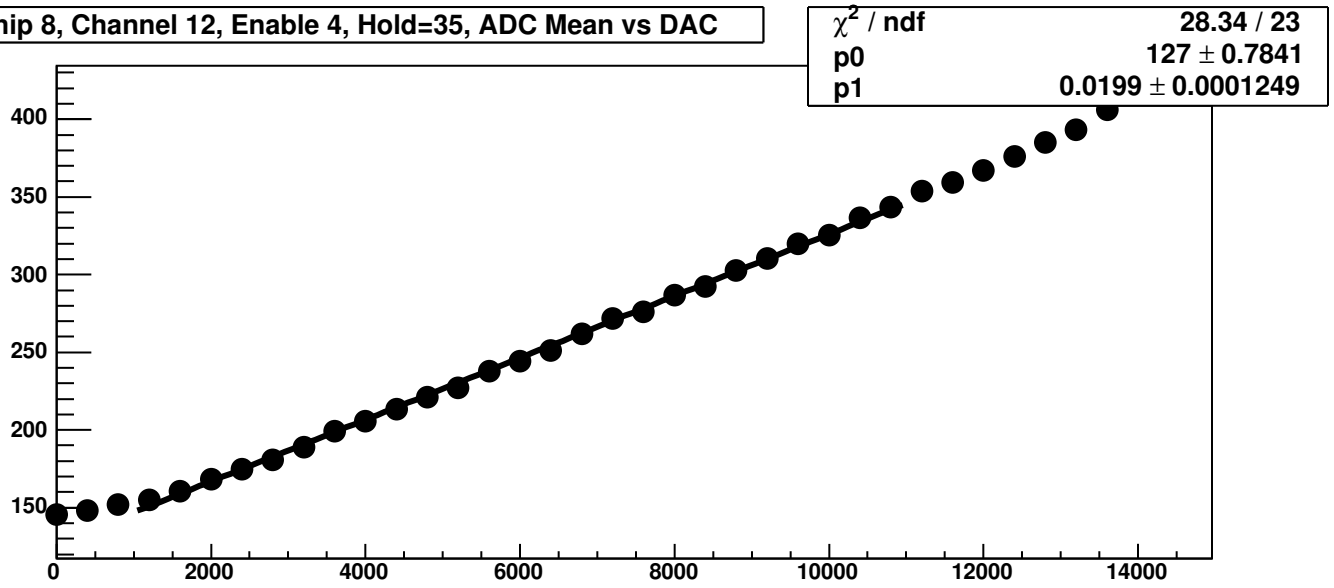

Chip 8, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.26 / 23
p0 139.9 ± 0.8047
p1 0.008405 ± 0.0001208
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000

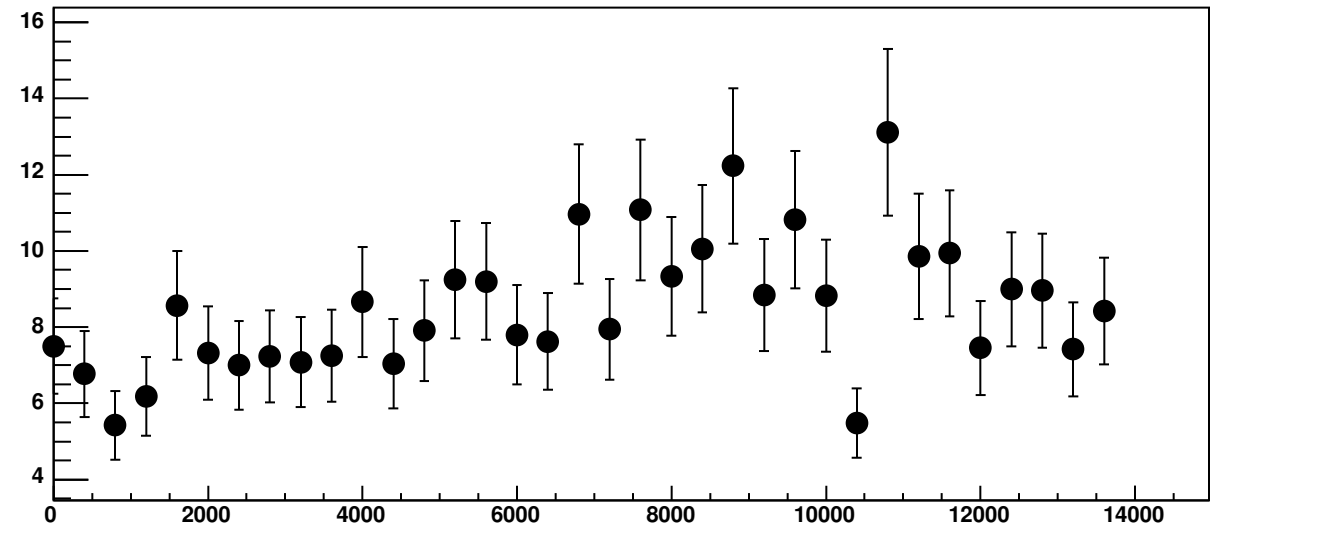

Chip 8, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

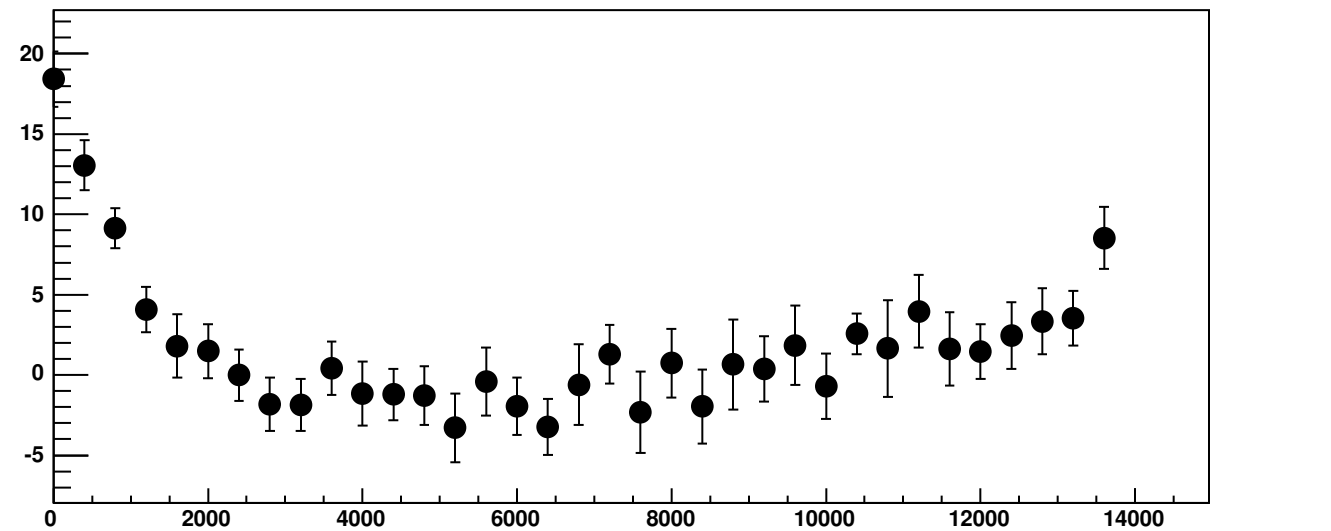
Chip 8, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



Chip 8, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC

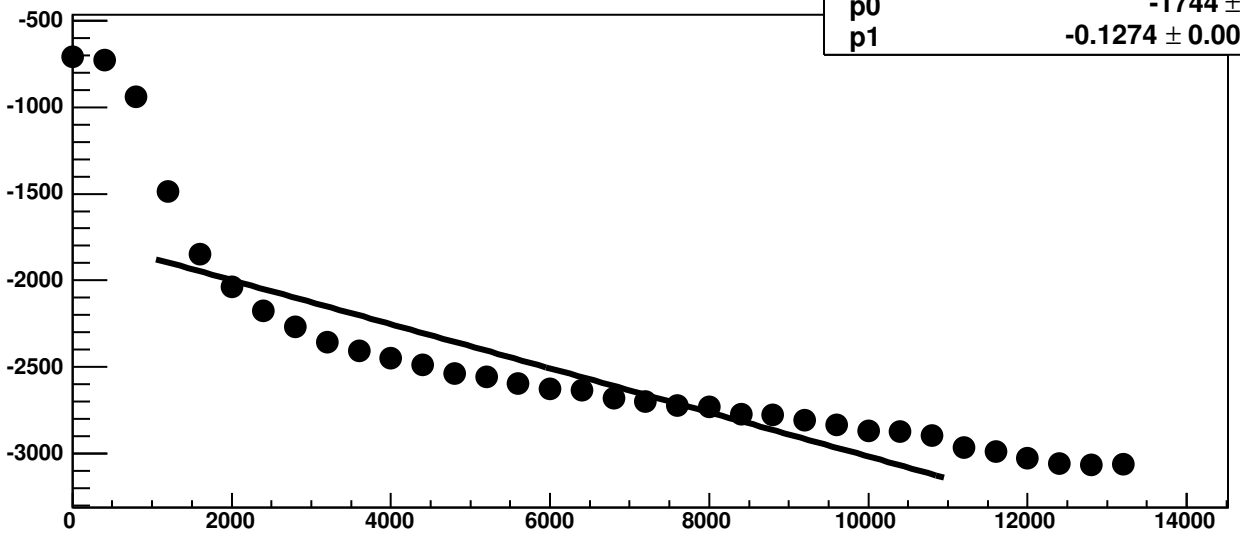


Chip 8, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

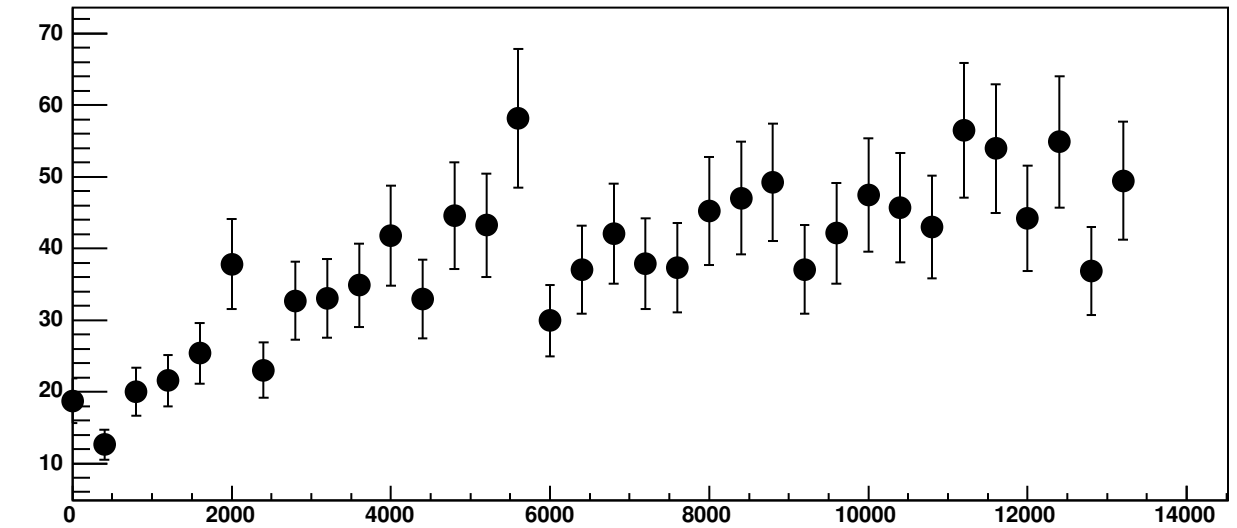


Chip 8, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

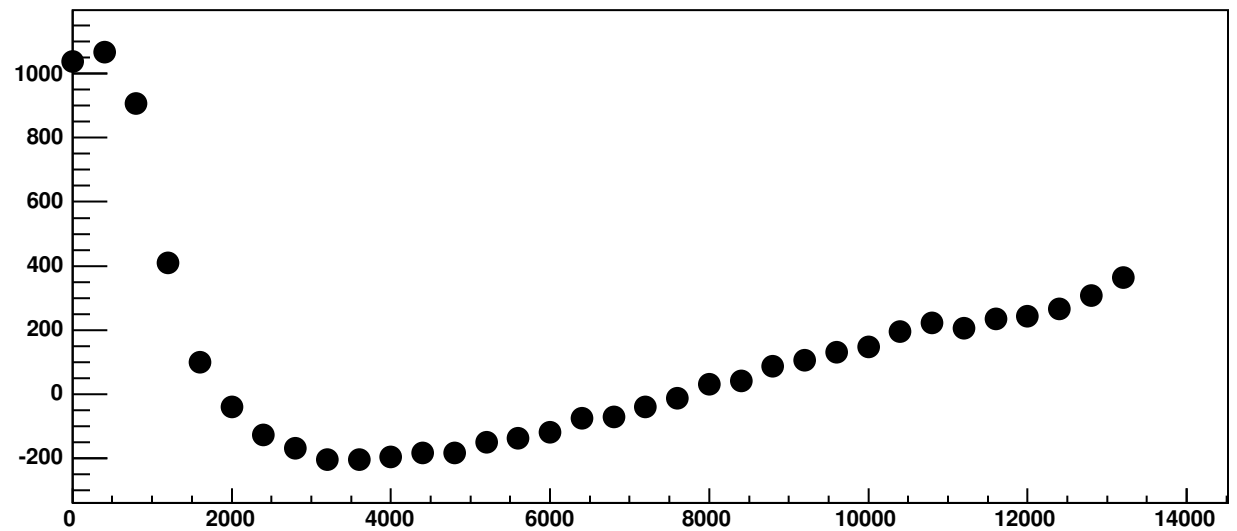
χ^2 / ndf	1.323e+04 / 23
p0	-1744 ± 3.159
p1	-0.1274 ± 0.0005495

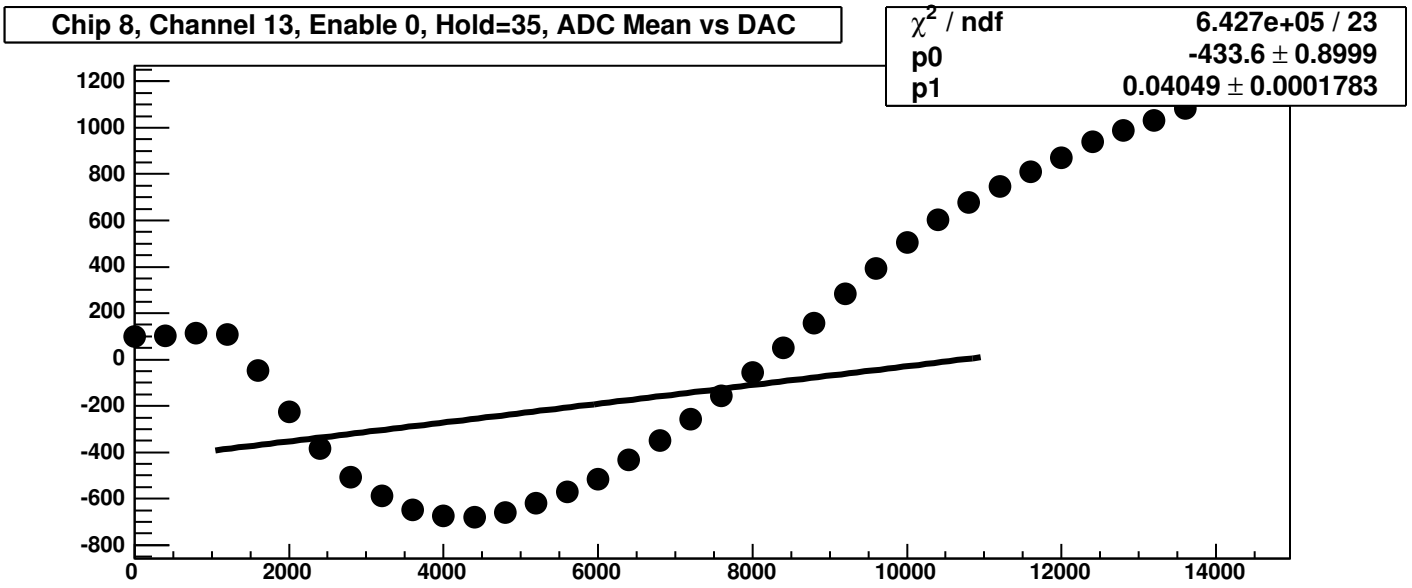


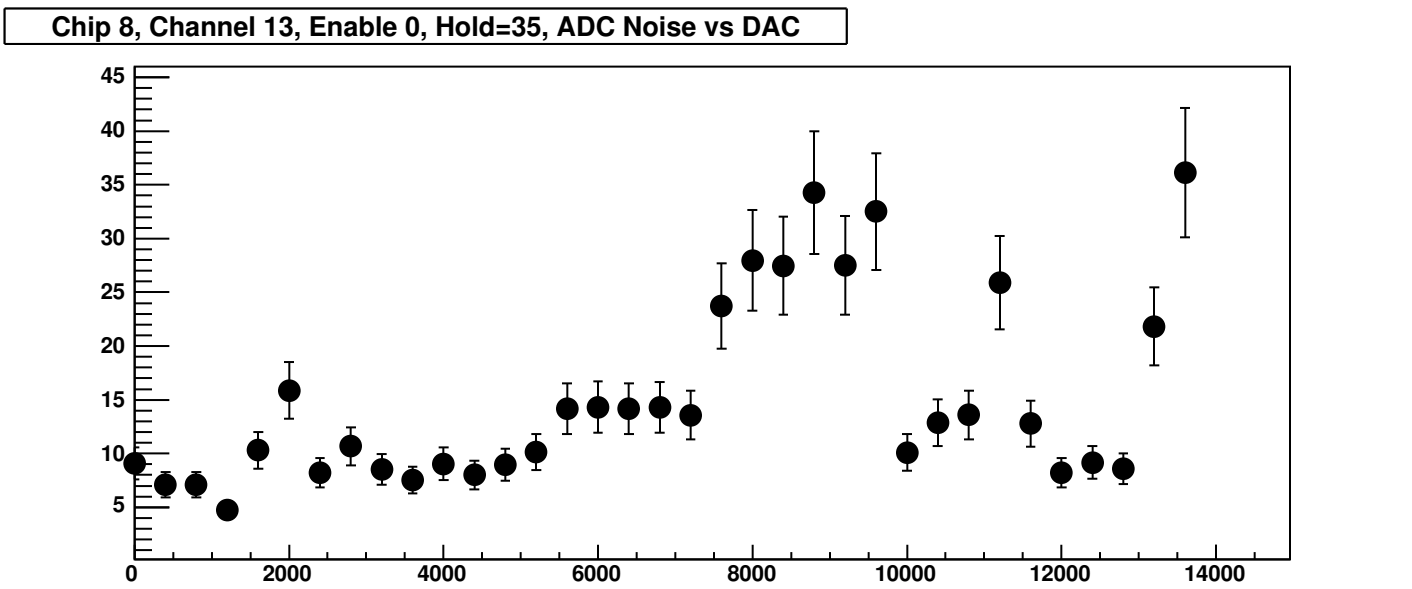
Chip 8, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

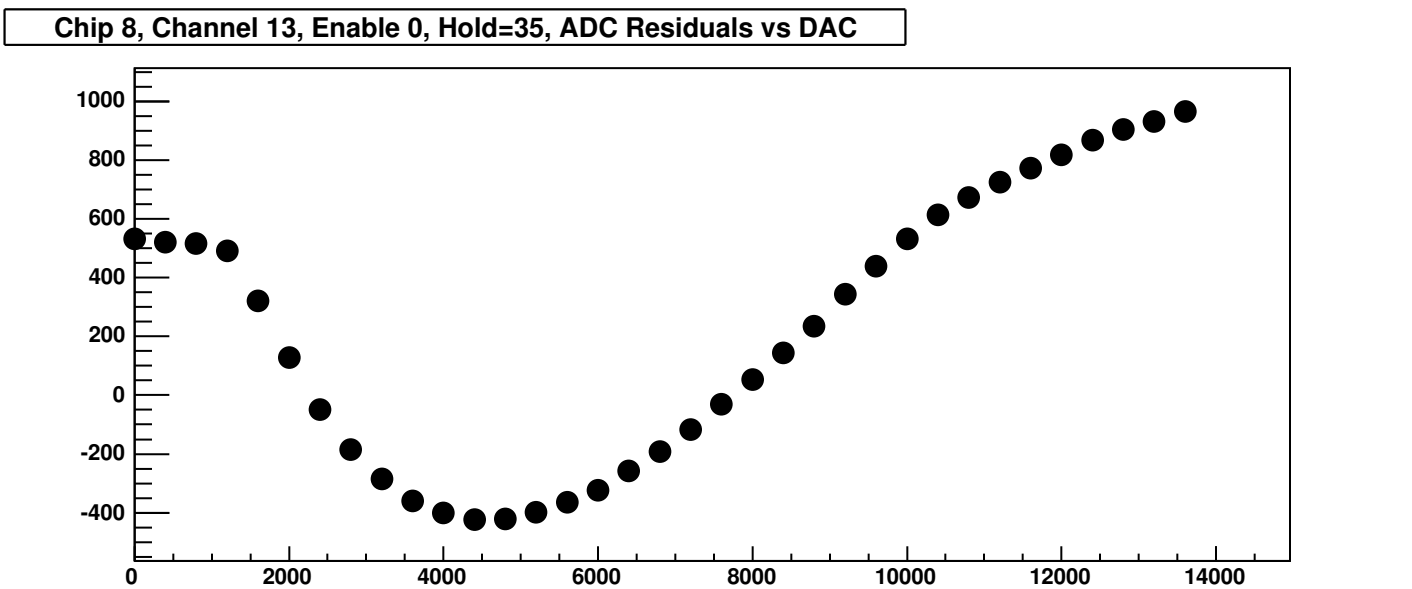


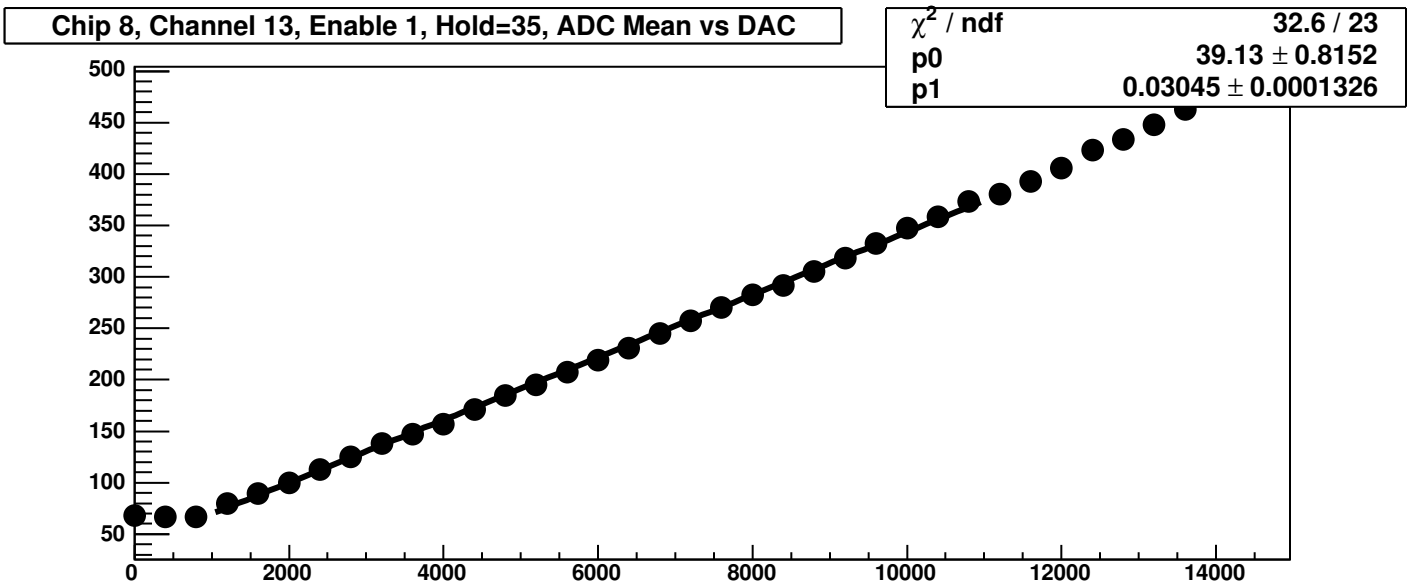
Chip 8, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

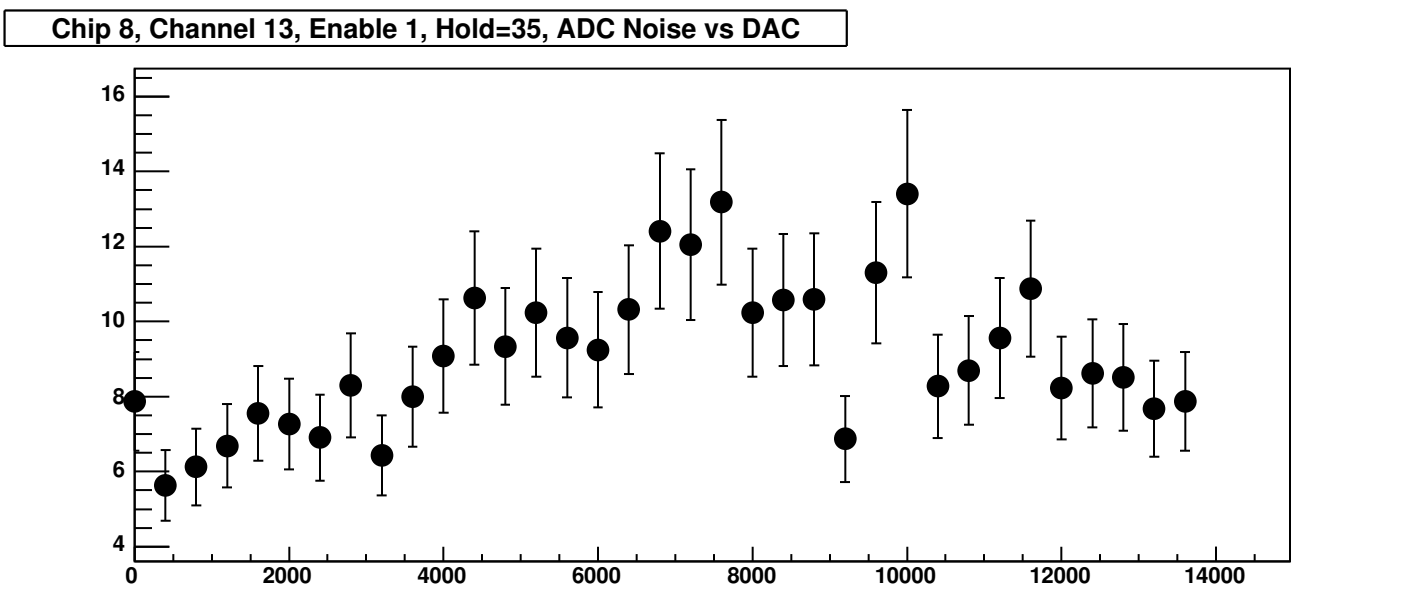


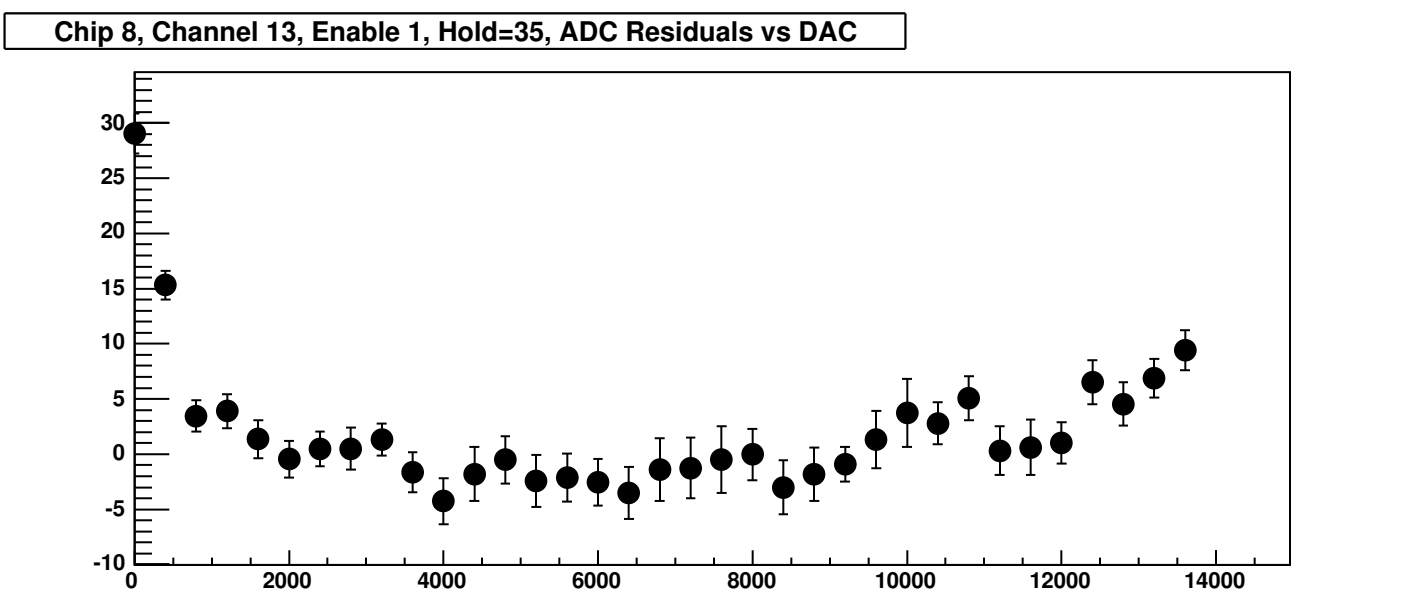

Chip 8, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.427e+05 / 23
p0 -433.6 ± 0.8999
p1 0.04049 ± 0.0001783
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

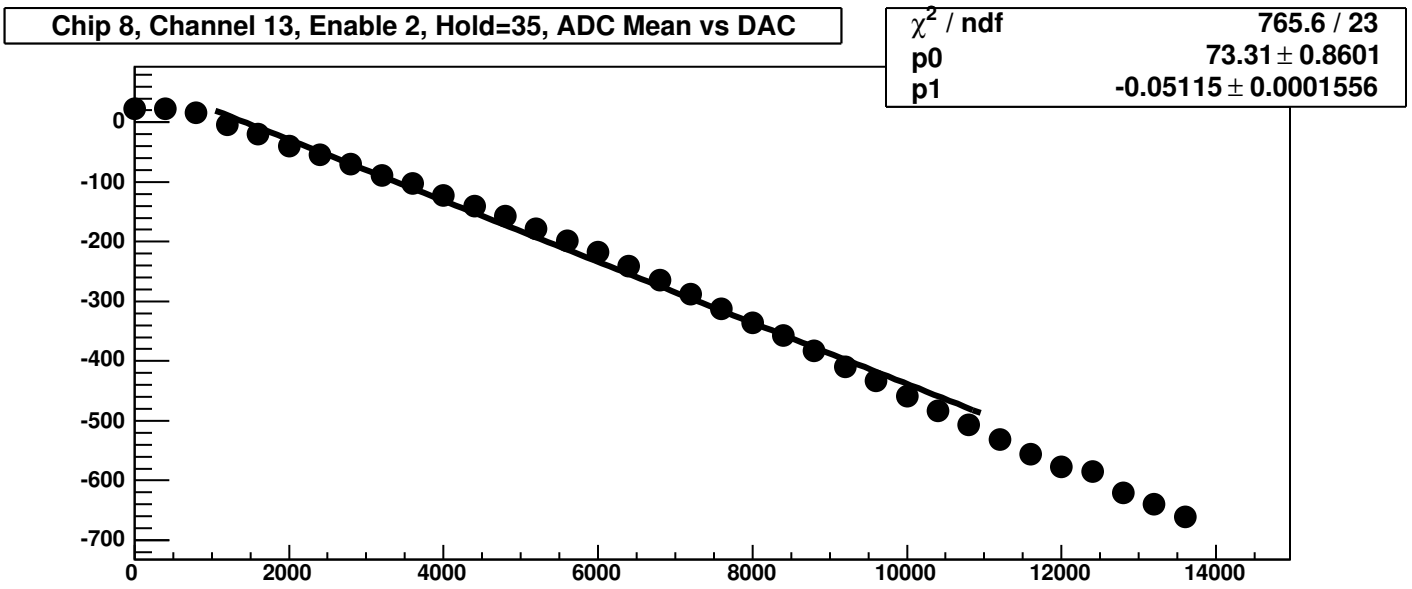

Chip 8, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

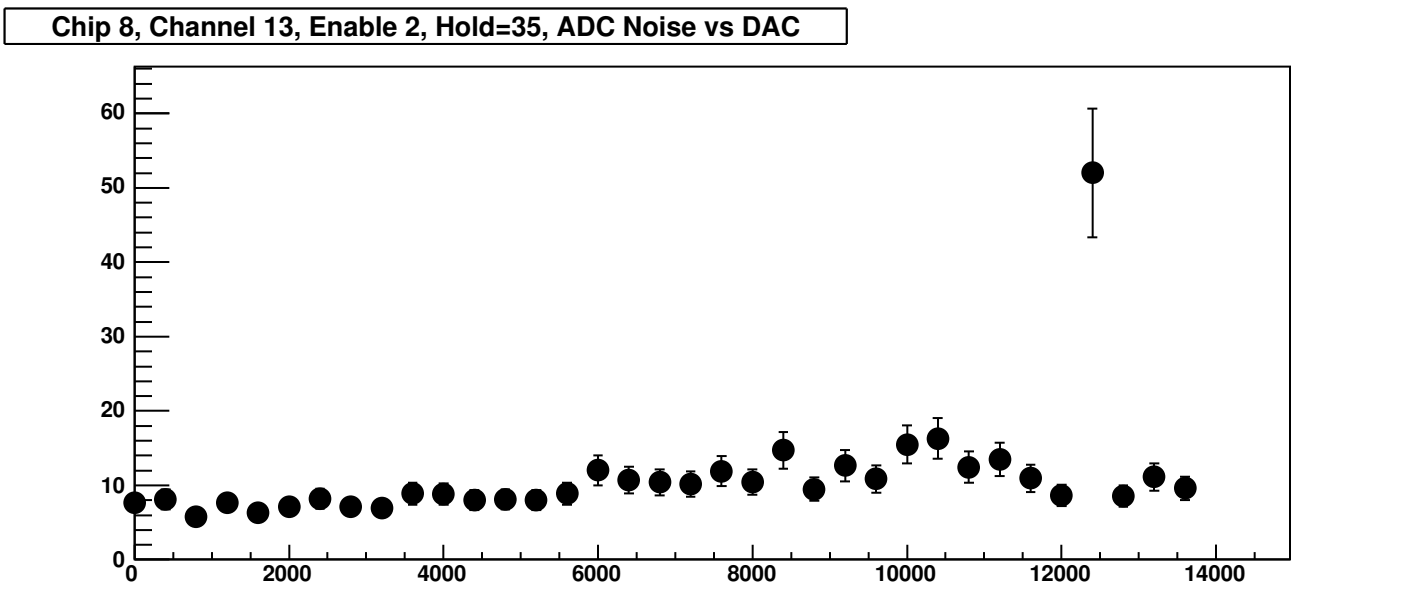

Chip 8, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

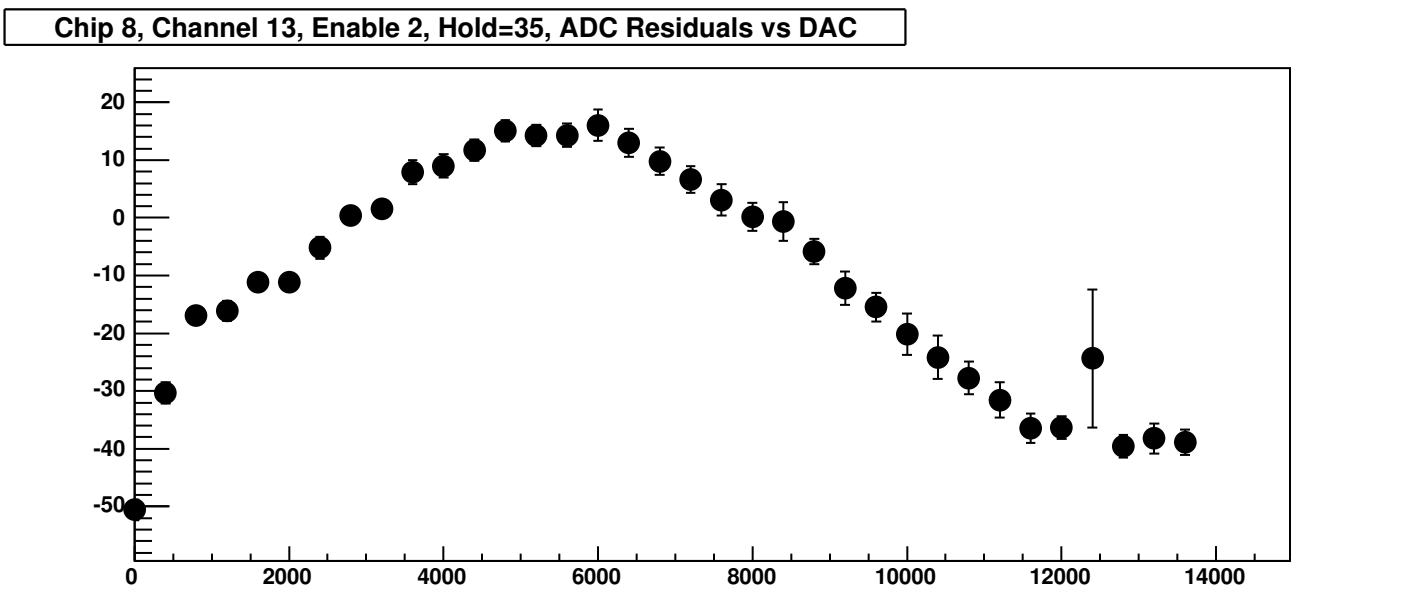

Chip 8, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.6 / 23
p0 39.13 ± 0.8152
p1 0.03045 ± 0.0001326
500
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

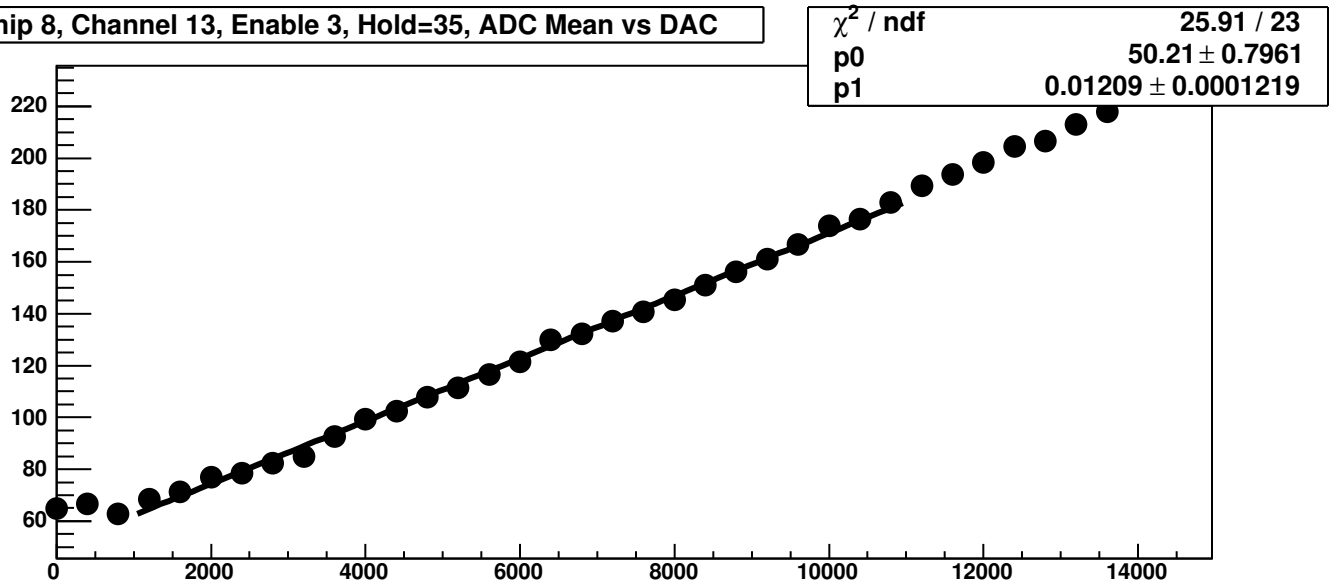

Chip 8, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 765.6 / 23
p0 73.31 ± 0.8601
p1 -0.05115 ± 0.0001556
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

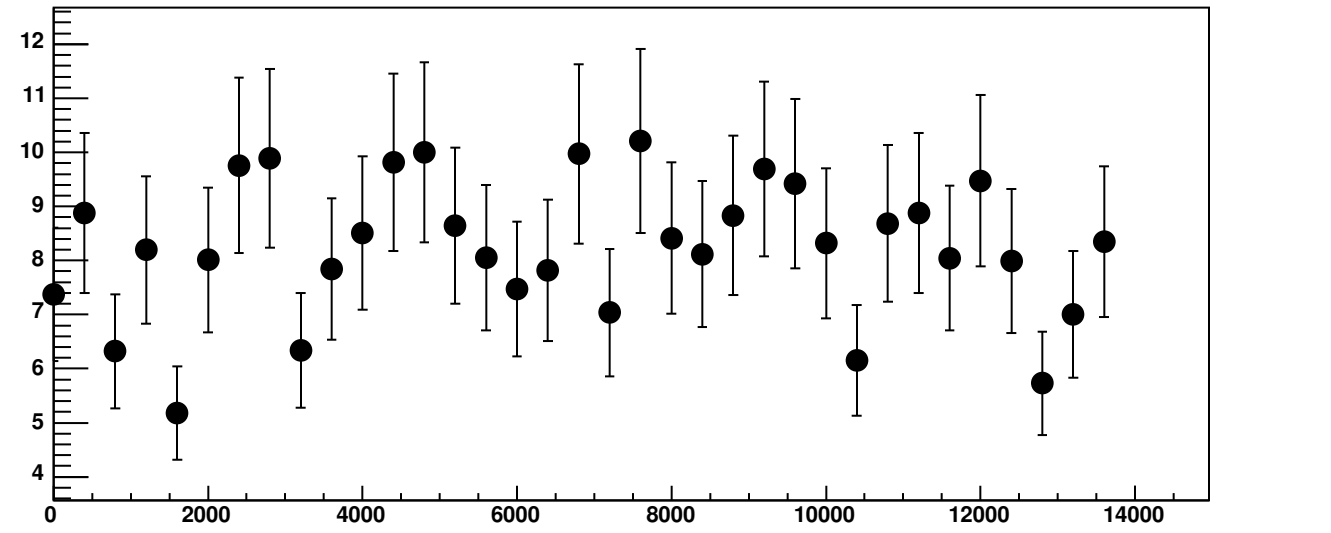

Chip 8, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

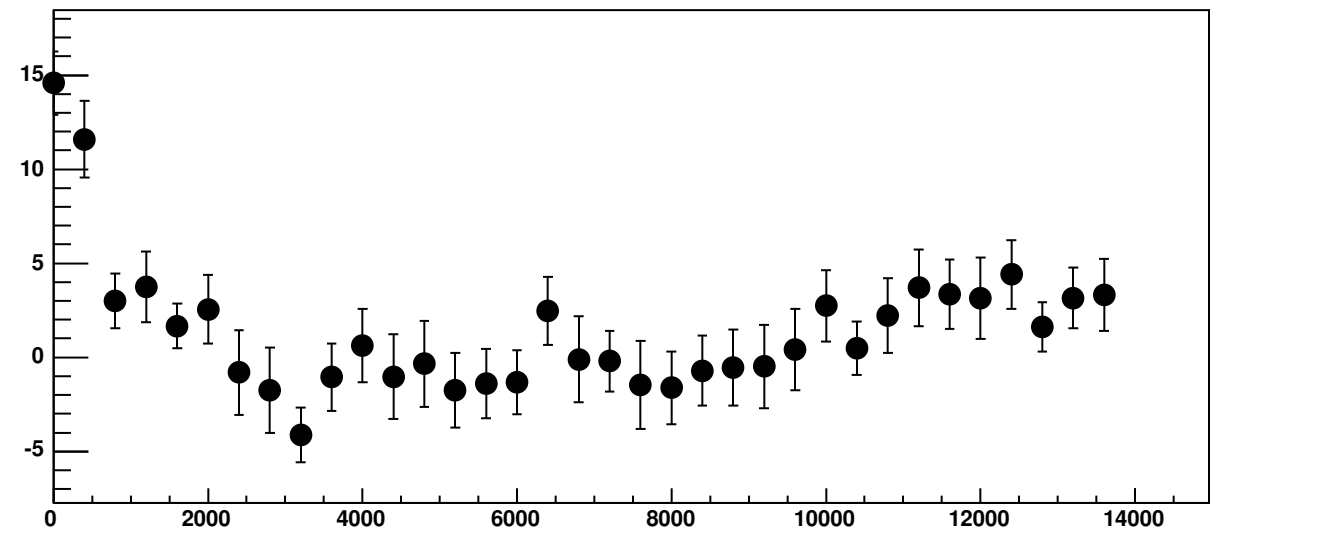
Chip 8, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

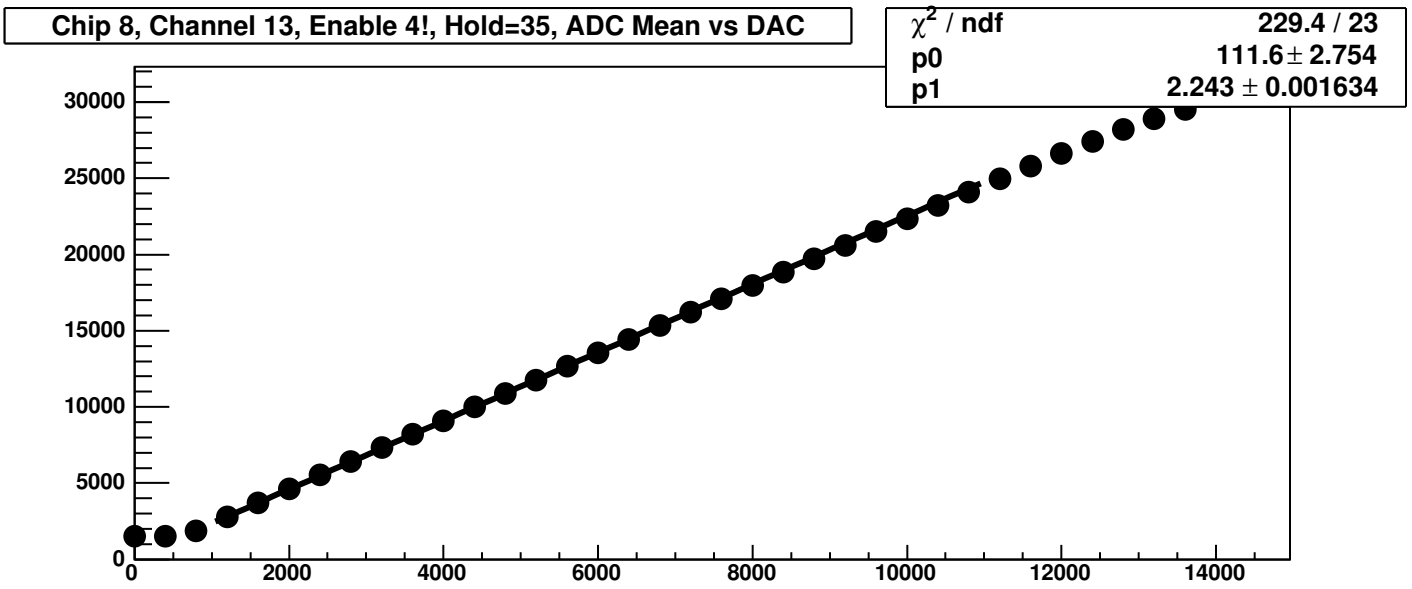


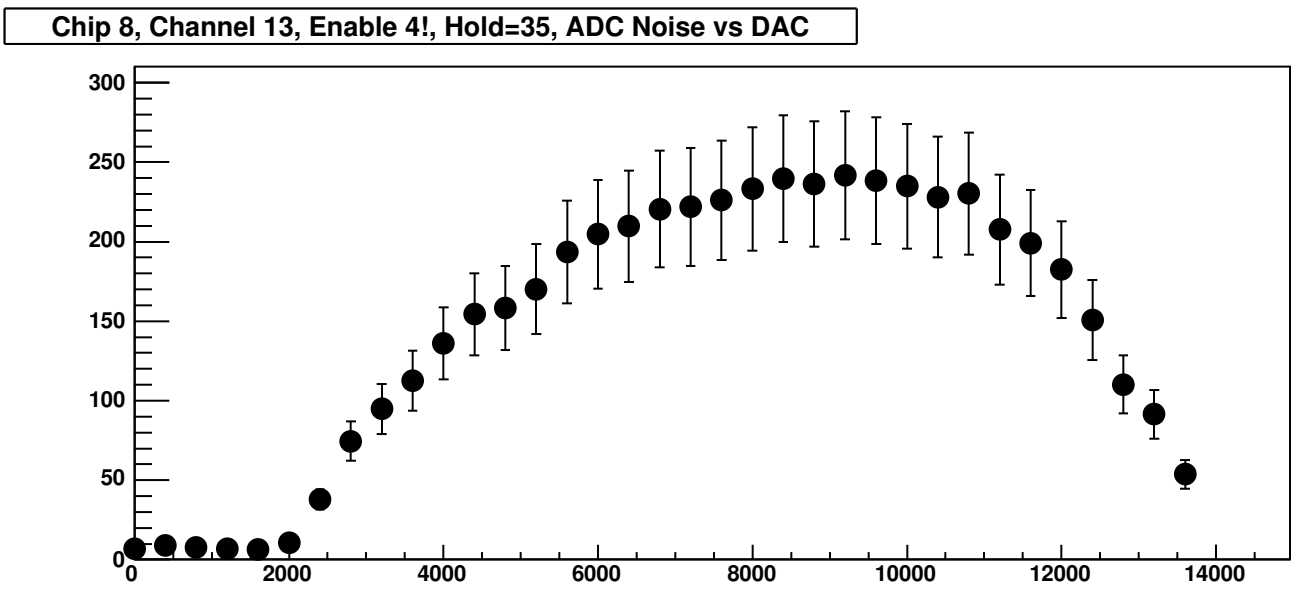
Chip 8, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

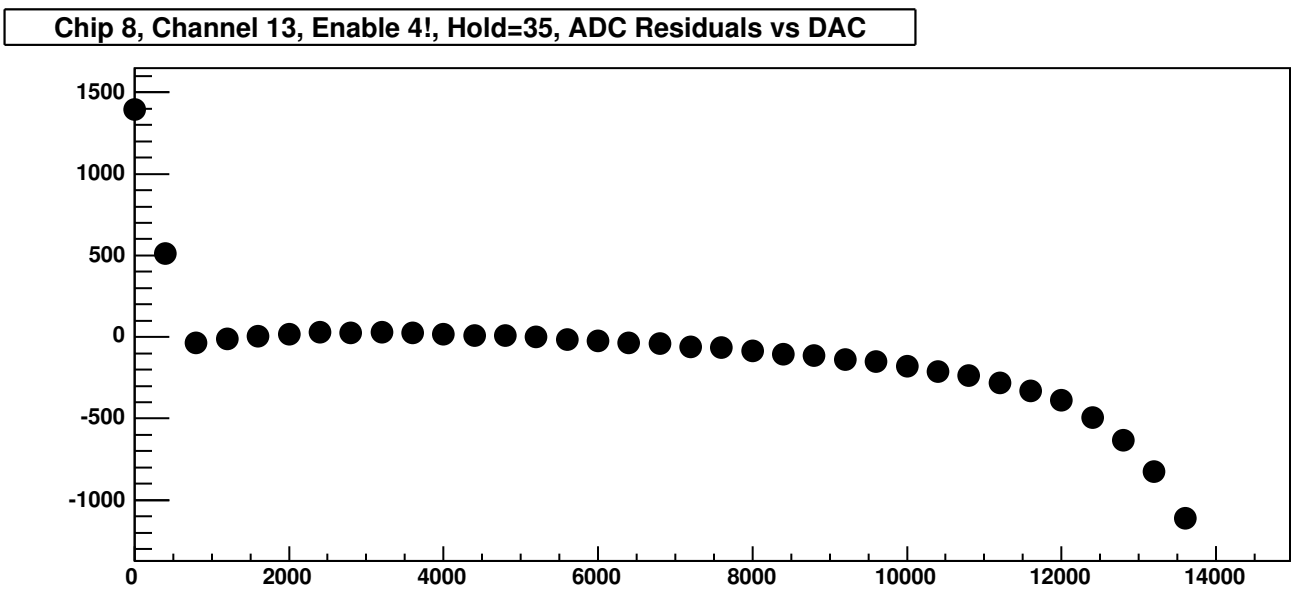


Chip 8, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

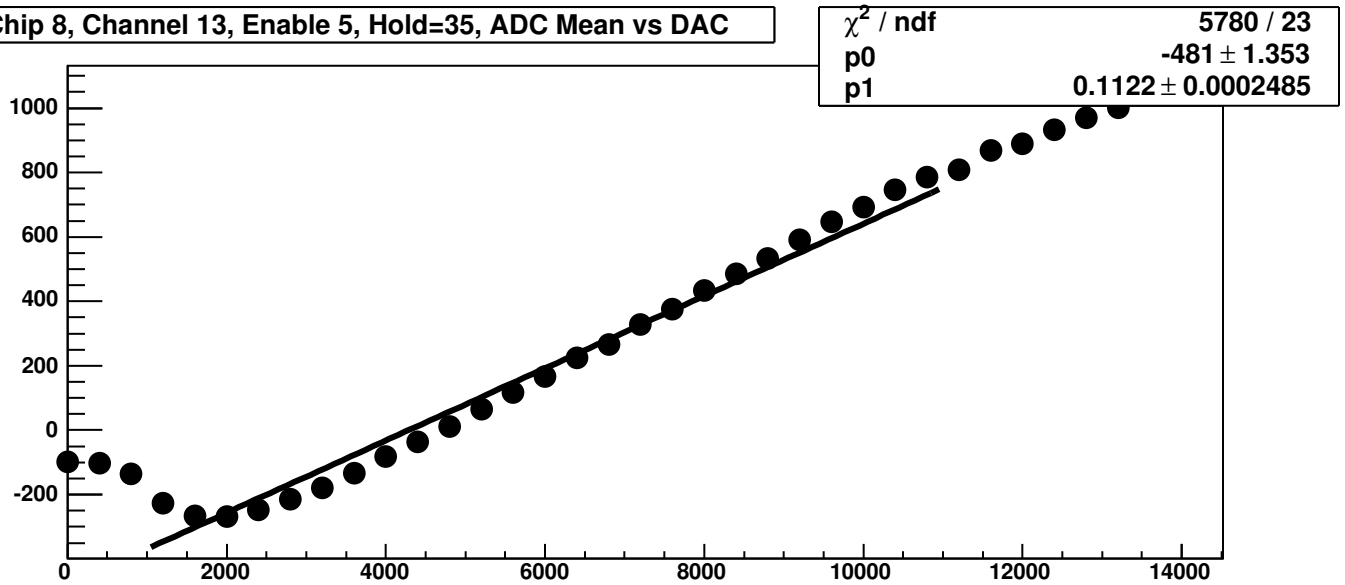



Chip 8, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 229.4 / 23
p0 111.6 ± 2.754
p1 2.243 ± 0.001634
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

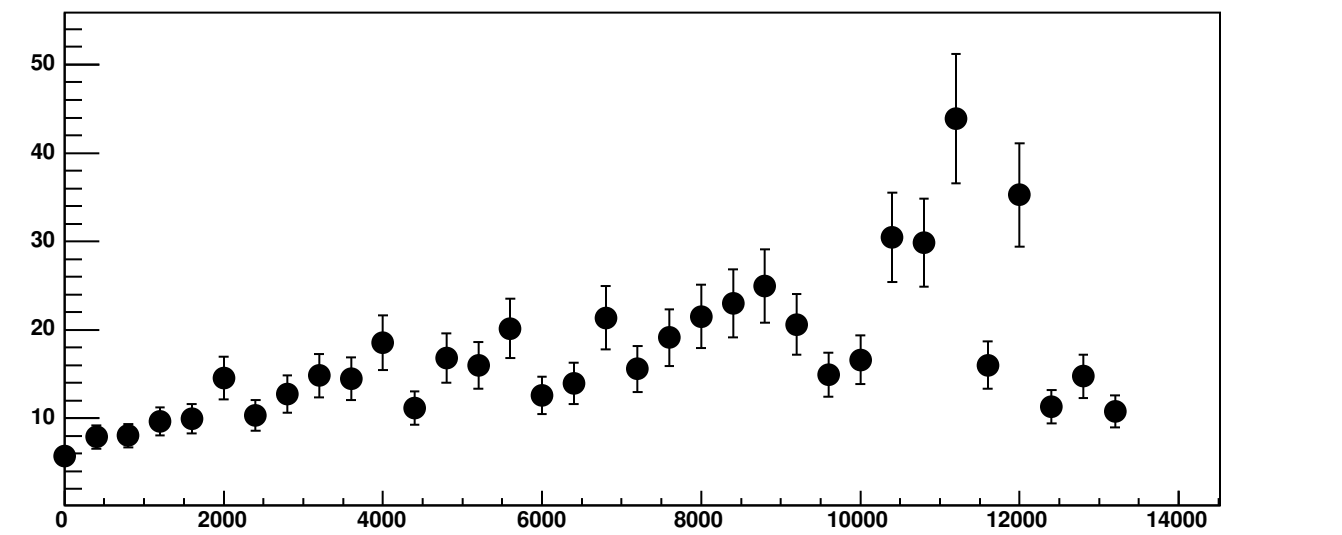

Chip 8, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

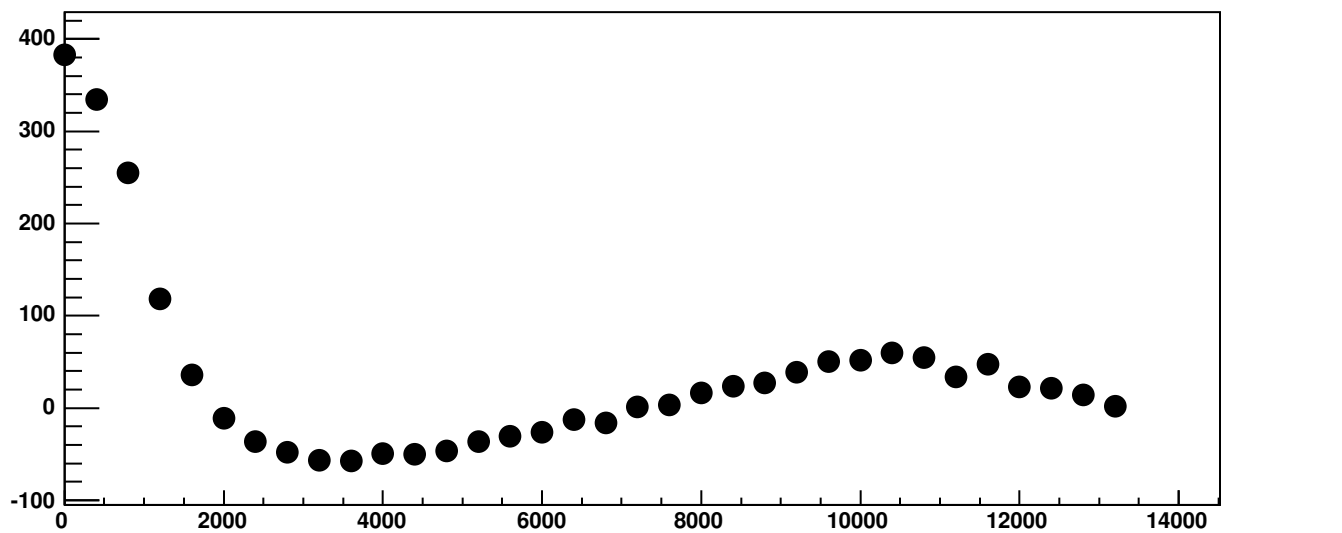
Chip 8, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



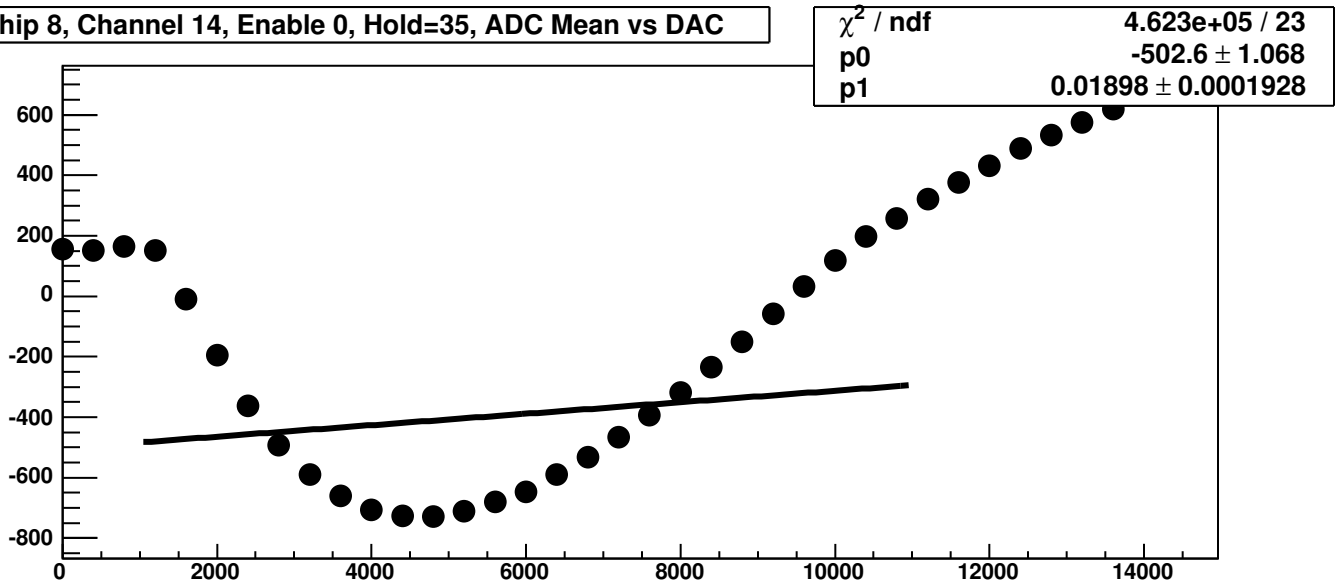
Chip 8, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



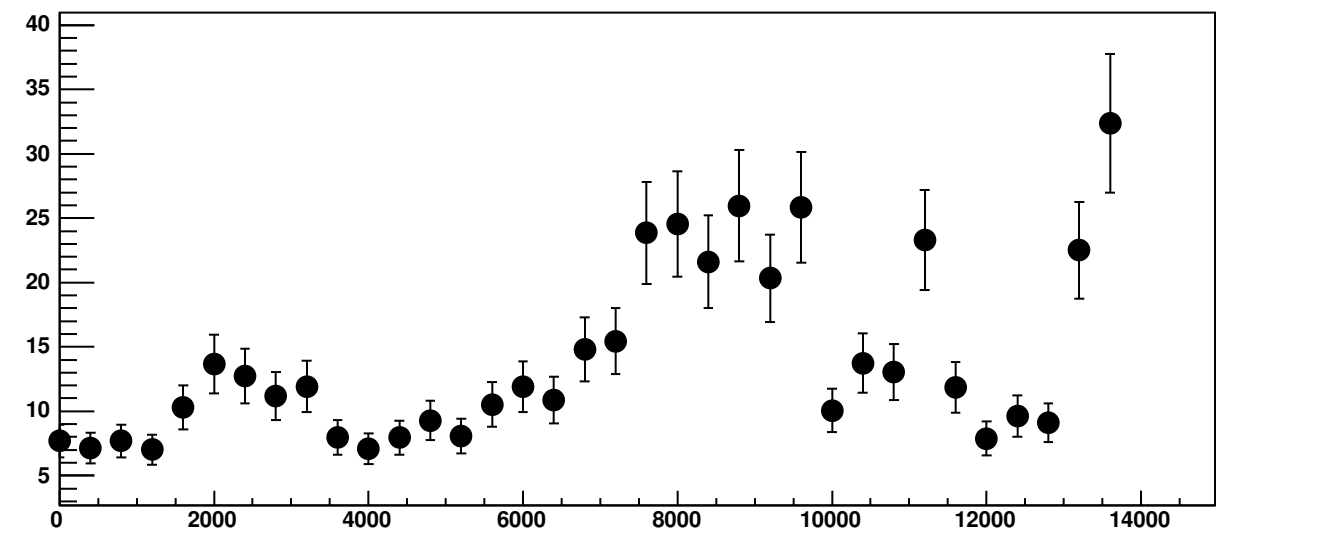
Chip 8, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



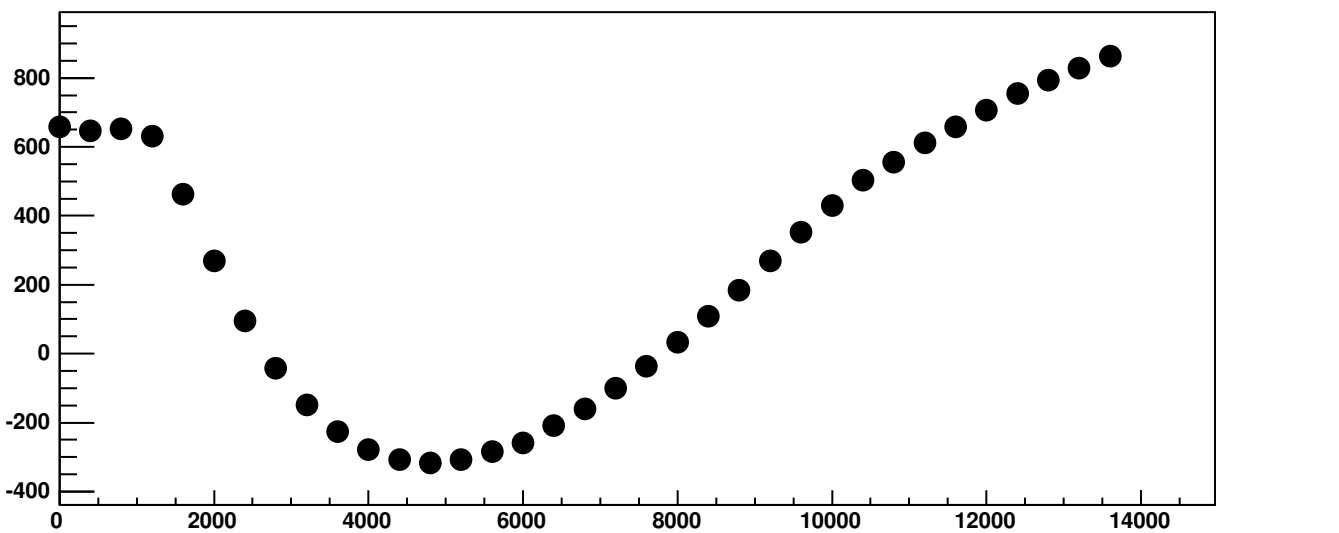
Chip 8, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

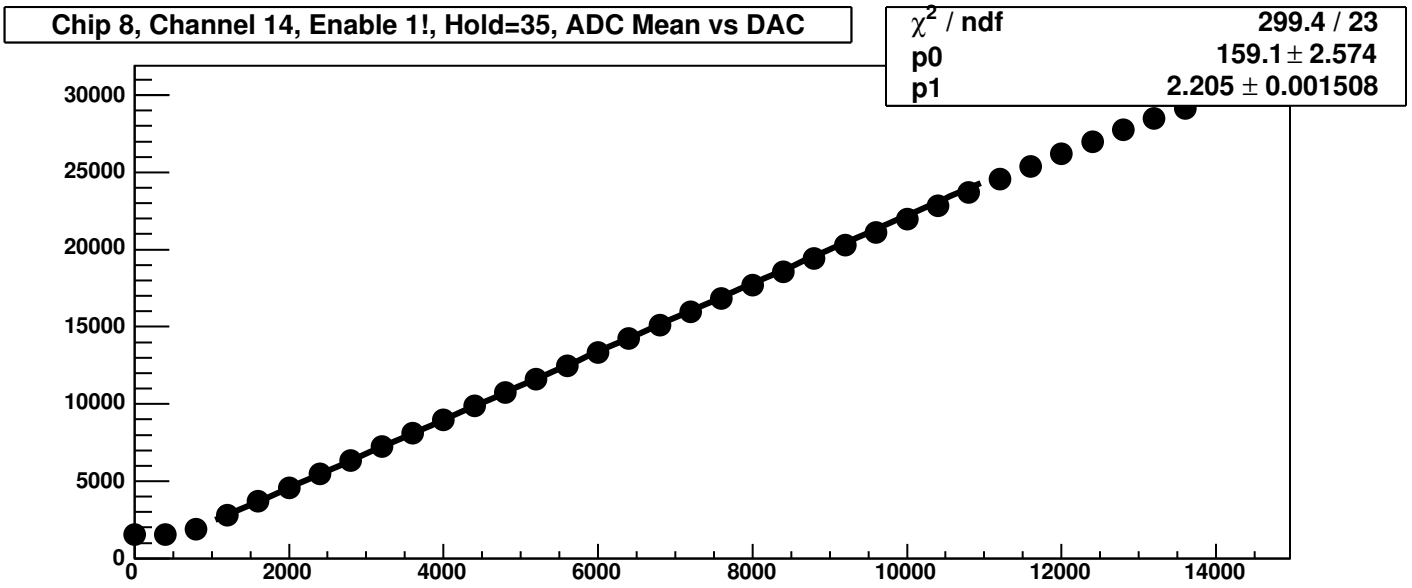


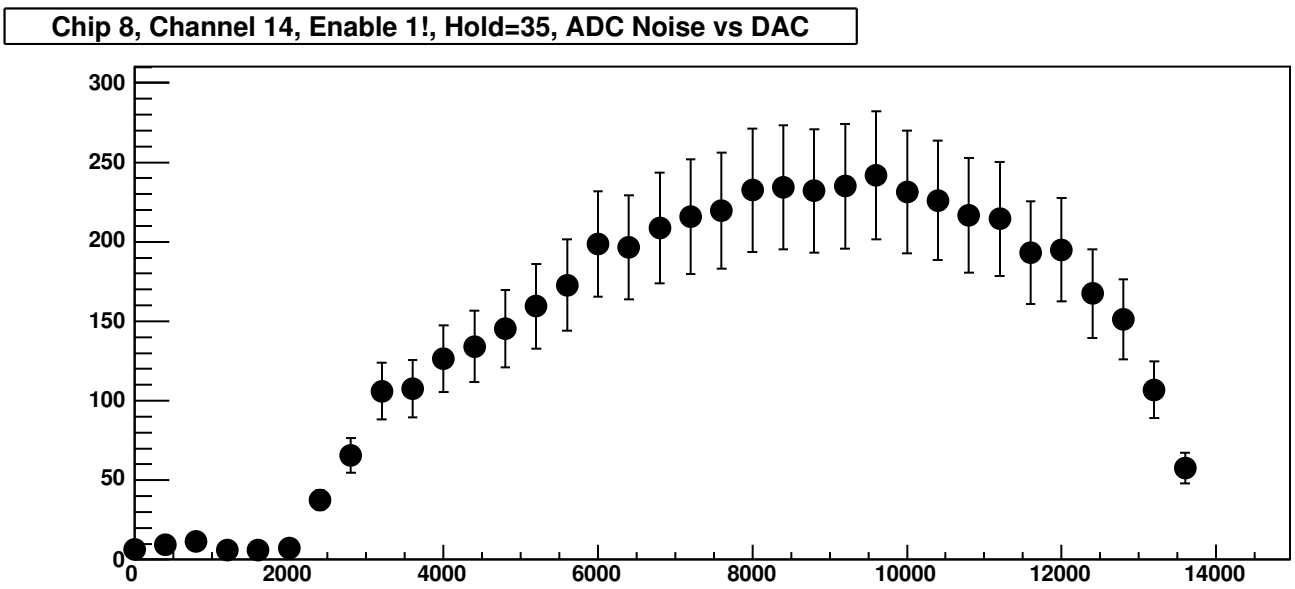
Chip 8, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

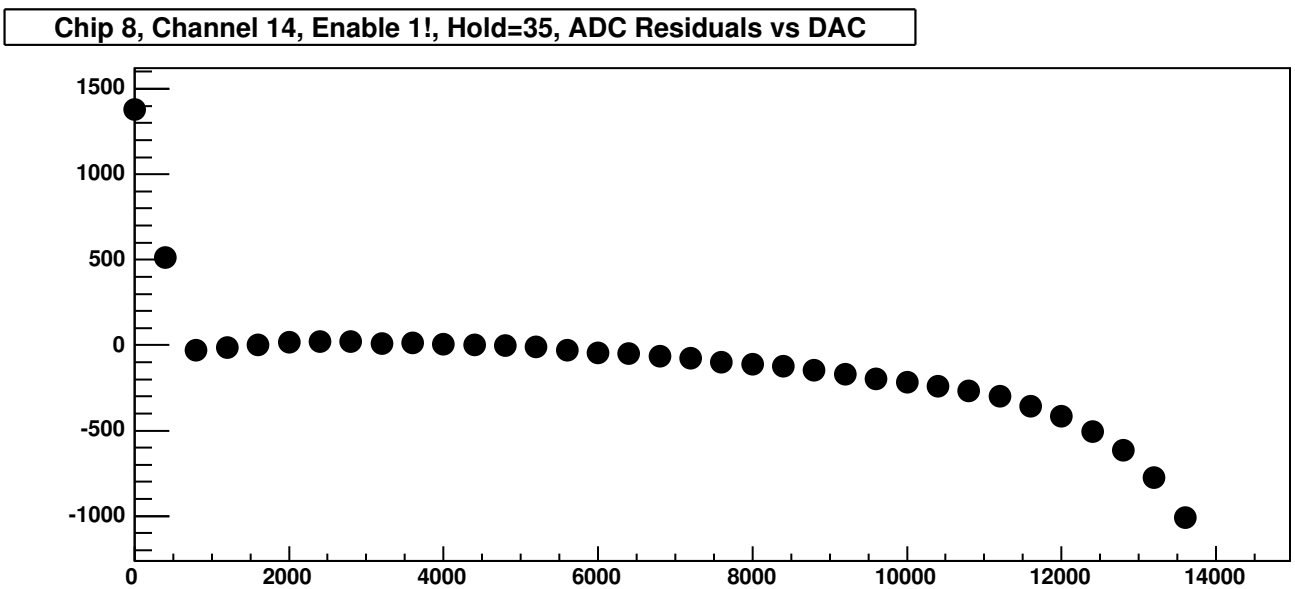


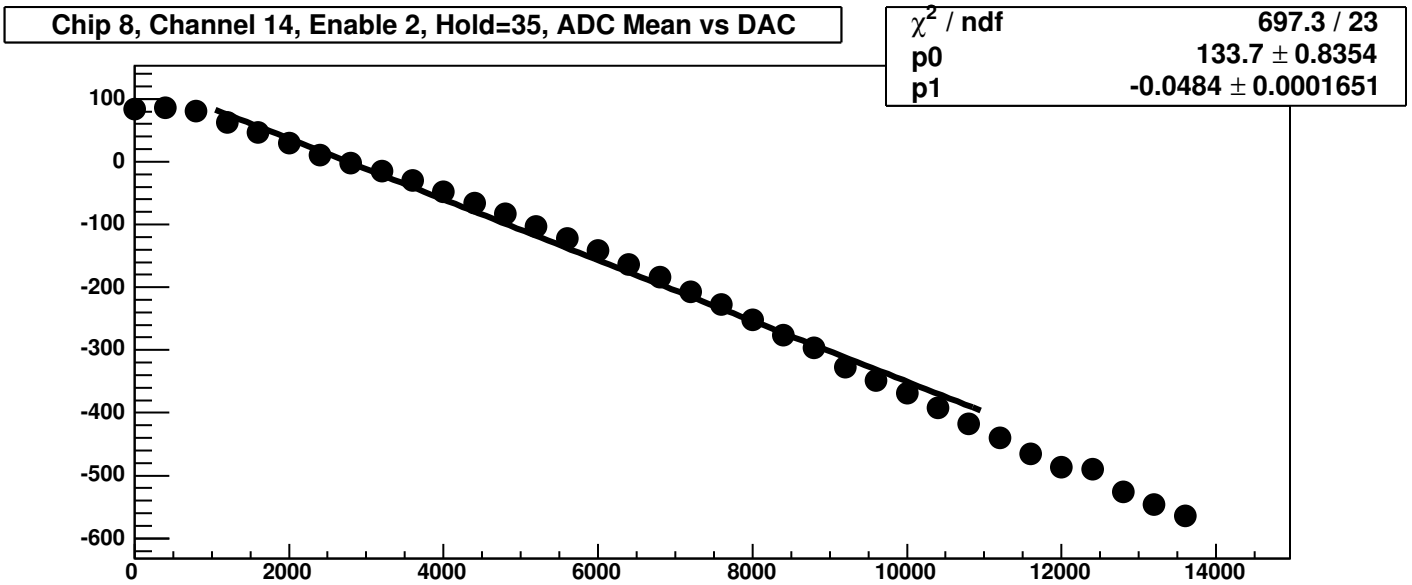
Chip 8, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

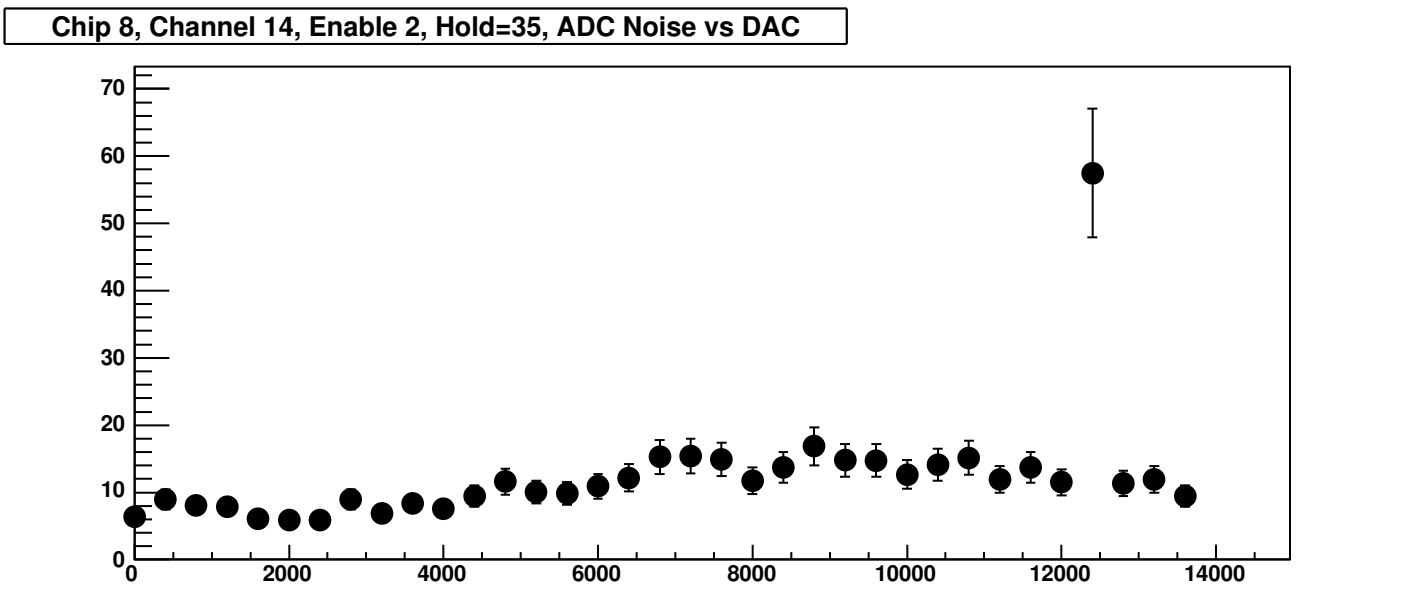


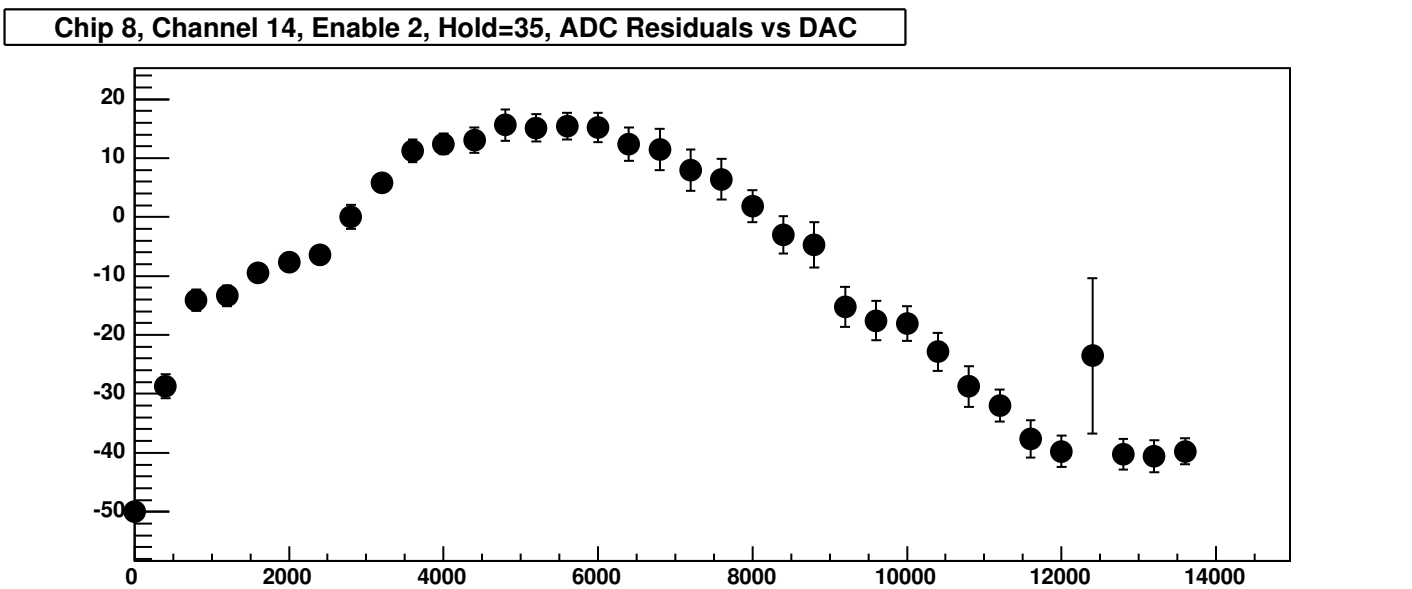

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 299.4 / 23
p0 159.1 ± 2.574
p1 2.205 ± 0.001508
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

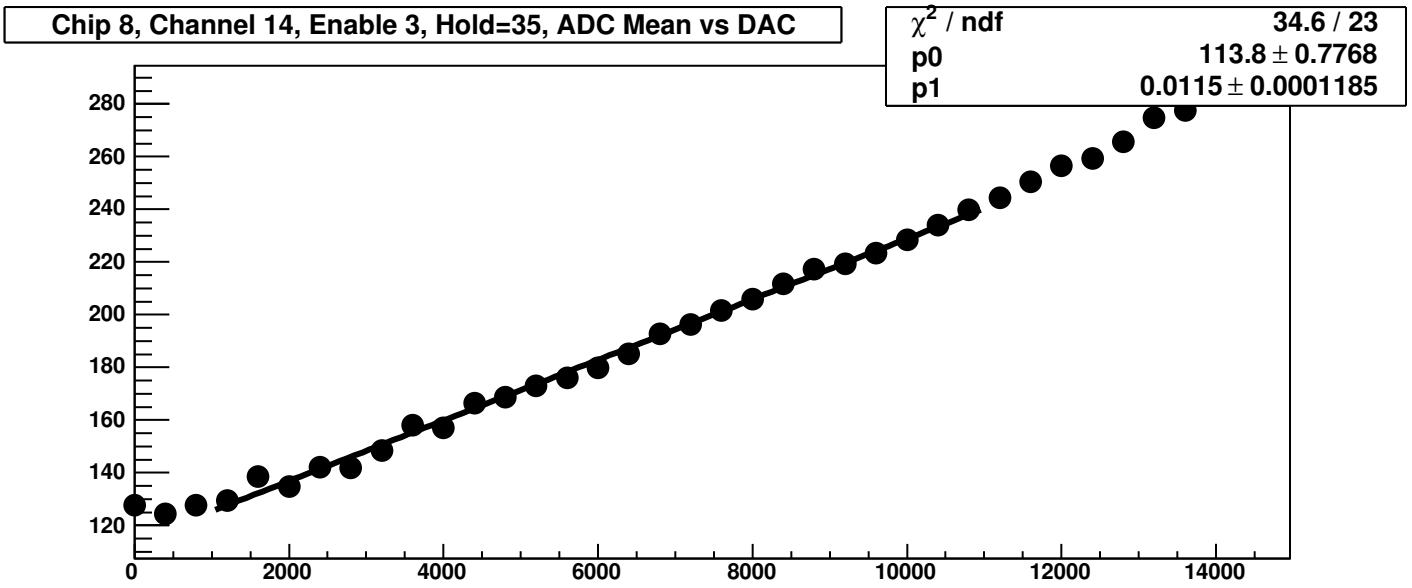

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

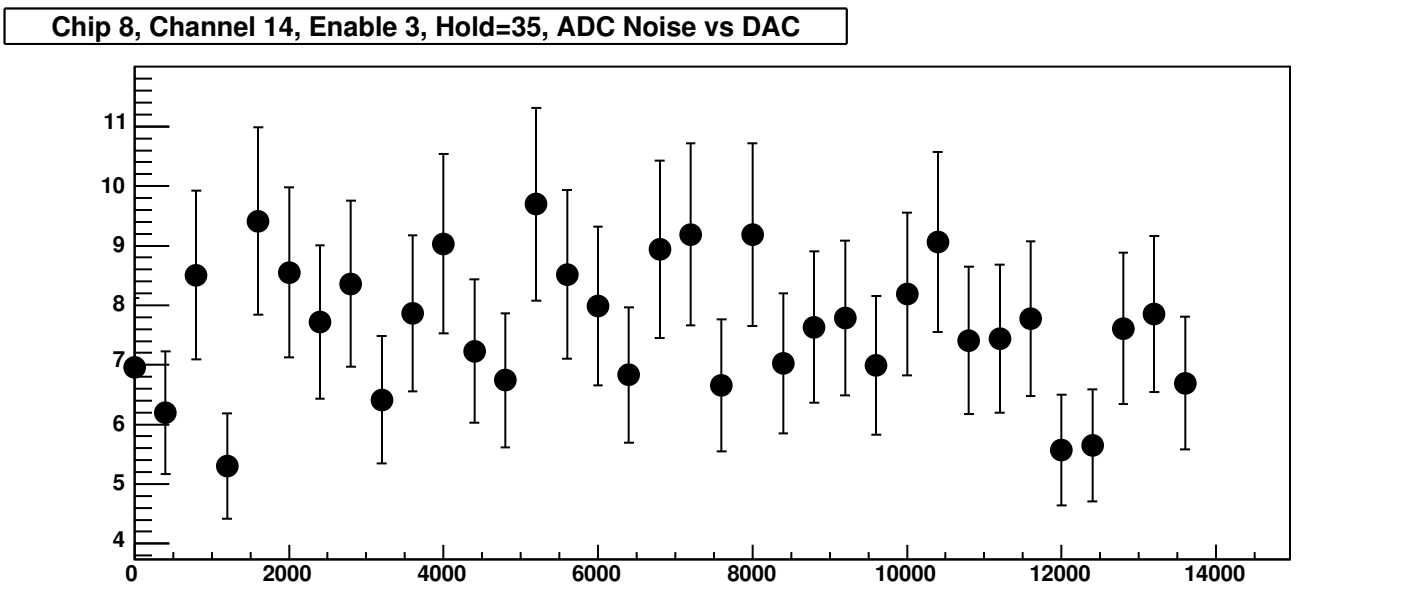

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

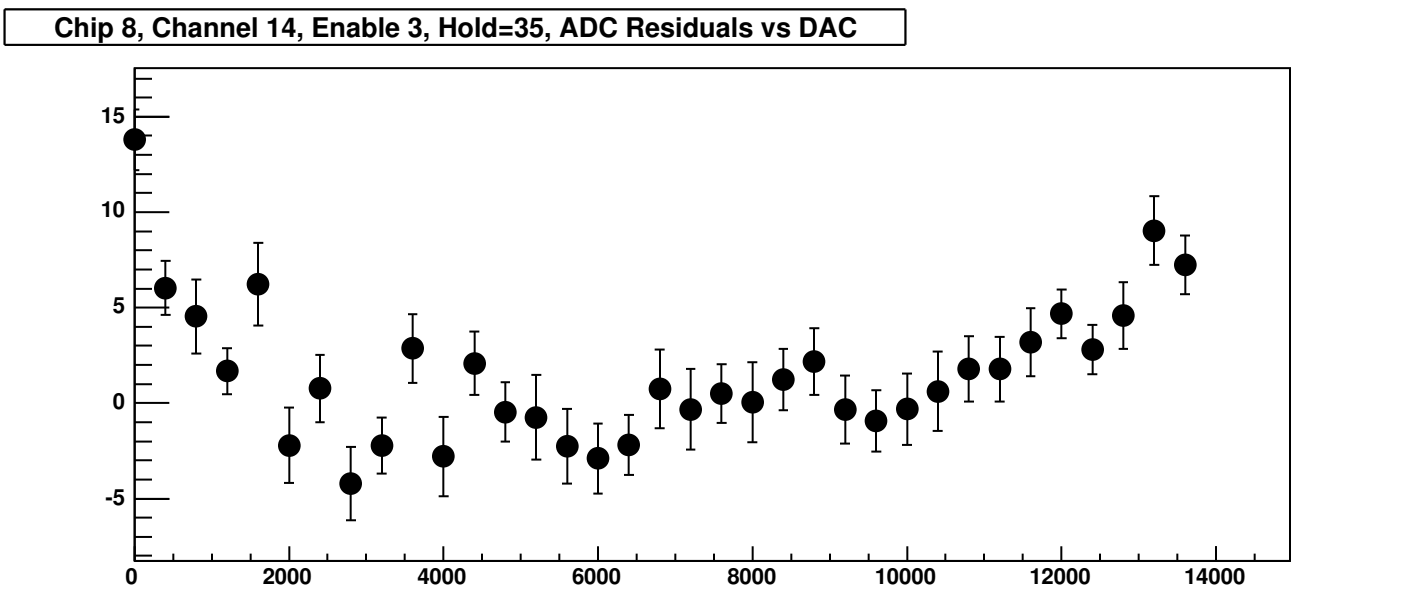

Chip 8, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 697.3 / 23
p0 133.7 ± 0.8354
p1 -0.0484 ± 0.0001651
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

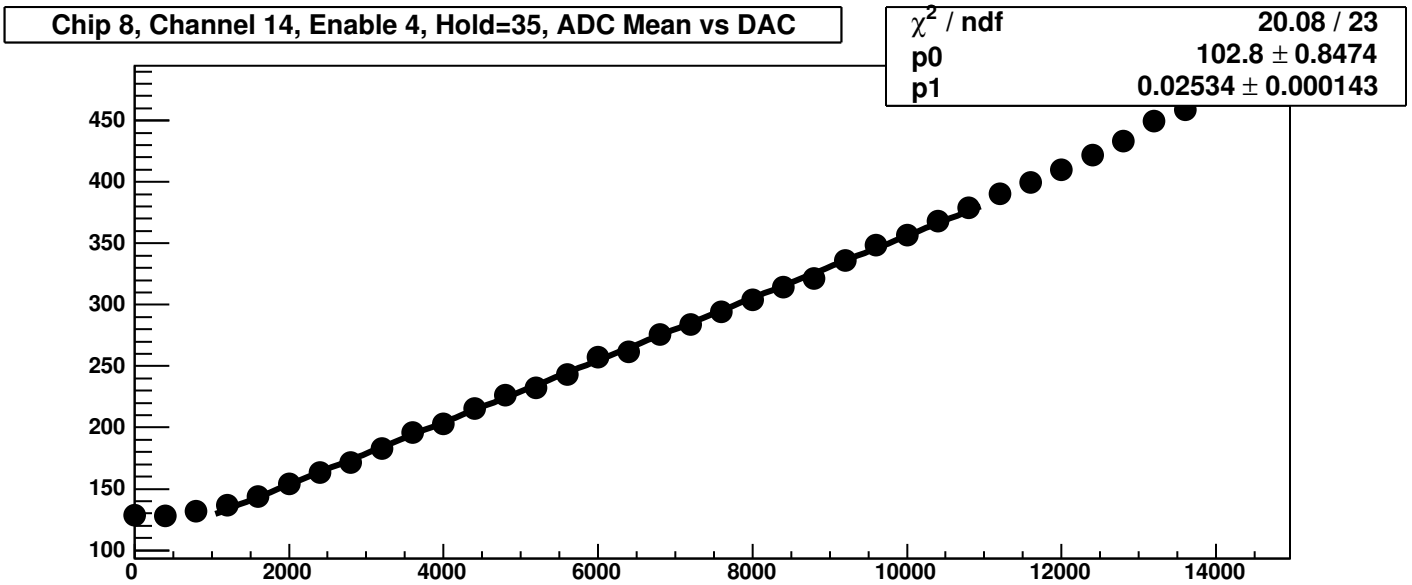

Chip 8, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

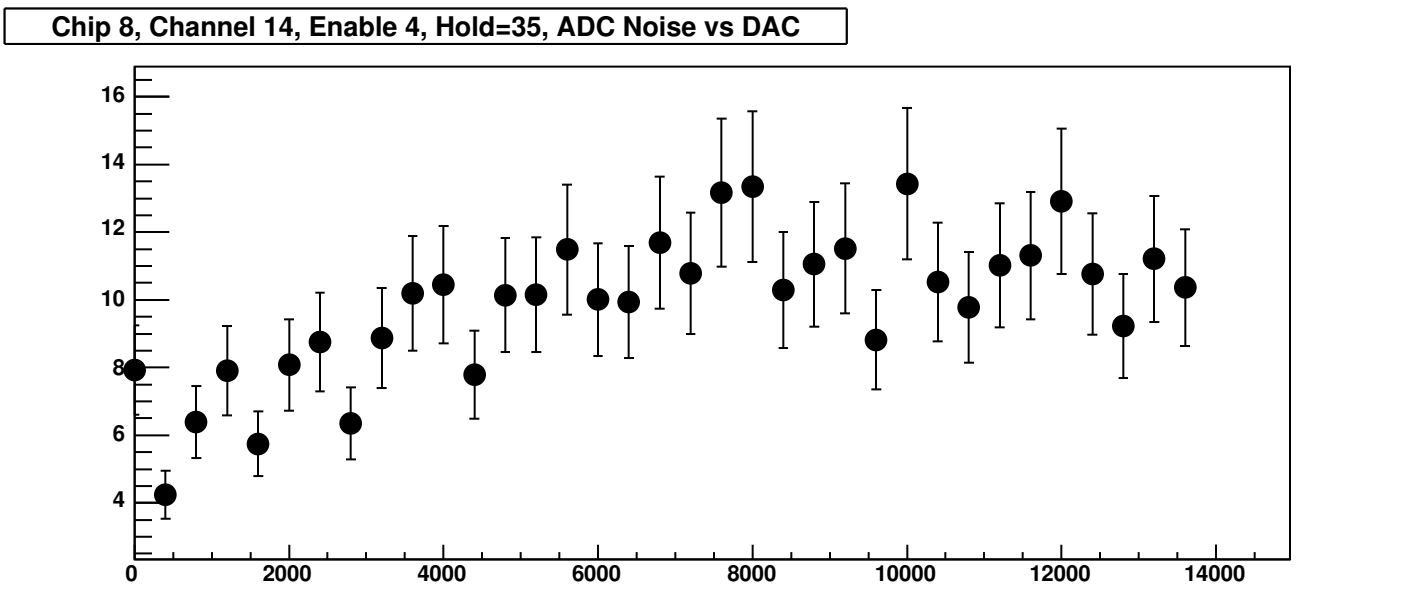

Chip 8, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

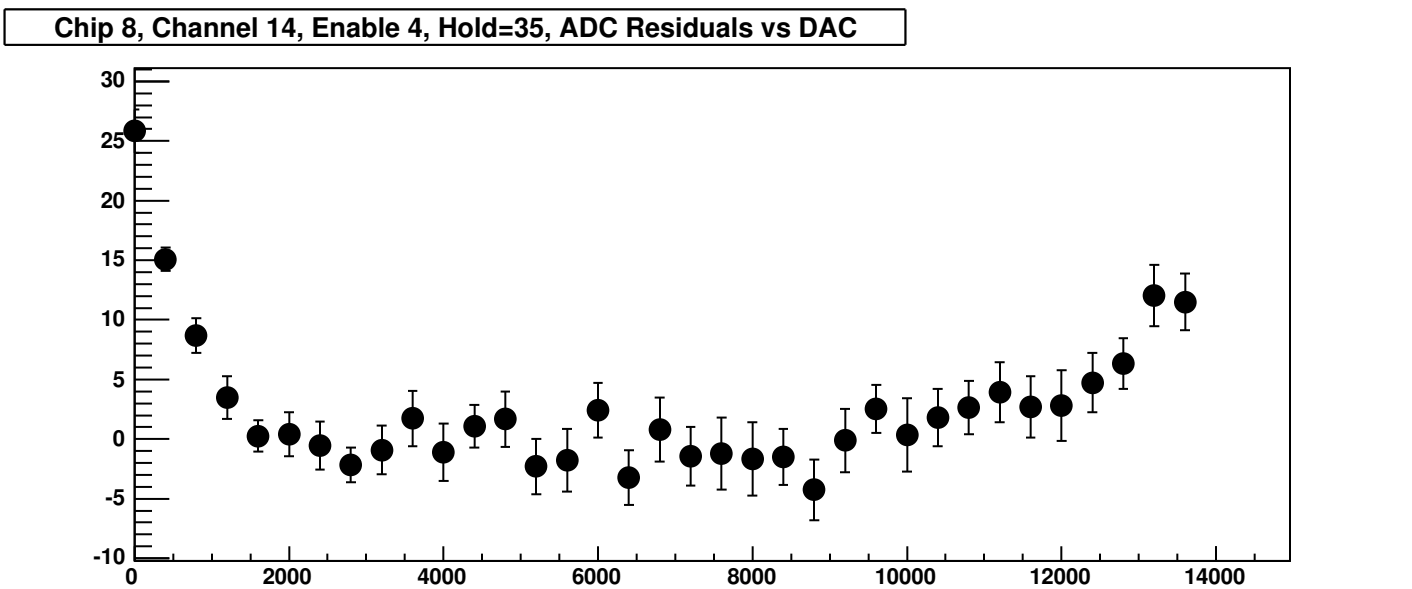

Chip 8, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.6 / 23
p0 113.8 ± 0.7768
p1 0.0115 ± 0.0001185
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

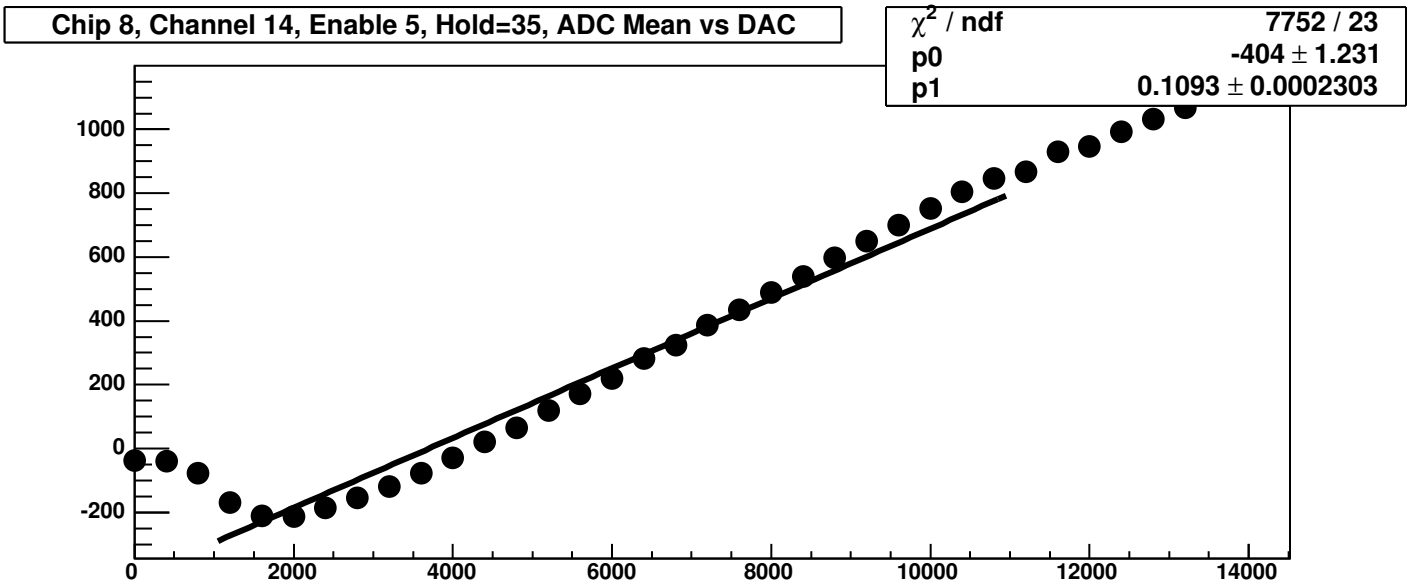

Chip 8, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

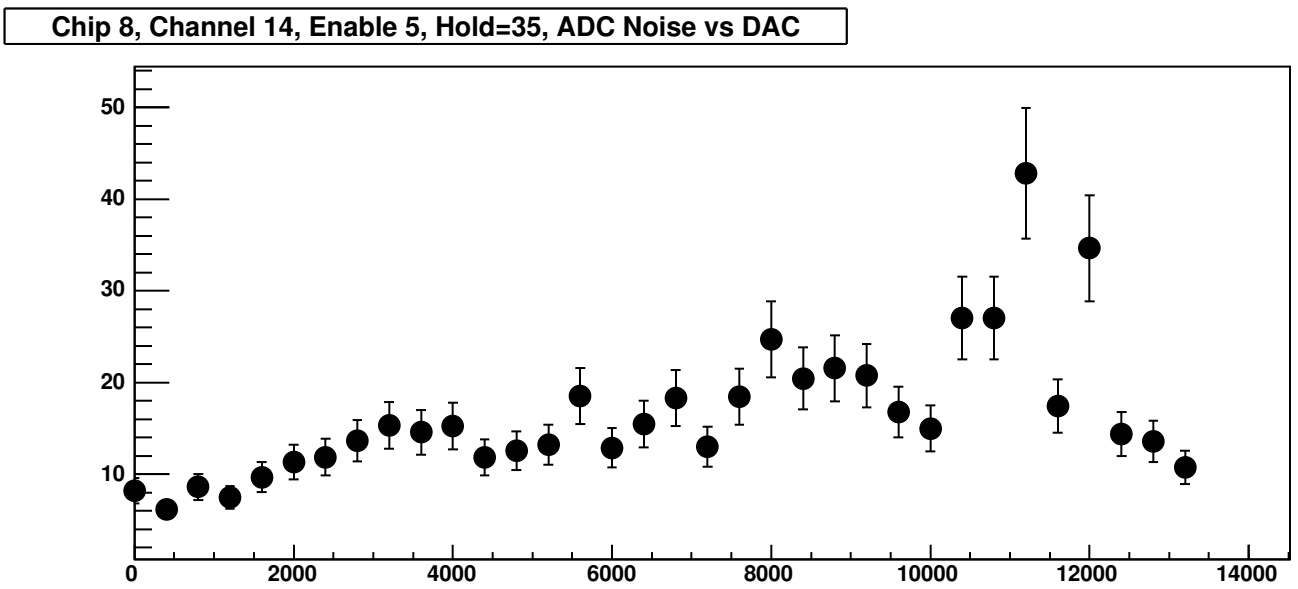

Chip 8, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

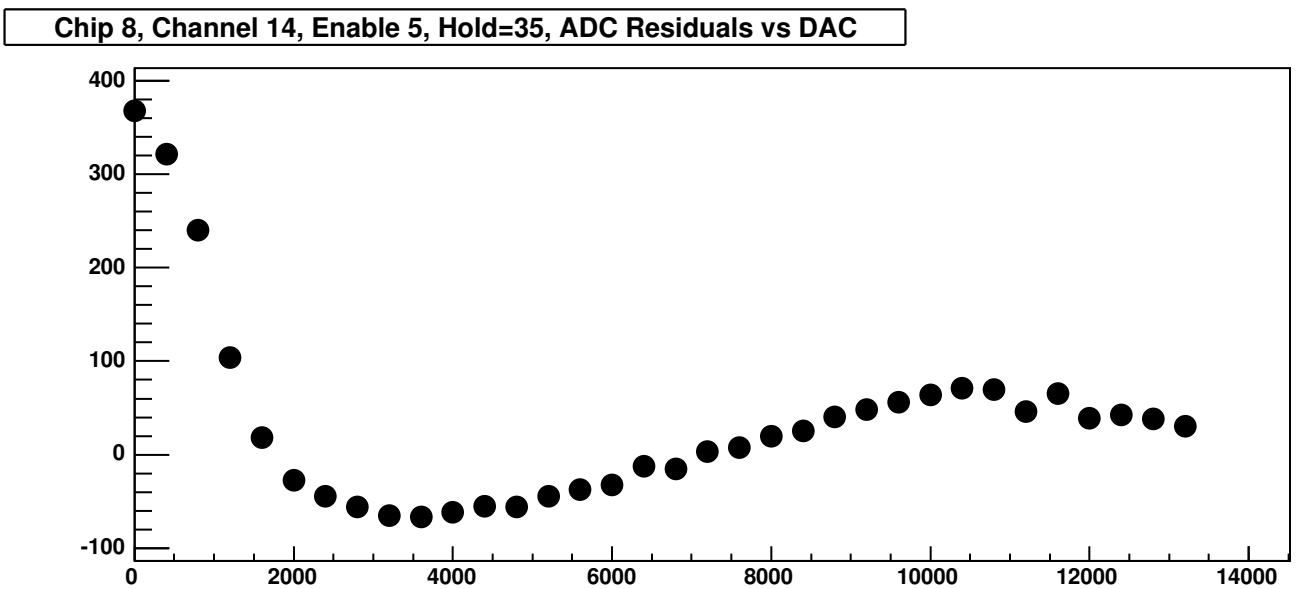

Chip 8, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.08 / 23
p0 102.8 ± 0.8474
p1 0.02534 ± 0.000143
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

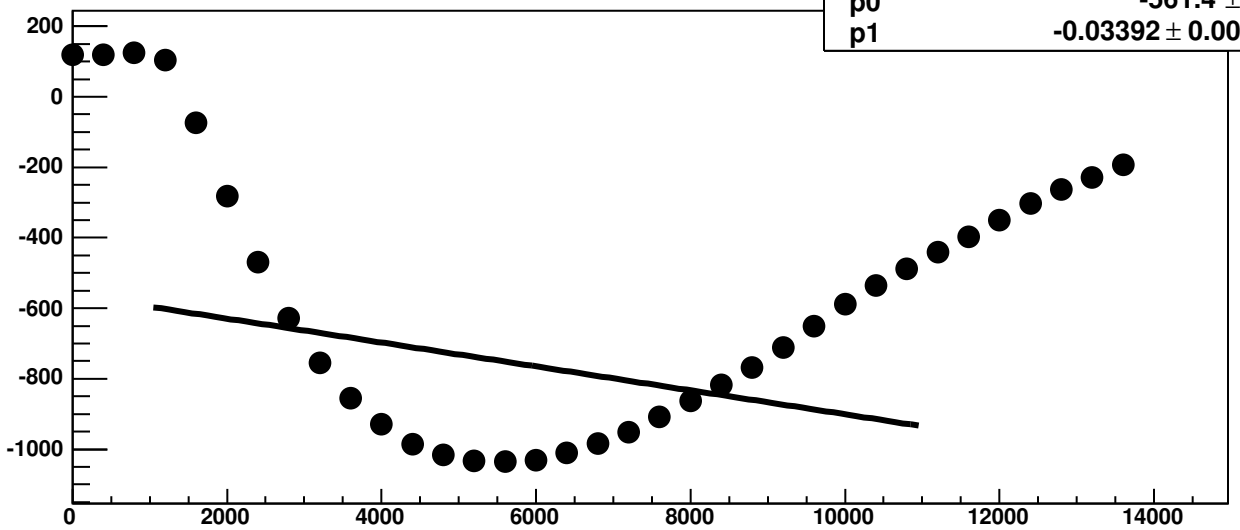

Chip 8, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7752 / 23
p0 -404 ± 1.231
p1 0.1093 ± 0.0002303
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

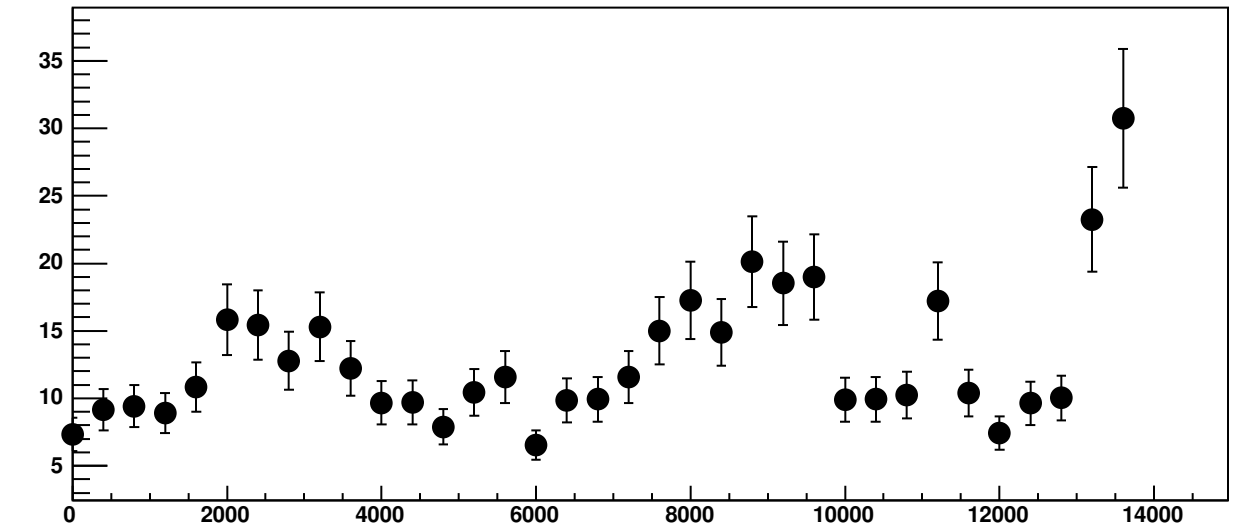

Chip 8, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

Chip 8, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

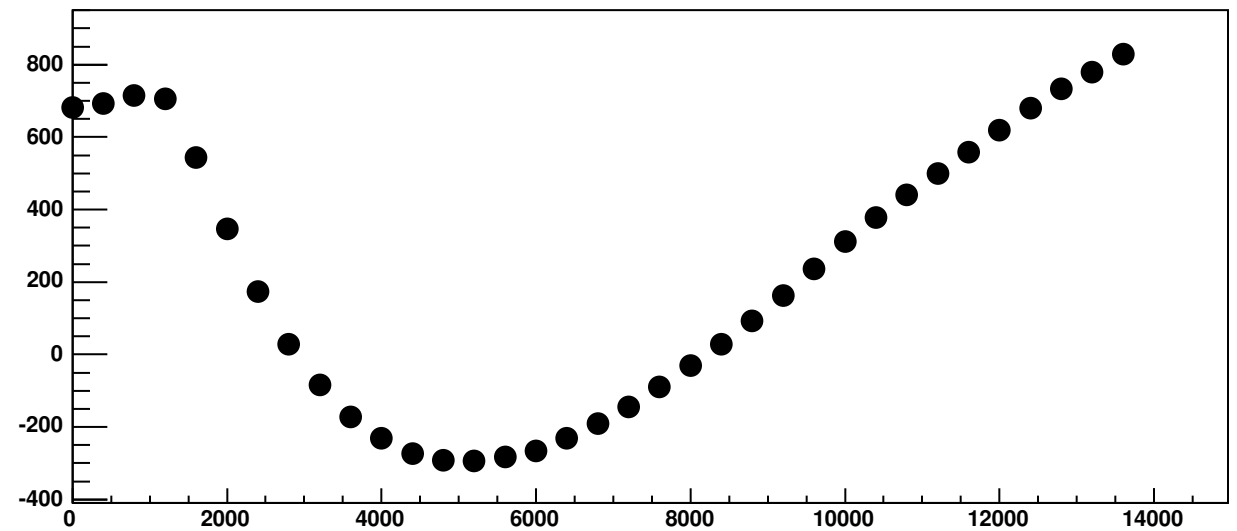
χ^2 / ndf	4.006e+05 / 23
p0	-561.4 ± 1.188
p1	-0.03392 ± 0.0001852

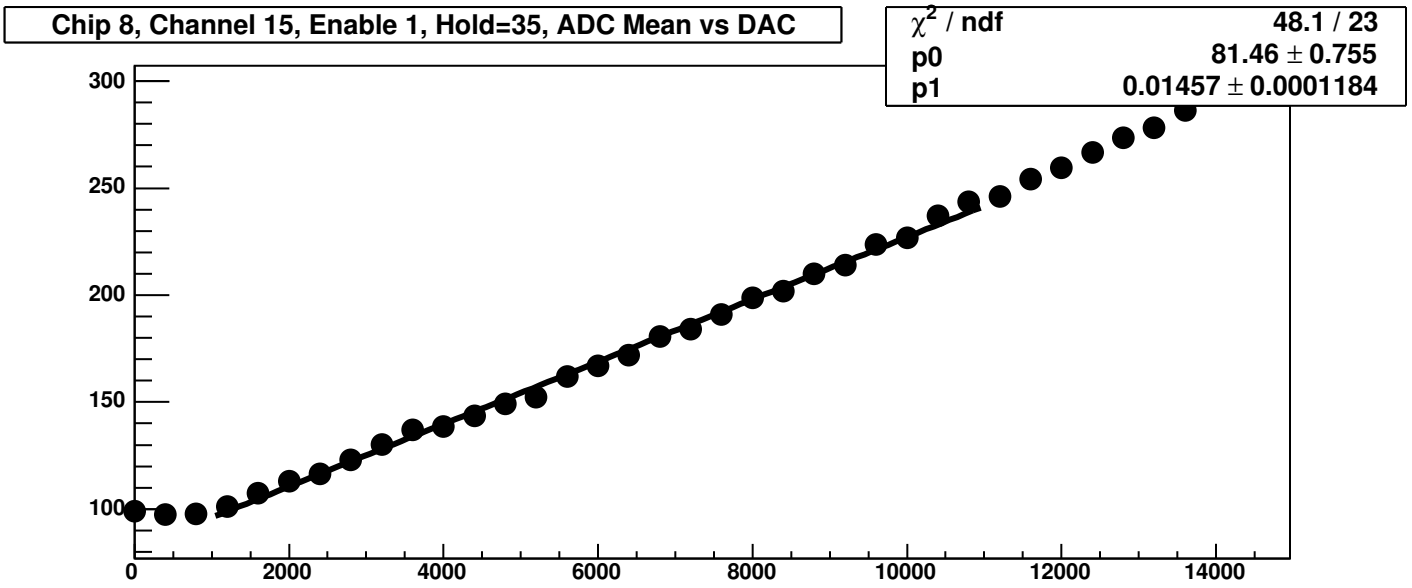


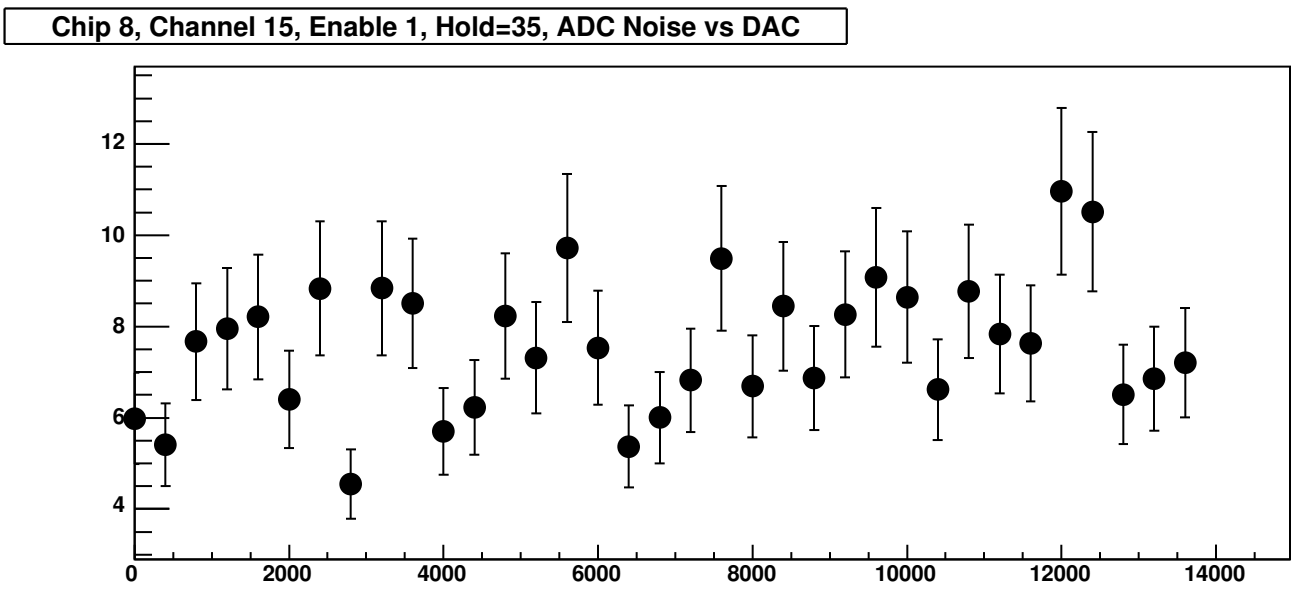
Chip 8, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

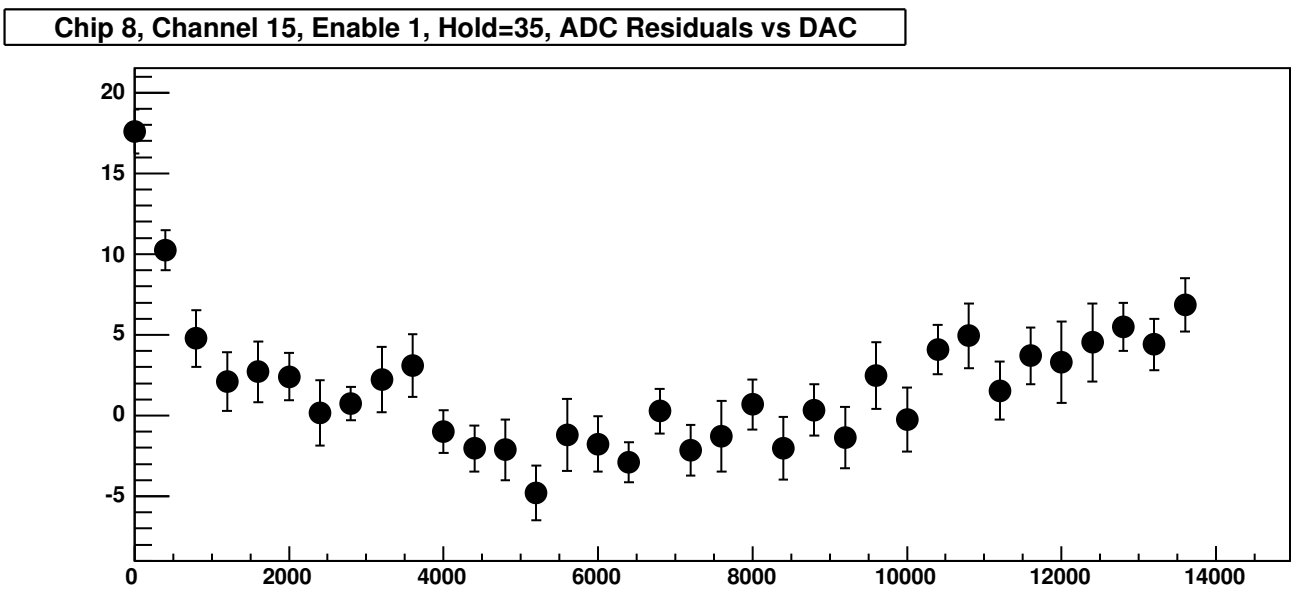


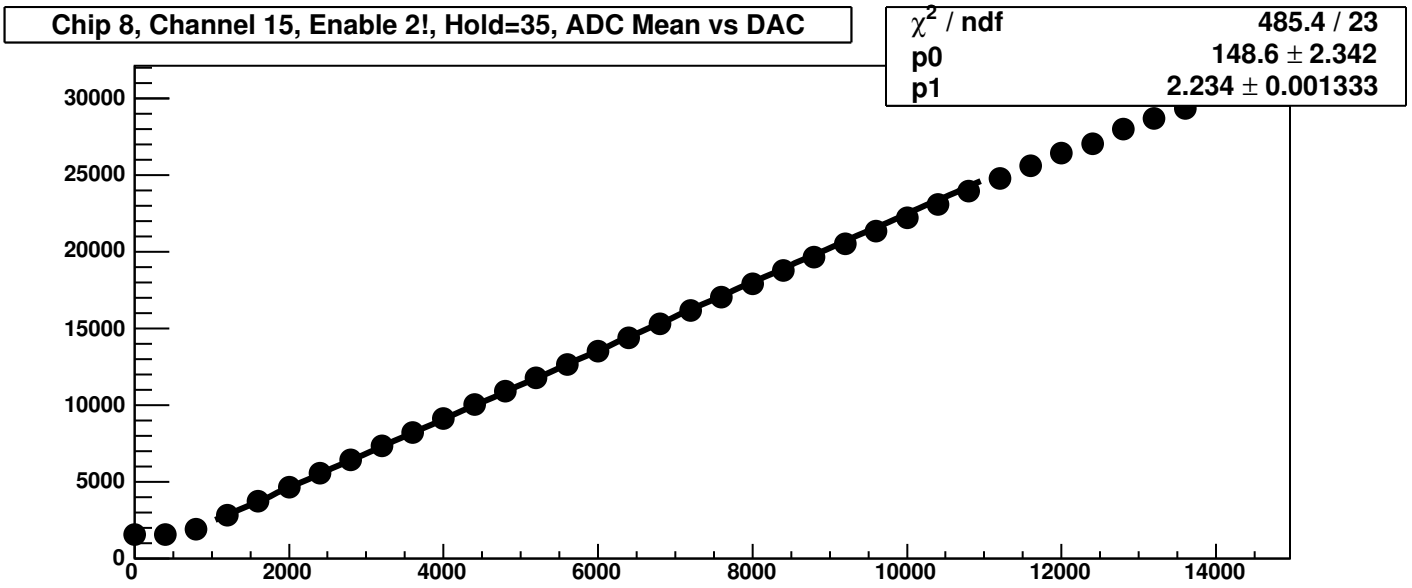
Chip 8, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

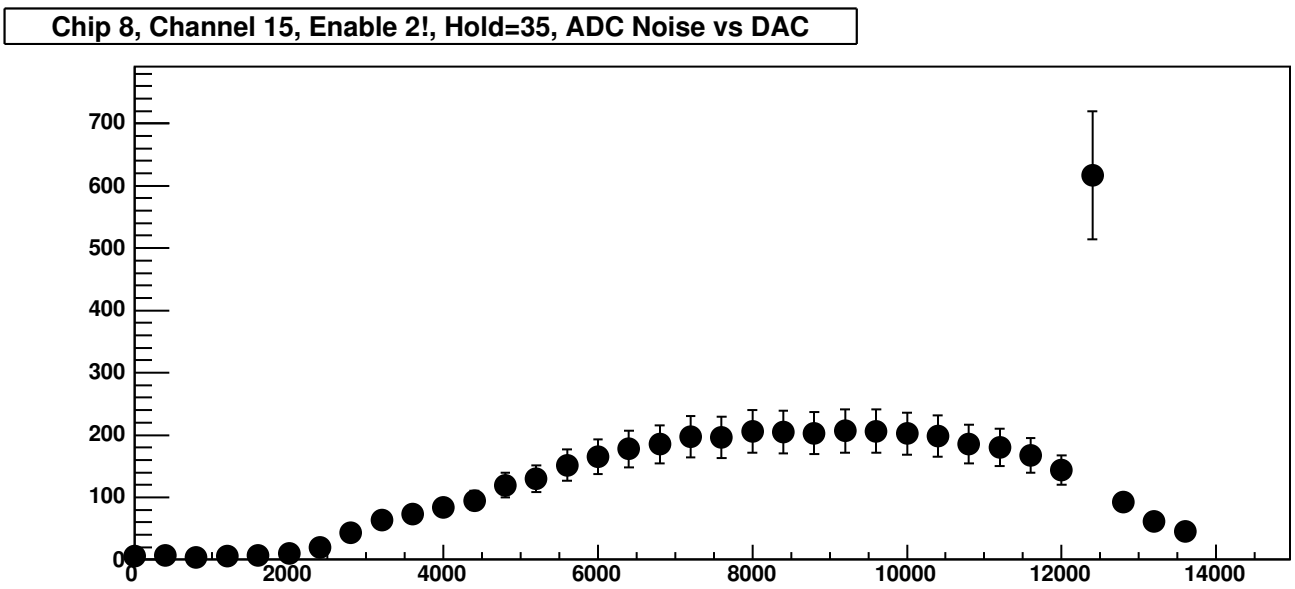


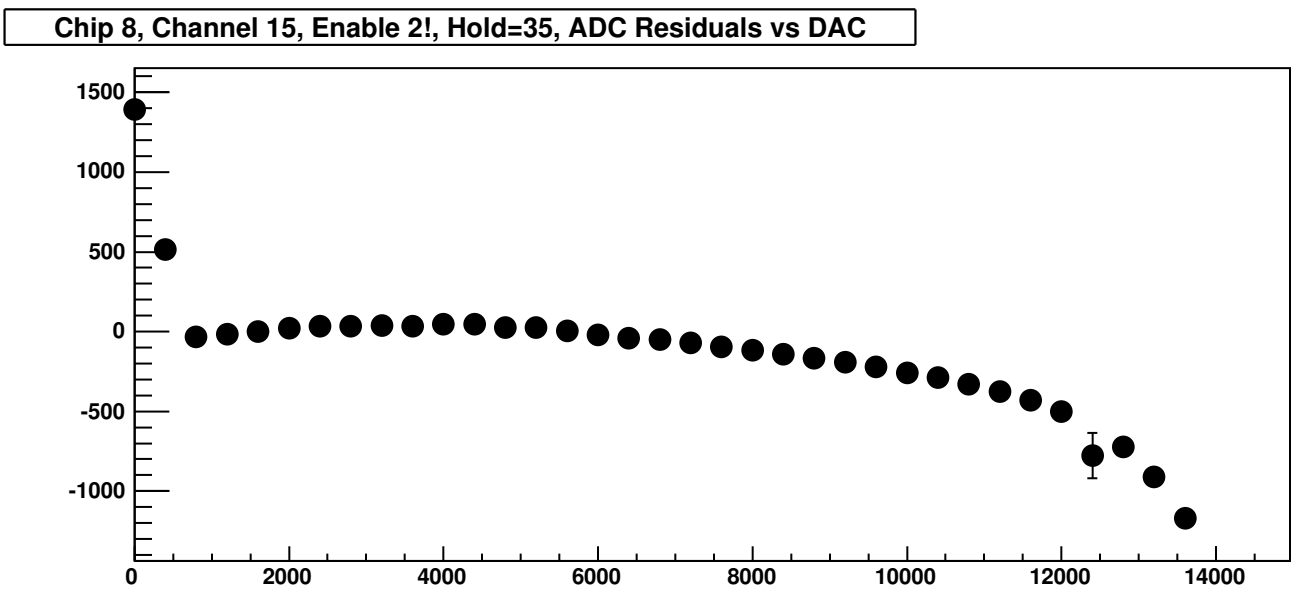

Chip 8, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 48.1 / 23
p0 81.46 ± 0.755
p1 0.01457 ± 0.0001184
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

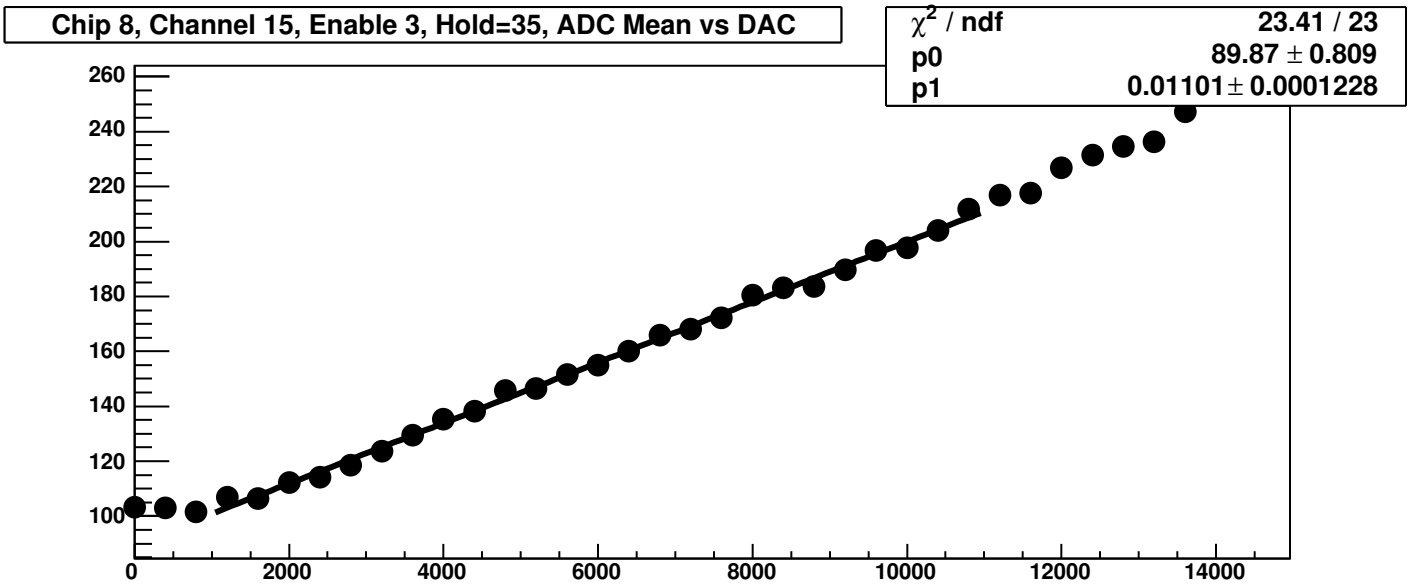

Chip 8, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

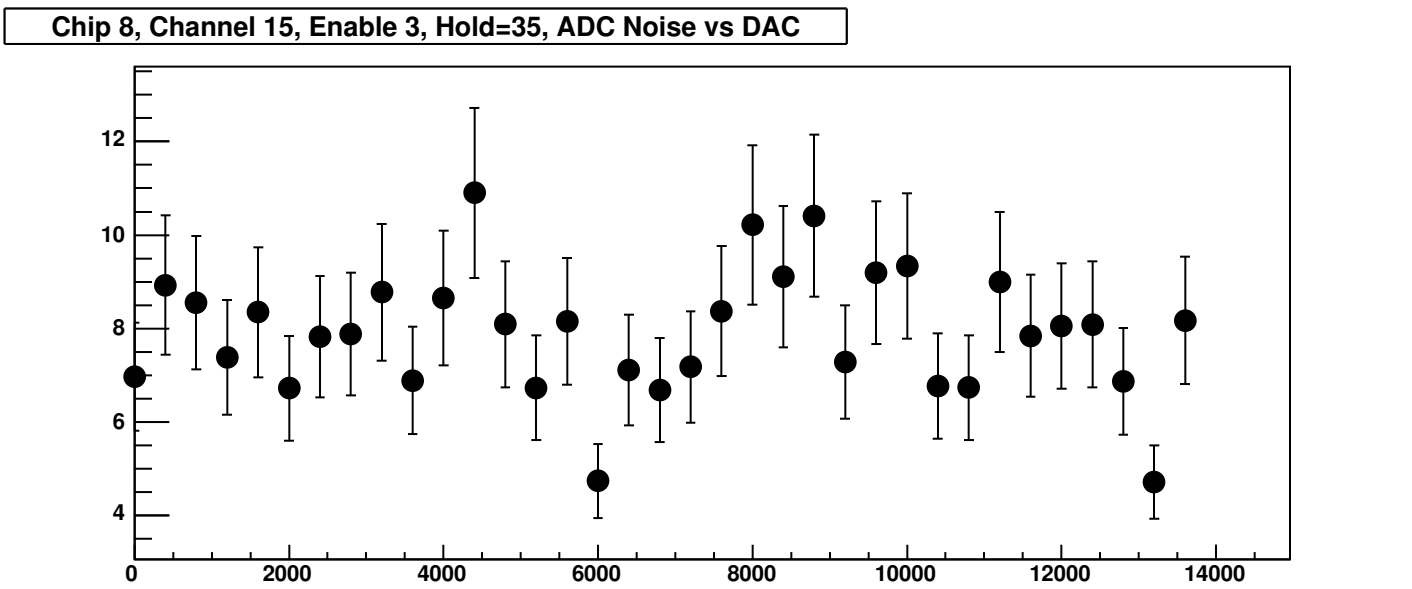

Chip 8, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

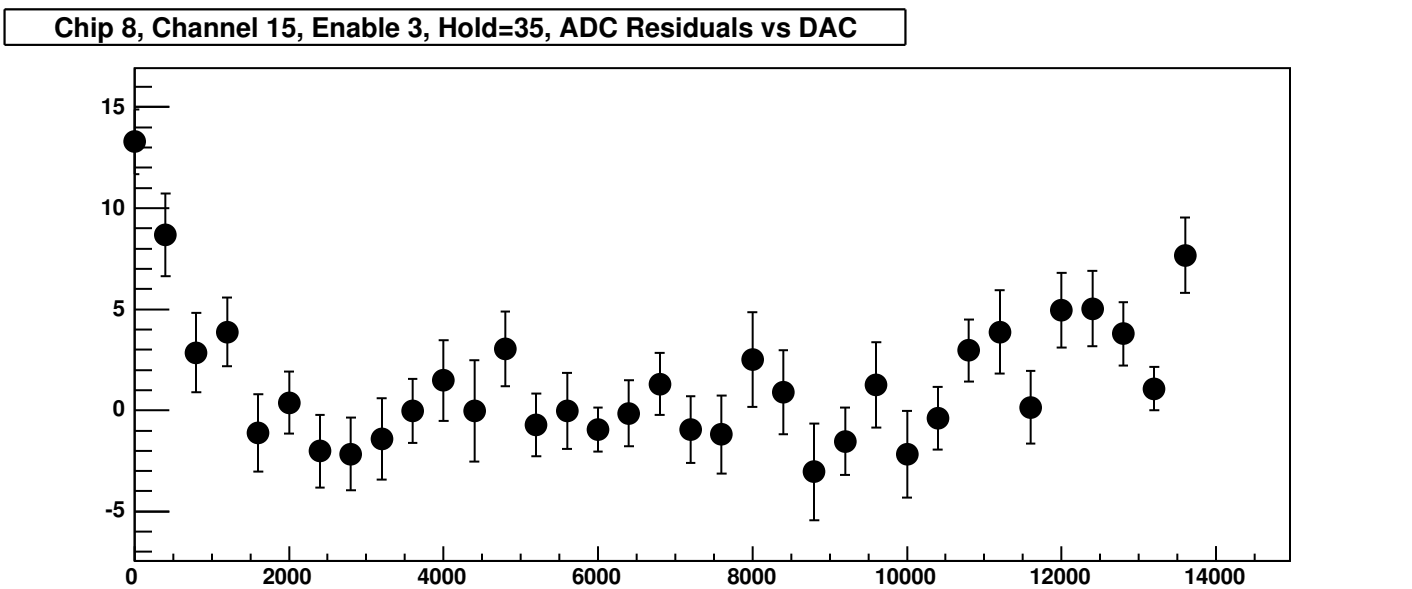

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 485.4 / 23
p0 148.6 ± 2.342
p1 2.234 ± 0.001333
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

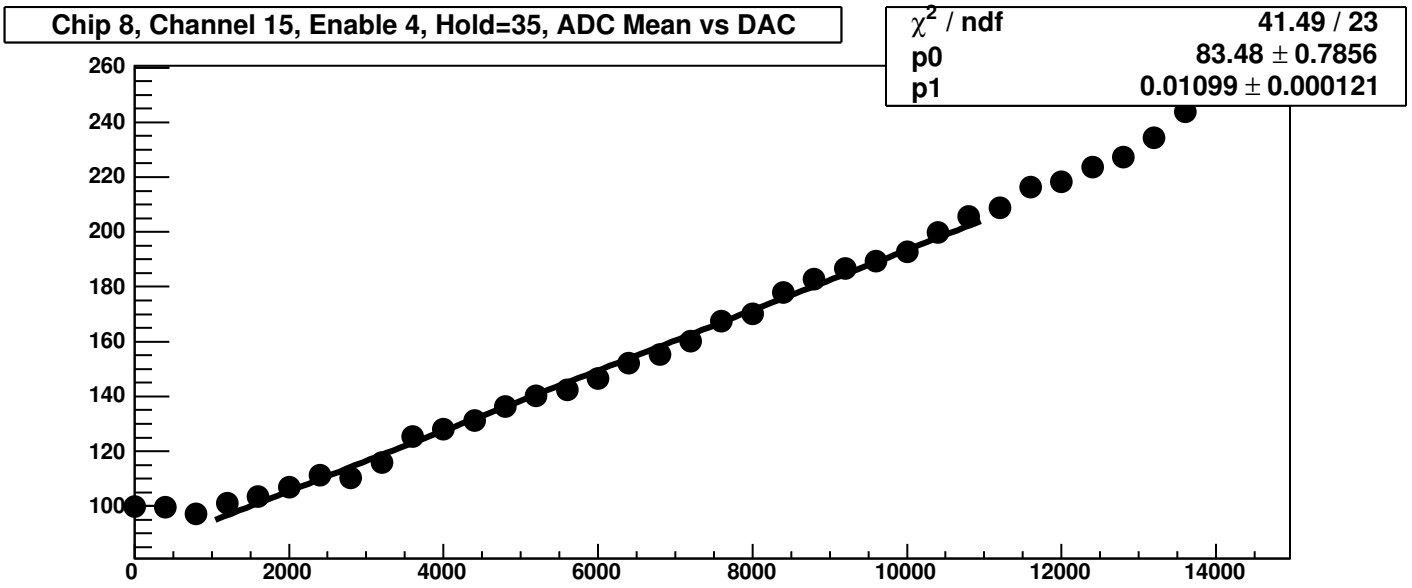

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

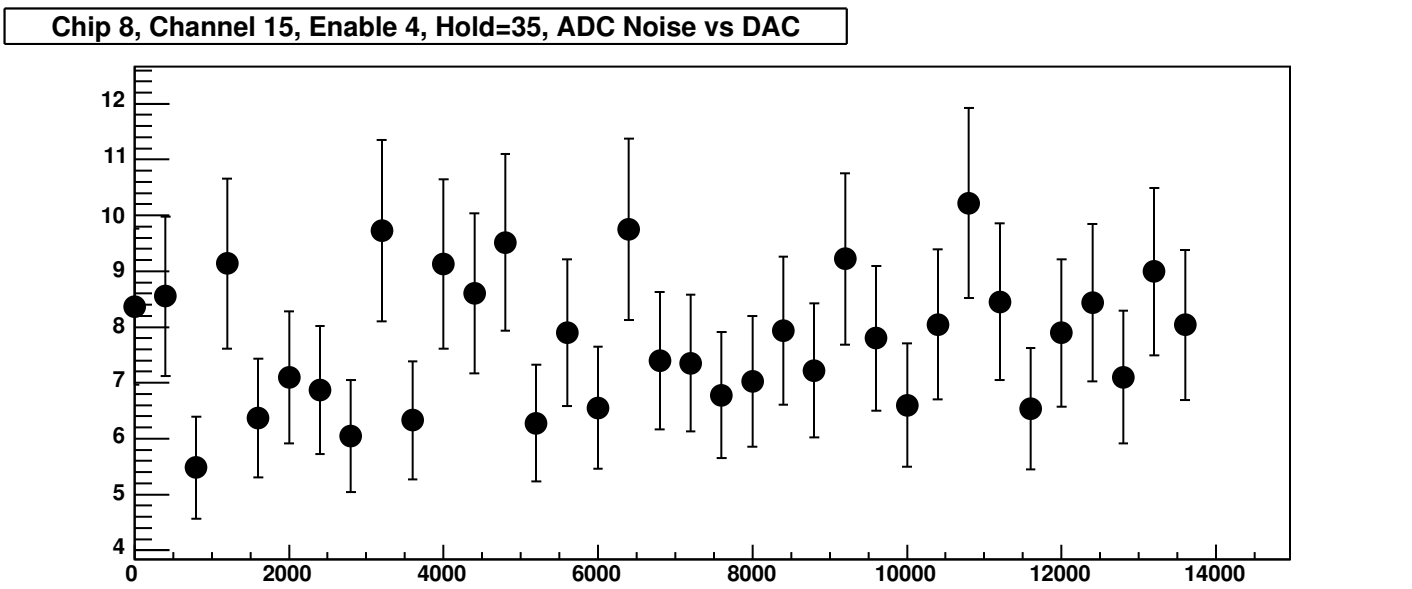

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

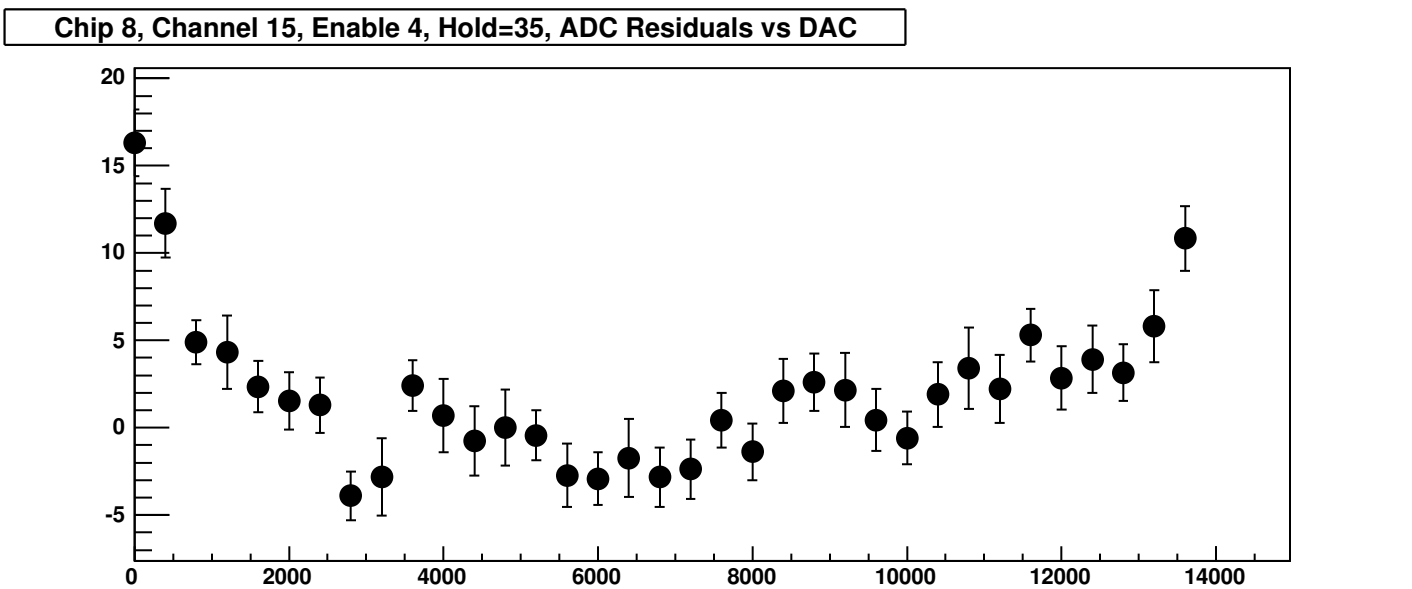

Chip 8, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.41 / 23
p0 89.87 ± 0.809
p1 0.01101 ± 0.0001228
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

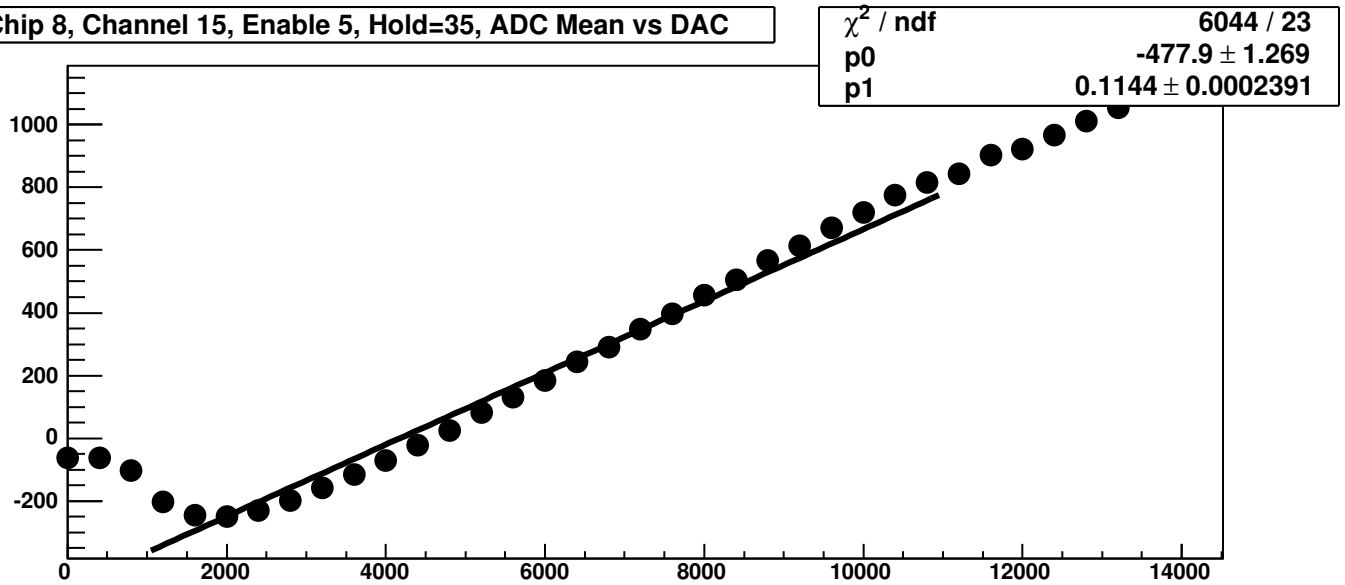

Chip 8, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 41.49 / 23
p0 83.48 ± 0.7856
p1 0.01099 ± 0.000121
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

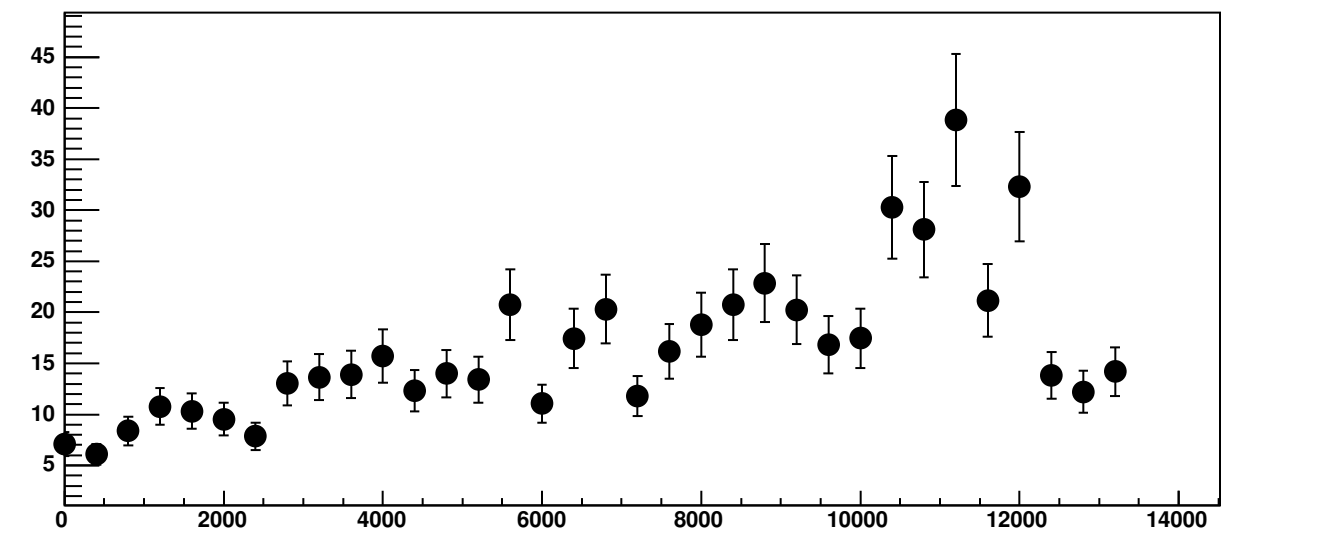

Chip 8, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

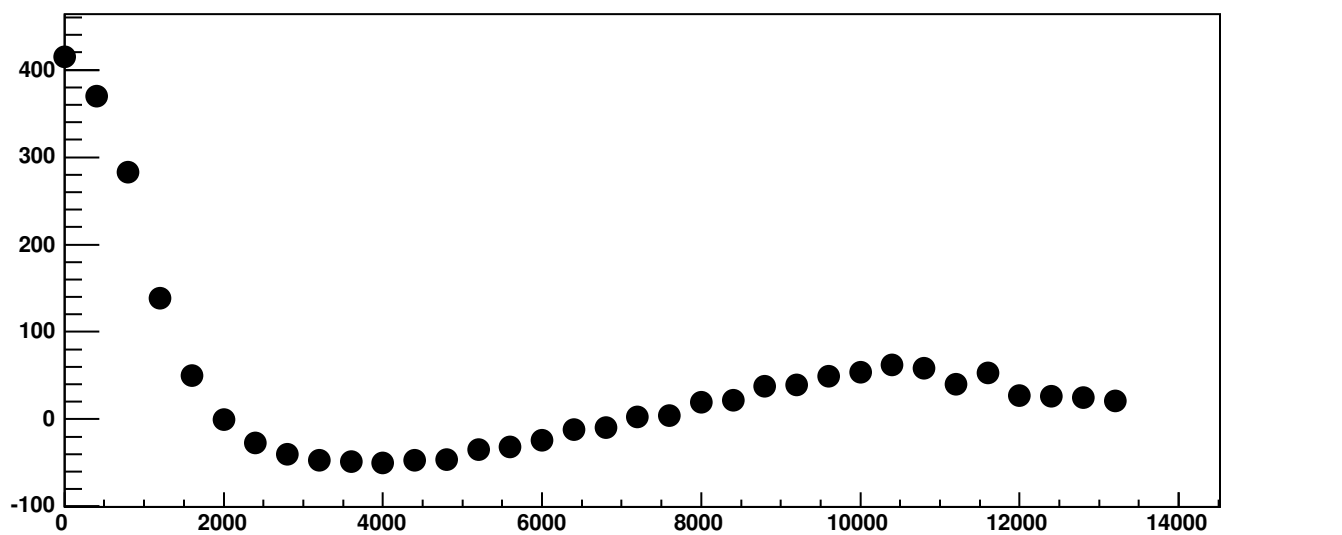
Chip 8, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC



Chip 8, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

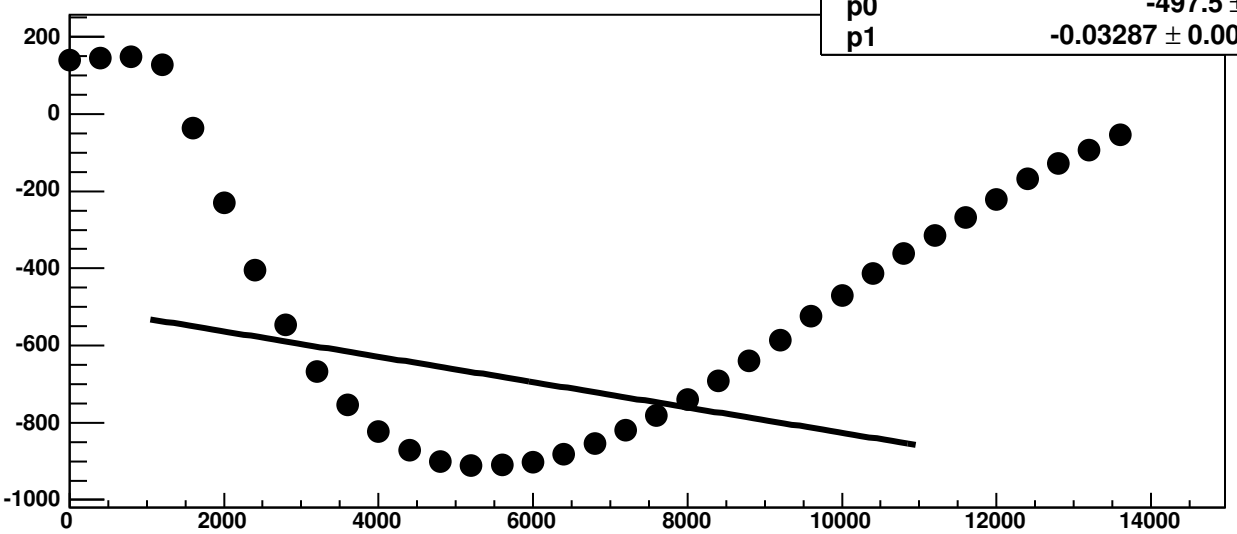


Chip 8, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

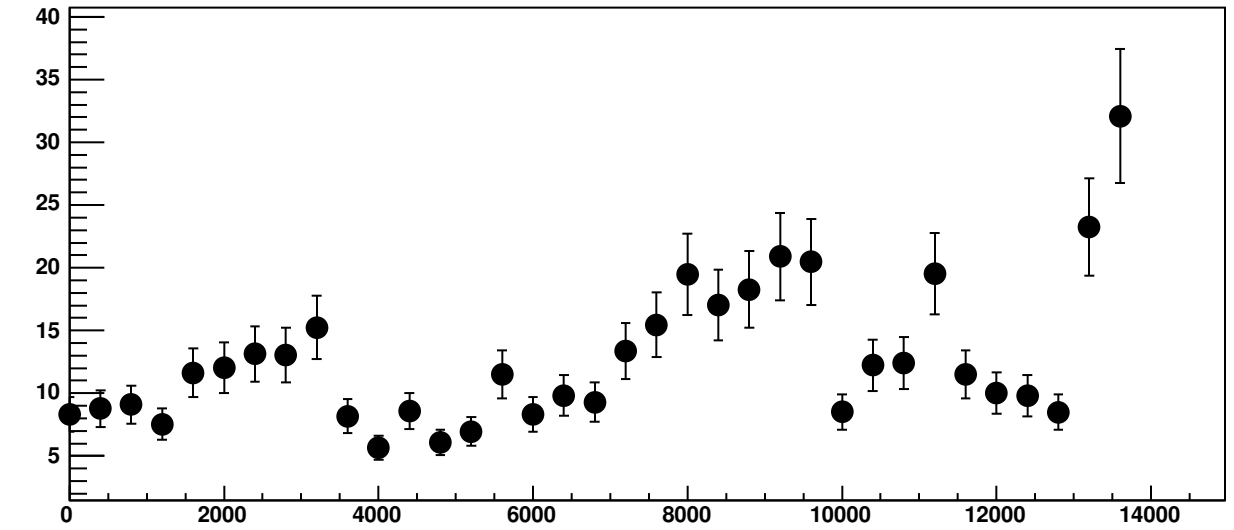


Chip 8, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

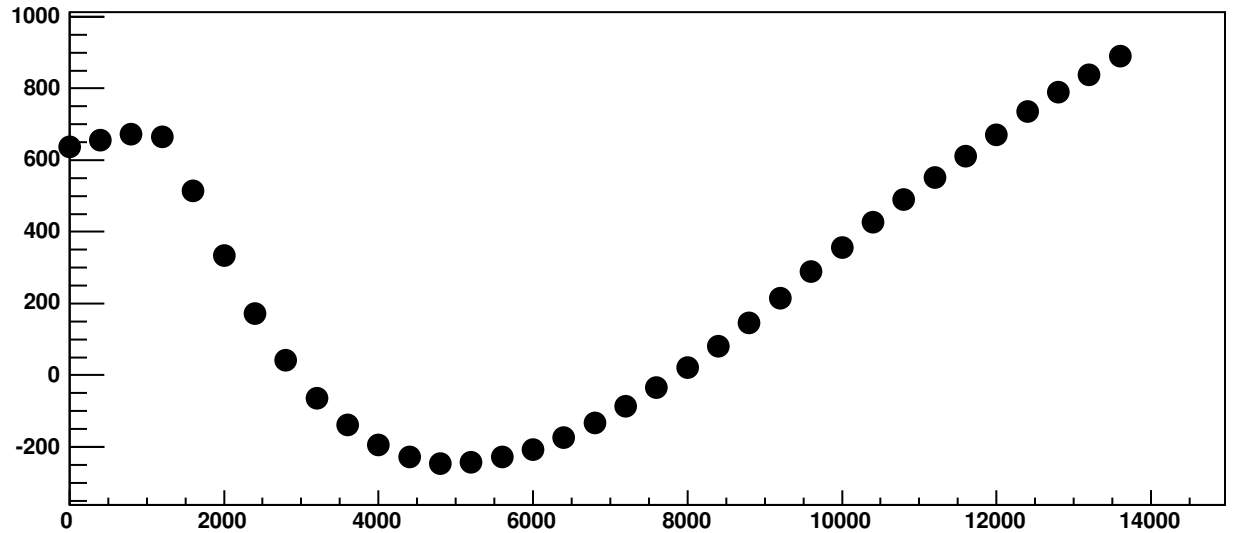
χ^2 / ndf	4.233e+05 / 23
p0	-497.5 ± 1.05
p1	-0.03287 ± 0.0001801

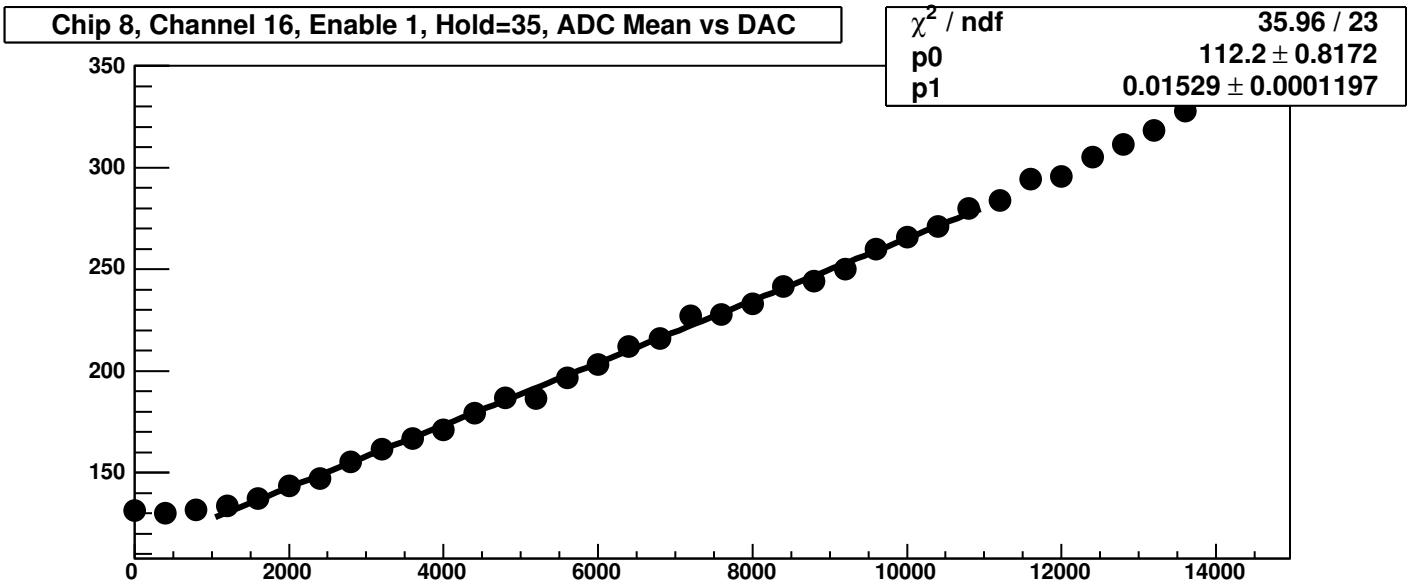


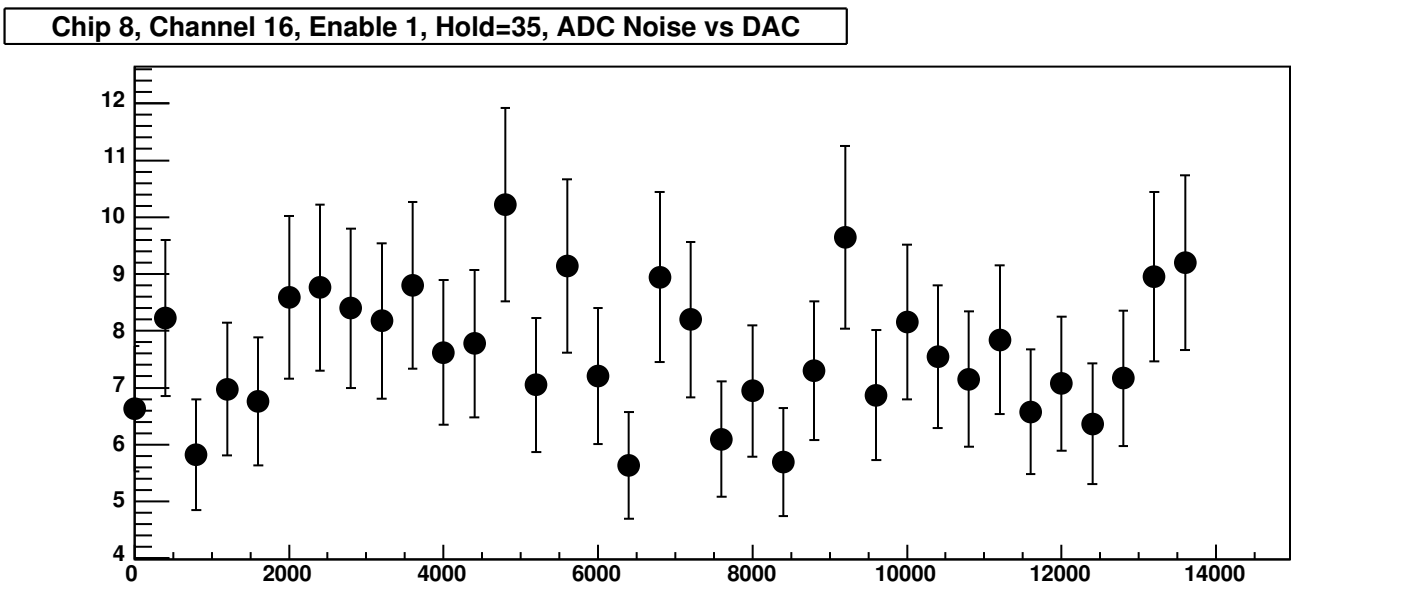
Chip 8, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

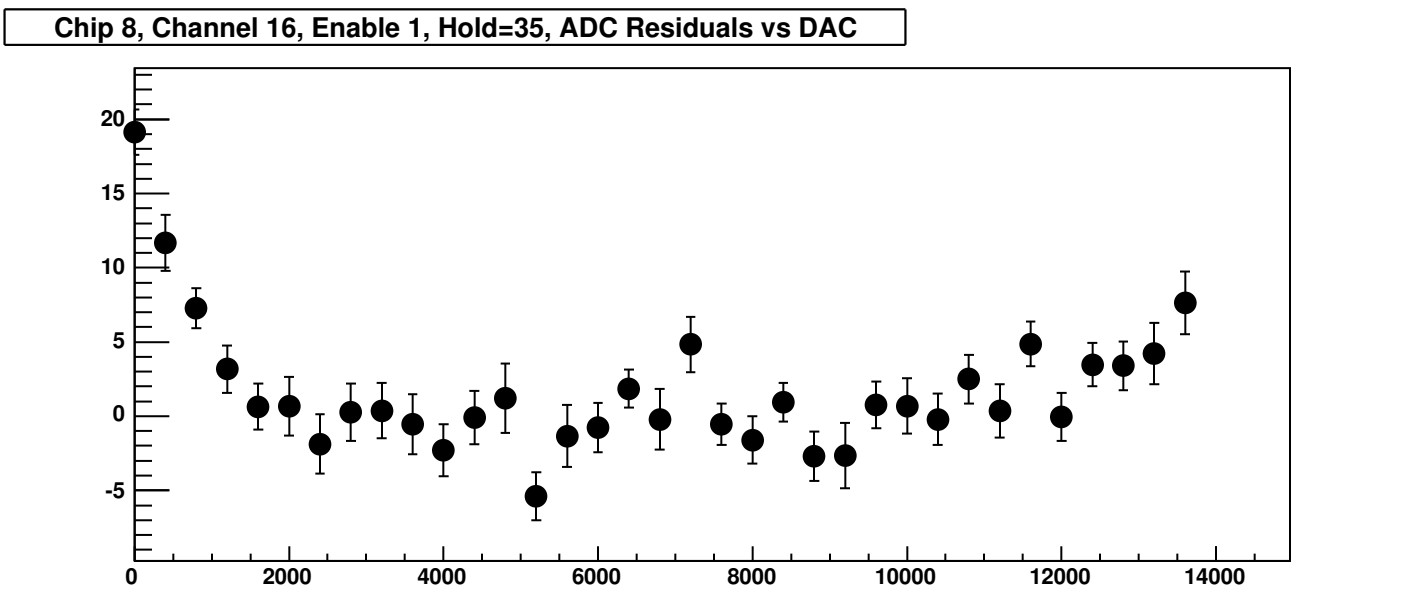


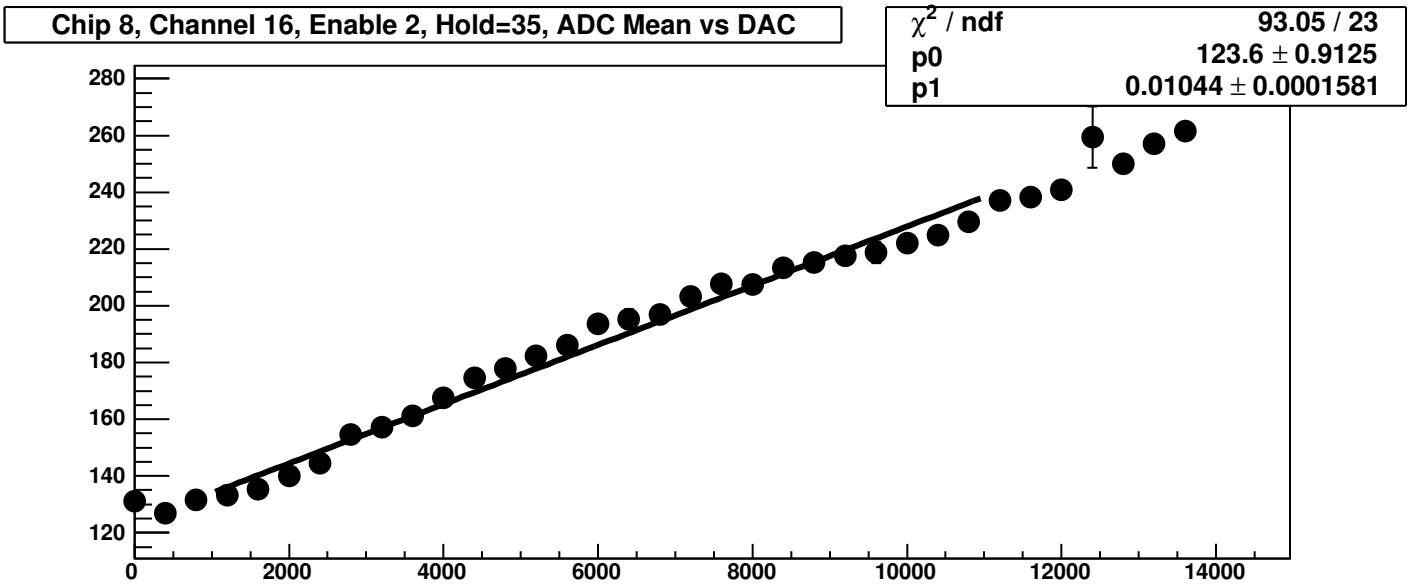
Chip 8, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

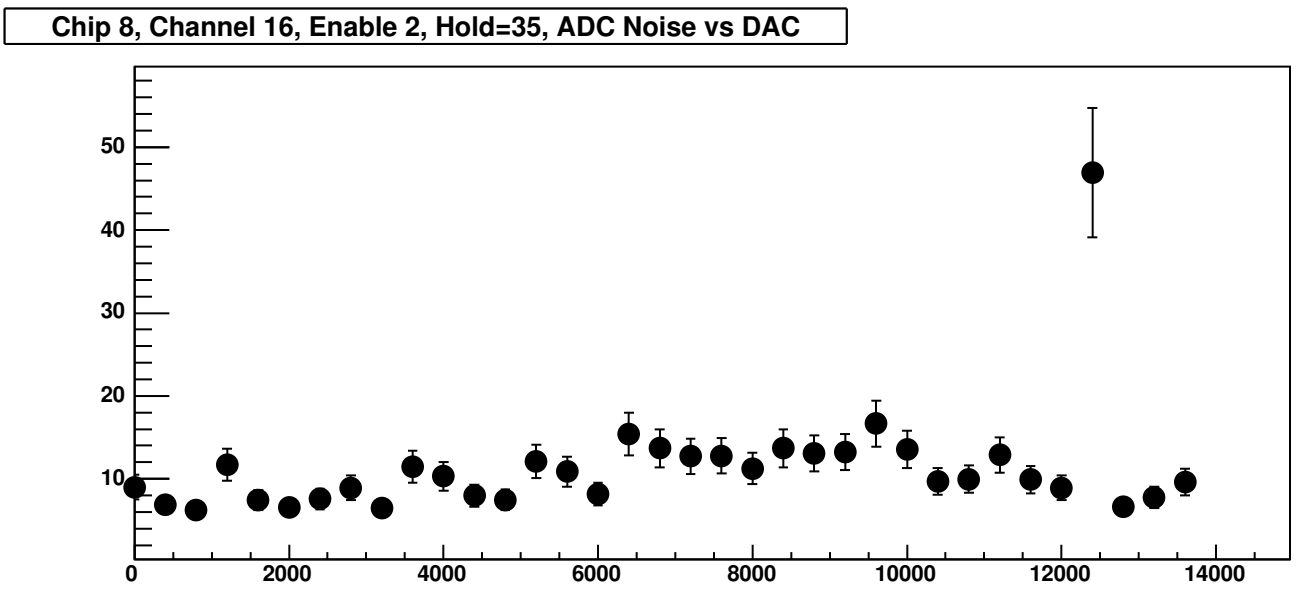


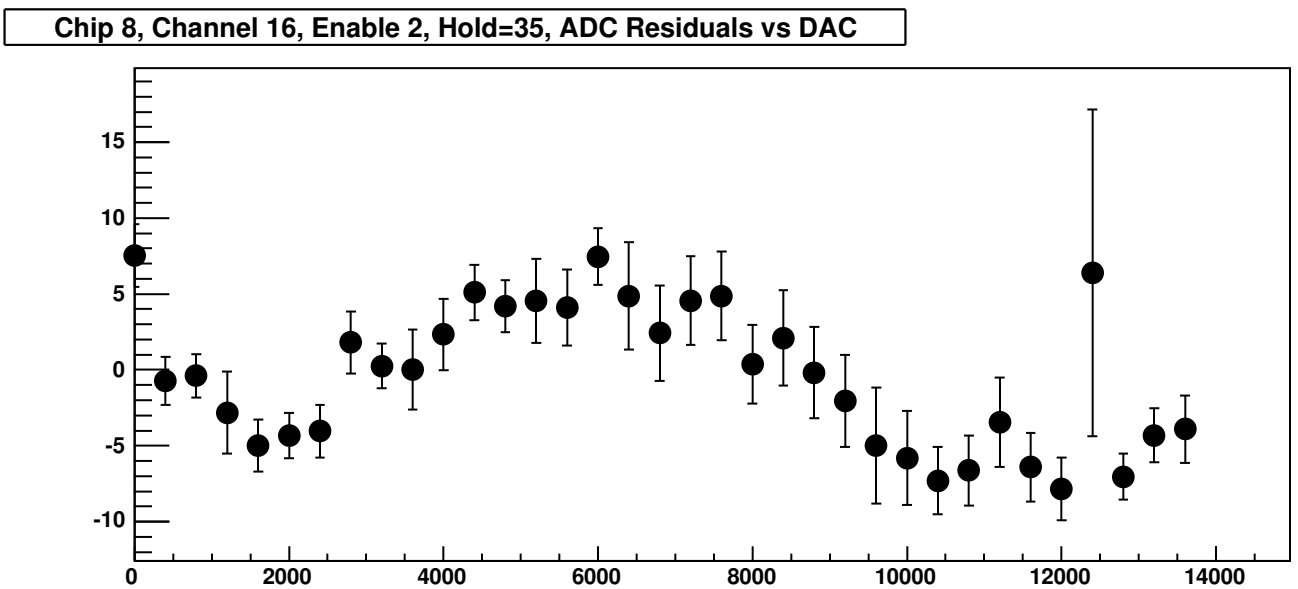

Chip 8, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.96 / 23
p0 112.2 ± 0.8172
p1 0.01529 ± 0.0001197
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

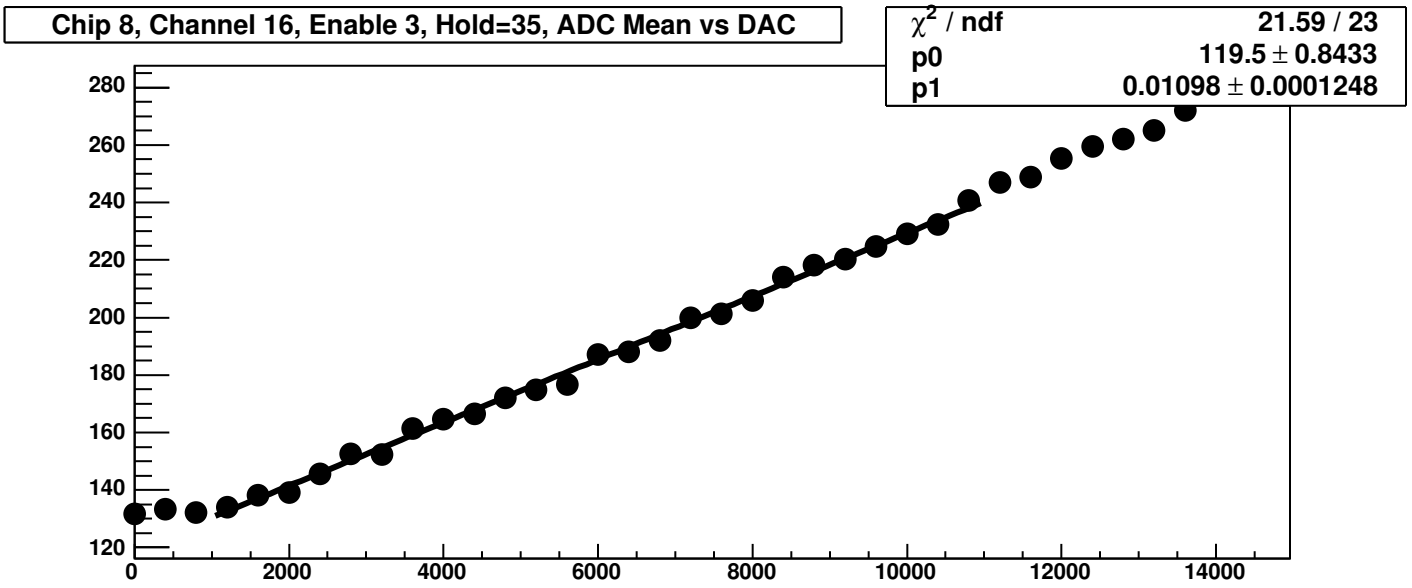

Chip 8, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

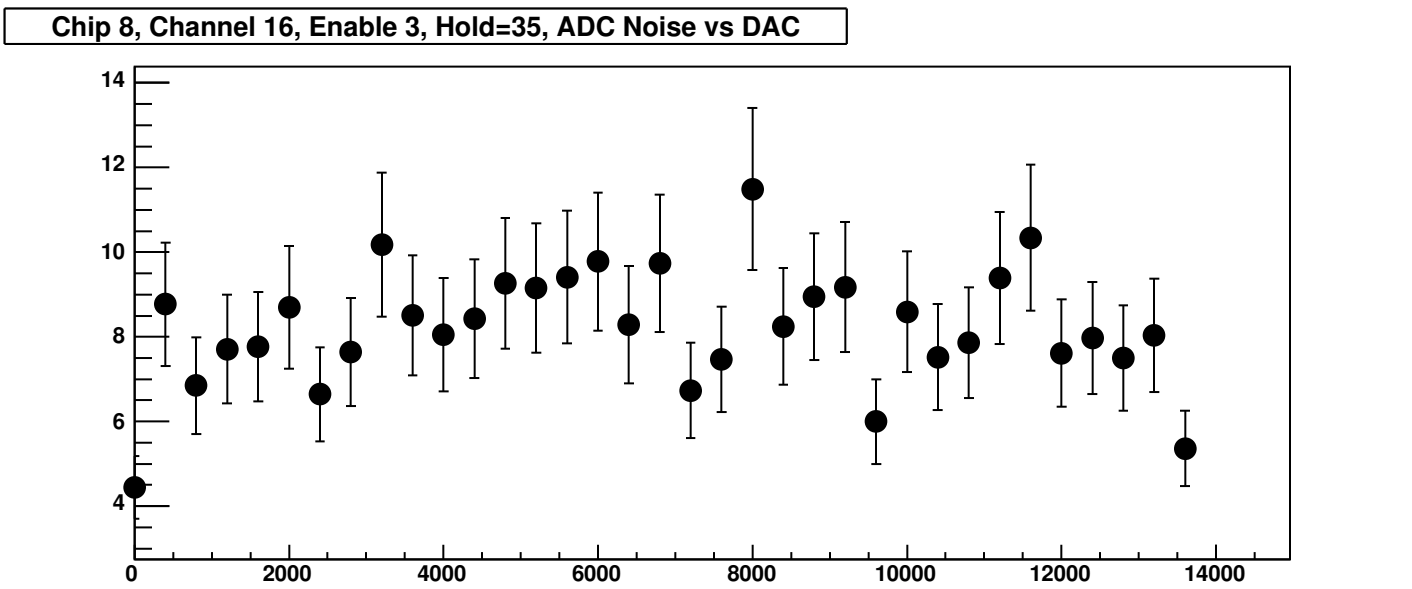

Chip 8, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

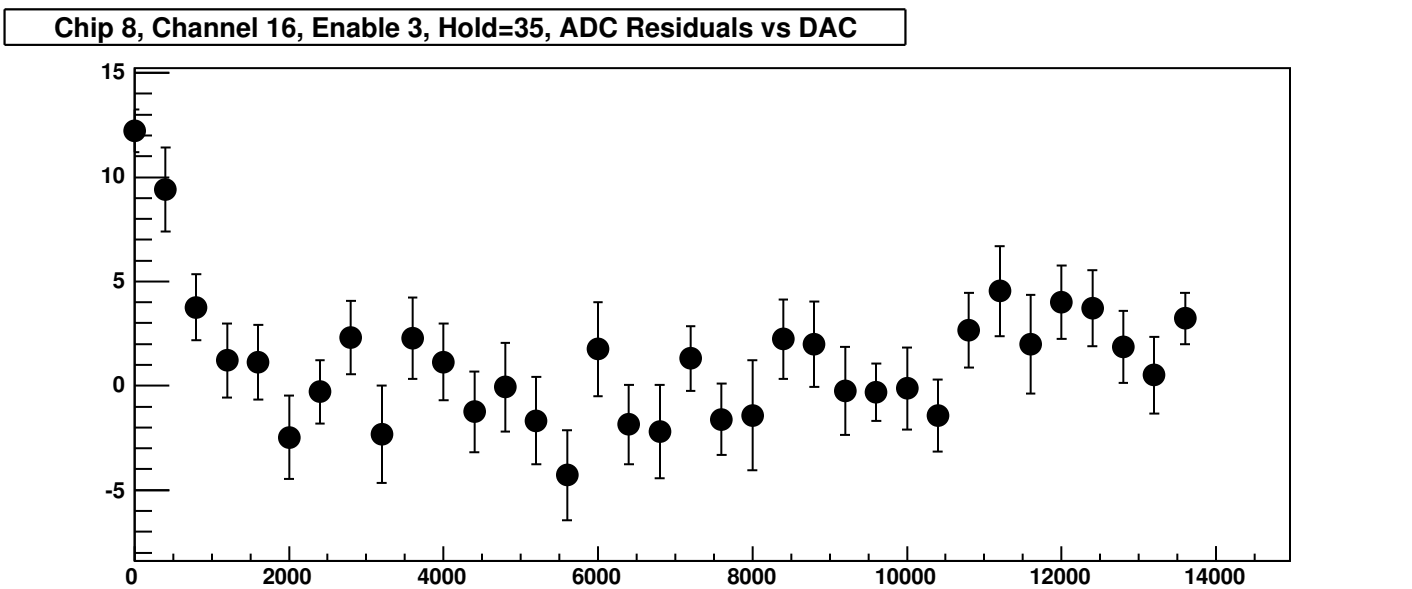

Chip 8, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 93.05 / 23
p0 123.6 ± 0.9125
p1 0.01044 ± 0.0001581
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

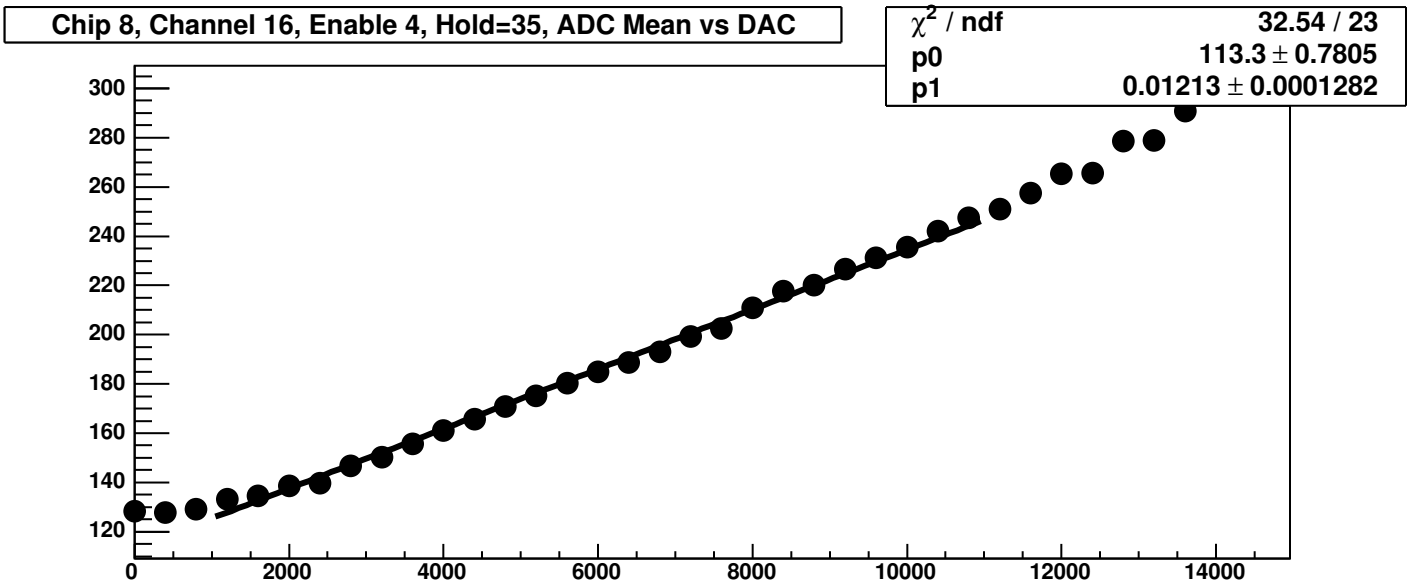

Chip 8, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

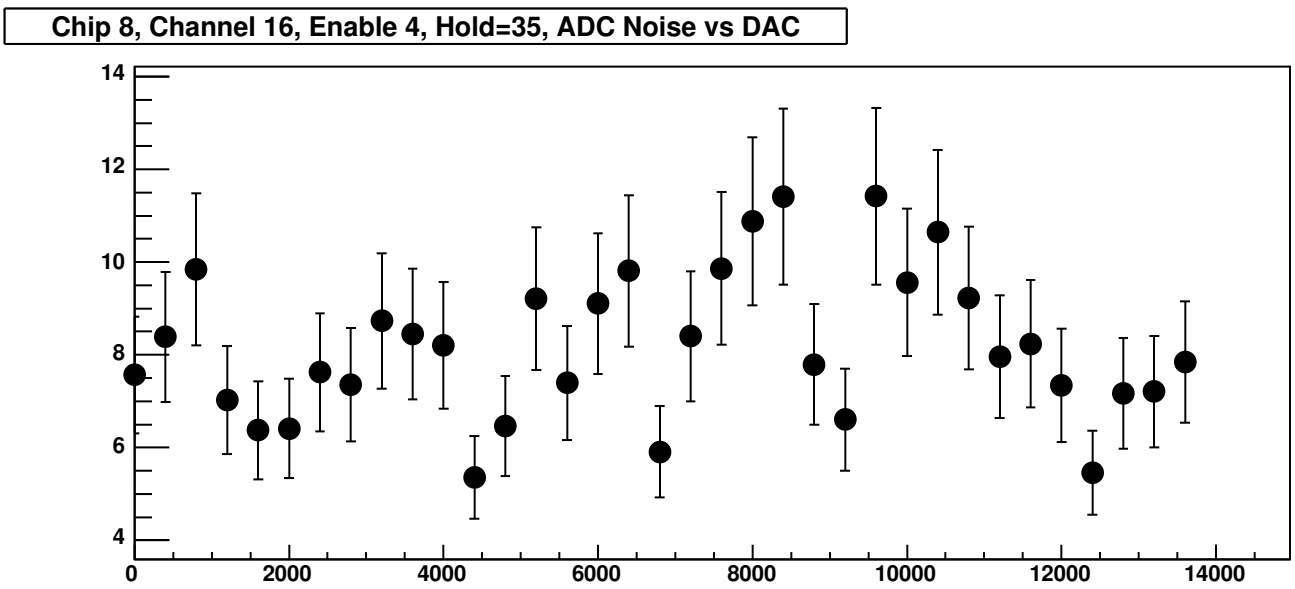

Chip 8, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

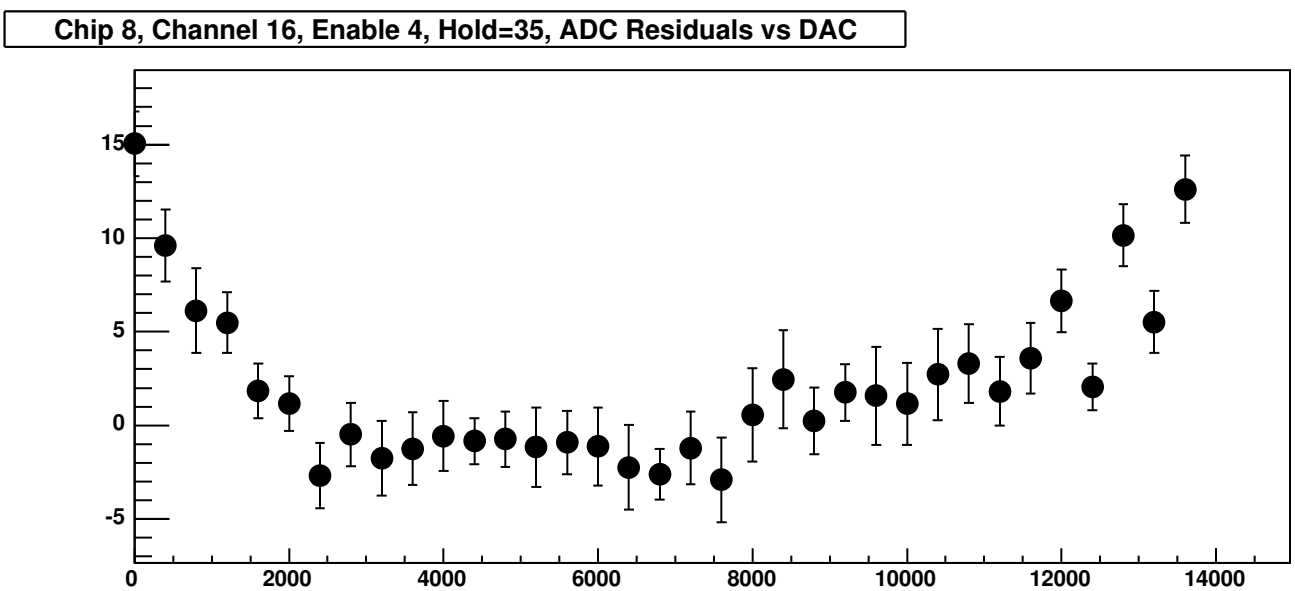

Chip 8, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.59 / 23
p0 119.5 ± 0.8433
p1 0.01098 ± 0.0001248
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

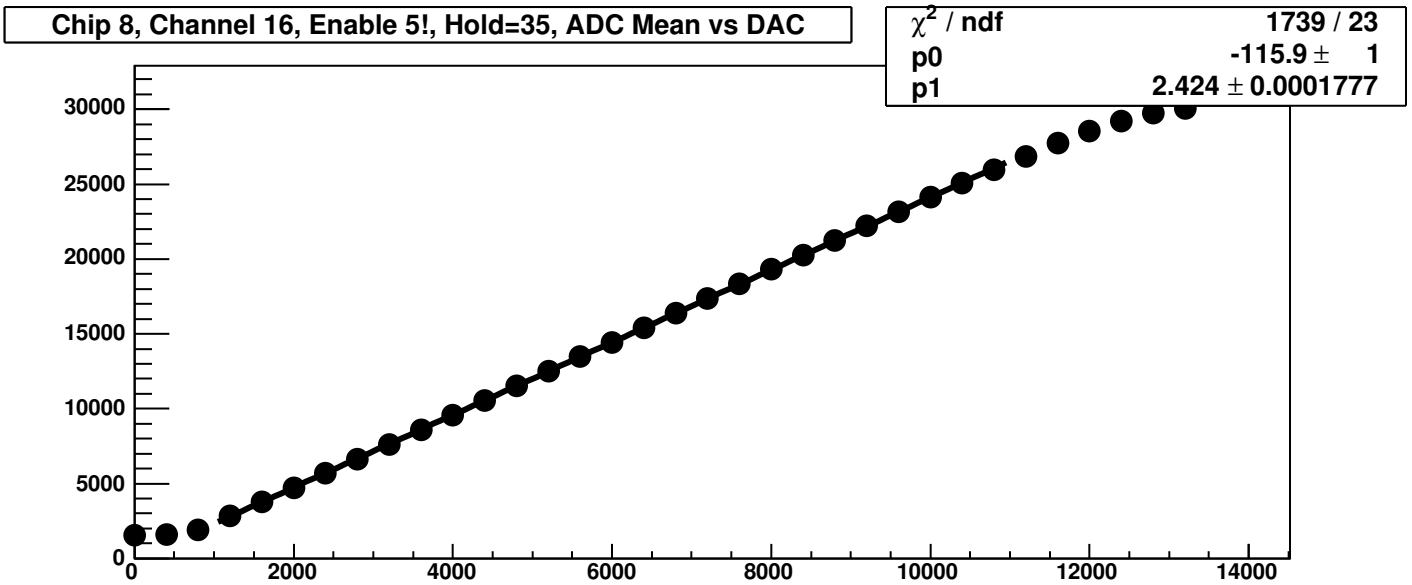

Chip 8, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

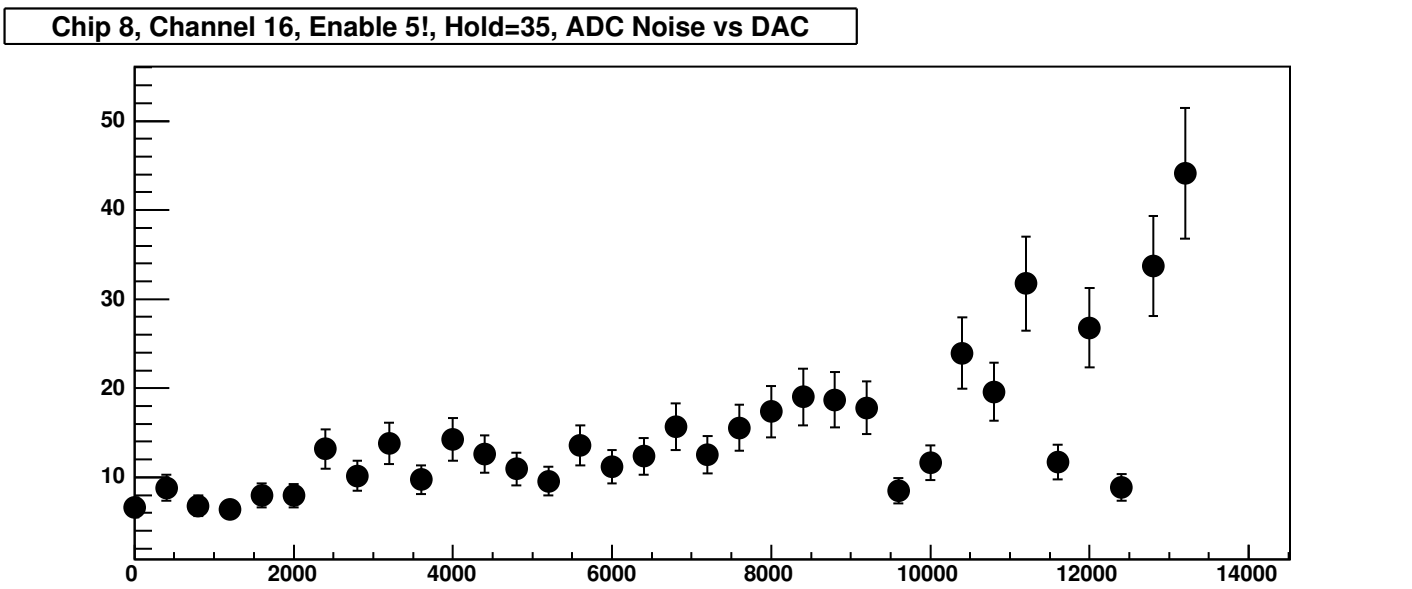

Chip 8, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

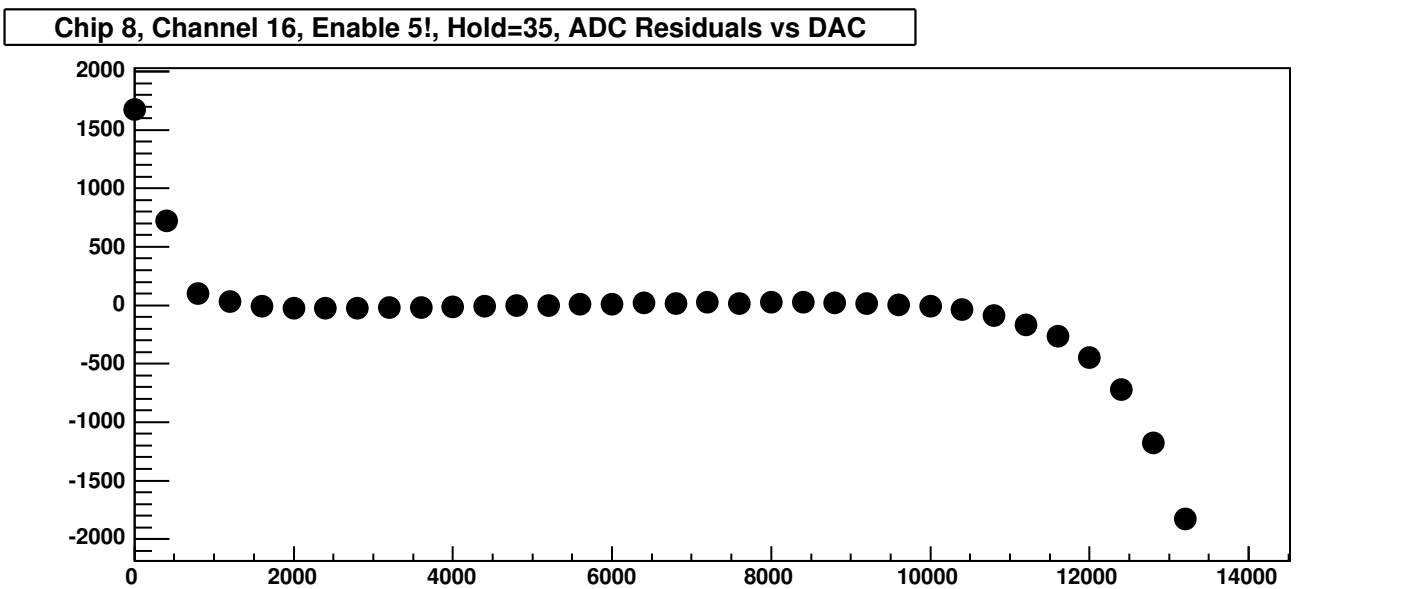

Chip 8, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.54 / 23
p0 113.3 ± 0.7805
p1 0.01213 ± 0.0001282
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

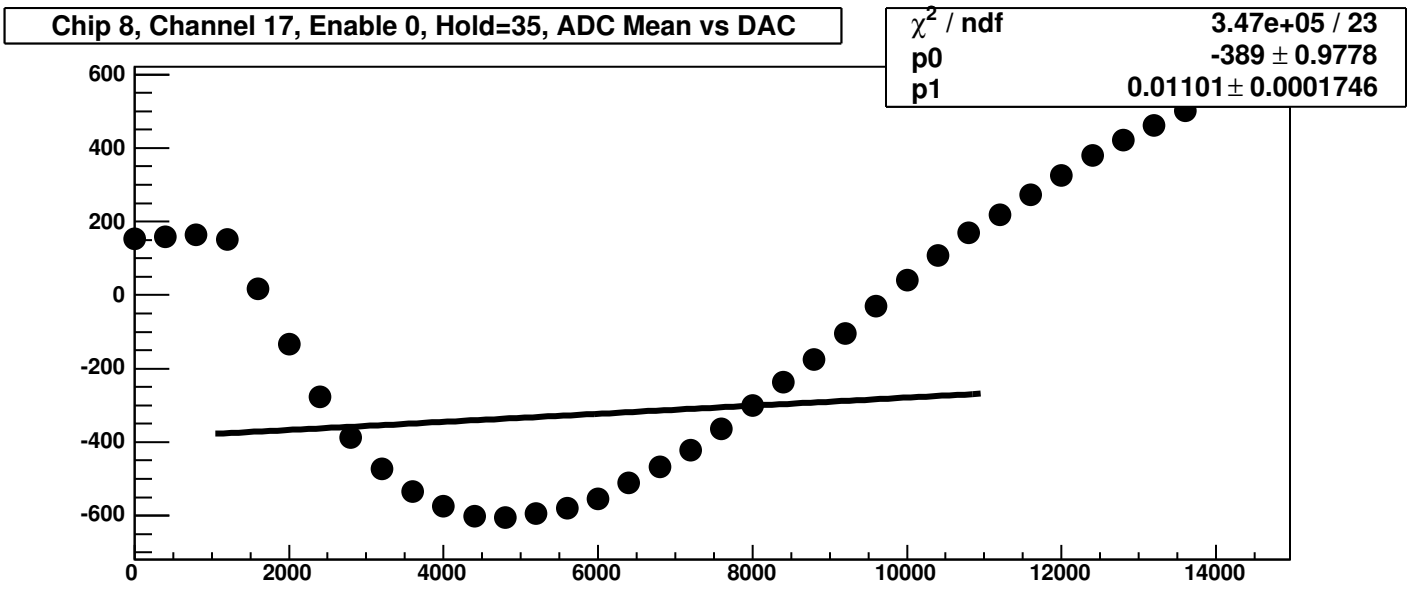

Chip 8, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

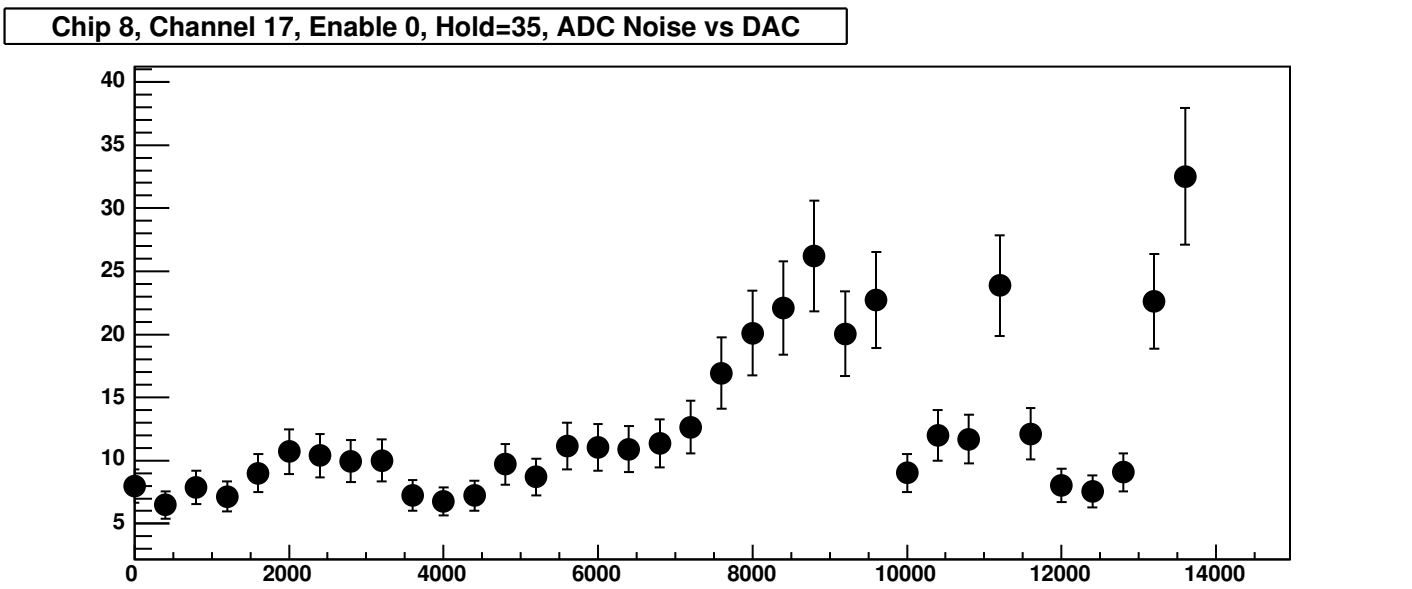

Chip 8, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

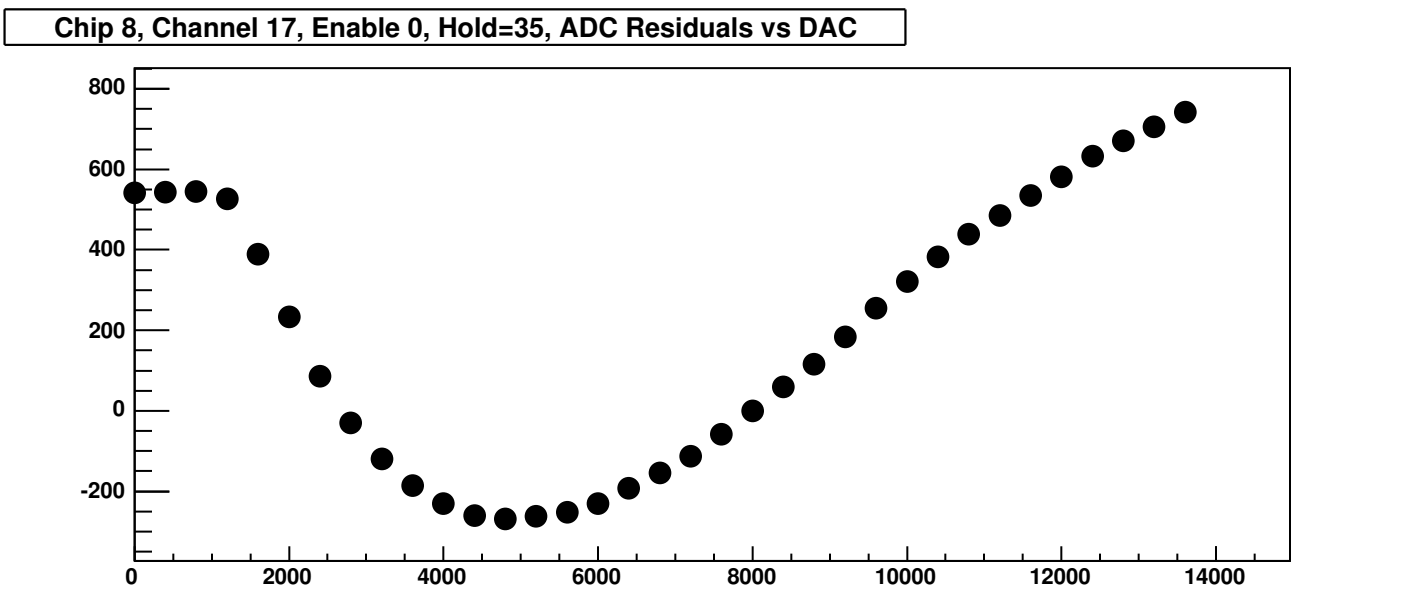

Chip 8, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1739 / 23
p0 -115.9 ± 1
p1 2.424 ± 0.0001777
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

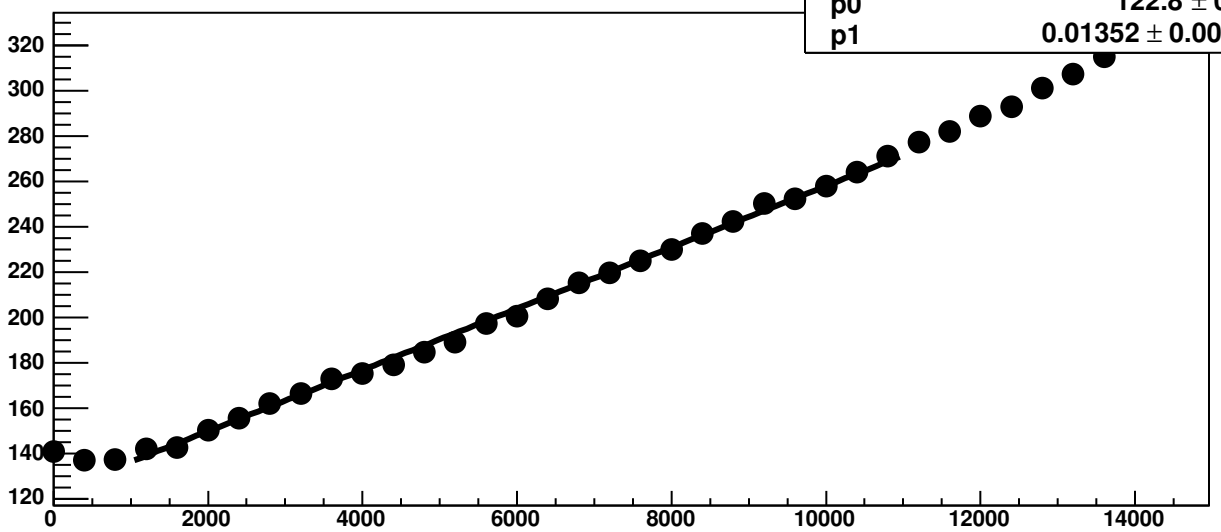

Chip 8, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.47e+05 / 23
p0 -389 ± 0.9778
p1 0.01101 ± 0.0001746
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

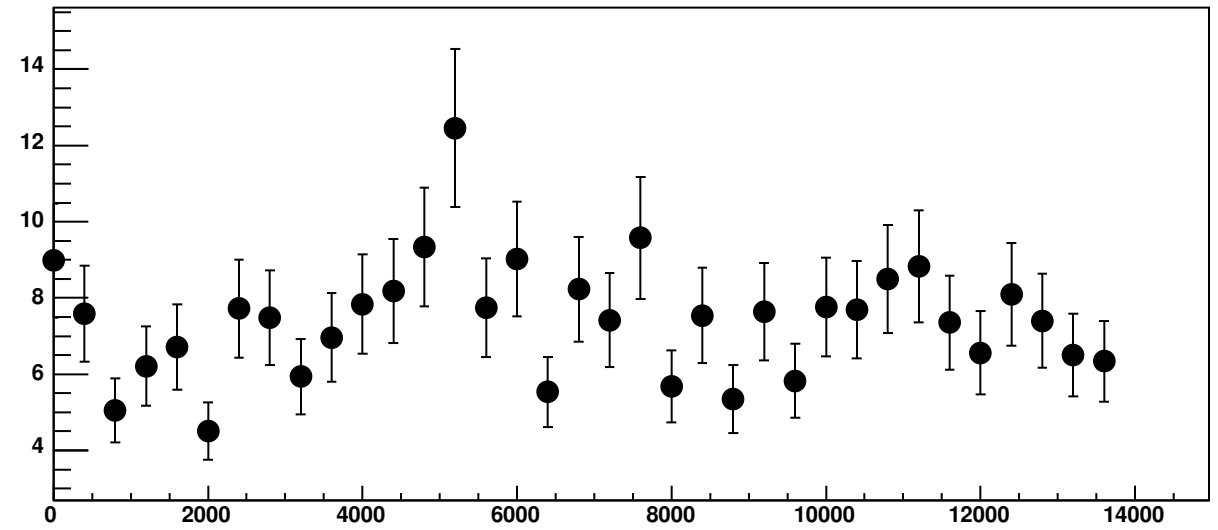

Chip 8, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

Chip 8, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

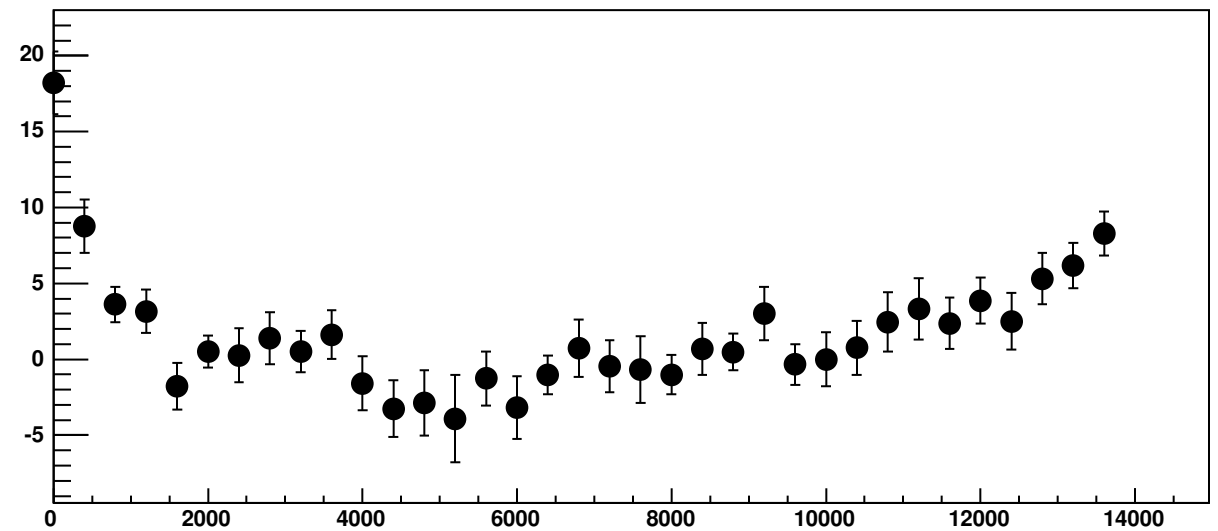
χ^2 / ndf	25.31 / 23
p0	122.8 ± 0.6908
p1	0.01352 ± 0.0001059

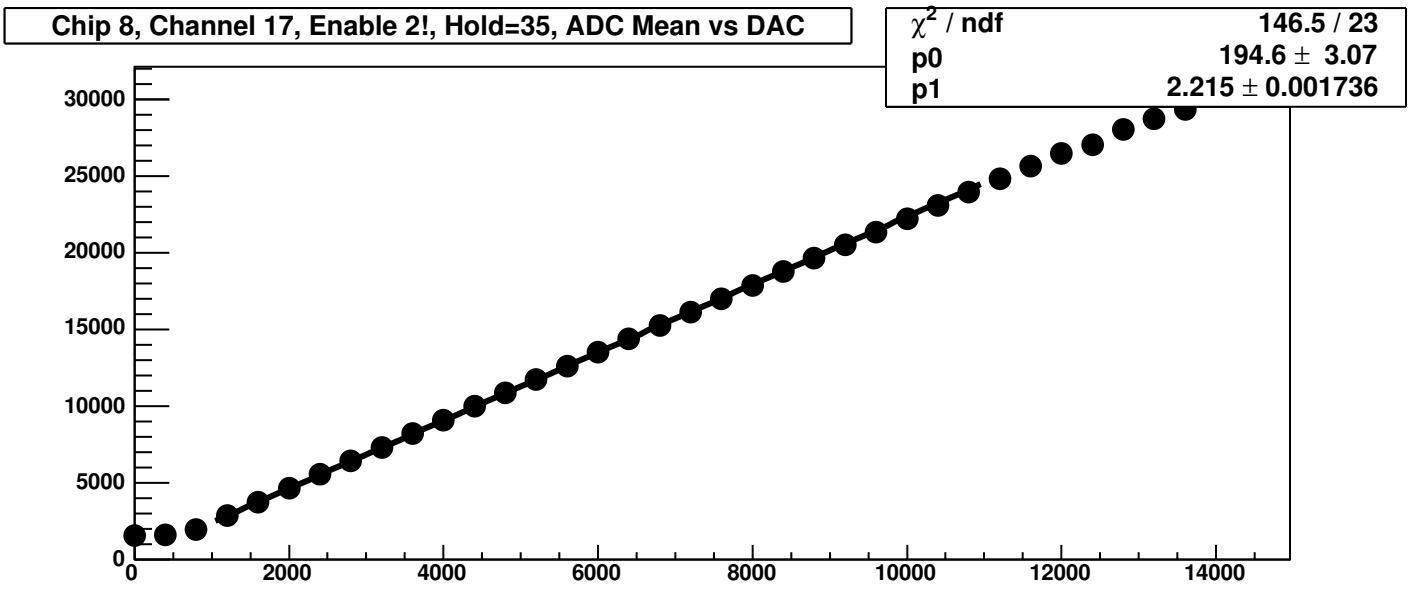


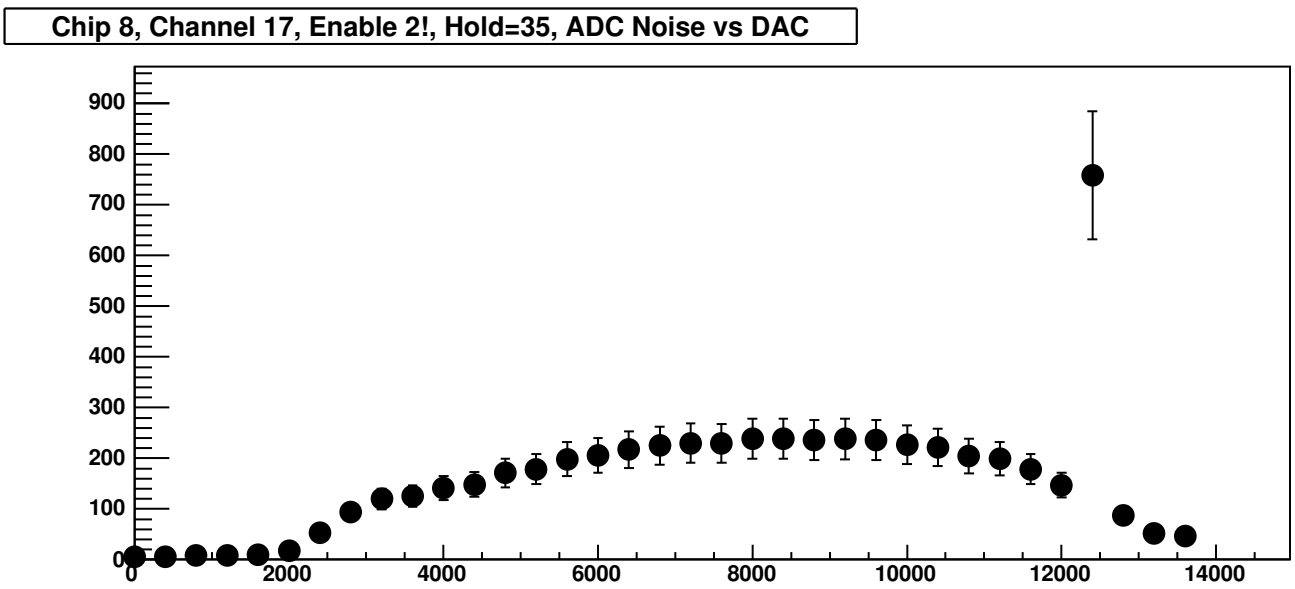
Chip 8, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

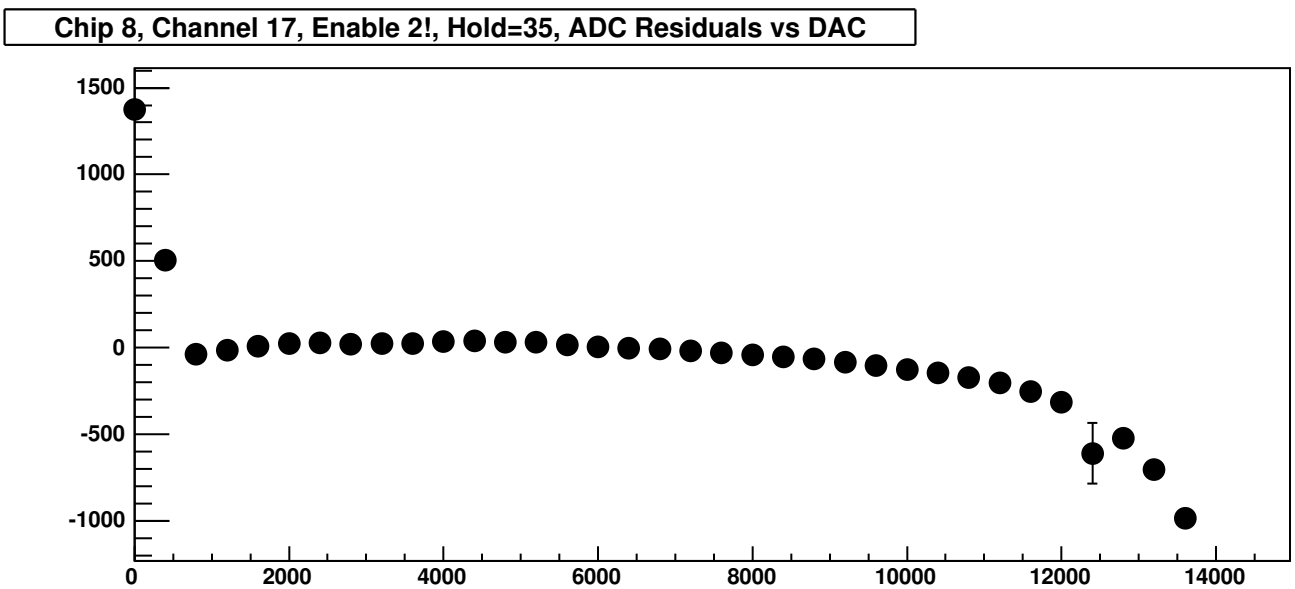


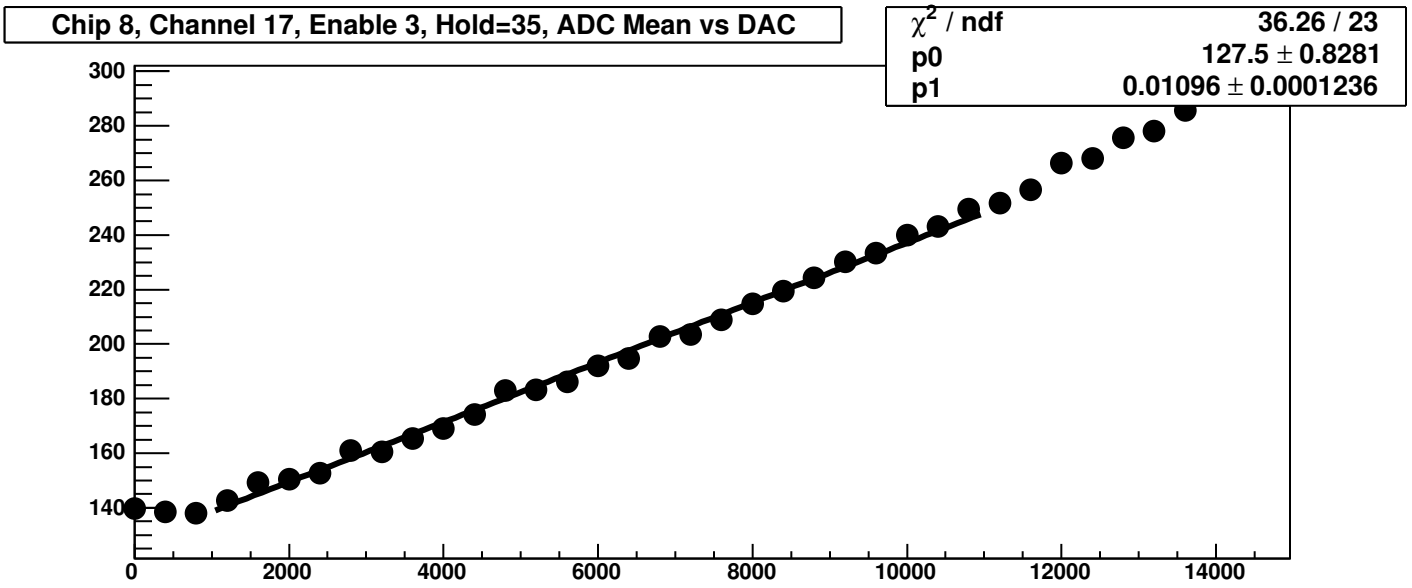
Chip 8, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

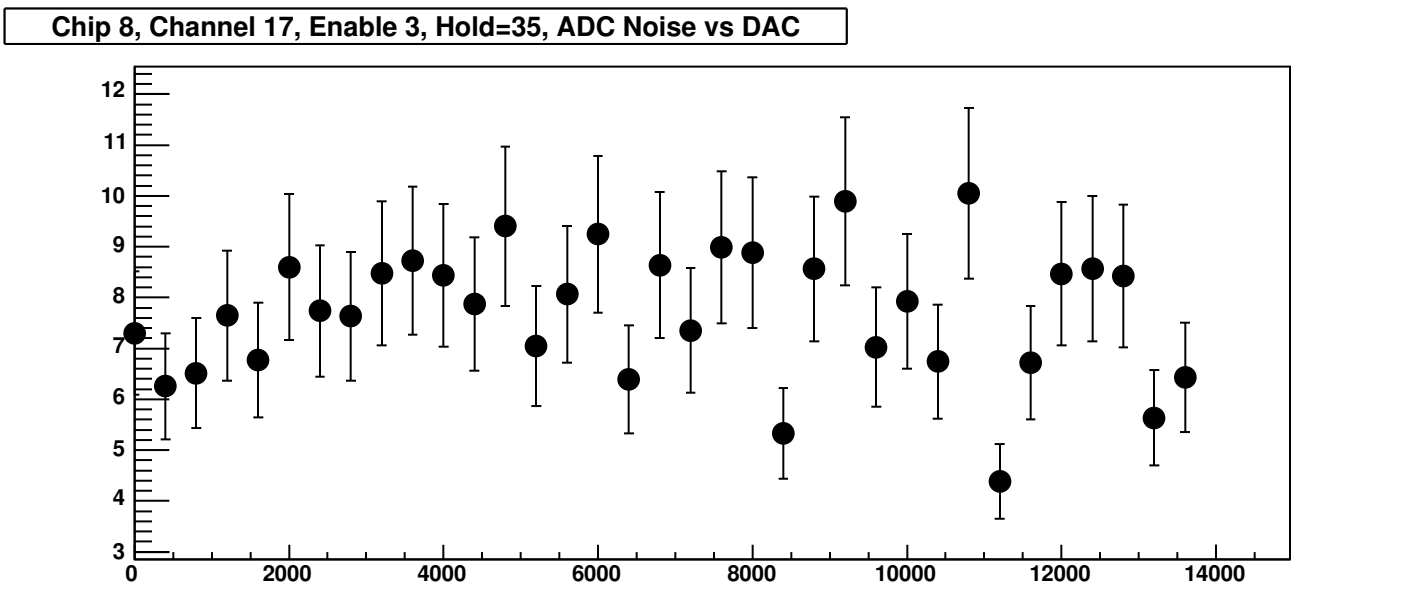


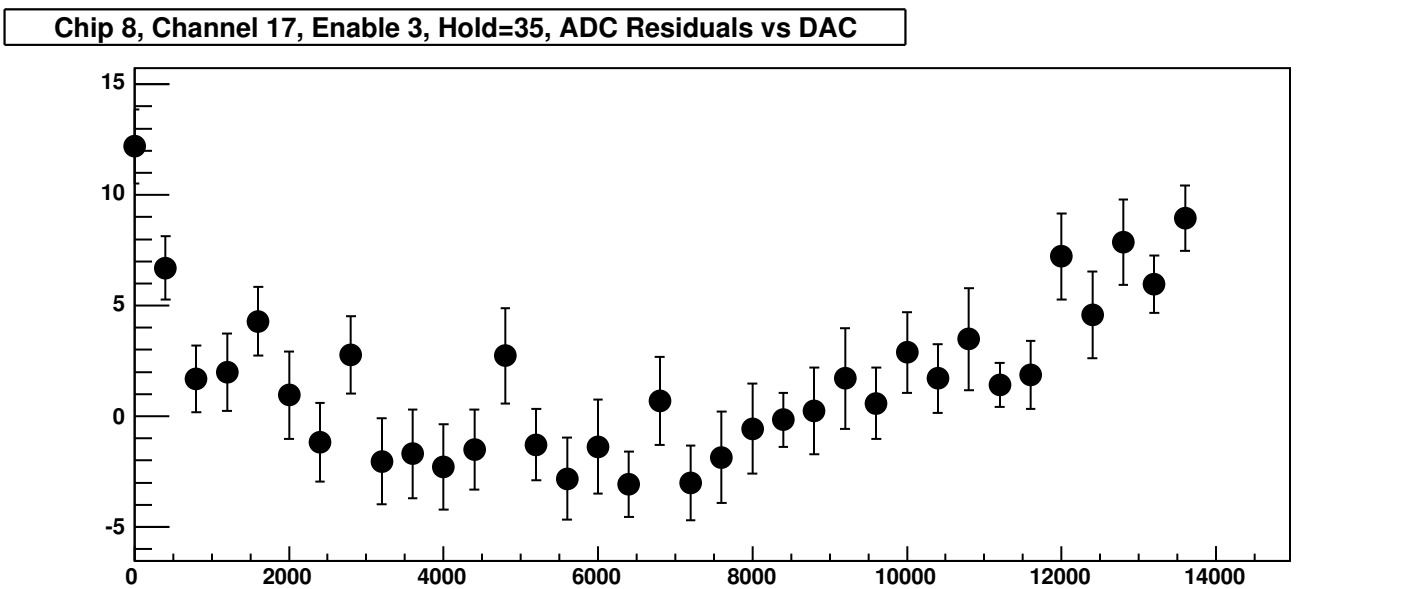

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 146.5 / 23
p0 194.6 ± 3.07
p1 2.215 ± 0.001736
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

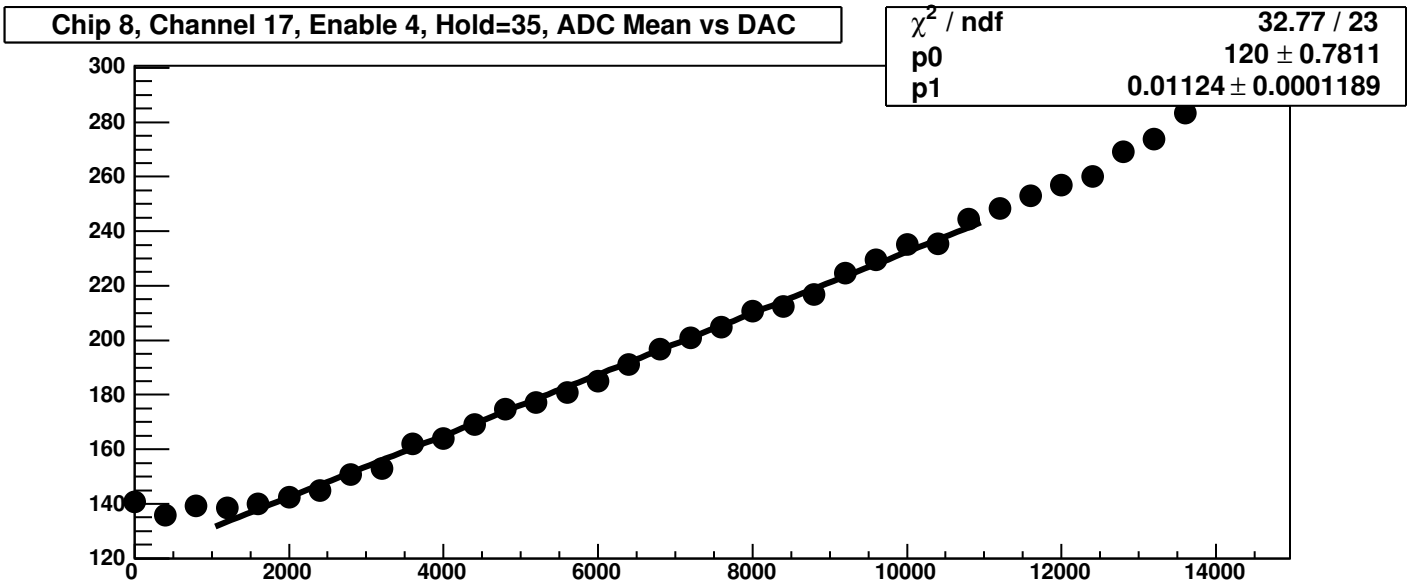

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

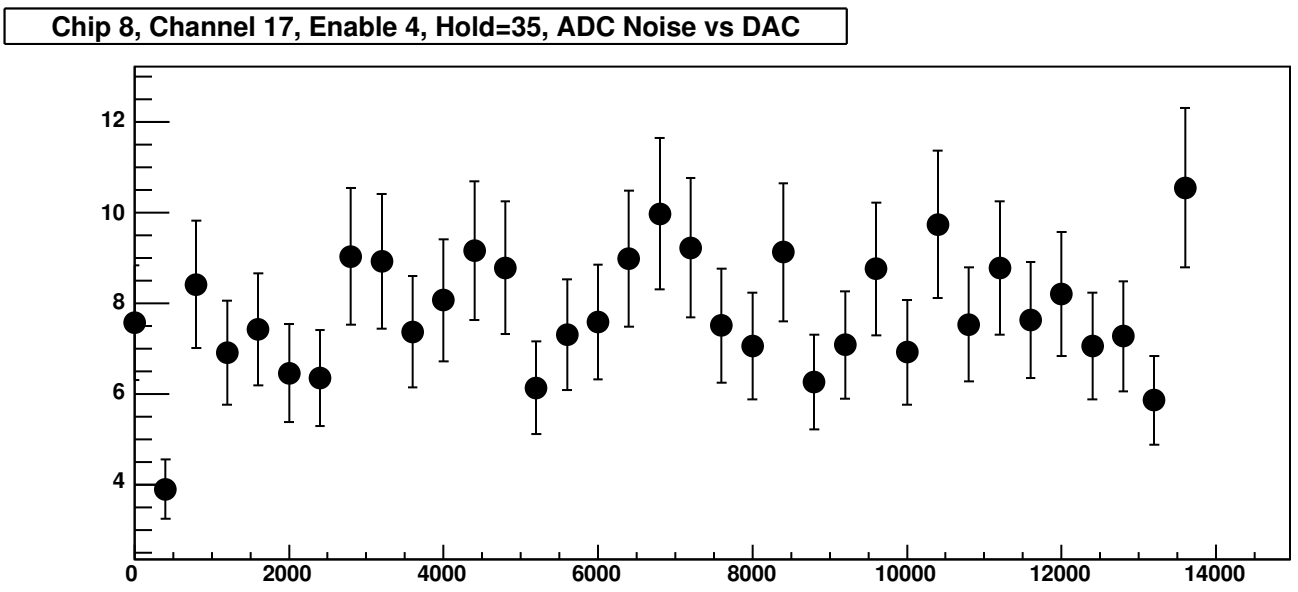

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

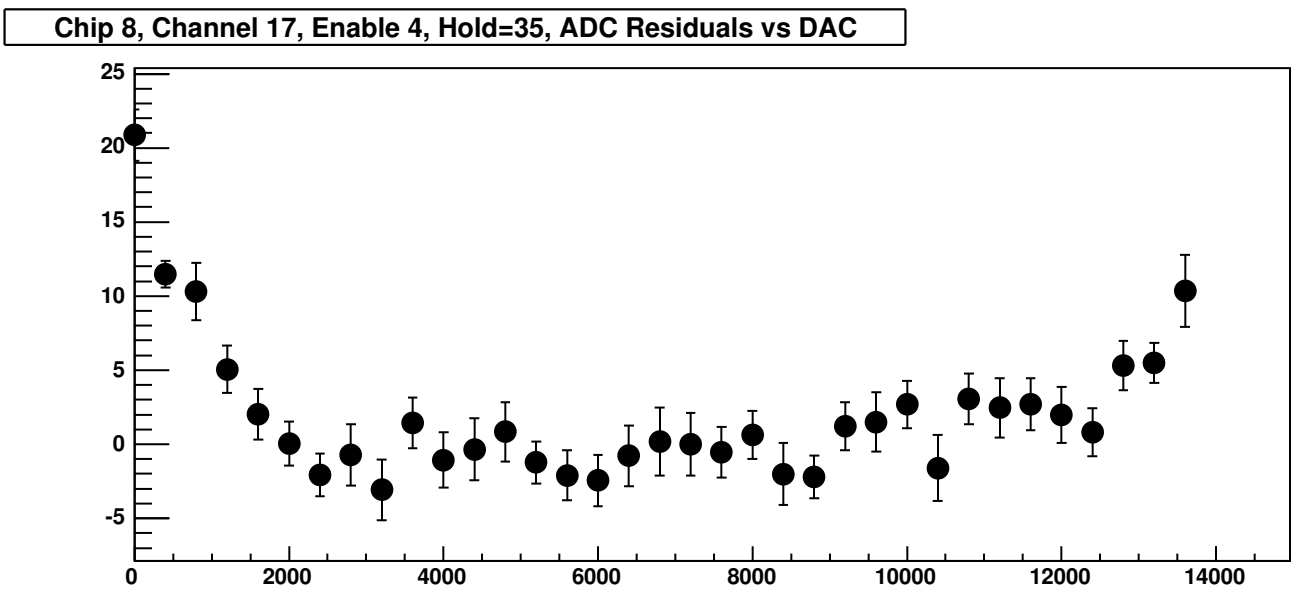

Chip 8, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.26 / 23
p0 127.5 ± 0.8281
p1 0.01096 ± 0.0001236
300
280
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000

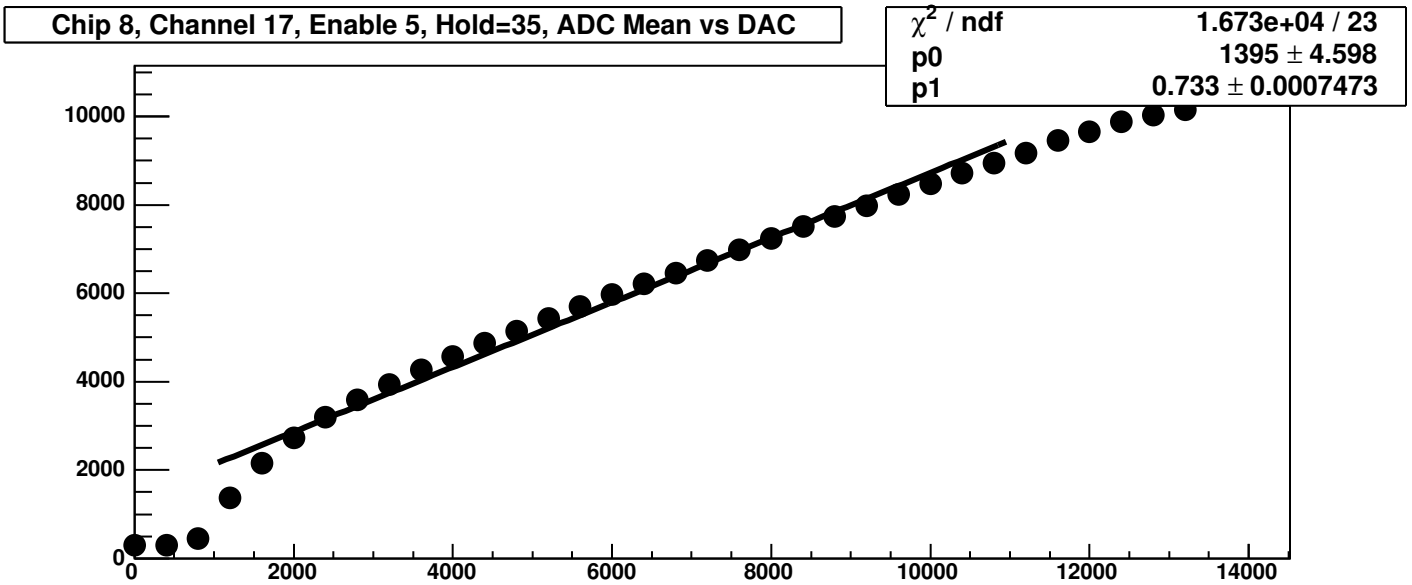

Chip 8, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

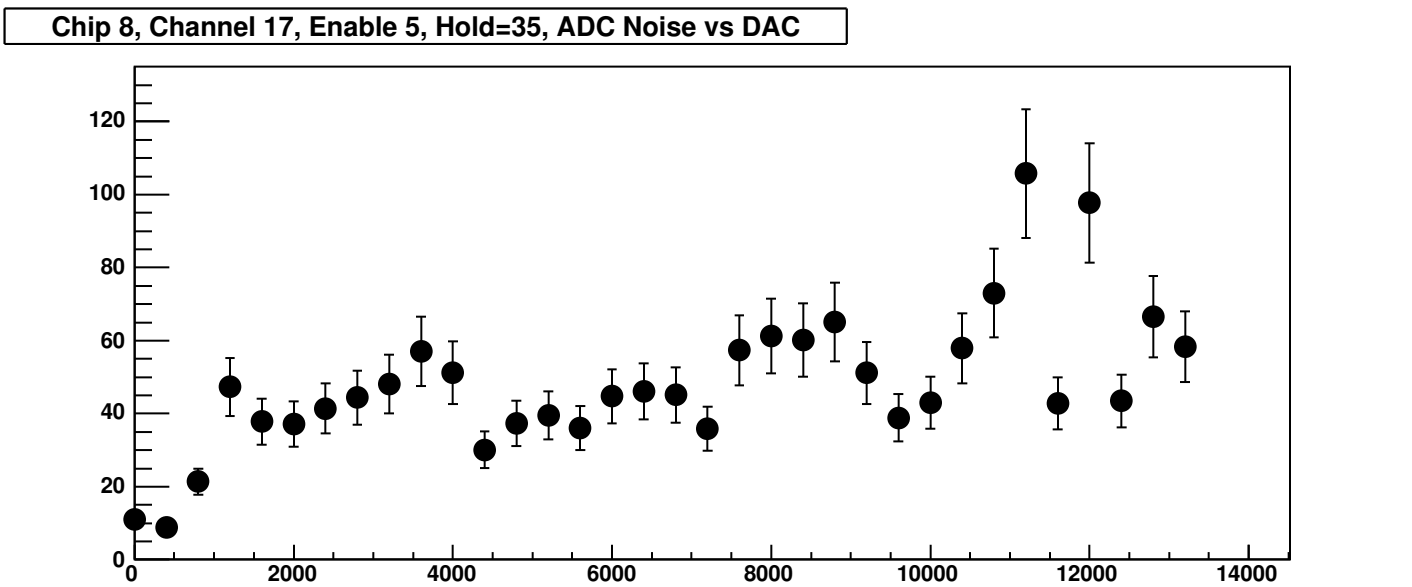

Chip 8, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

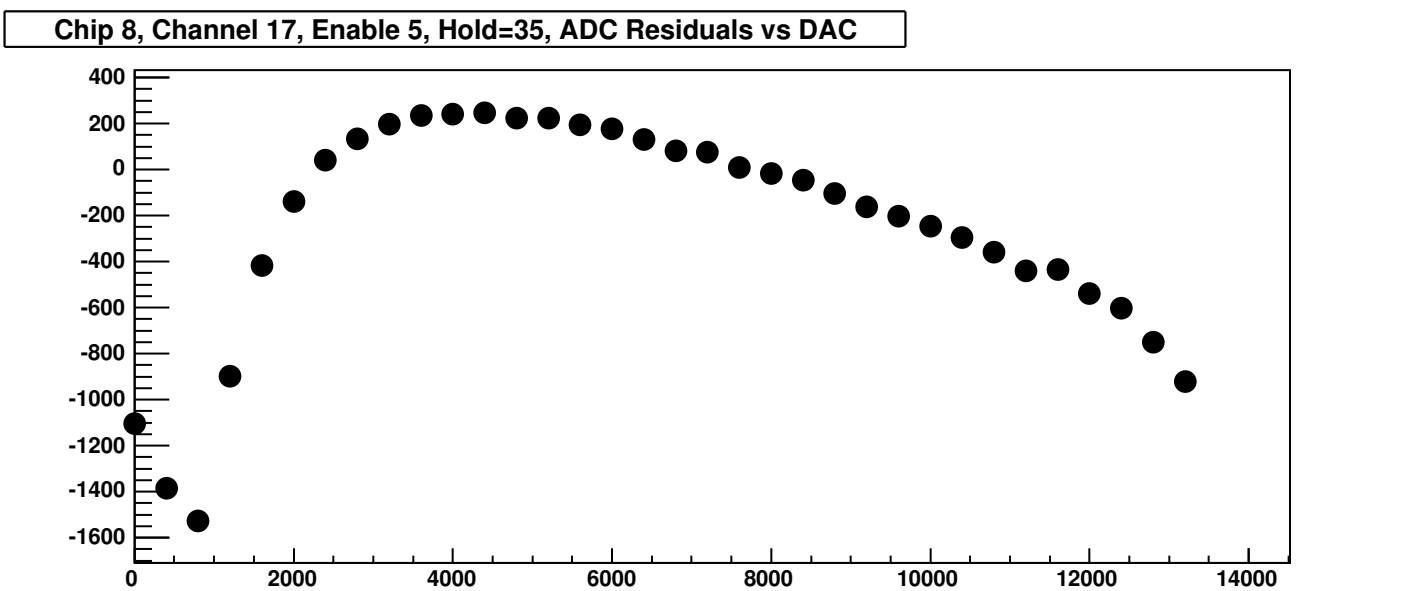

Chip 8, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.77 / 23
p0 120 ± 0.7811
p1 0.01124 ± 0.0001189
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

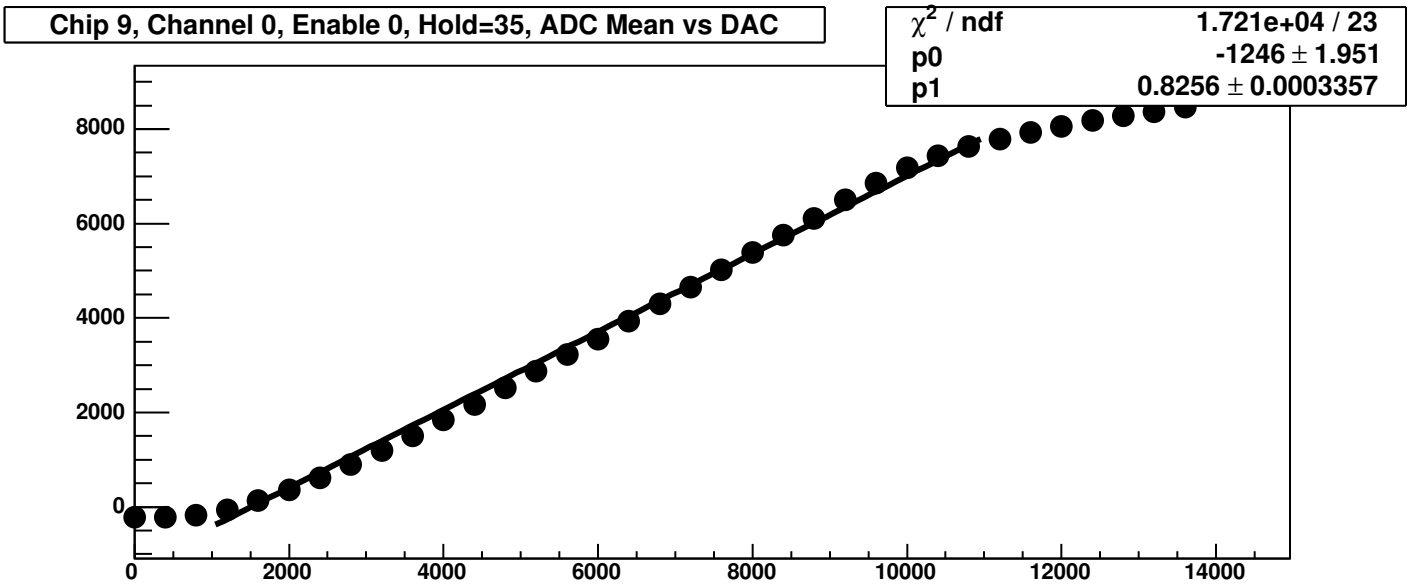

Chip 8, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

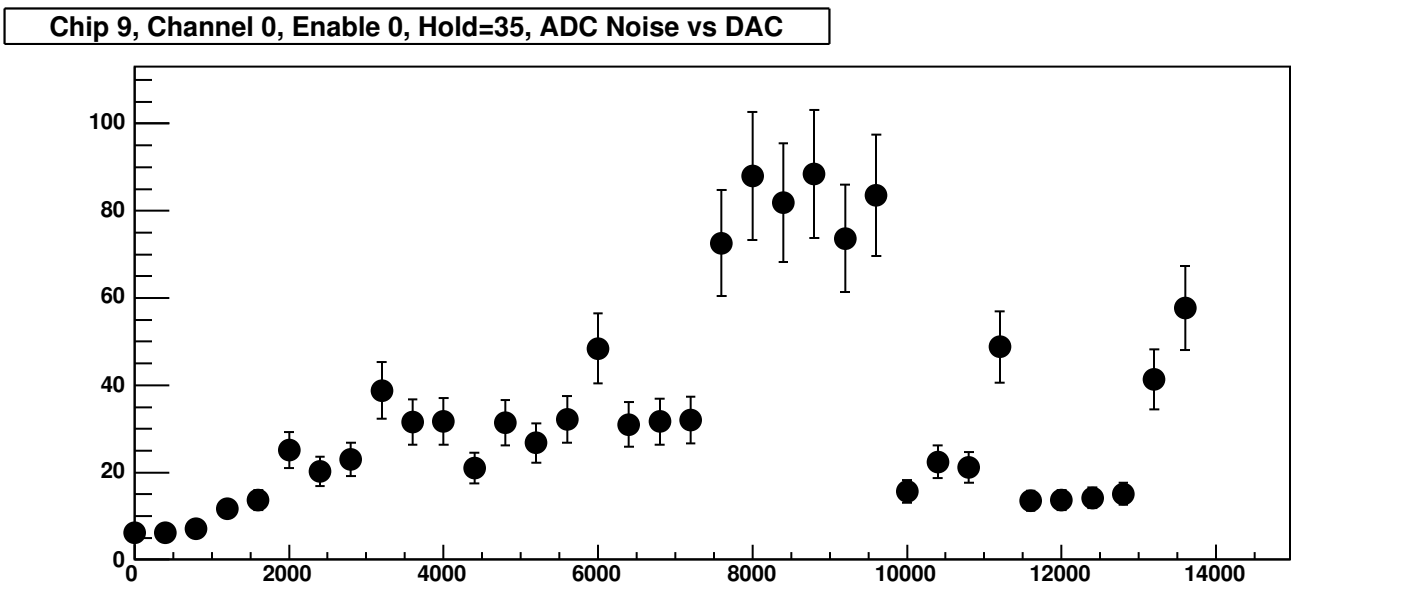

Chip 8, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

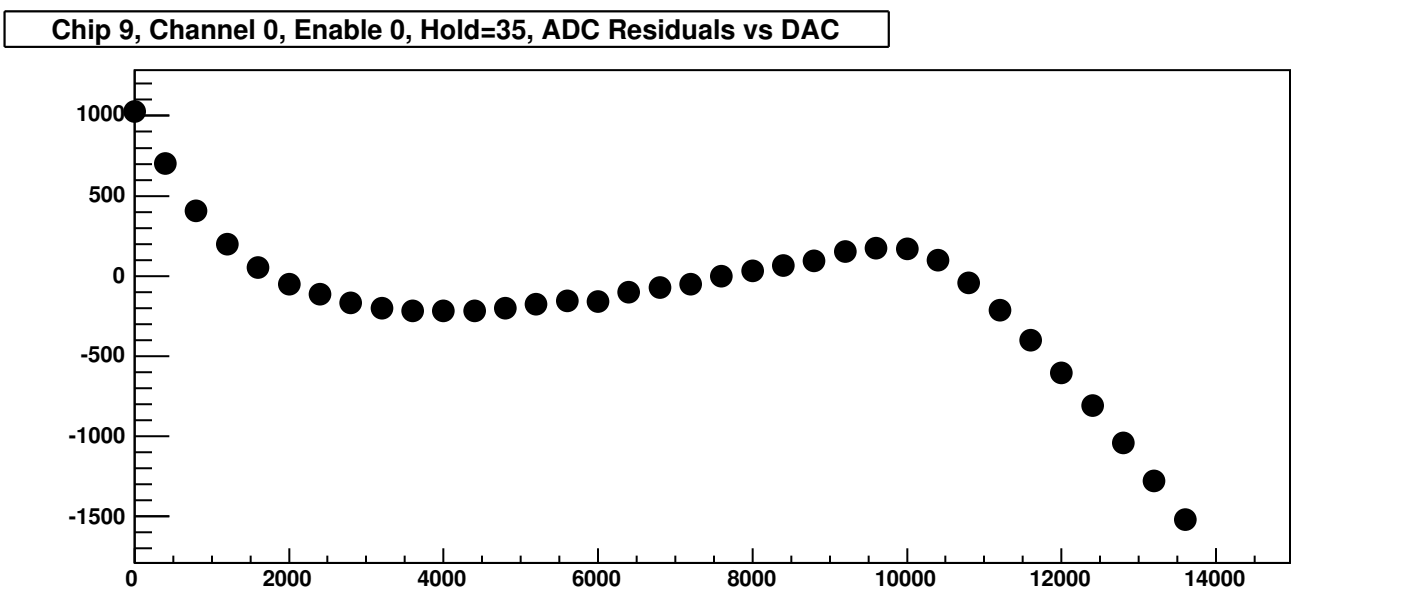

Chip 8, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.673e+04 / 23
p0 1395 ± 4.598
p1 0.733 ± 0.0007473
0
2000
4000
6000
8000
10000
12000
14000

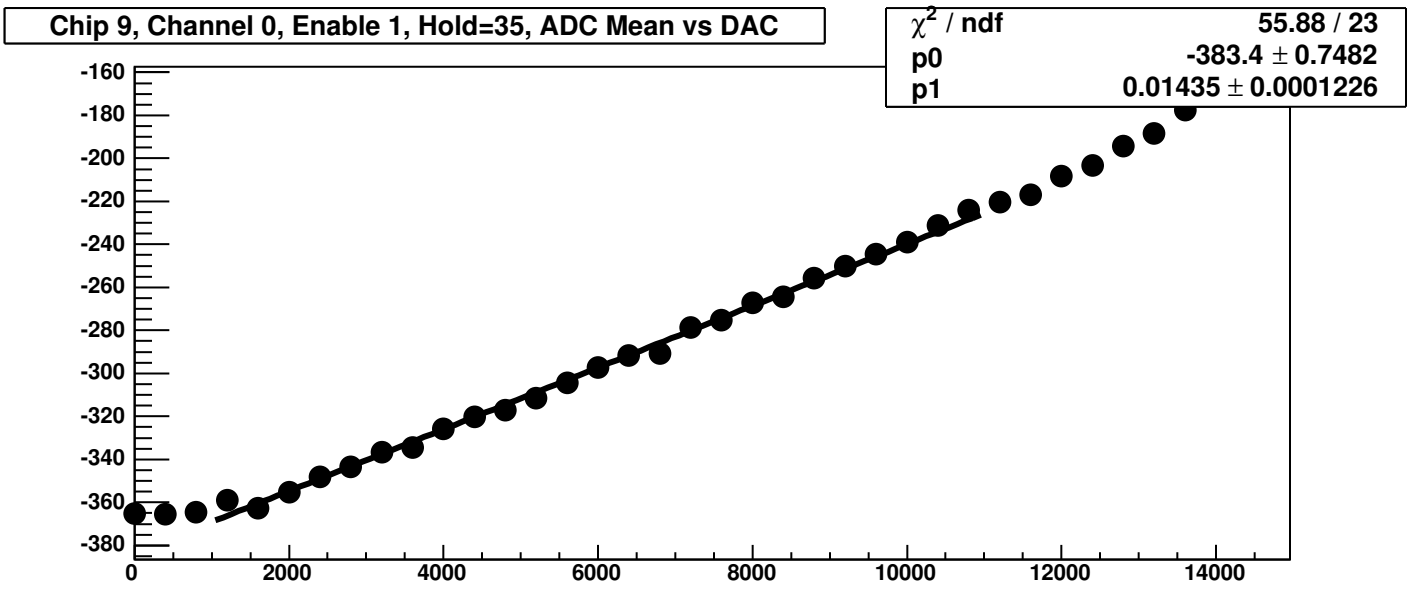

Chip 8, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
0
20
40
60
80
100
120
2000
4000
6000
8000
10000
12000
14000

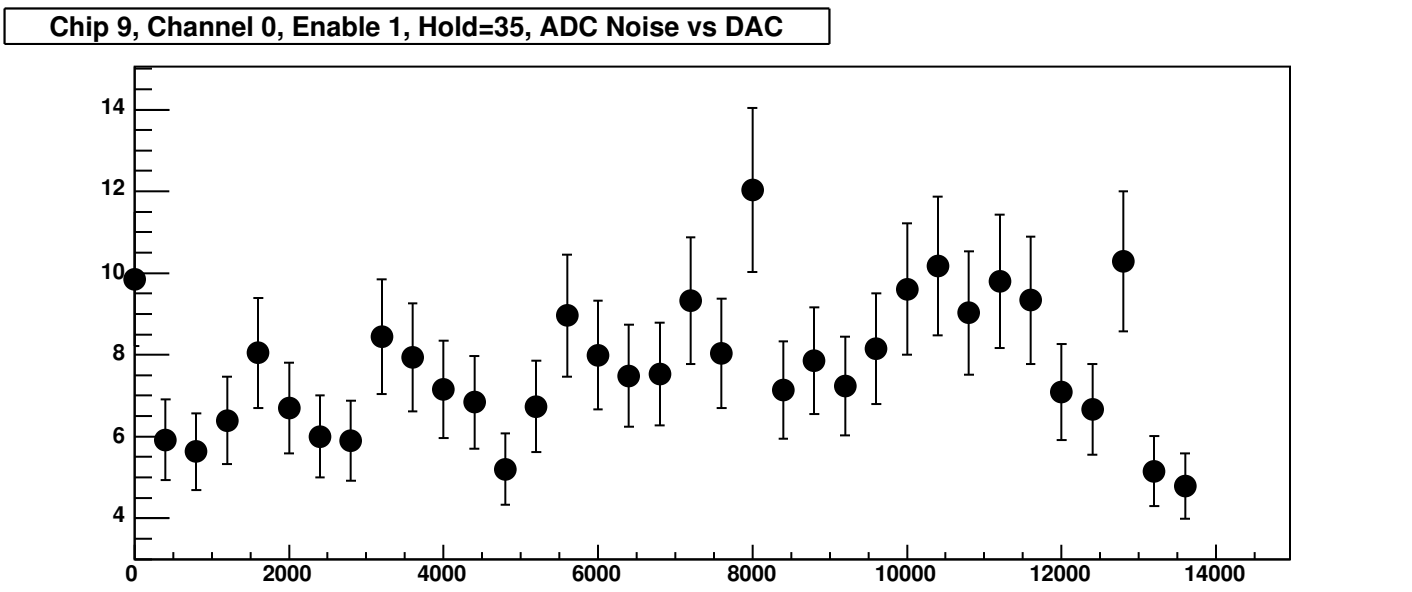

Chip 8, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
2000
4000
6000
8000
10000
12000
14000

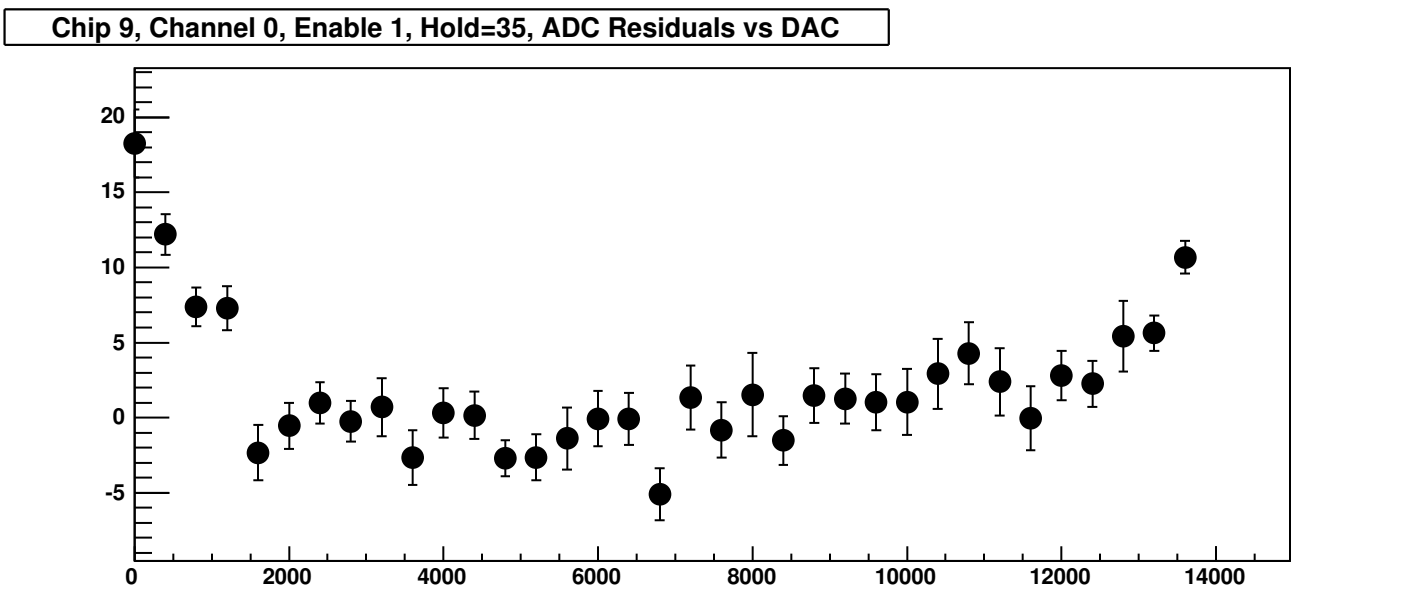

Chip 9, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.721e+04 / 23
p0 -1246 ± 1.951
p1 0.8256 ± 0.0003357
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

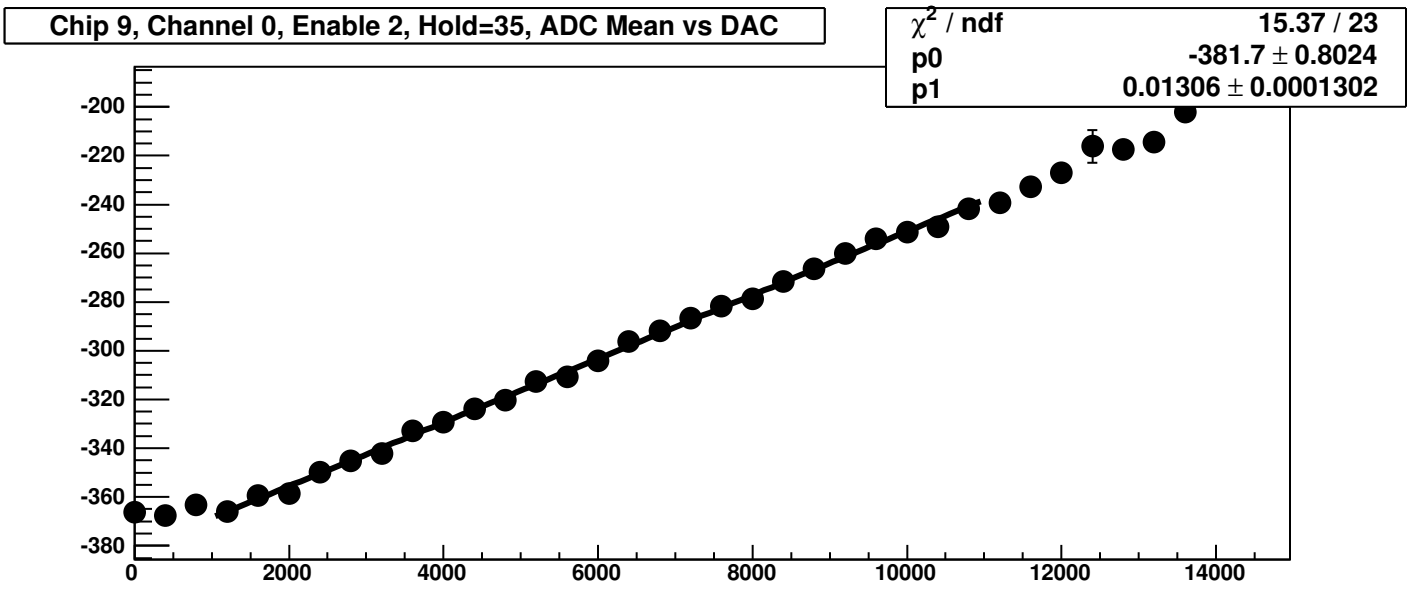

Chip 9, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

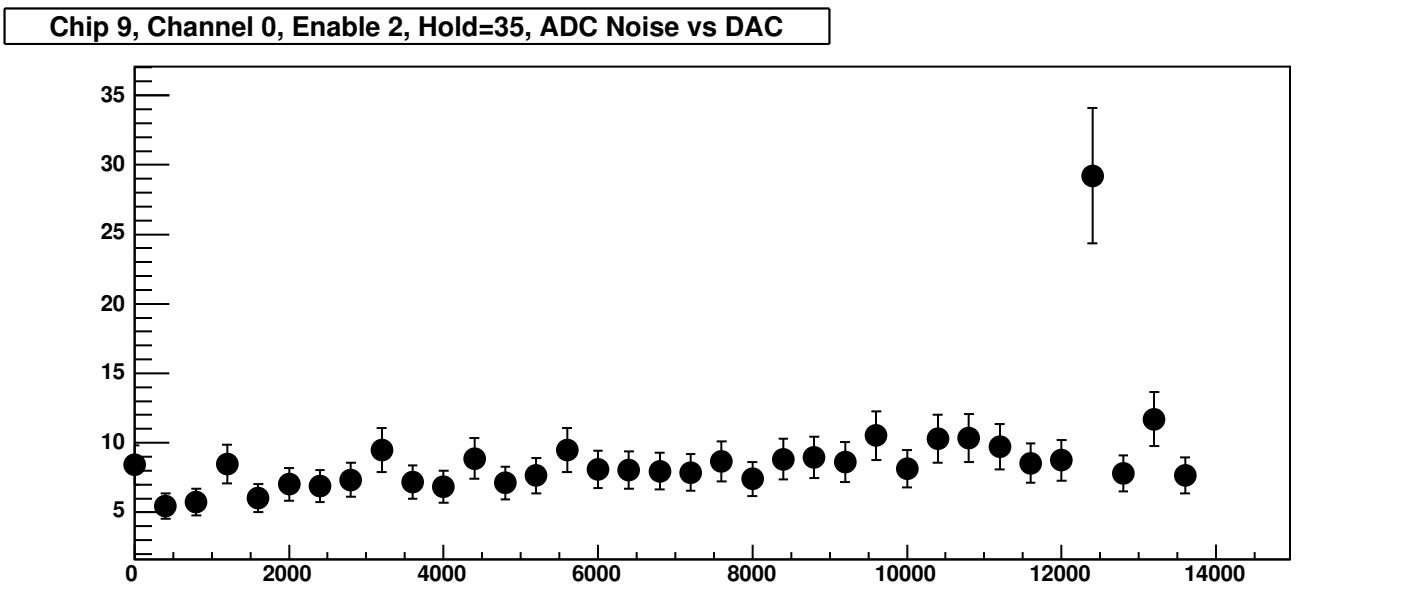

Chip 9, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

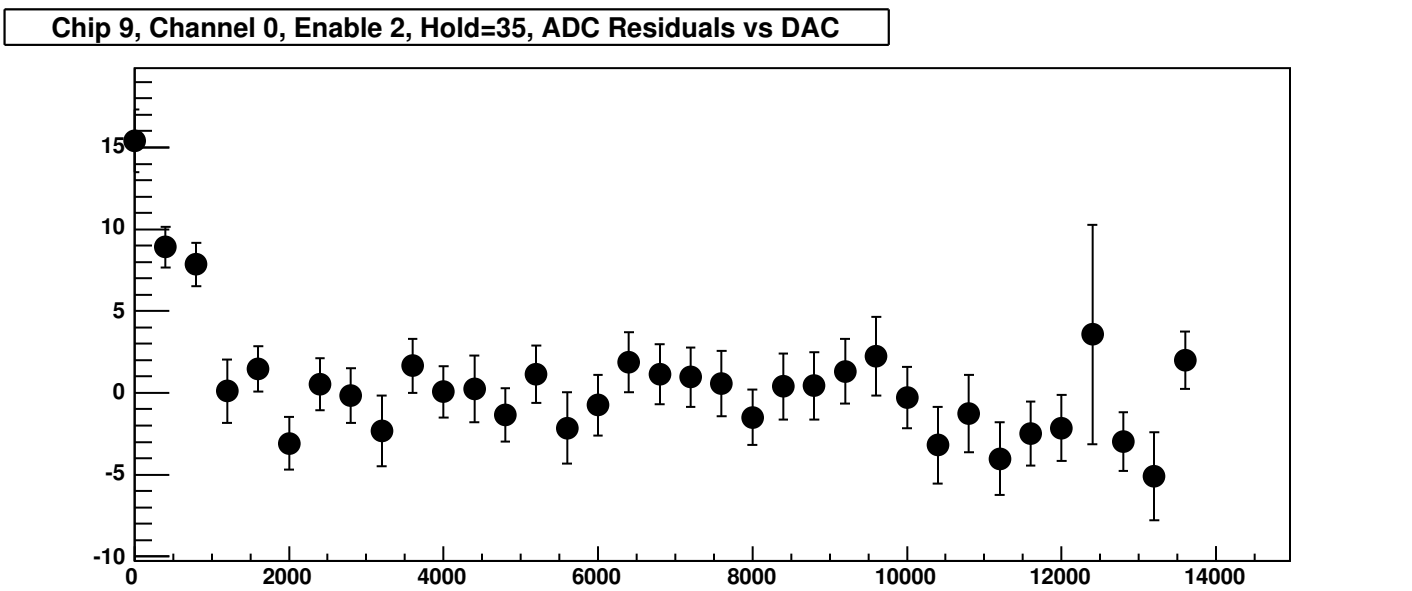

Chip 9, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.88 / 23
p0 -383.4 ± 0.7482
p1 0.01435 ± 0.0001226
-160
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

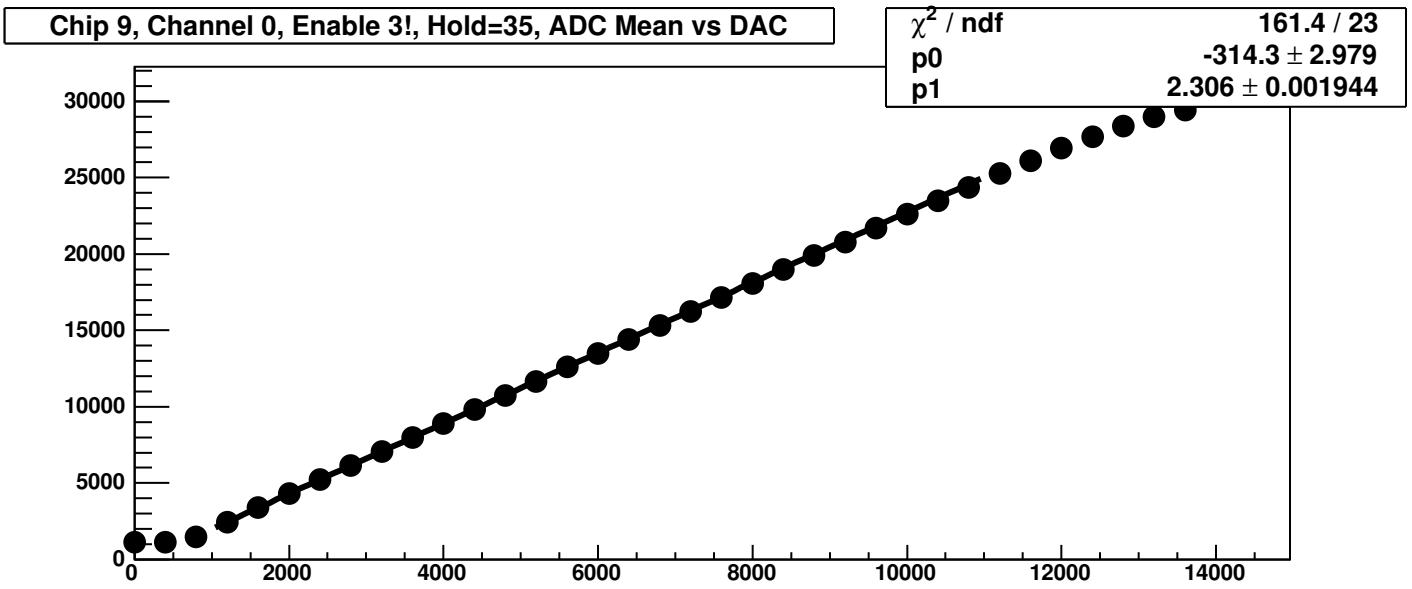

Chip 9, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

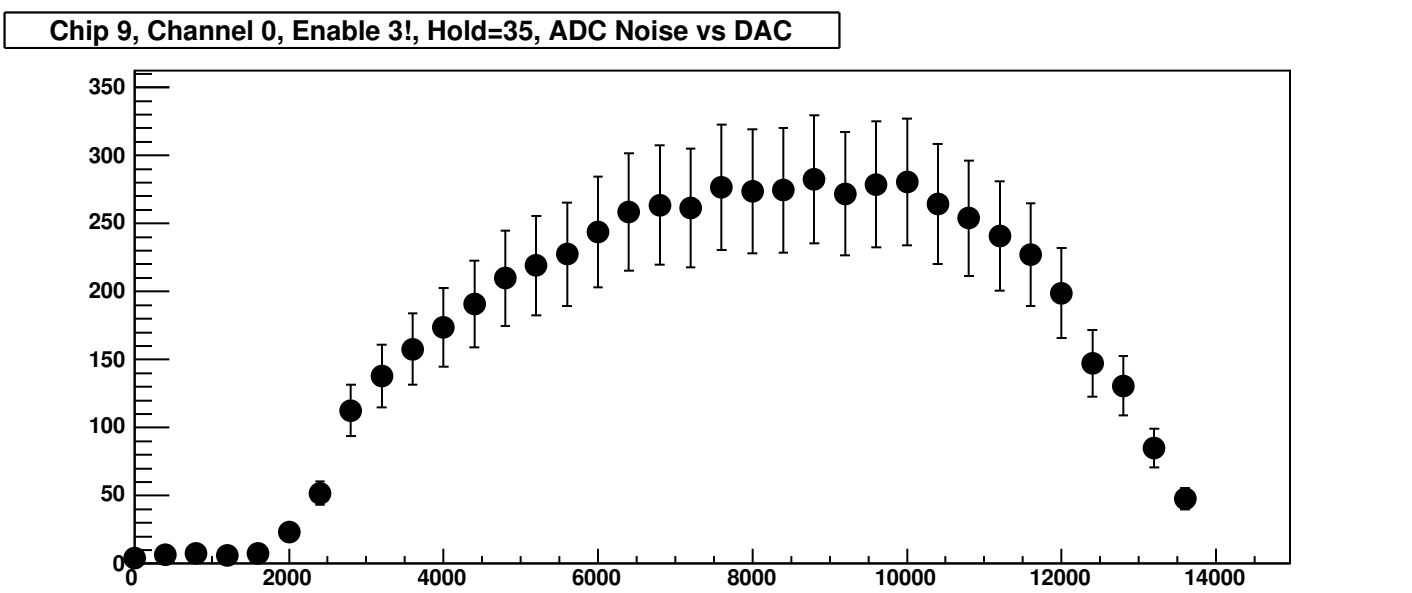

Chip 9, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

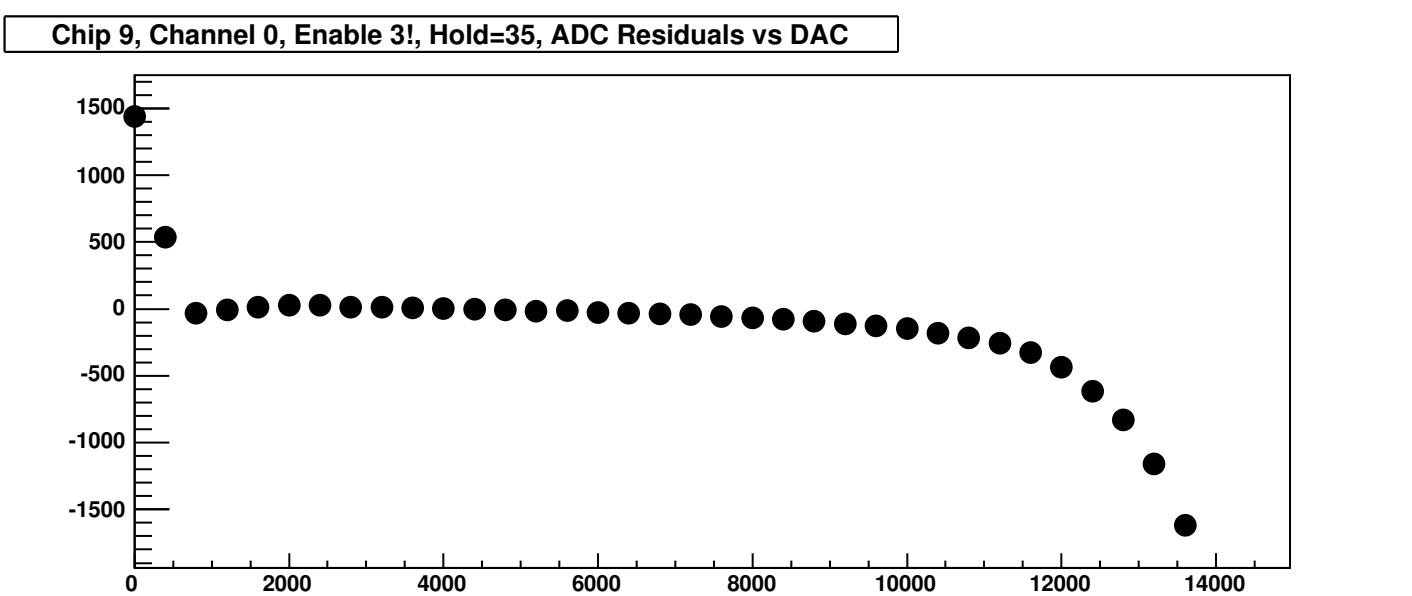

Chip 9, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.37 / 23
p0 -381.7 ± 0.8024
p1 0.01306 ± 0.0001302
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

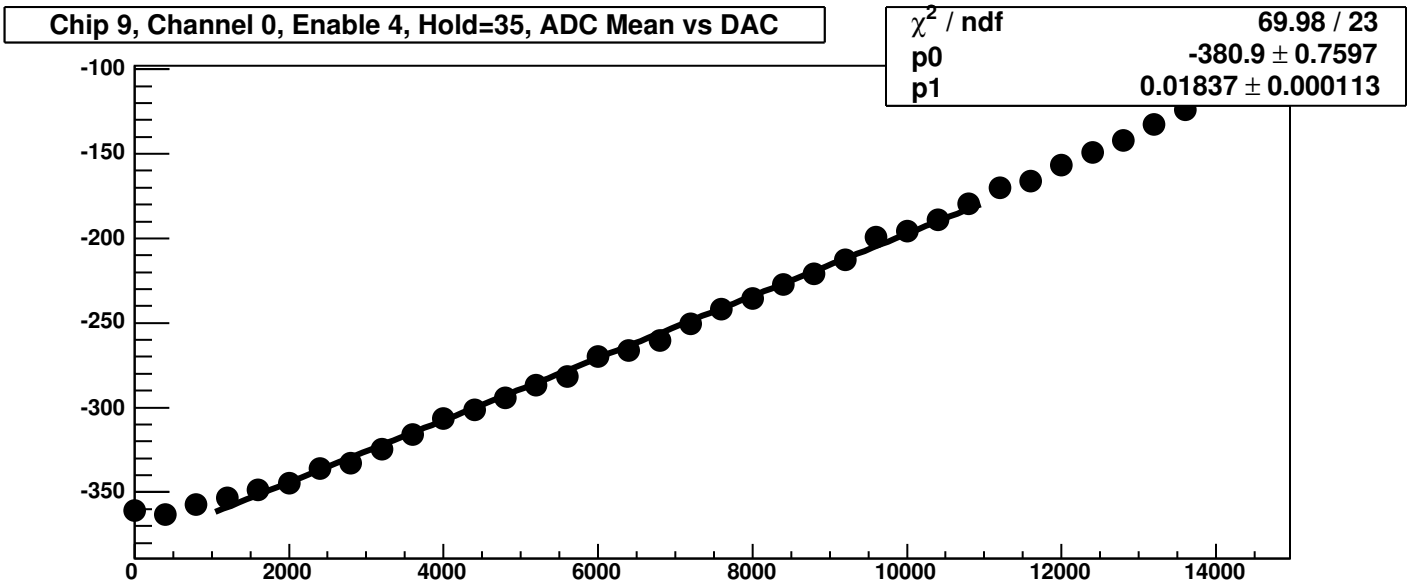

Chip 9, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

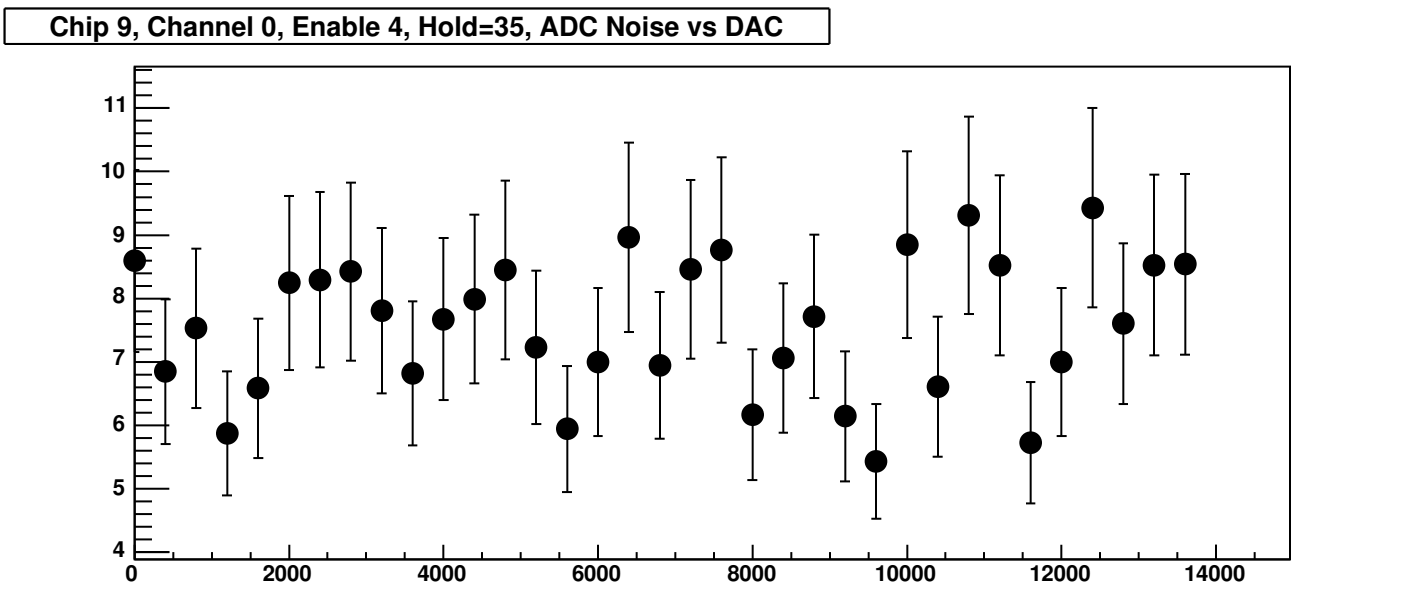

Chip 9, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

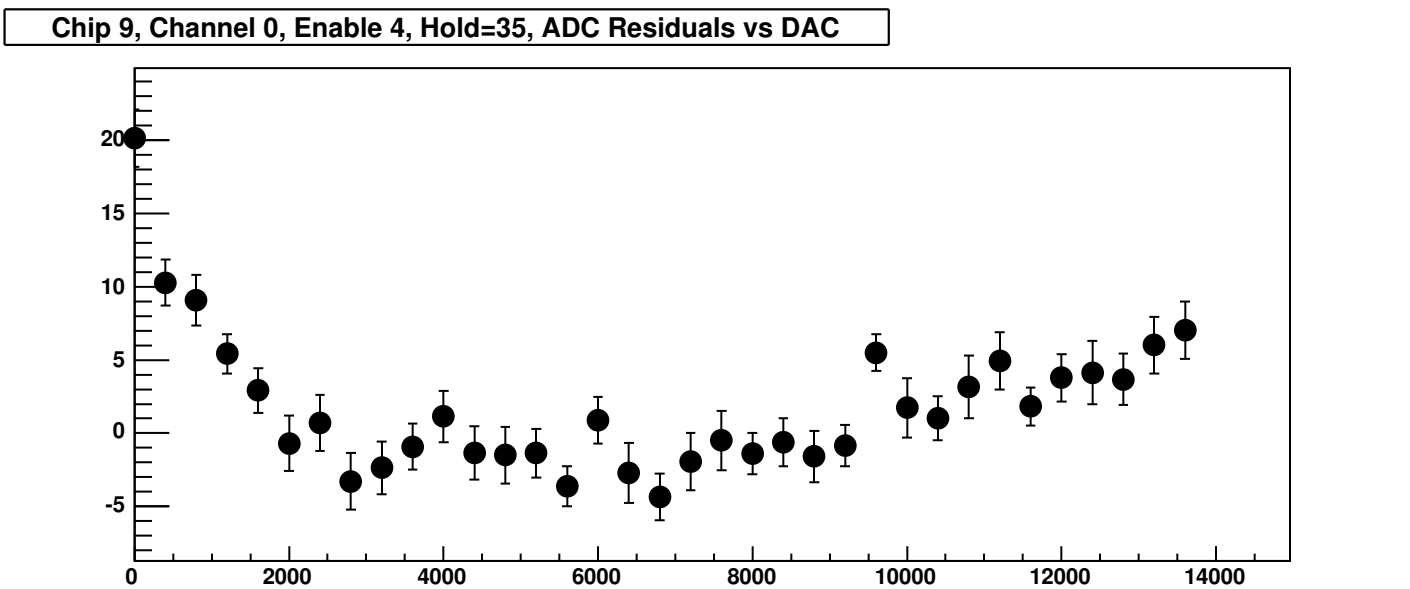

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 161.4 / 23
p0 -314.3 ± 2.979
p1 2.306 ± 0.001944
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

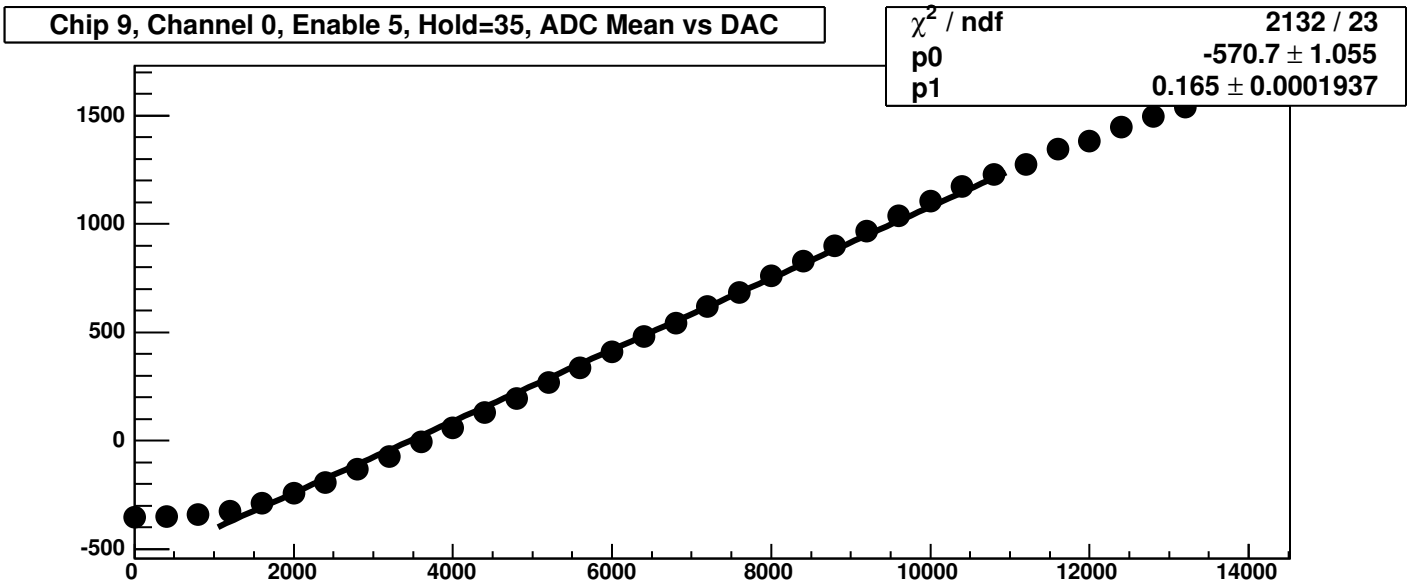

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

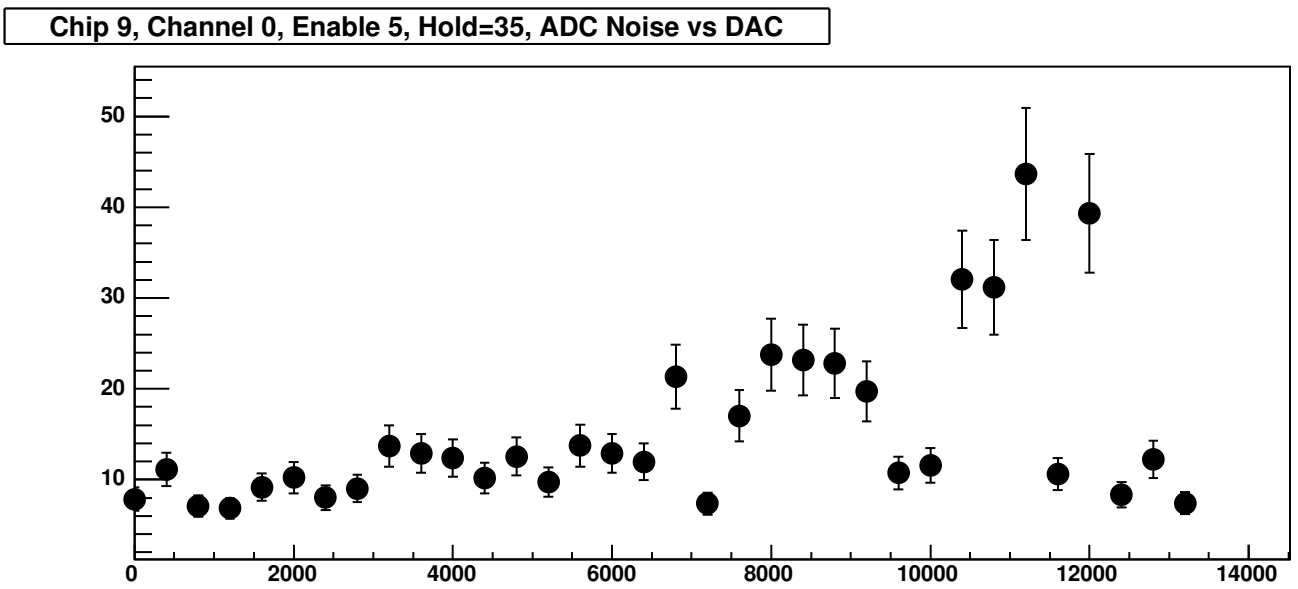

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

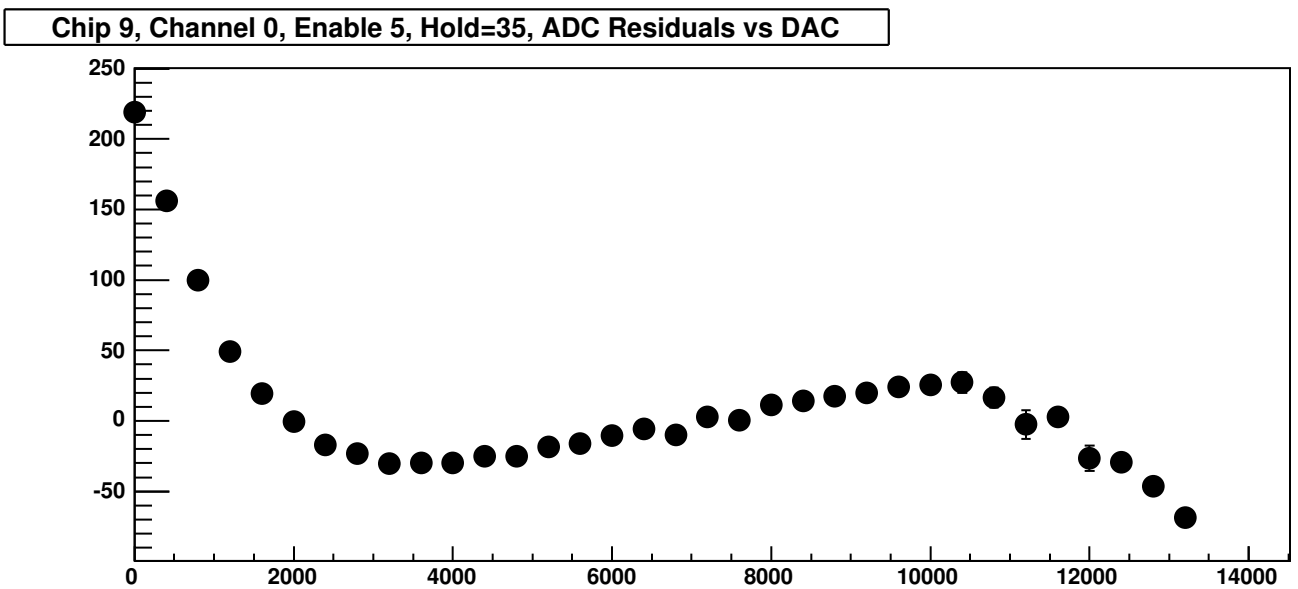

Chip 9, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 69.98 / 23
p0 -380.9 ± 0.7597
p1 0.01837 ± 0.000113
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

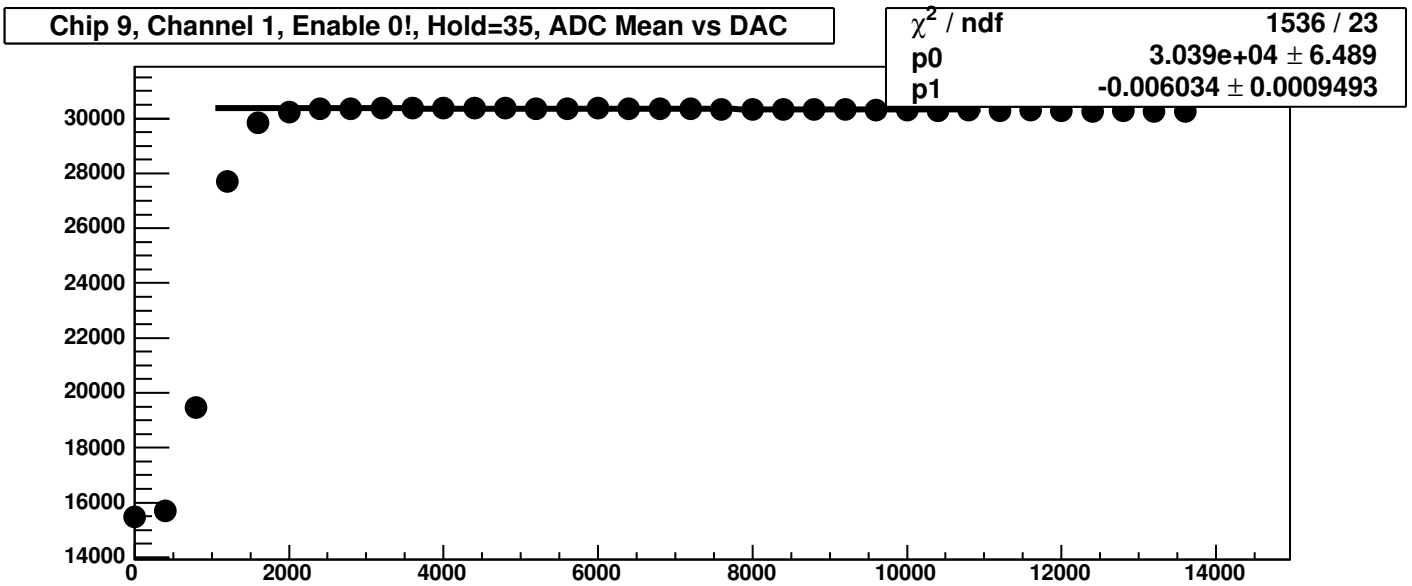

Chip 9, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

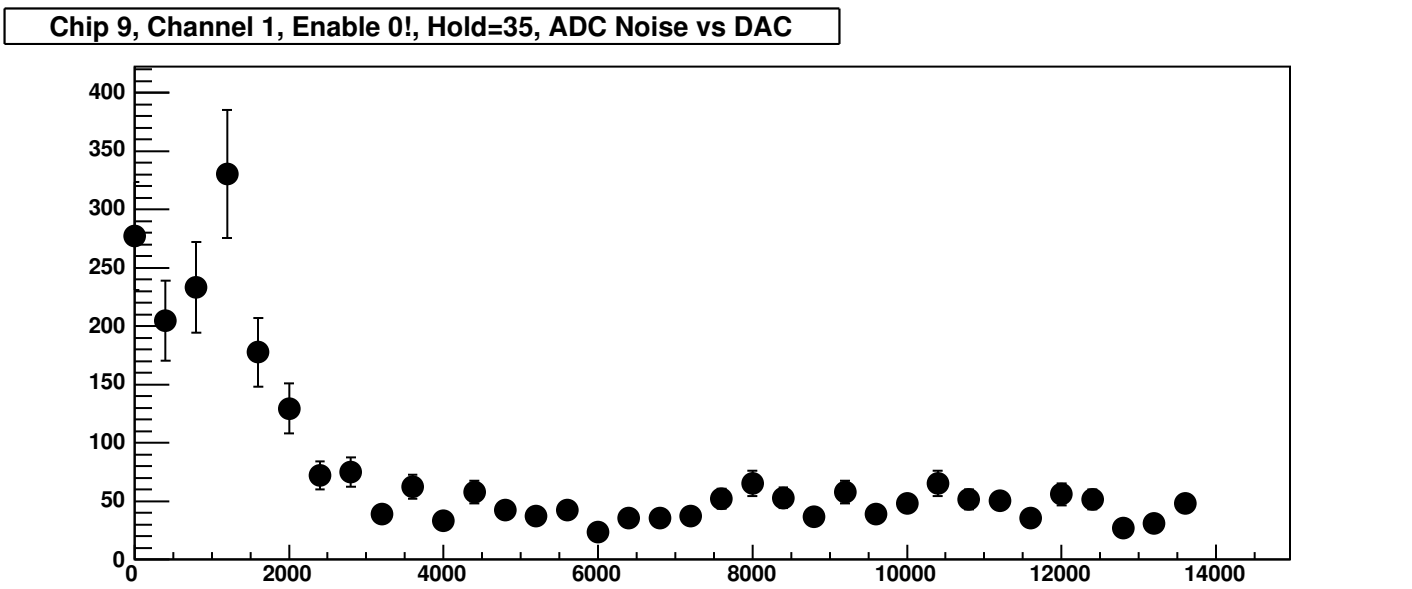

Chip 9, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

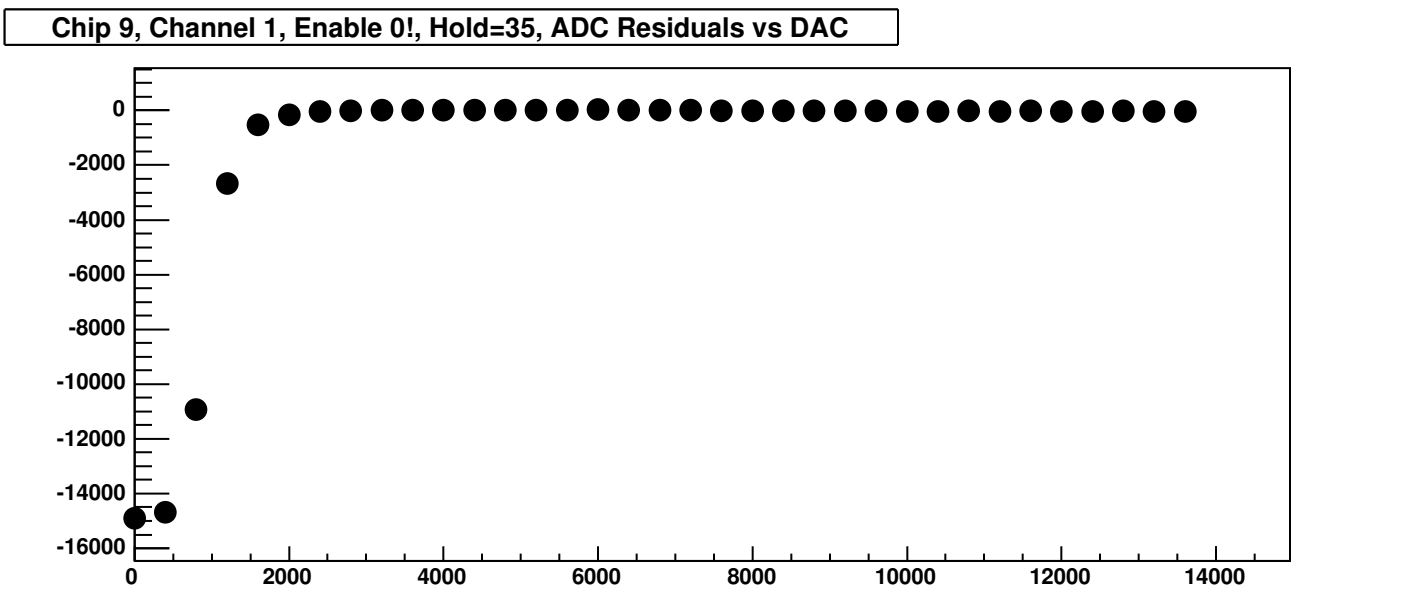

Chip 9, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2132 / 23
p0 -570.7 ± 1.055
p1 0.165 ± 0.0001937
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

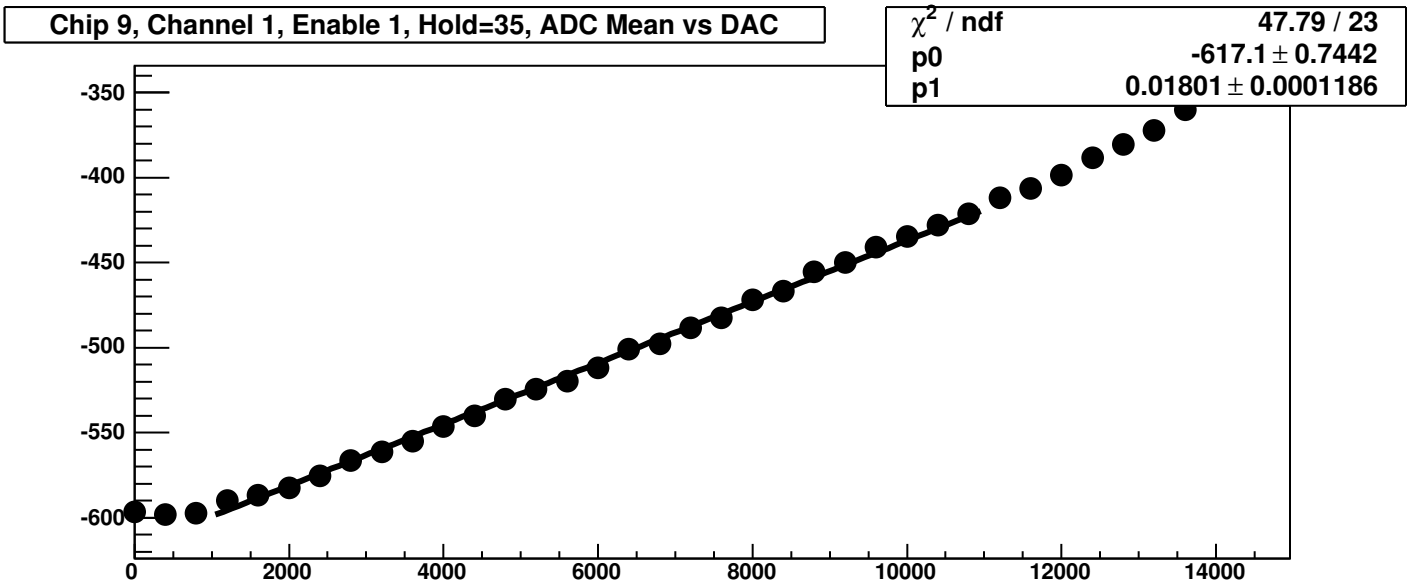

Chip 9, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

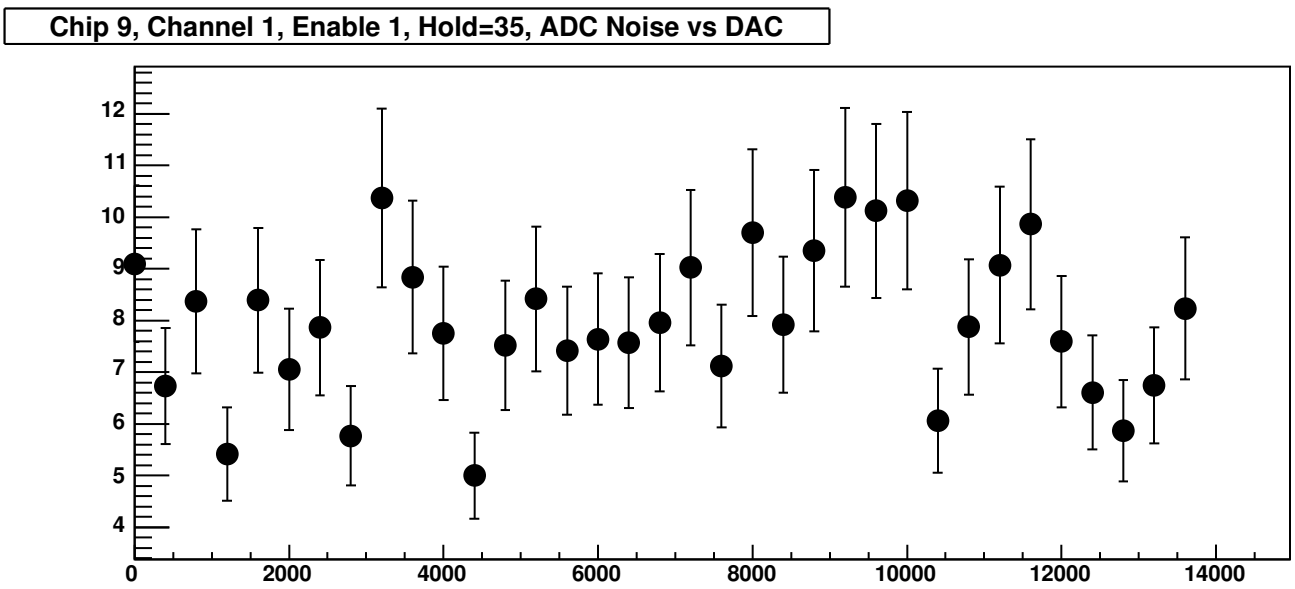

Chip 9, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

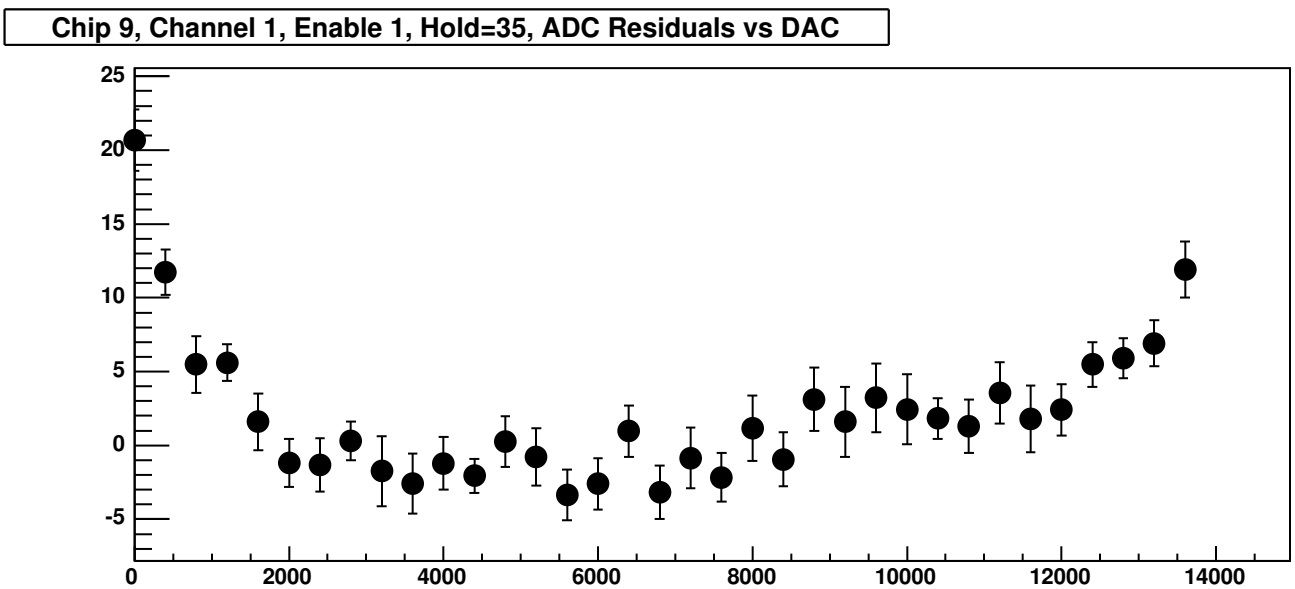

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1536 / 23
p0 3.039e+04 ± 6.489
p1 -0.006034 ± 0.0009493
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

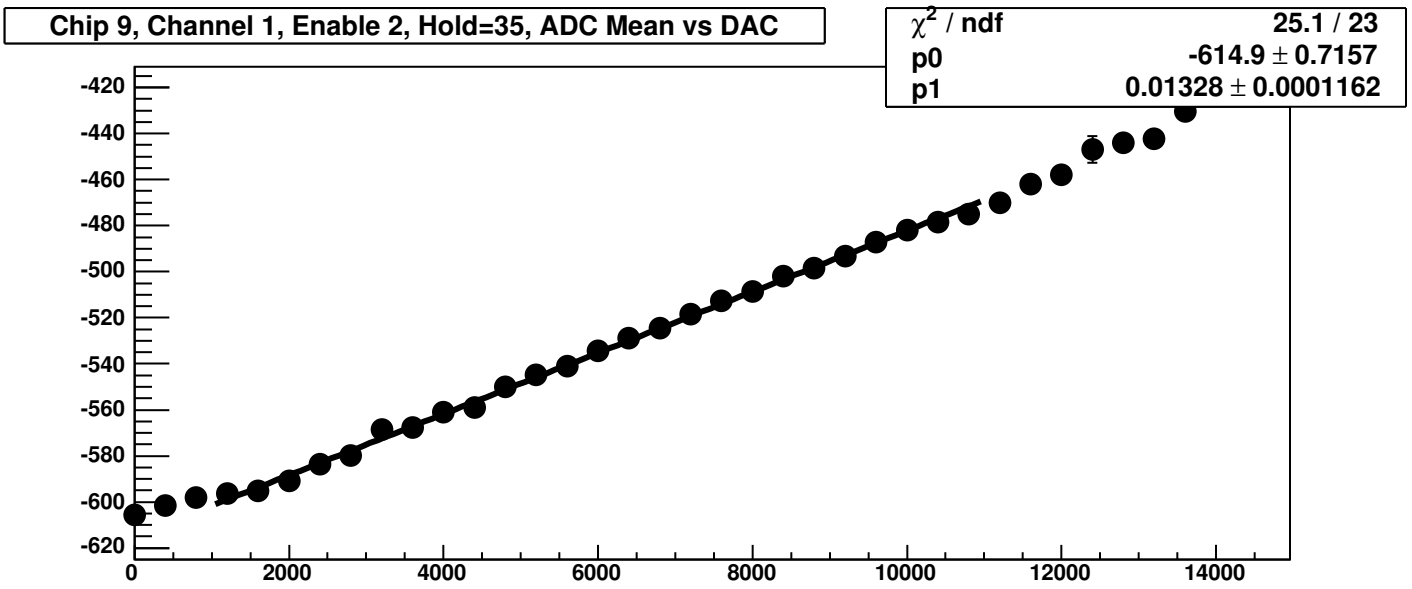

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

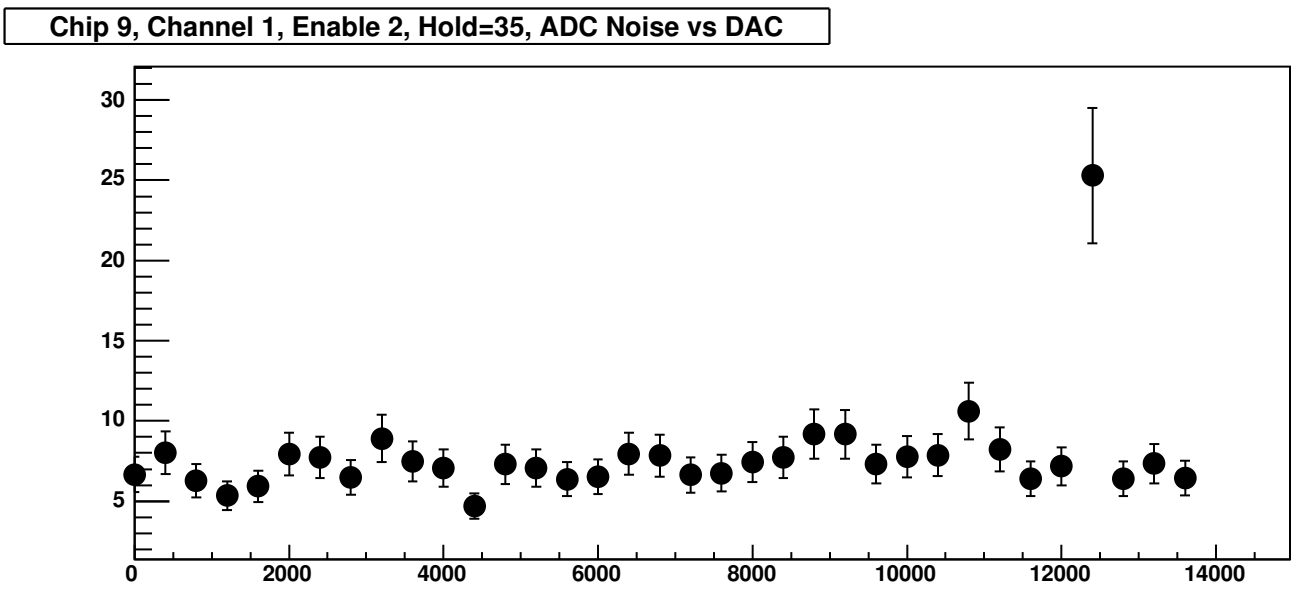

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

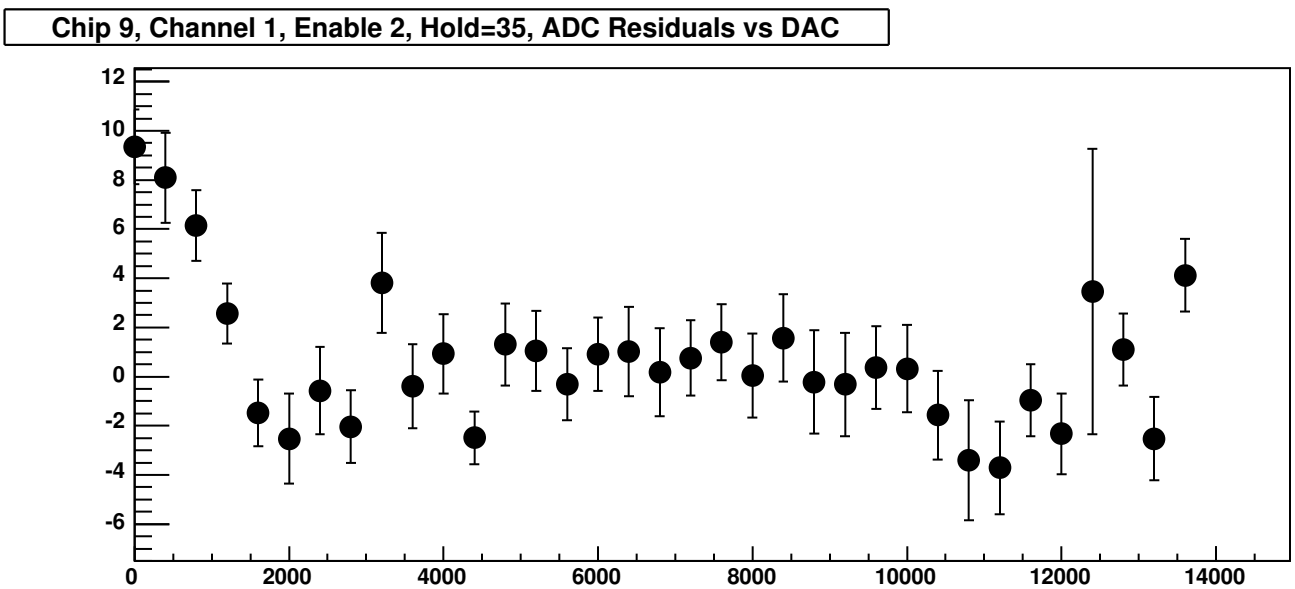

Chip 9, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.79 / 23
p0 -617.1 ± 0.7442
p1 0.01801 ± 0.0001186
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

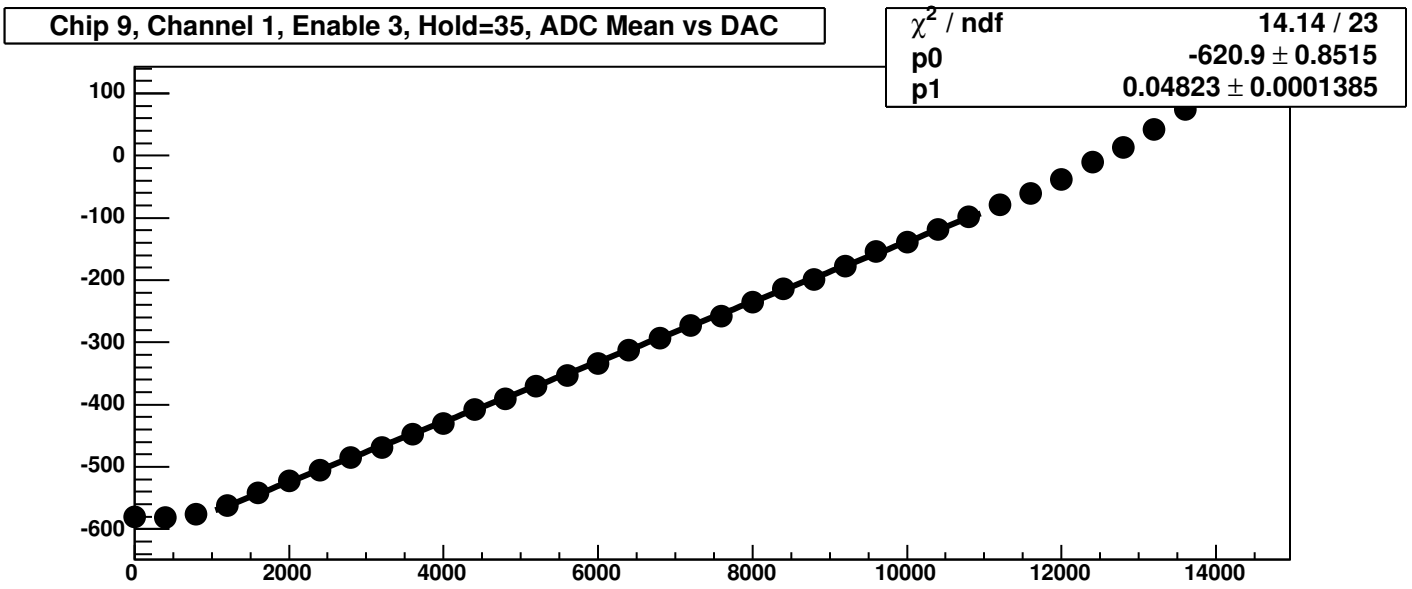

Chip 9, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

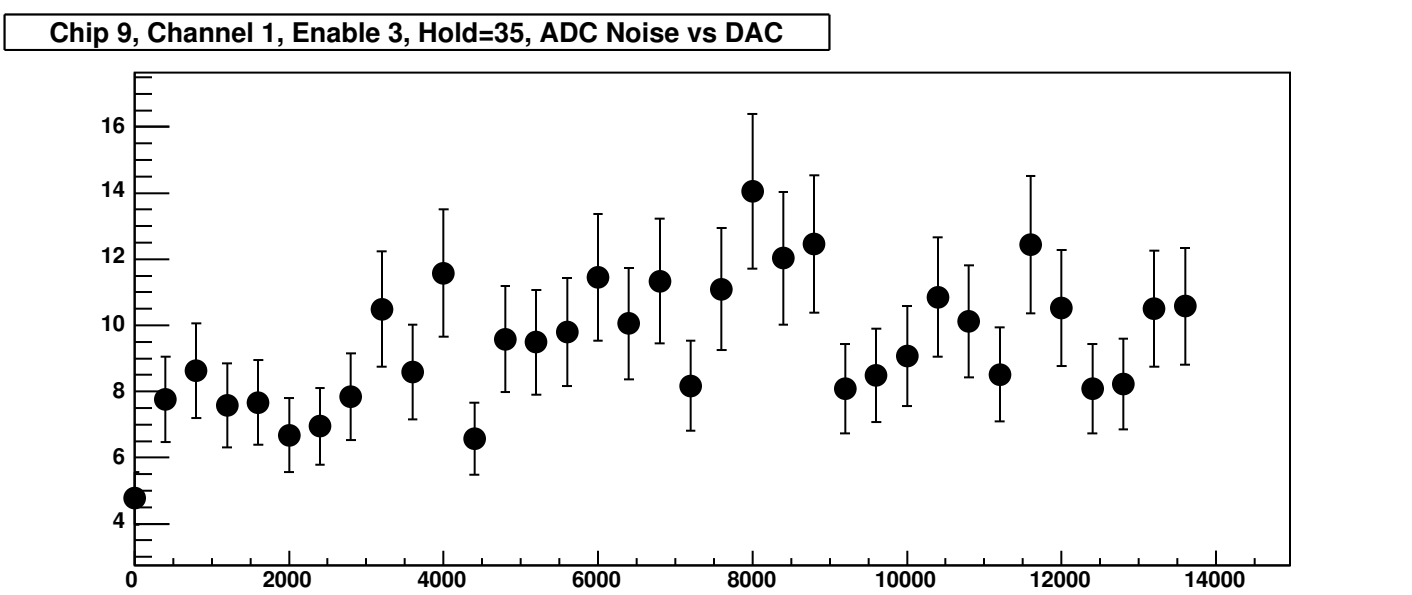

Chip 9, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

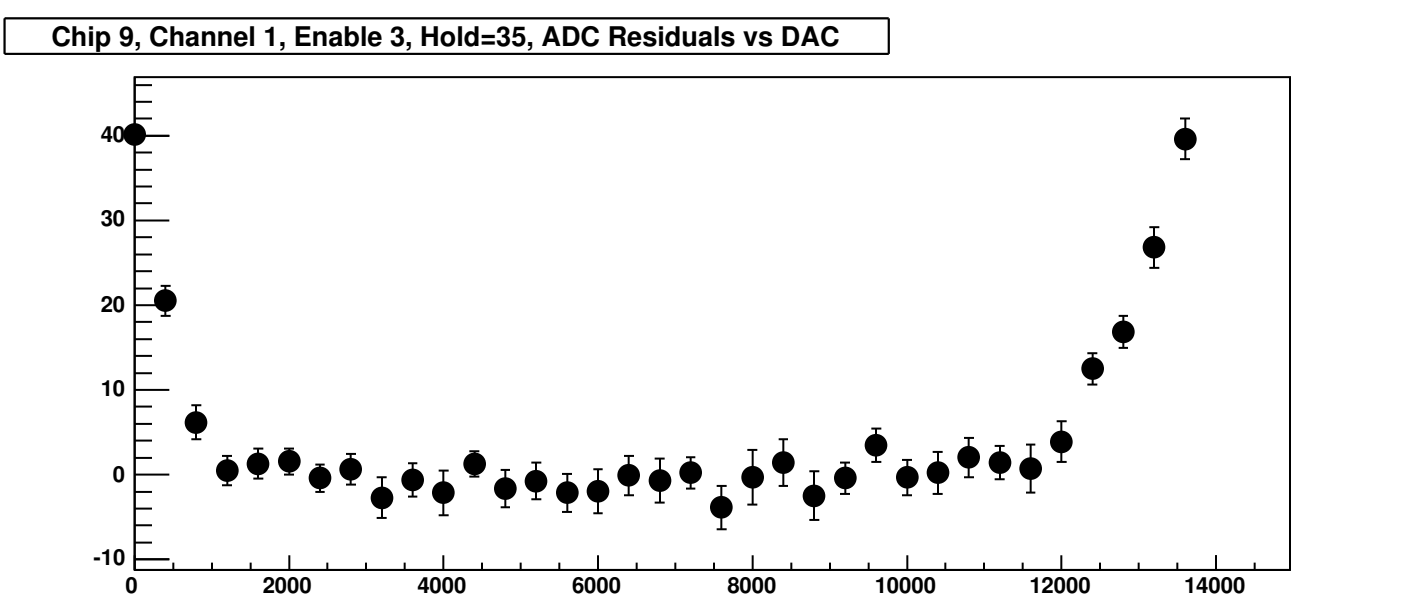

Chip 9, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.1 / 23
p0 -614.9 ± 0.7157
p1 0.01328 ± 0.0001162
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
2000
4000
6000
8000
10000
12000
14000

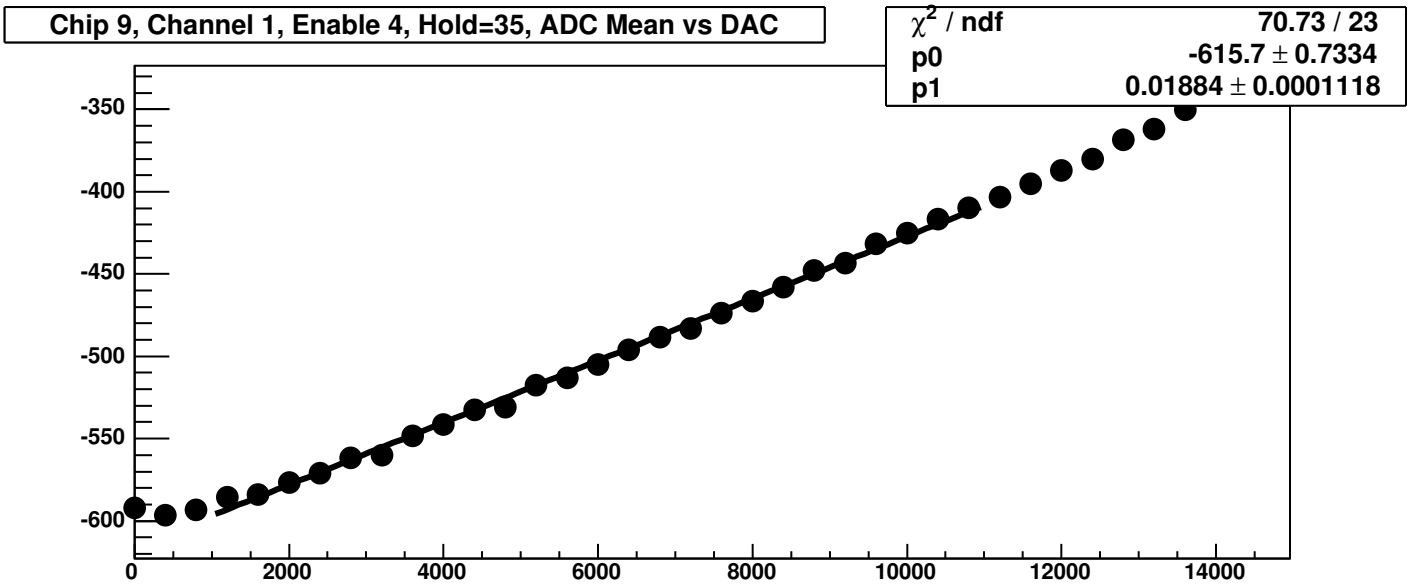

Chip 9, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

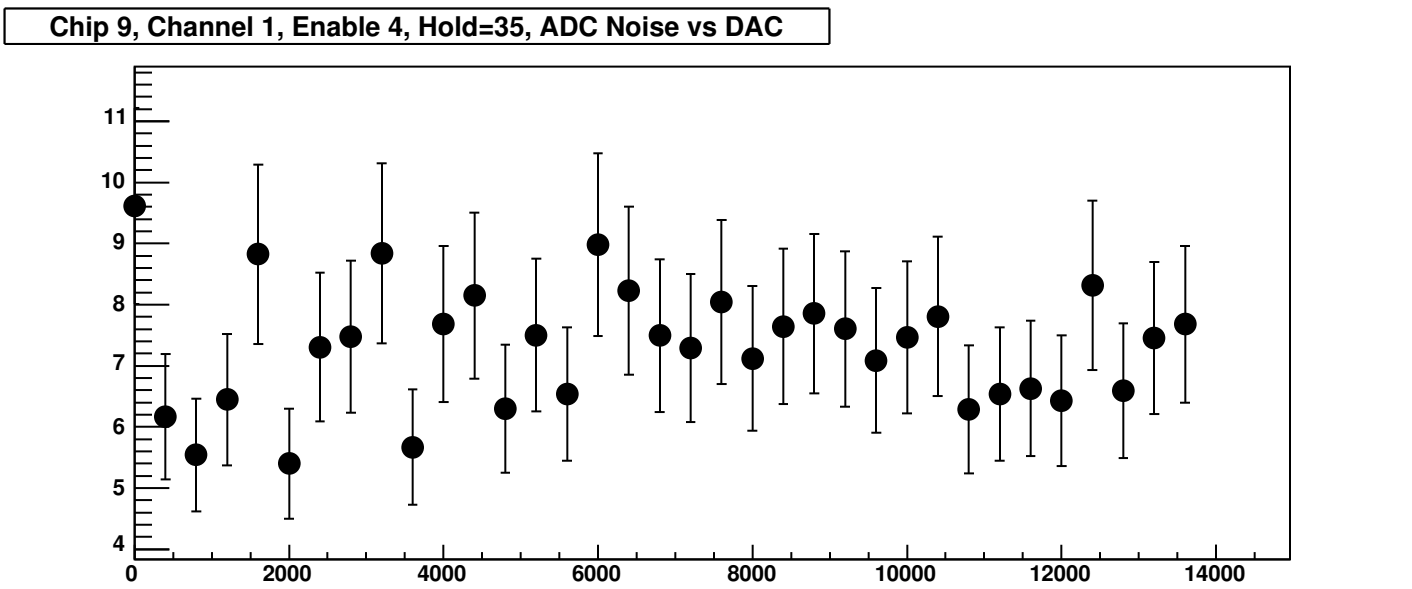

Chip 9, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

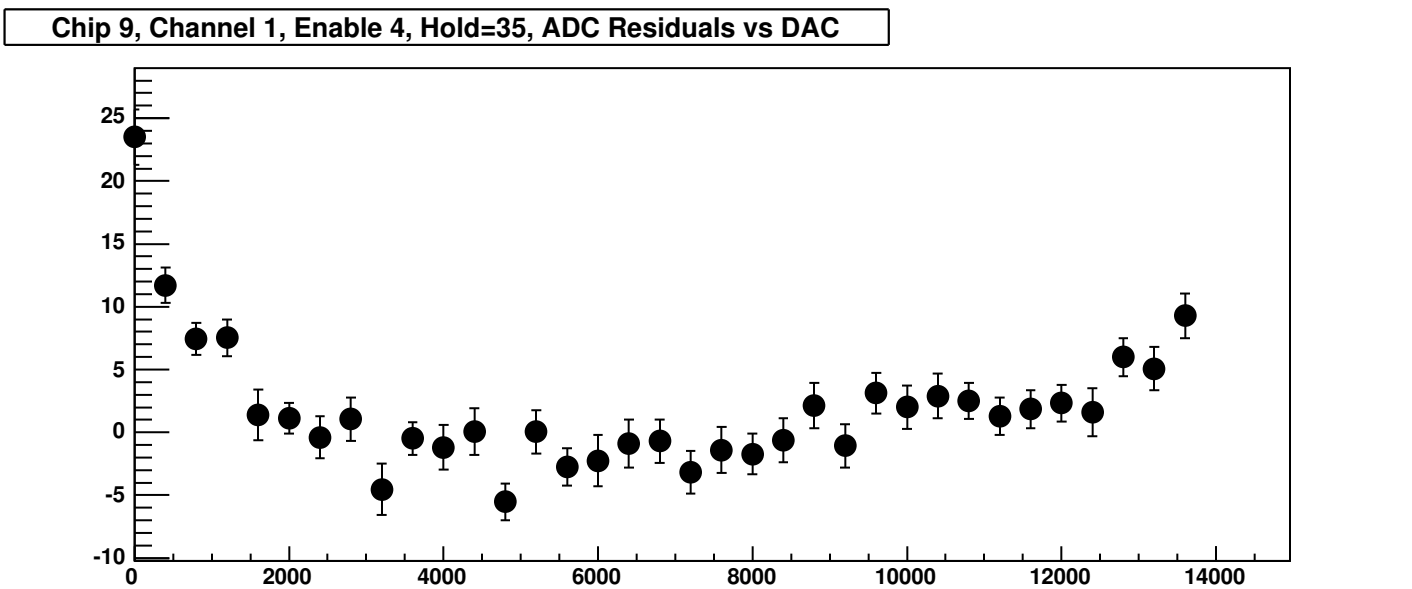

Chip 9, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.14 / 23
p0 -620.9 ± 0.8515
p1 0.04823 ± 0.0001385
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

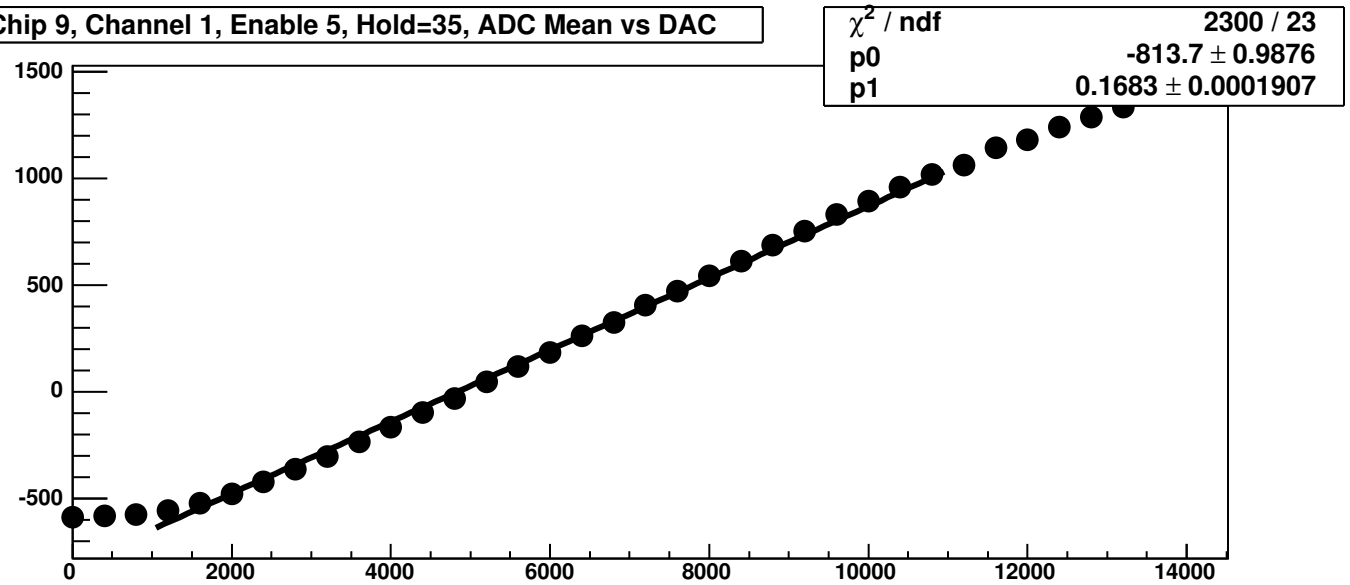

Chip 9, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 70.73 / 23
p0 -615.7 ± 0.7334
p1 0.01884 ± 0.0001118
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

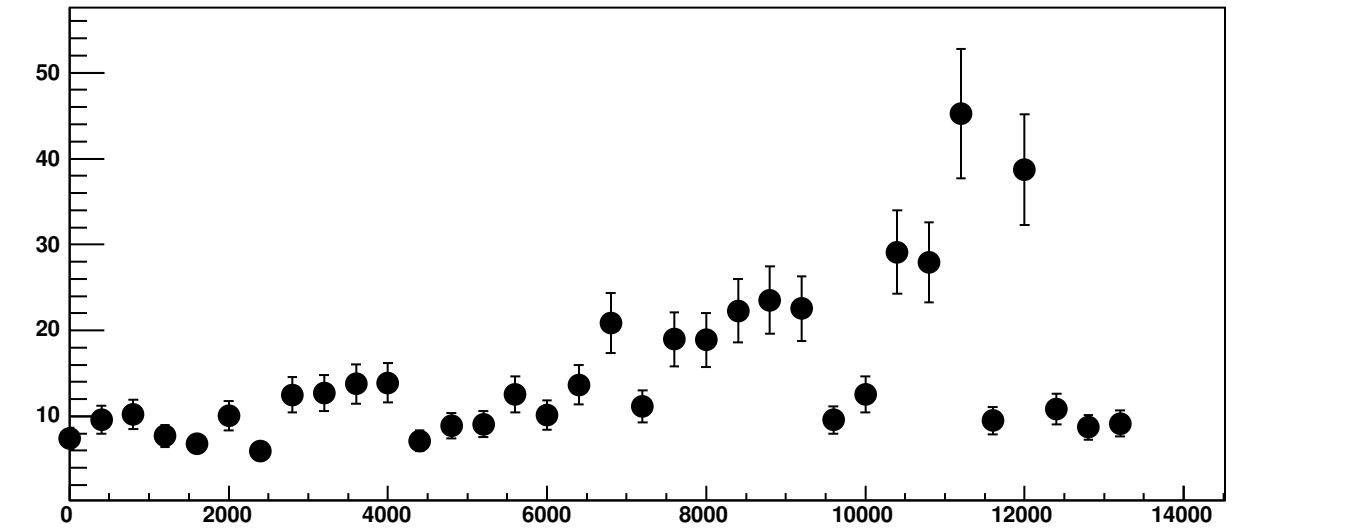

Chip 9, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

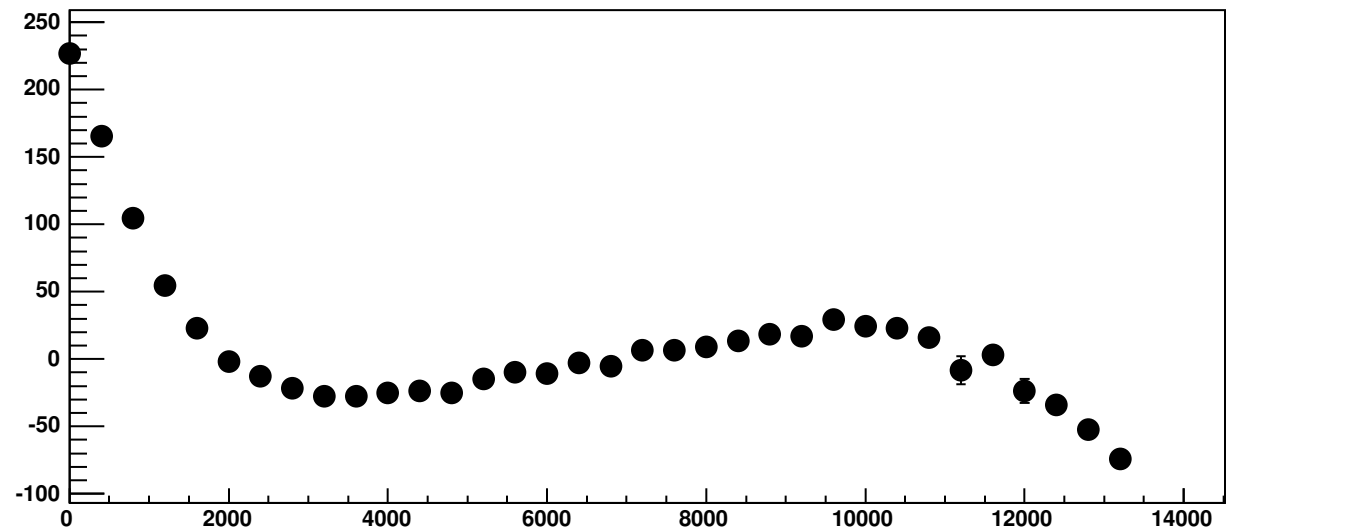
Chip 9, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

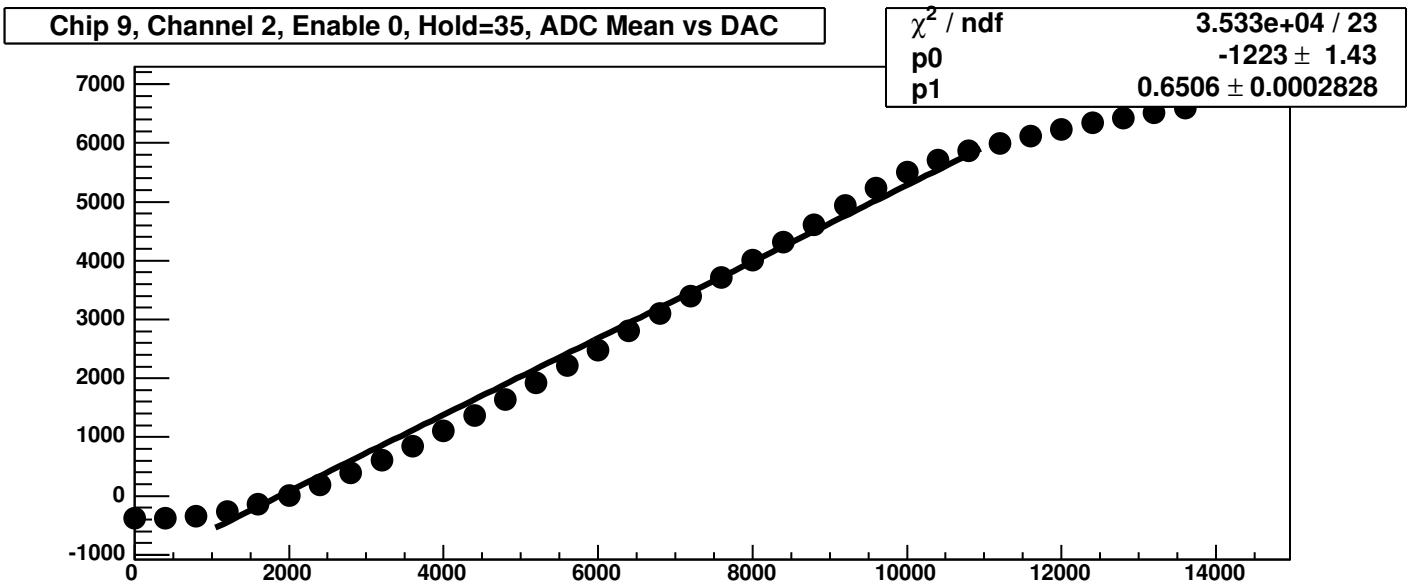


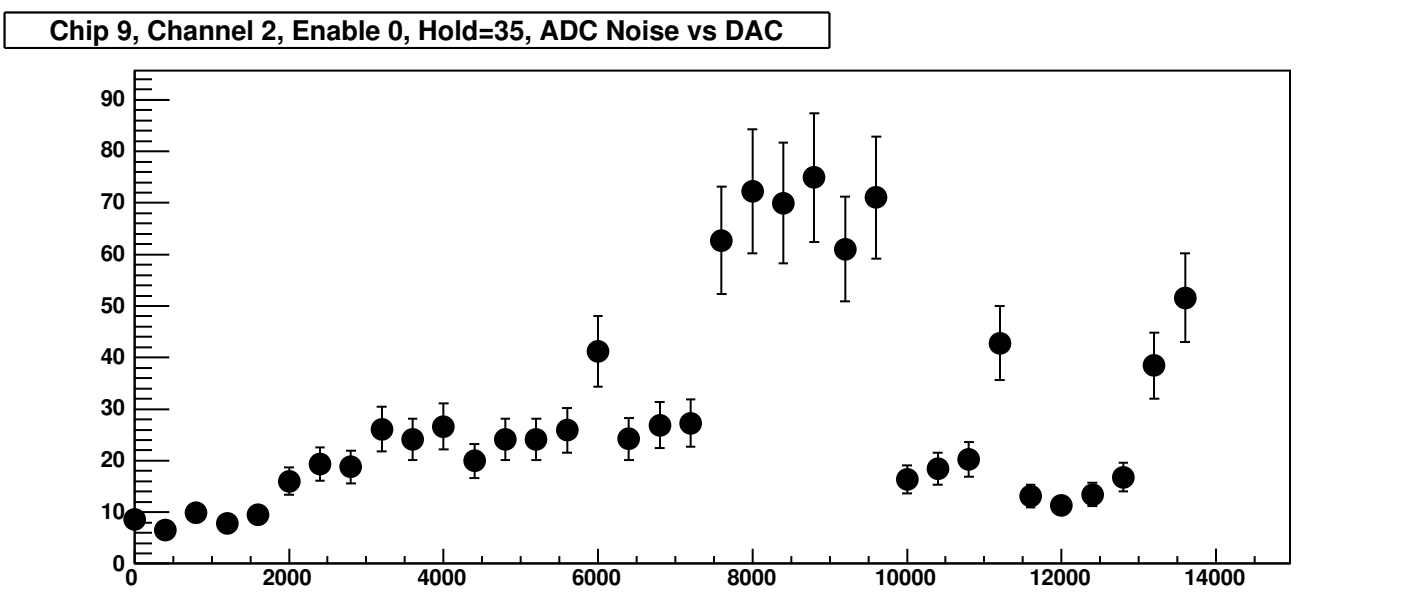
Chip 9, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

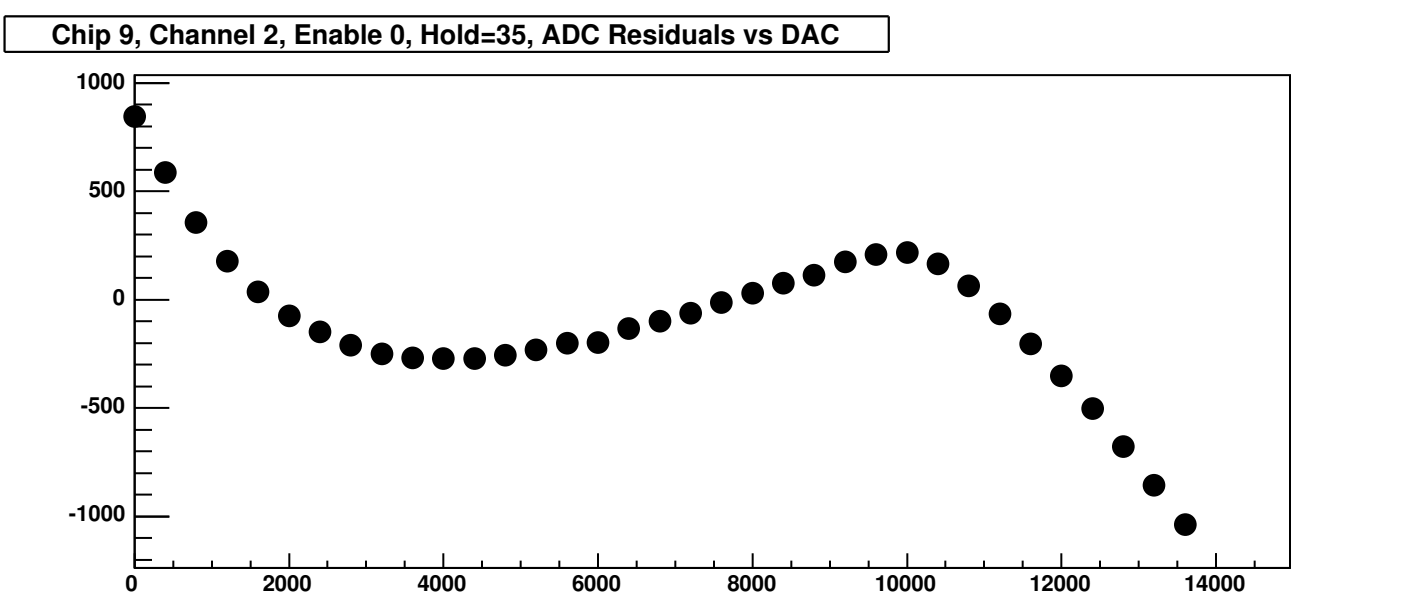


Chip 9, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

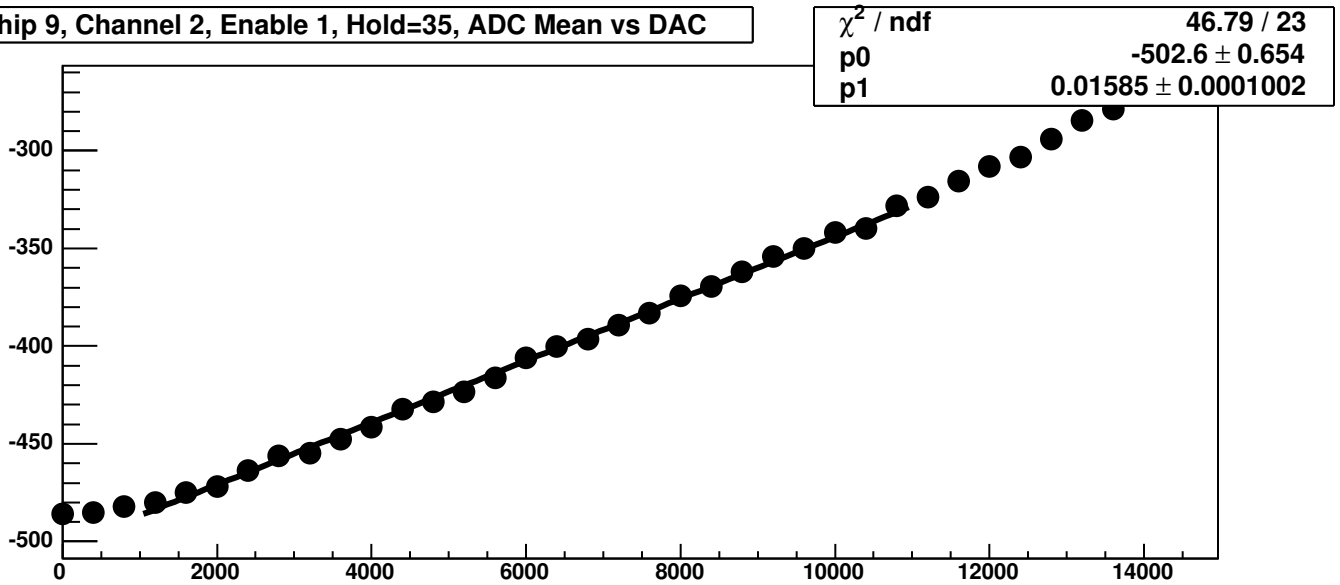



Chip 9, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.533e+04 / 23
p0 -1223 ± 1.43
p1 0.6506 ± 0.0002828
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

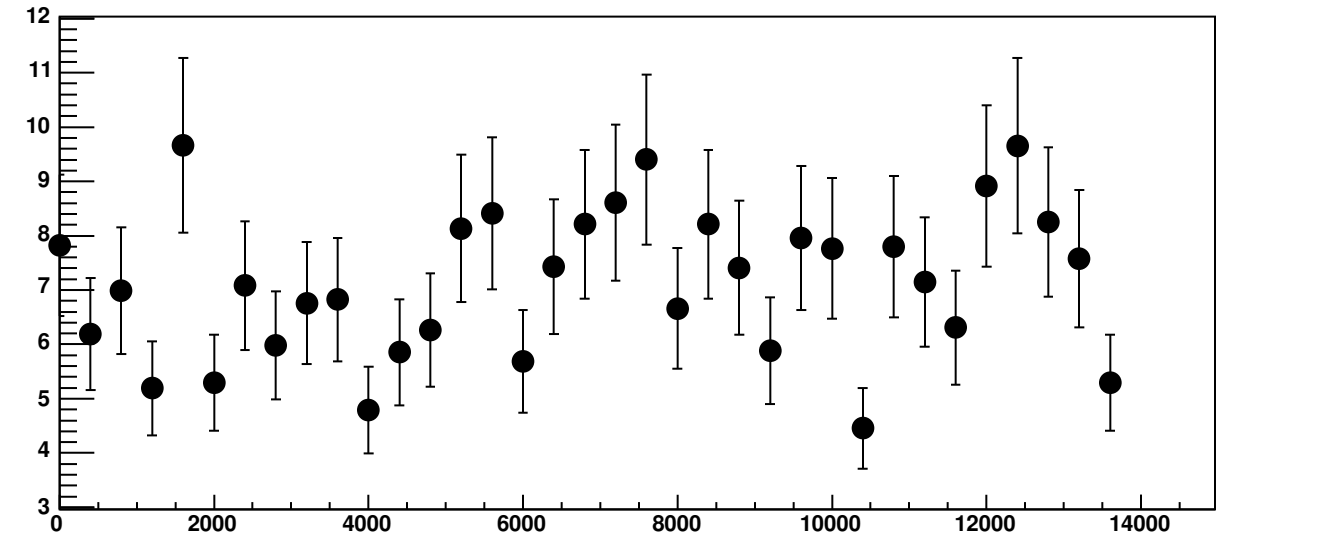

Chip 9, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

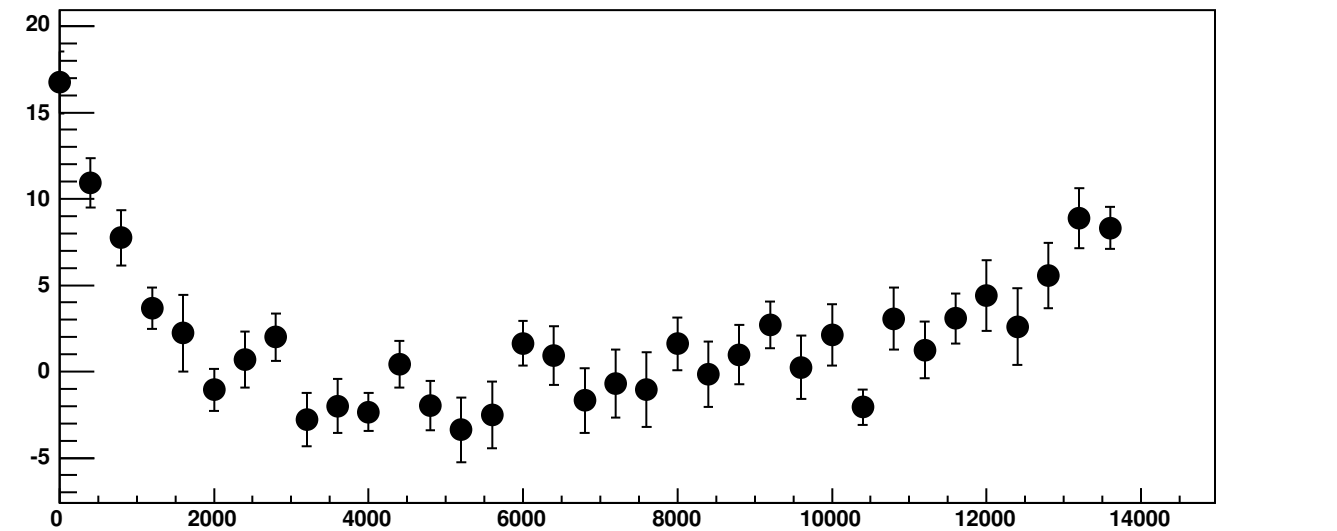
Chip 9, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

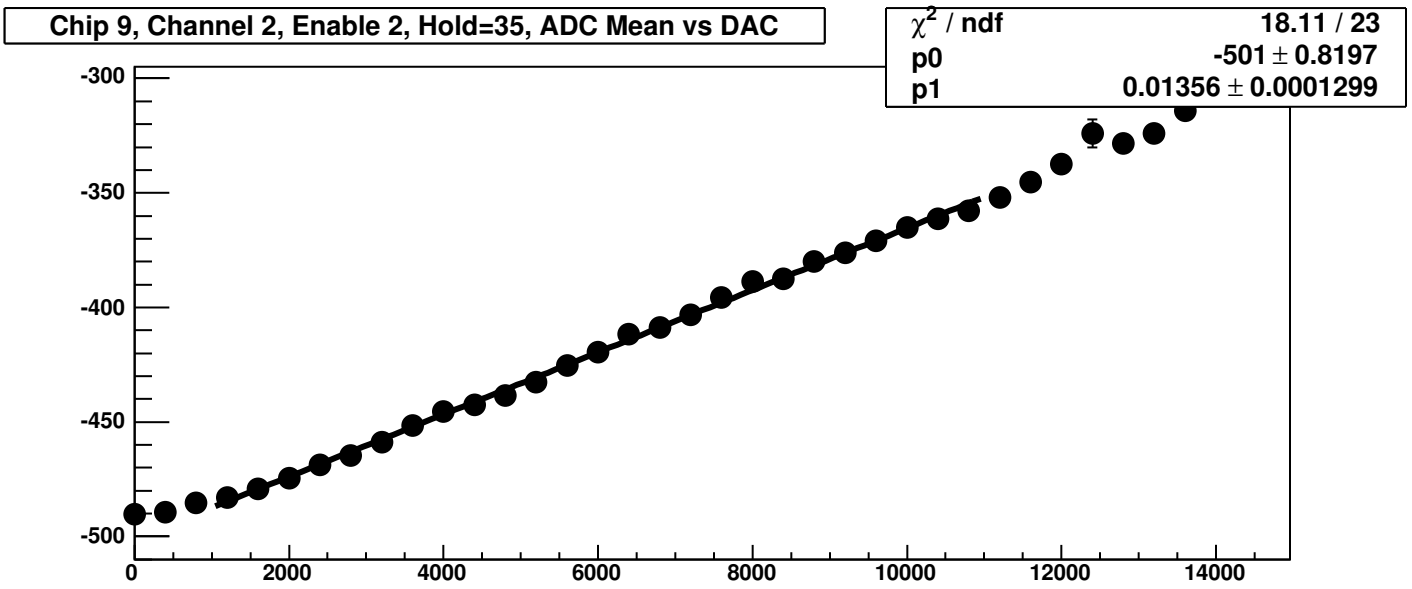


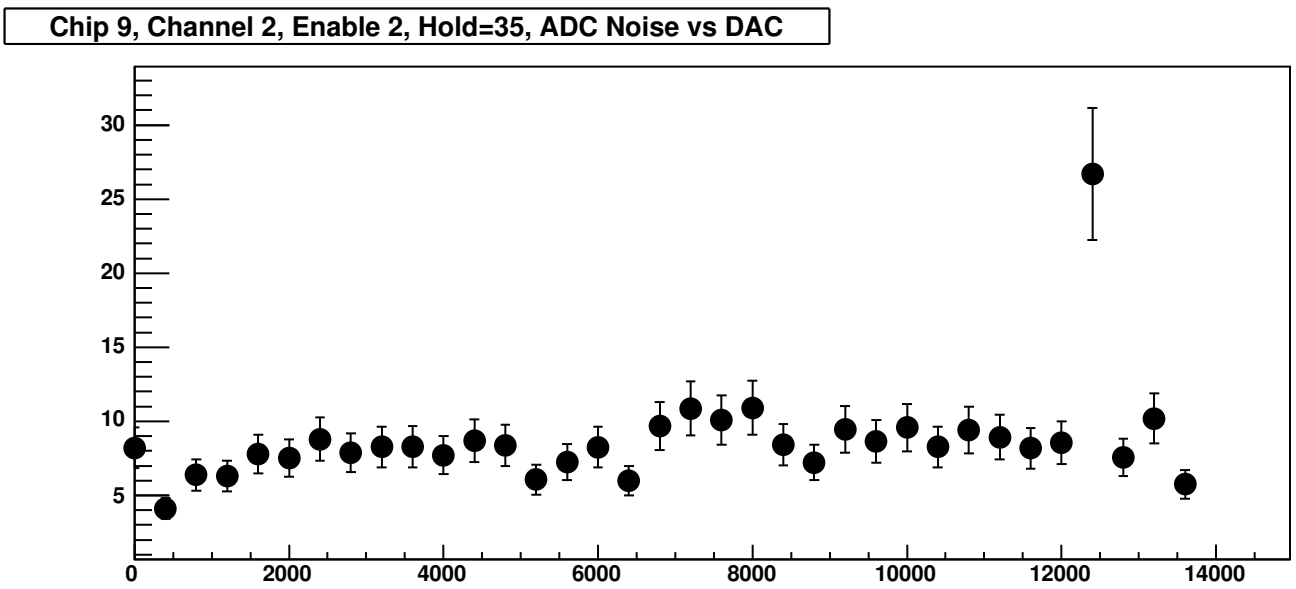
Chip 9, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

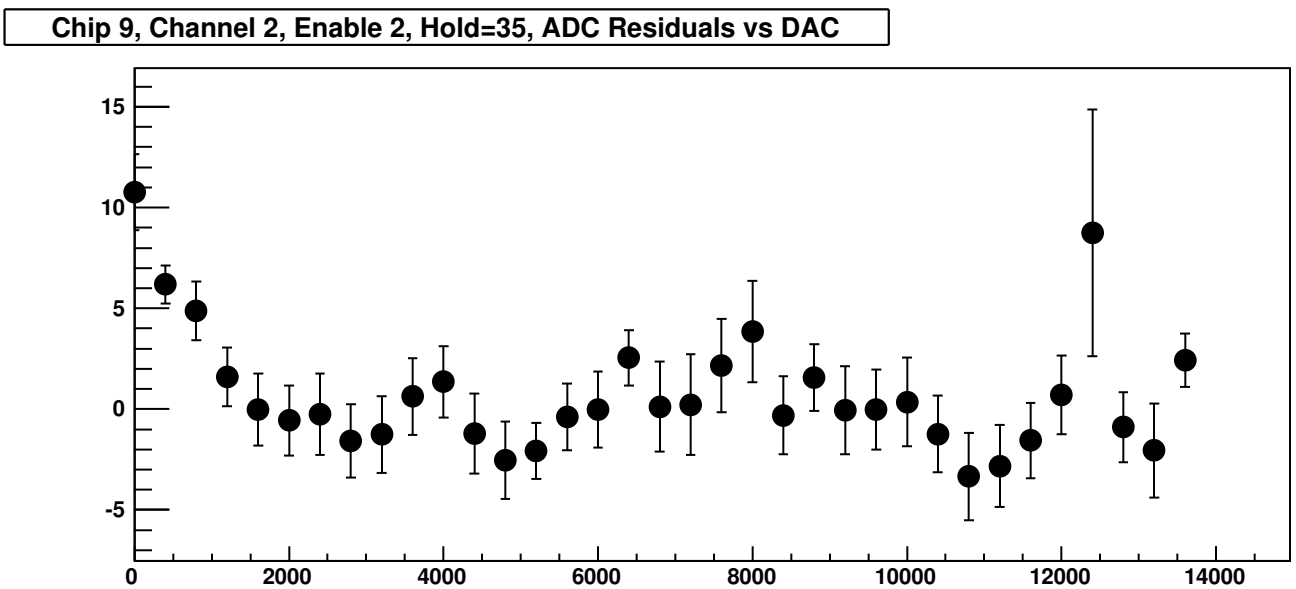


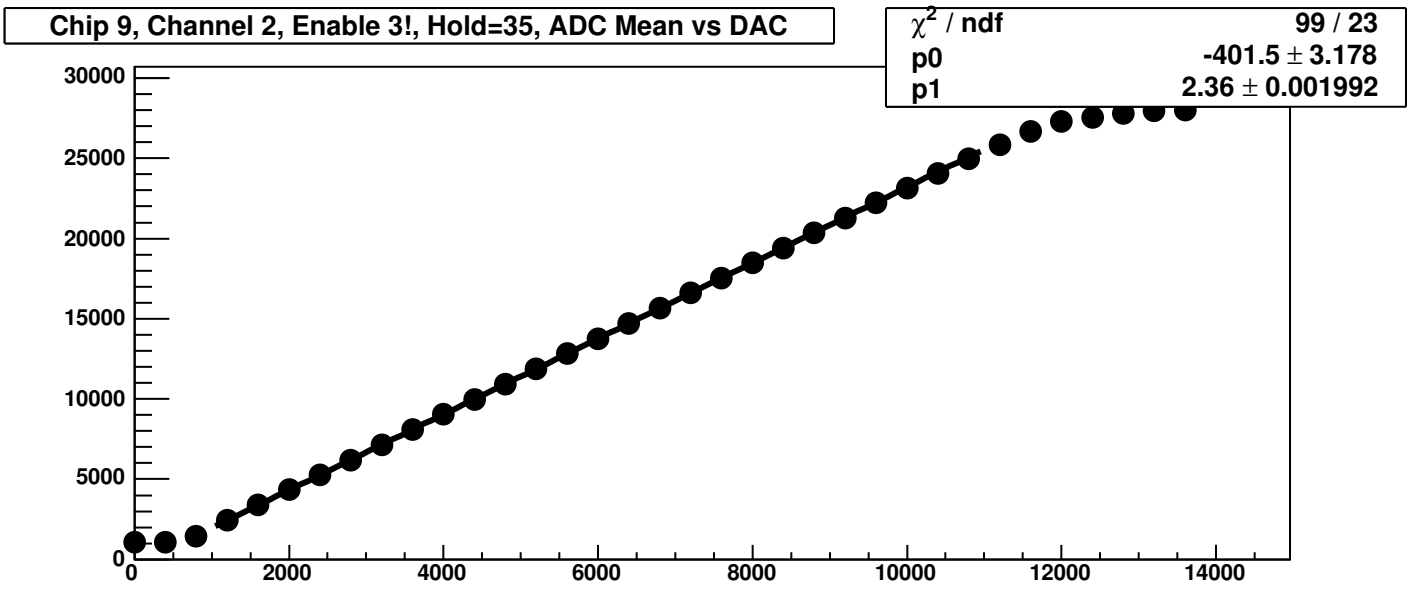
Chip 9, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

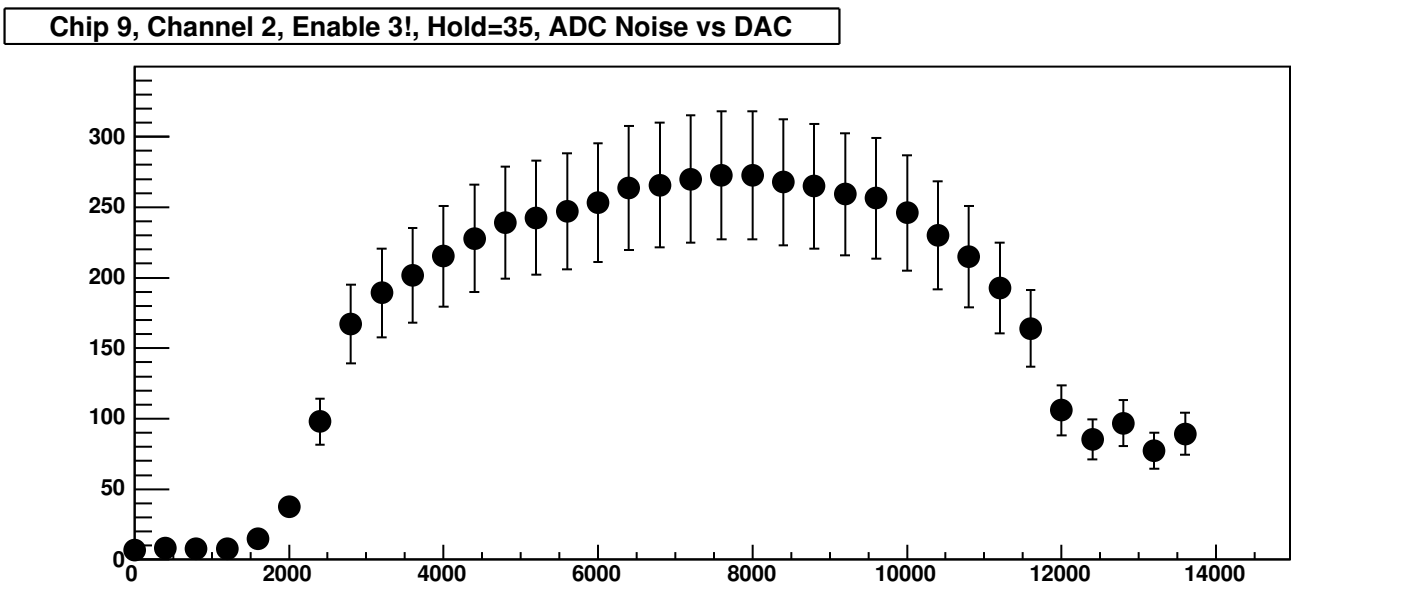


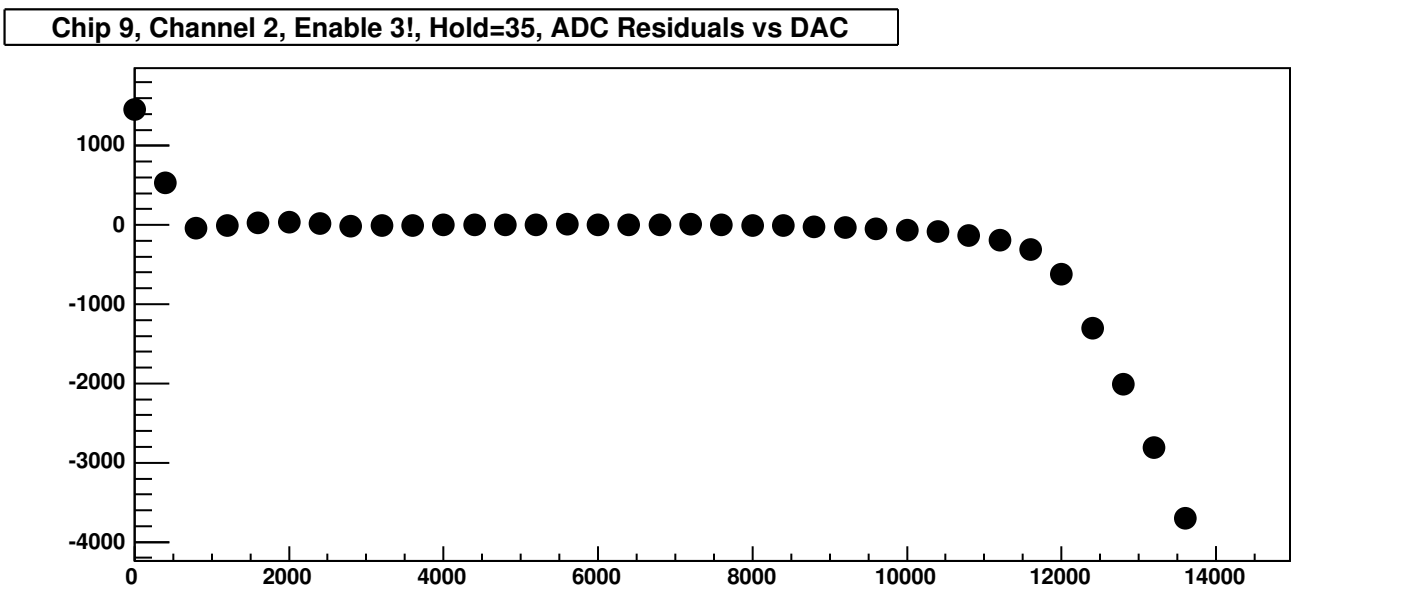

Chip 9, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.11 / 23
p0 -501 ± 0.8197
p1 0.01356 ± 0.0001299
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

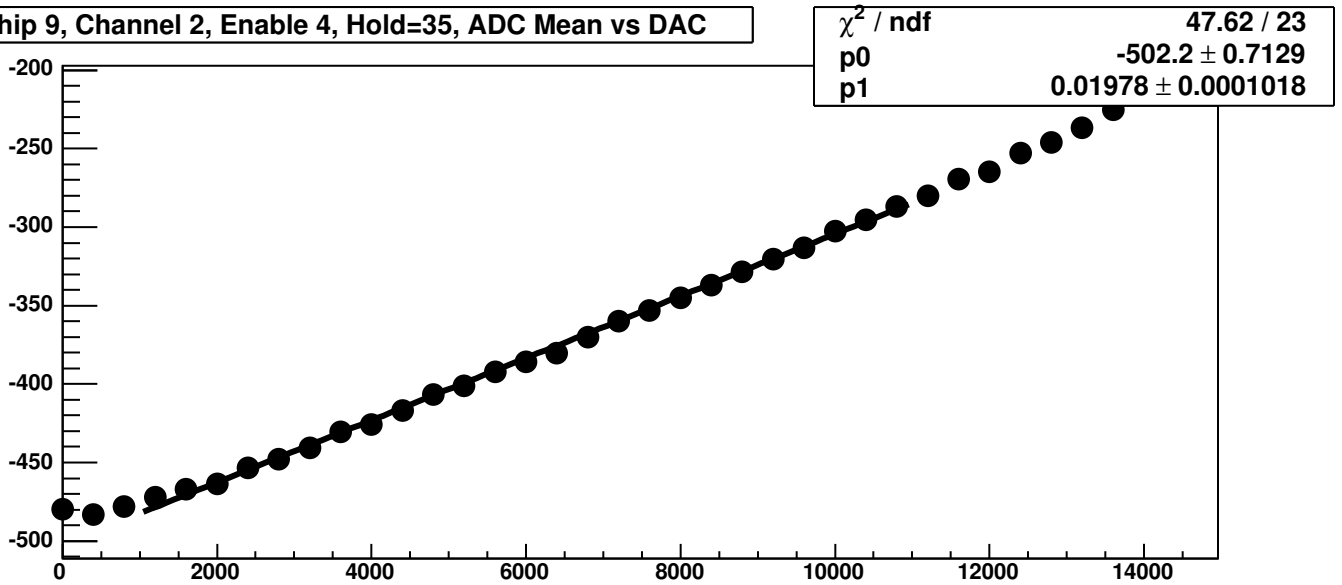

Chip 9, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 99 / 23
p0 -401.5 ± 3.178
p1 2.36 ± 0.001992
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

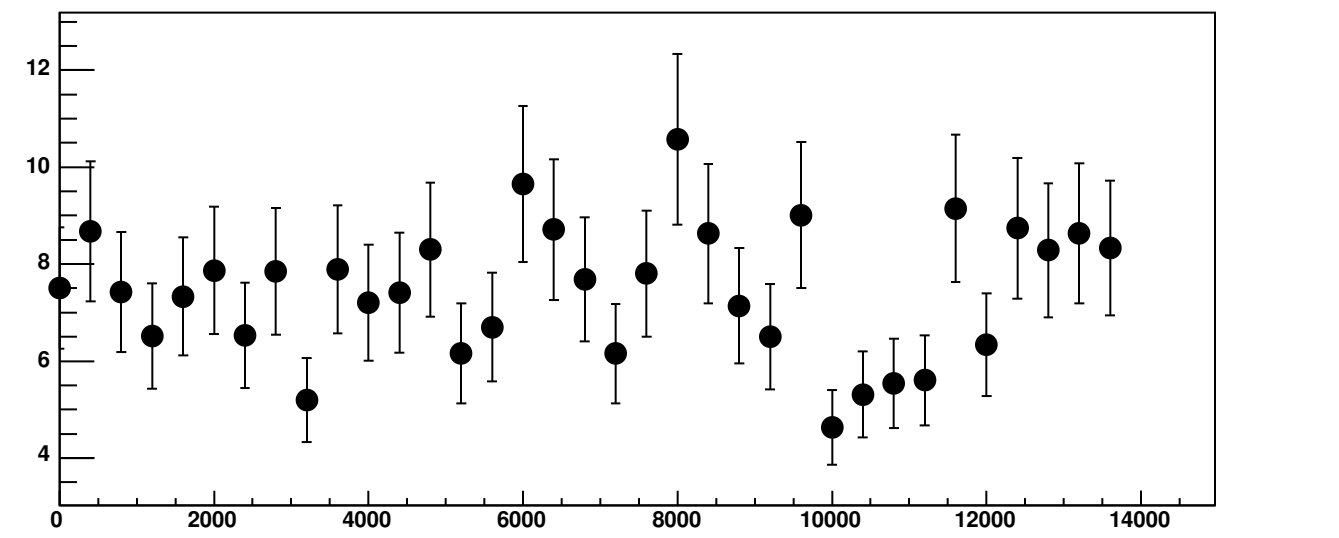

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

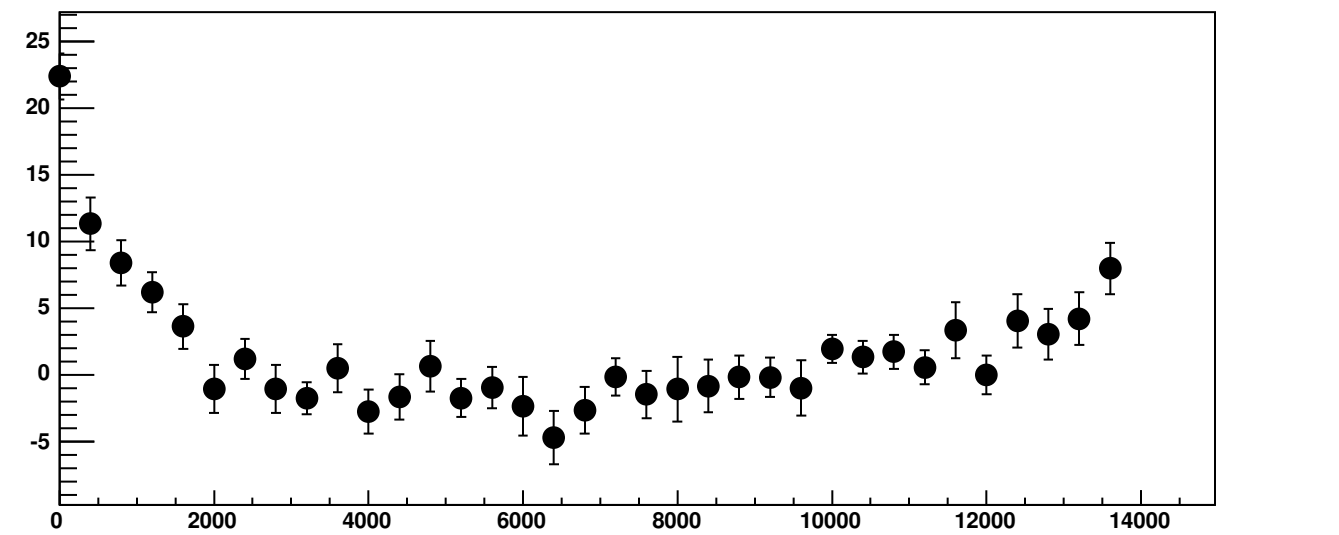
Chip 9, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

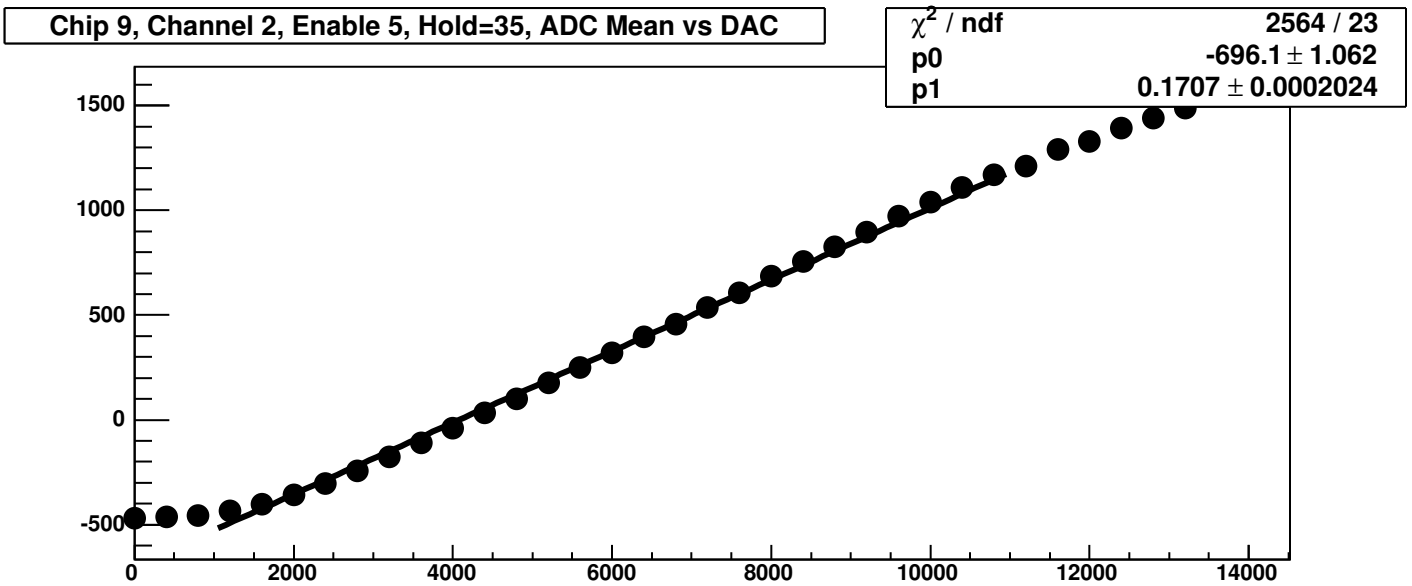


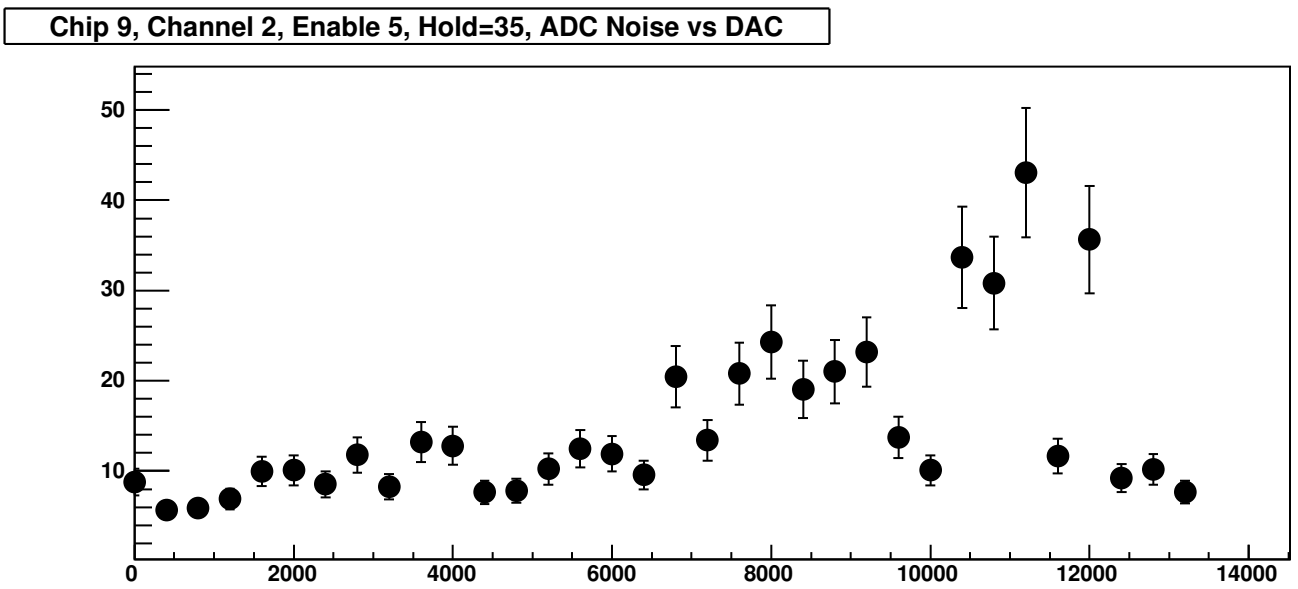
Chip 9, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

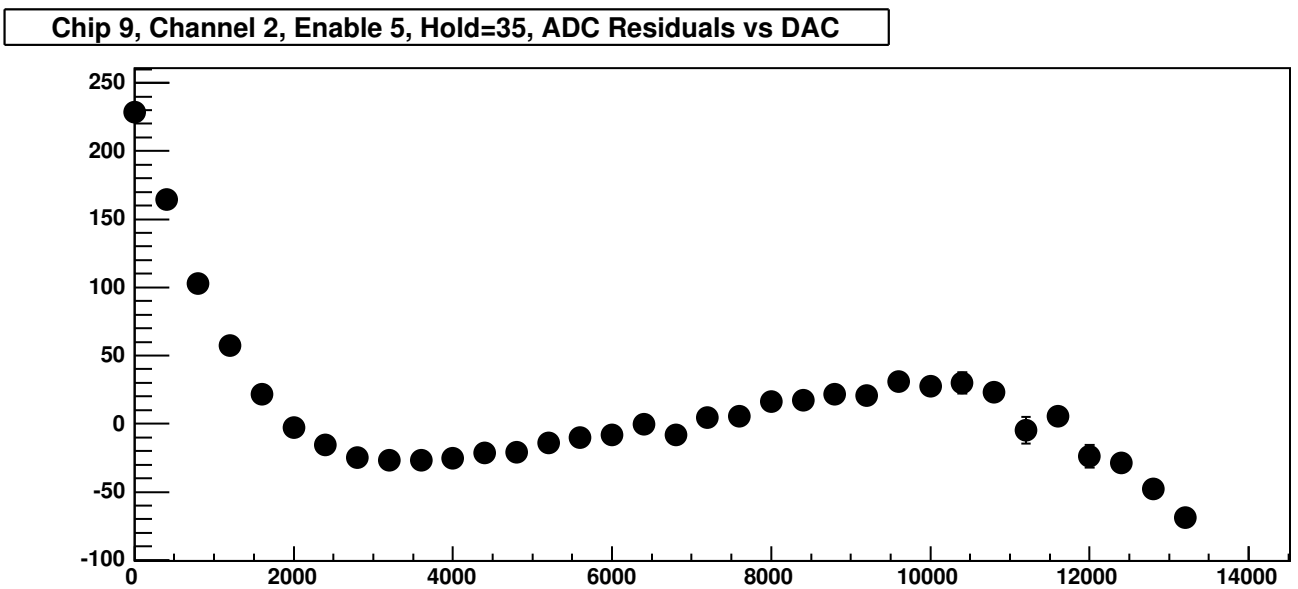


Chip 9, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

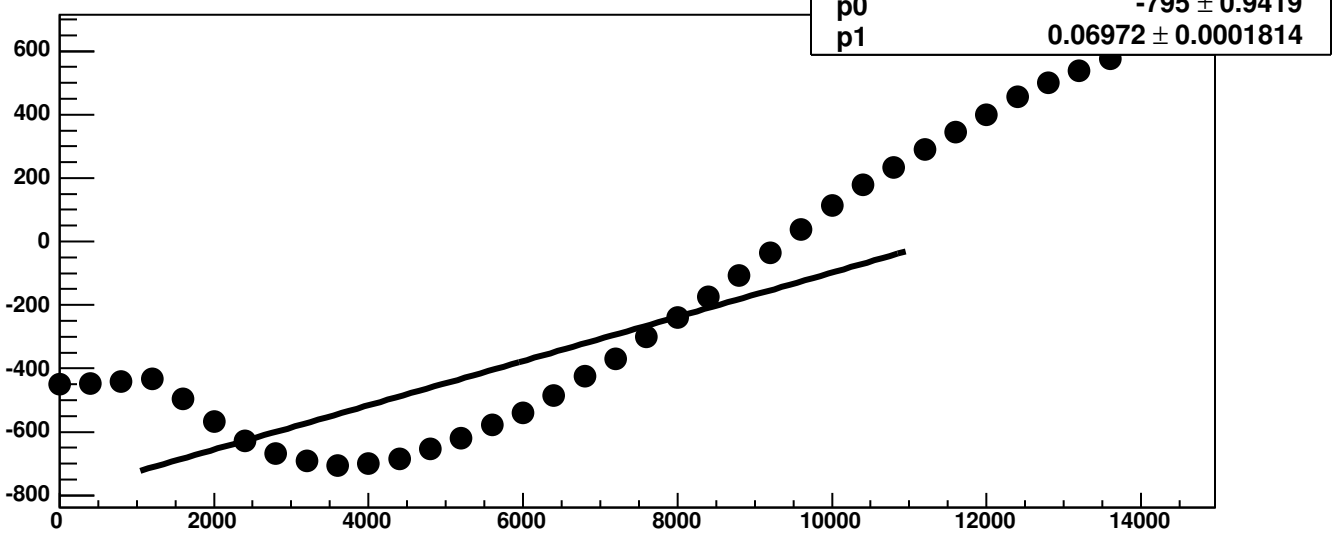



Chip 9, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2564 / 23
p0 -696.1 ± 1.062
p1 0.1707 ± 0.0002024
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

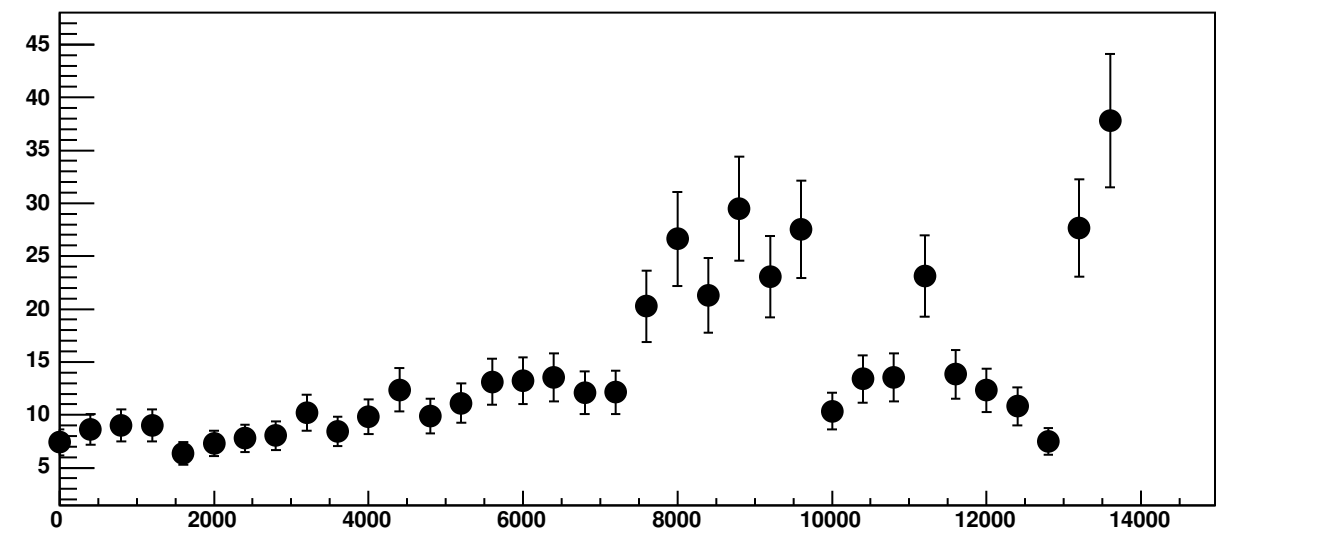

Chip 9, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

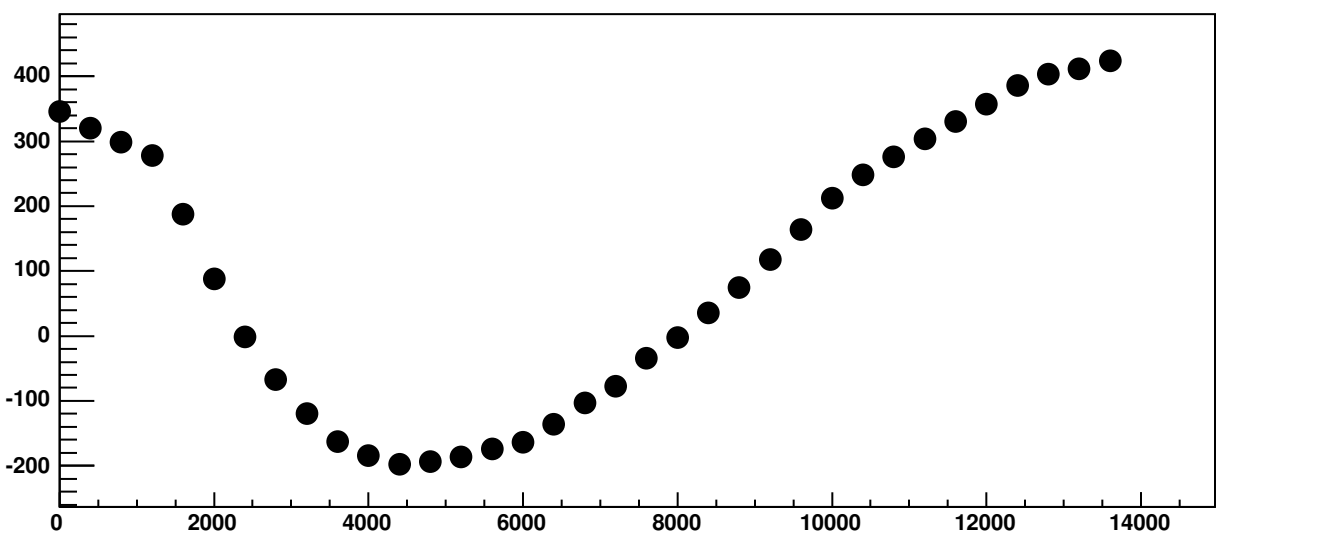
Chip 9, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

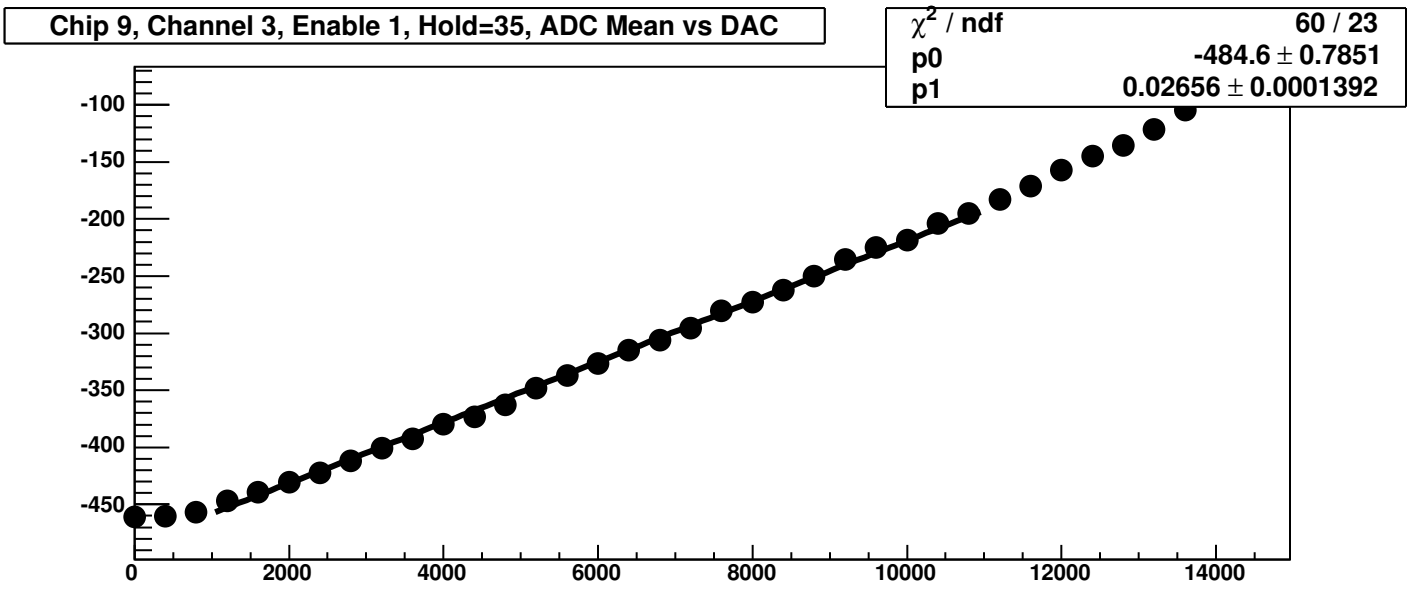


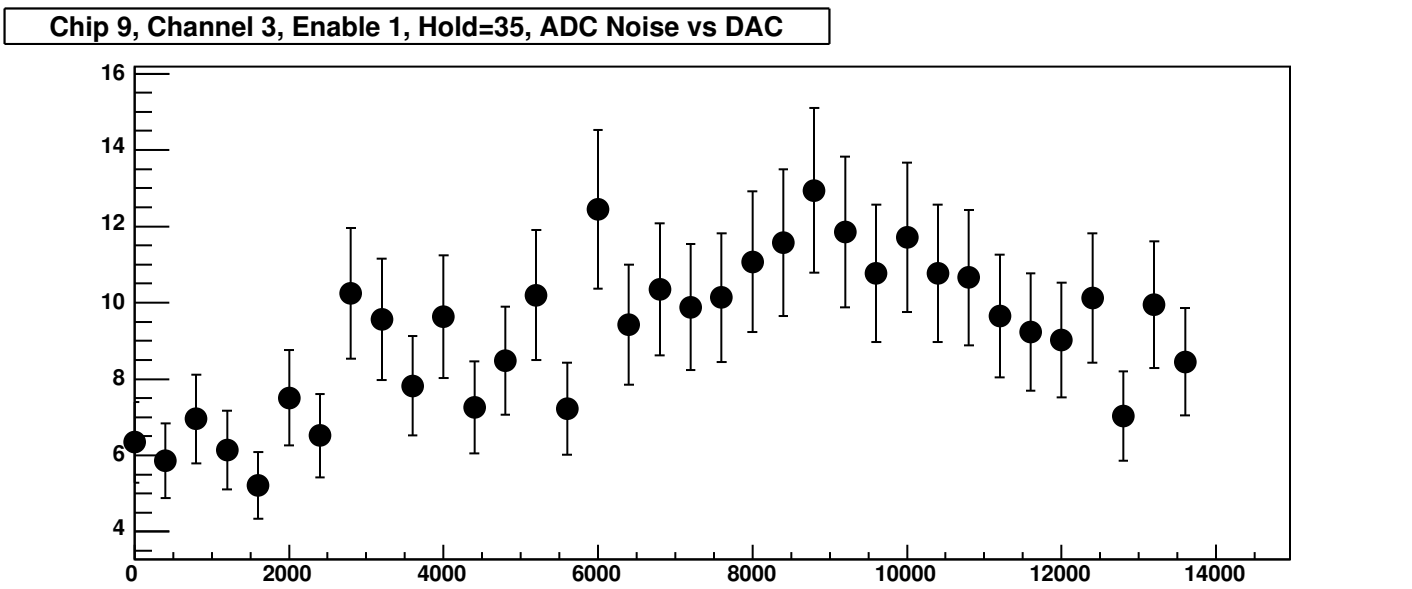
Chip 9, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

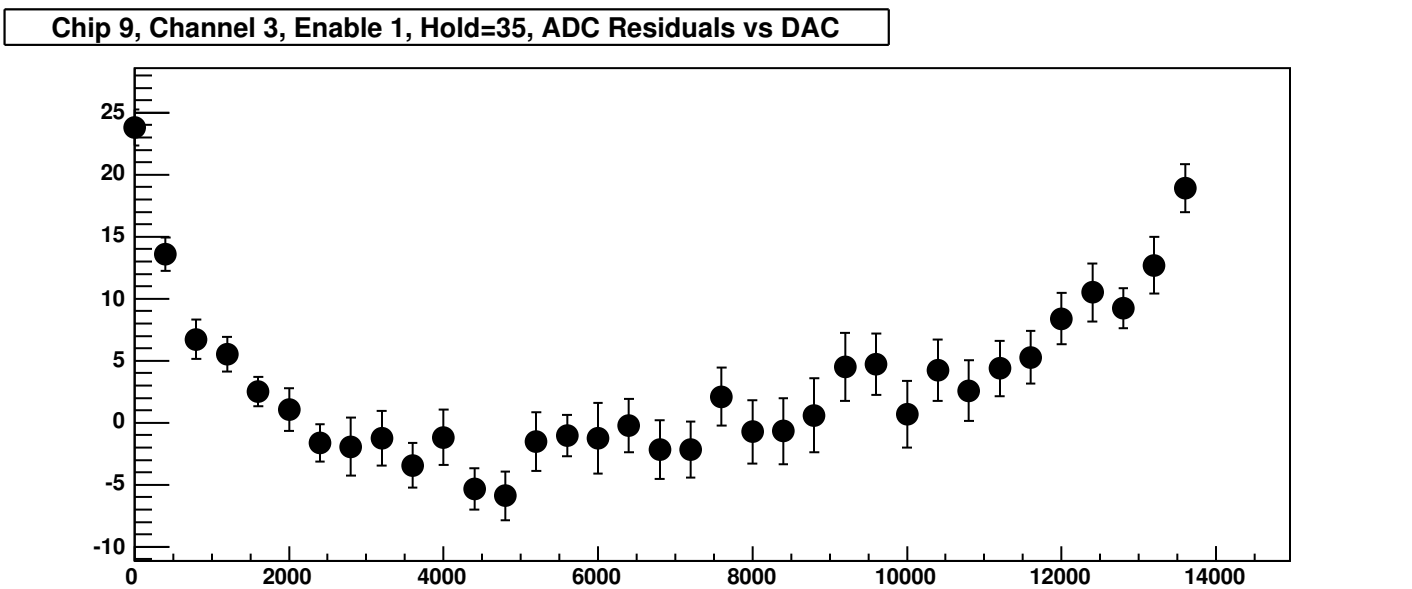


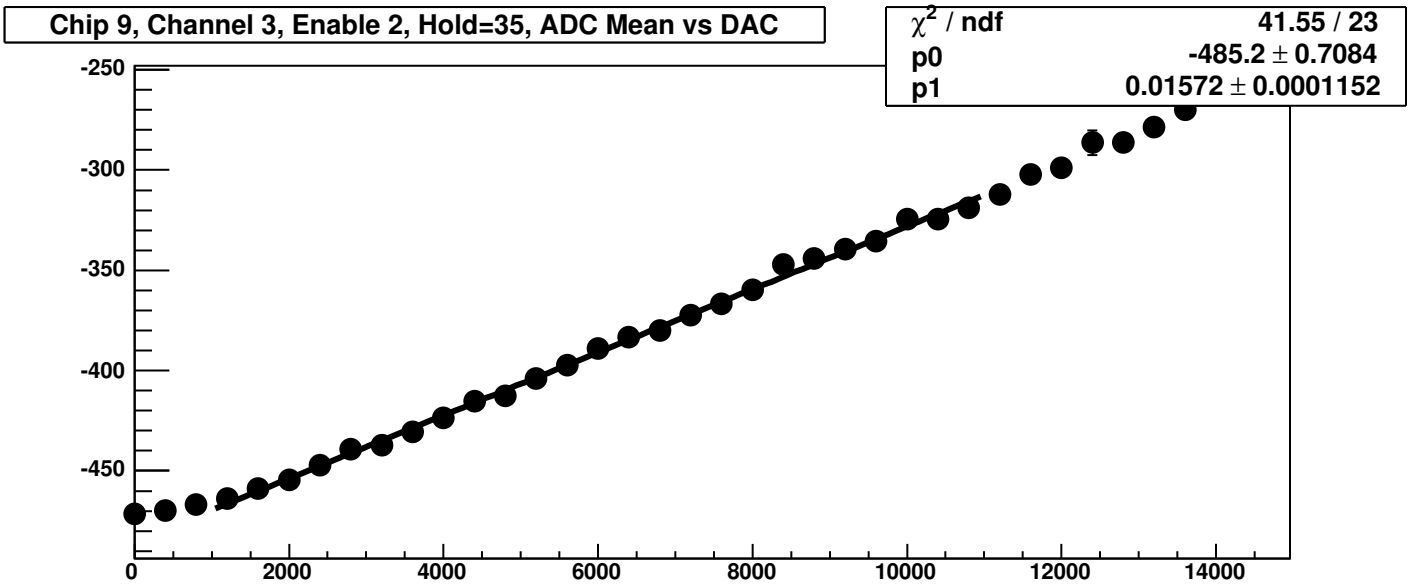
Chip 9, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

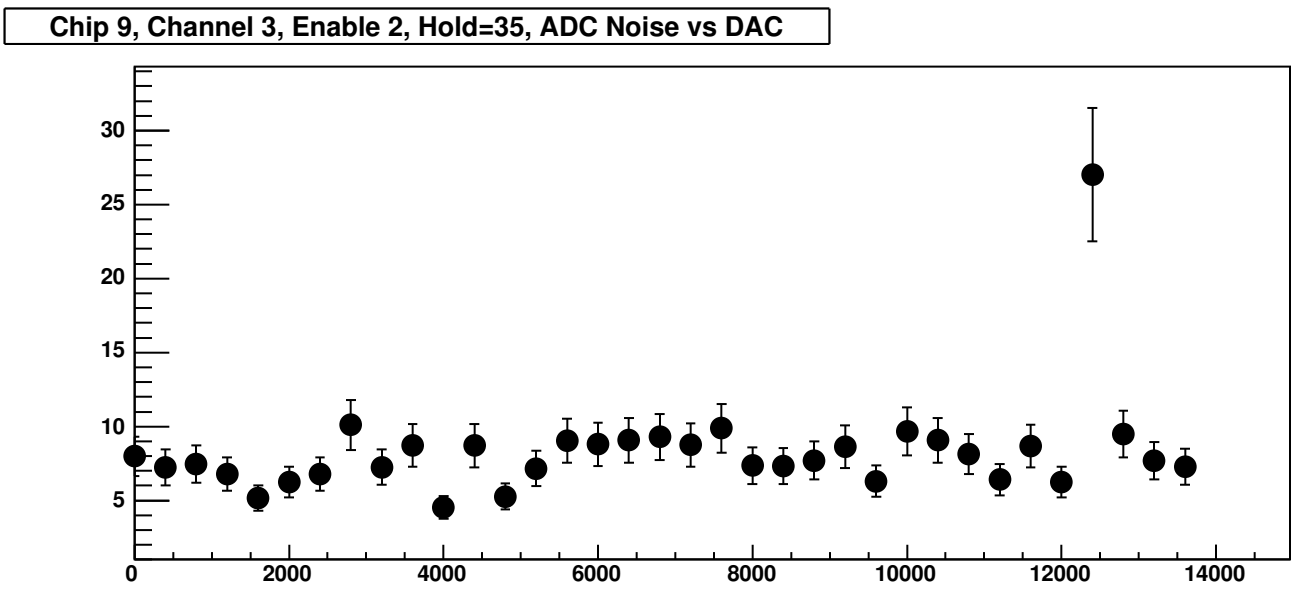


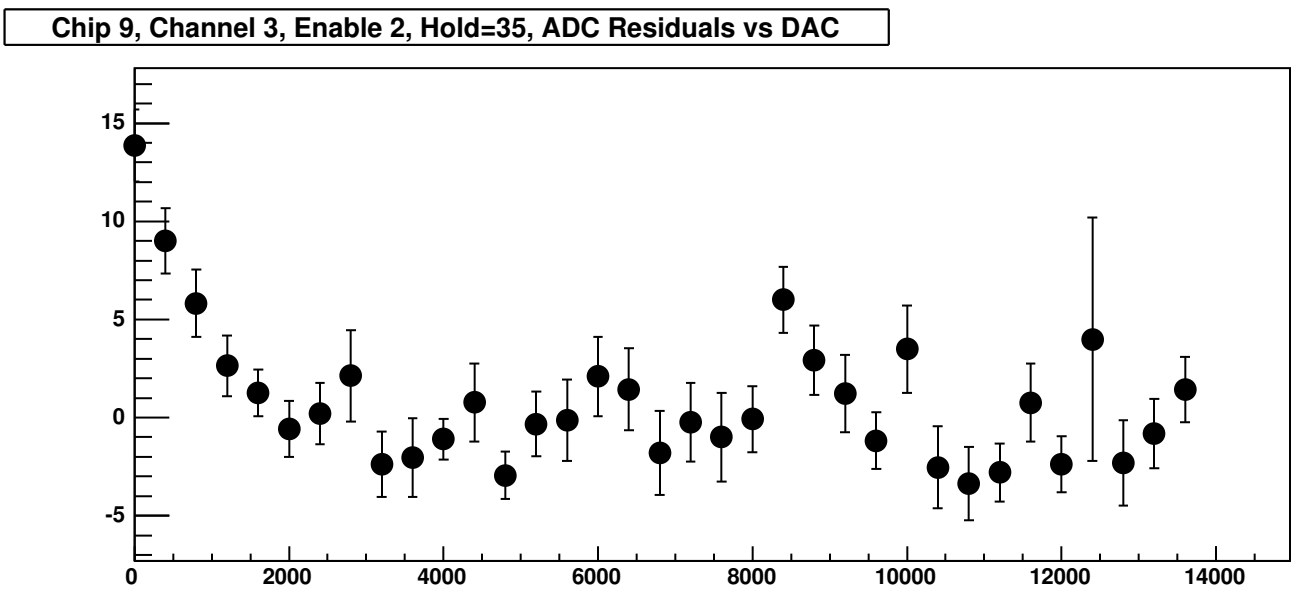

Chip 9, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60 / 23
p0 -484.6 ± 0.7851
p1 0.02656 ± 0.0001392
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

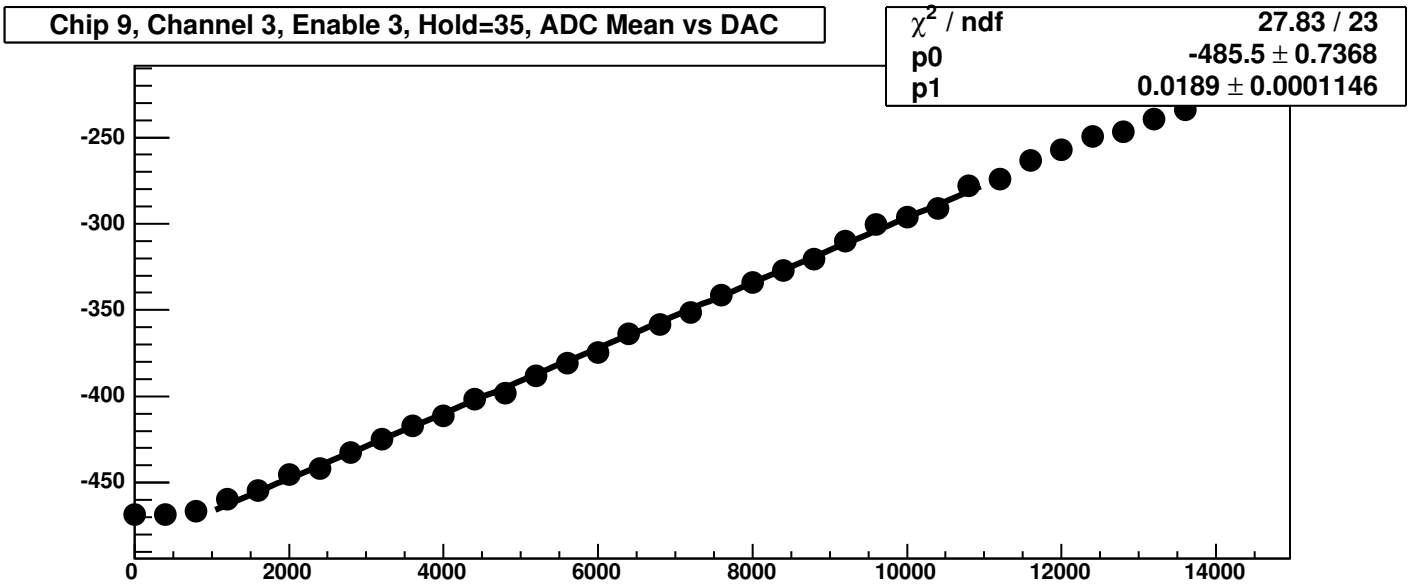

Chip 9, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

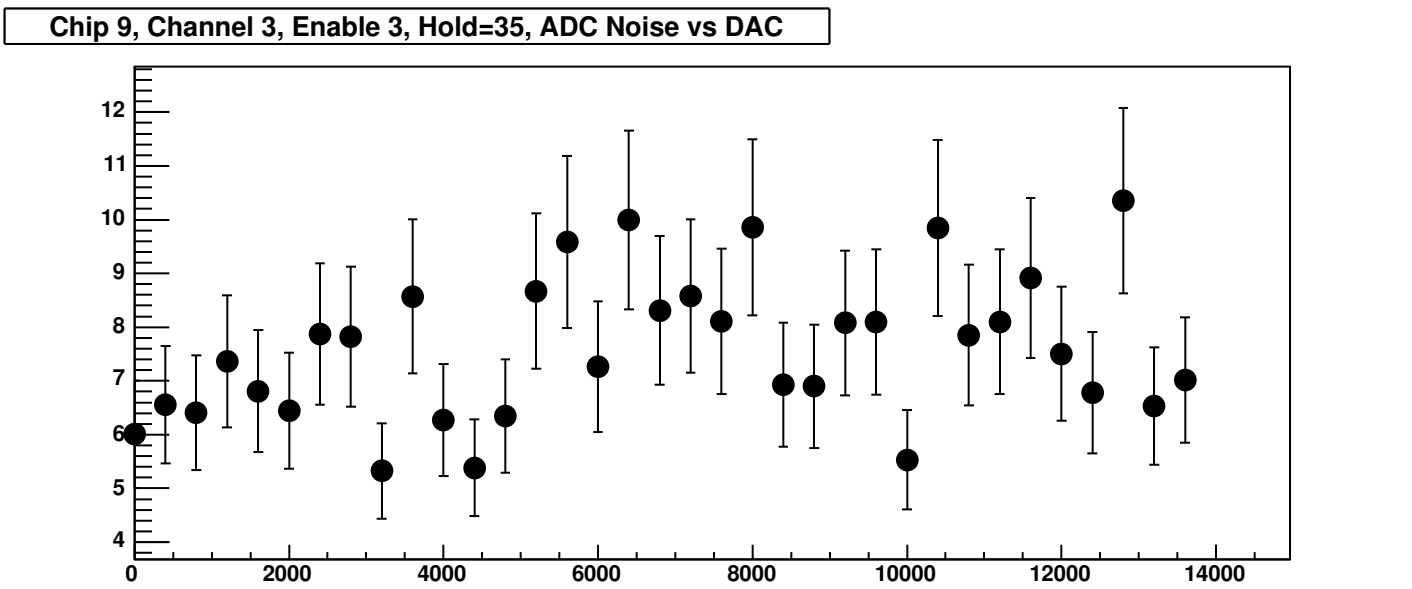

Chip 9, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

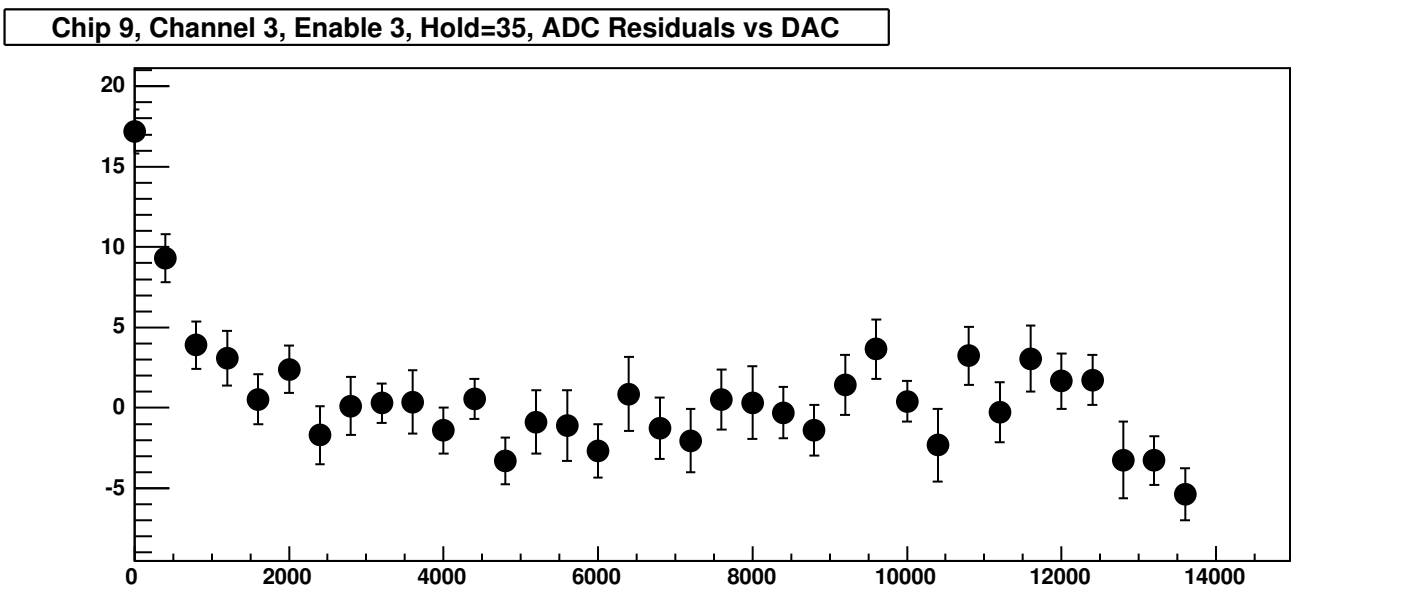

Chip 9, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.55 / 23
p0 -485.2 ± 0.7084
p1 0.01572 ± 0.0001152
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

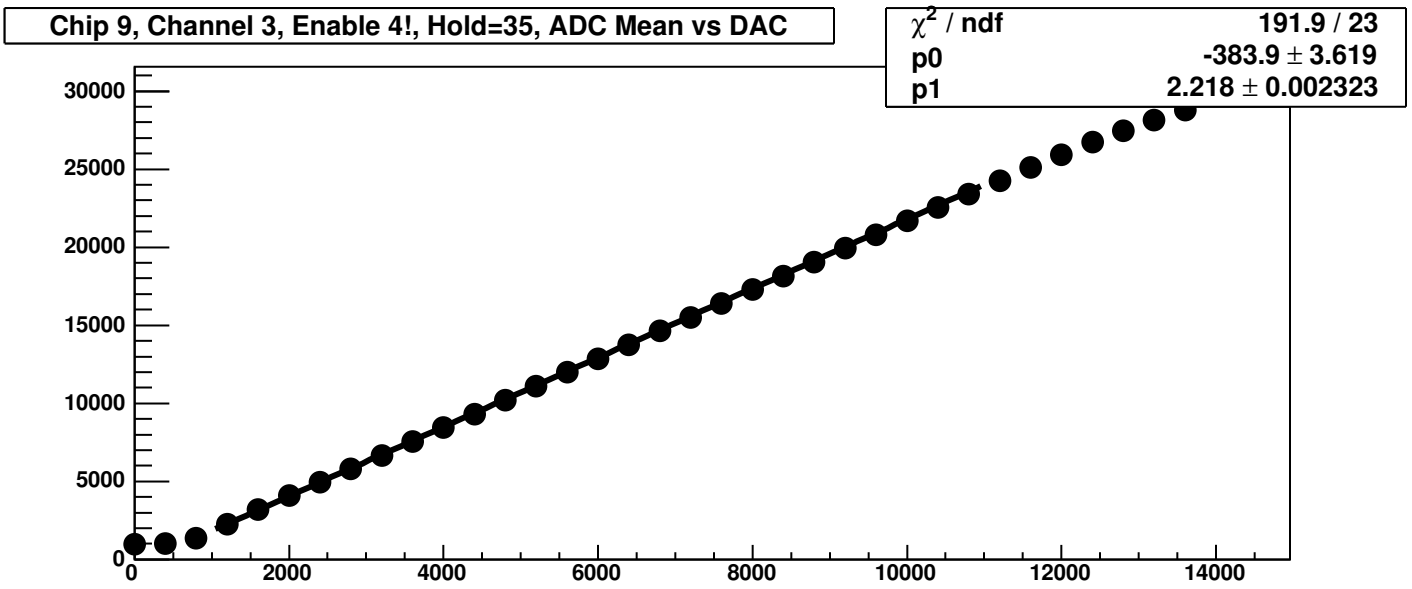

Chip 9, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

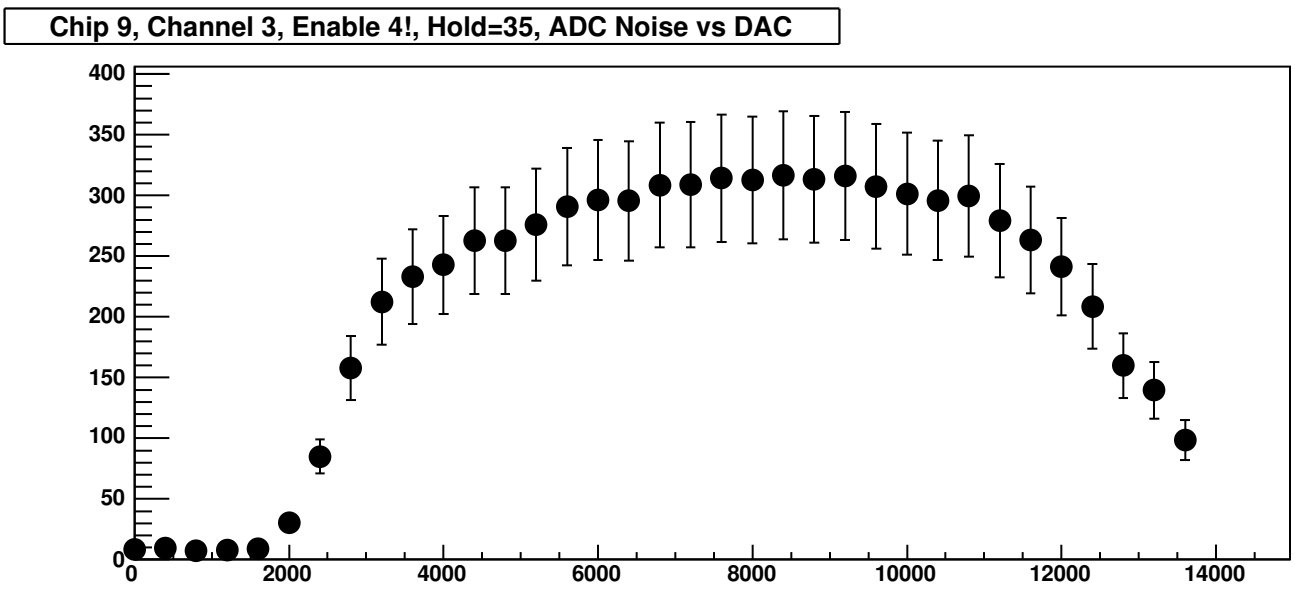

Chip 9, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

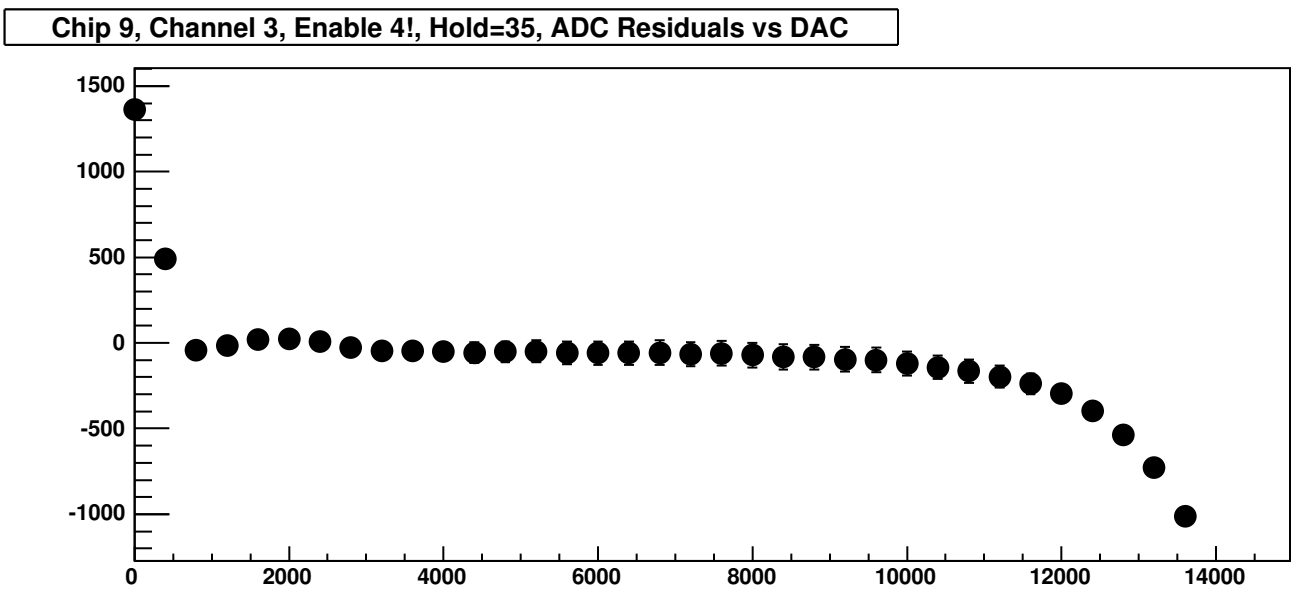

Chip 9, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.83 / 23
p0 -485.5 ± 0.7368
p1 0.0189 ± 0.0001146
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

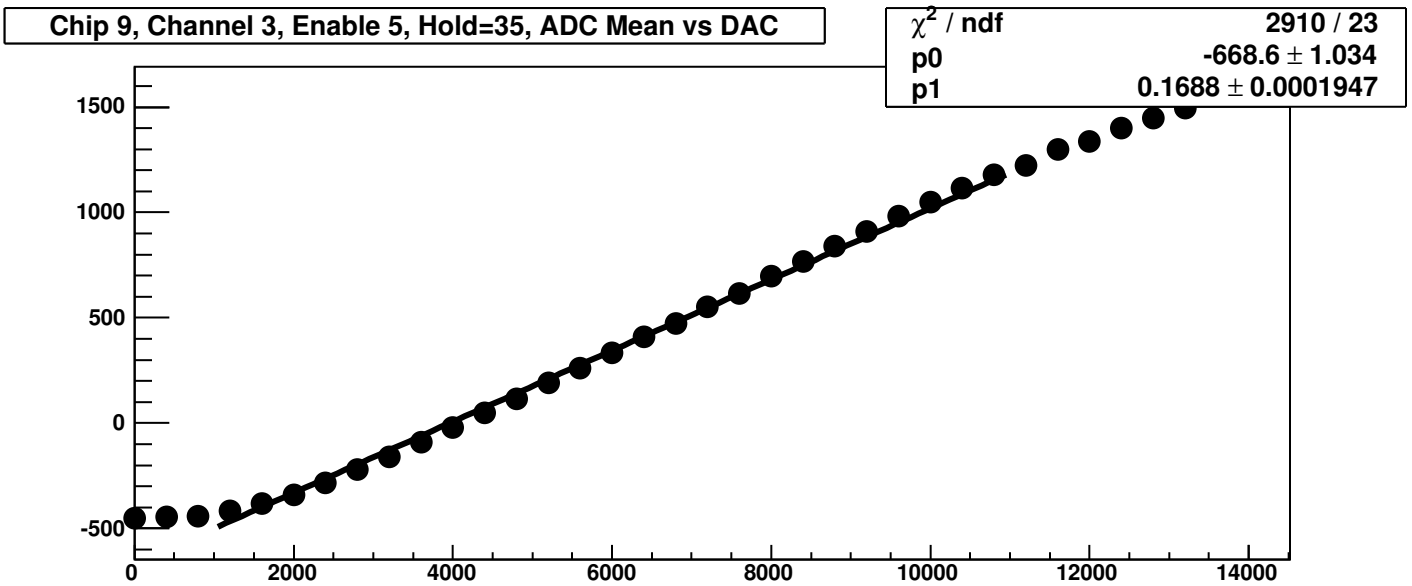

Chip 9, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

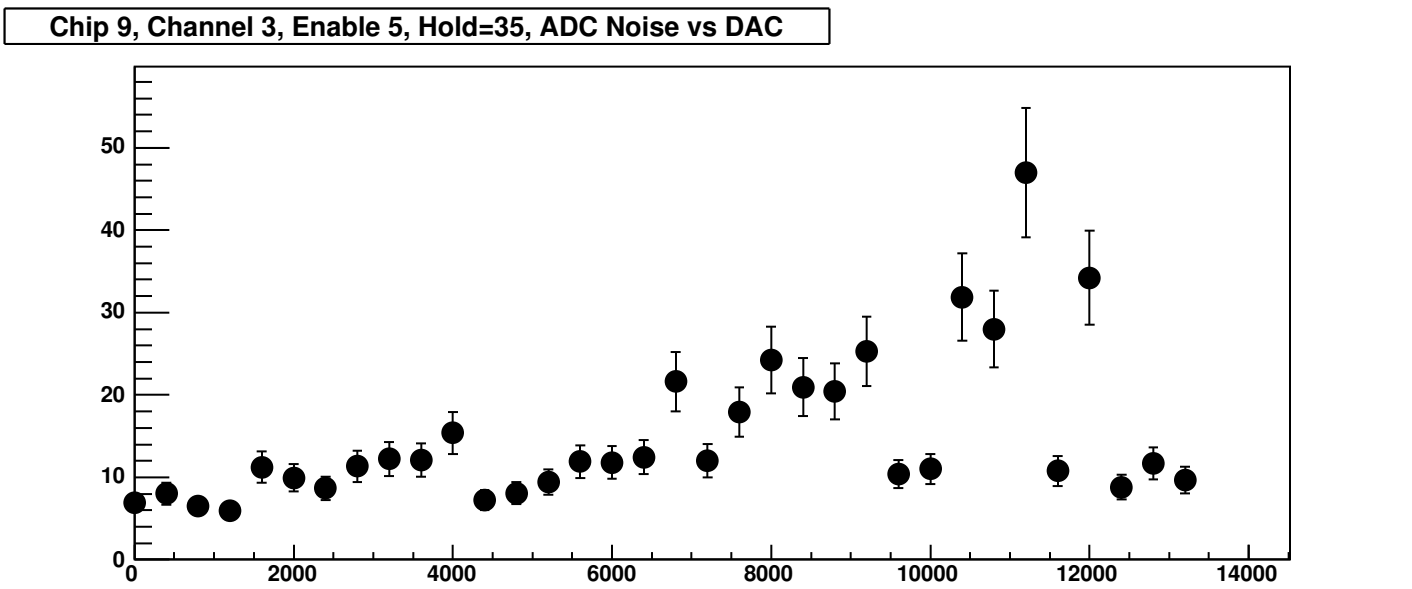

Chip 9, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

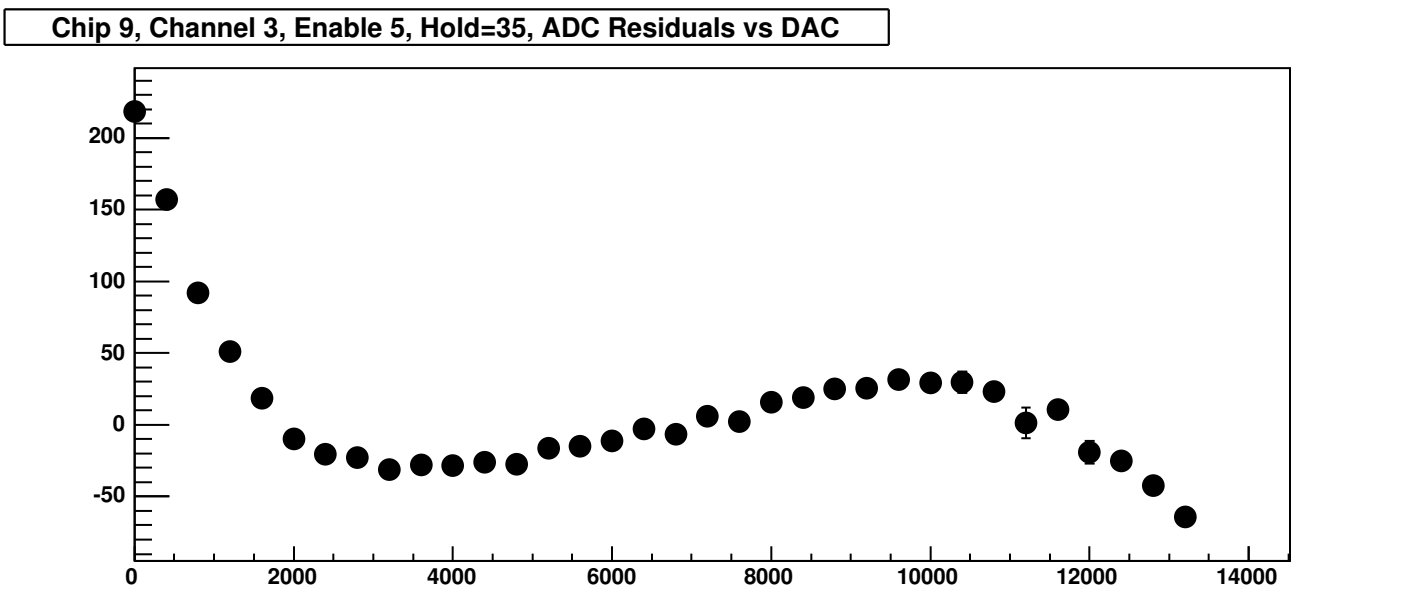

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 191.9 / 23
p0 -383.9 ± 3.619
p1 2.218 ± 0.002323
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

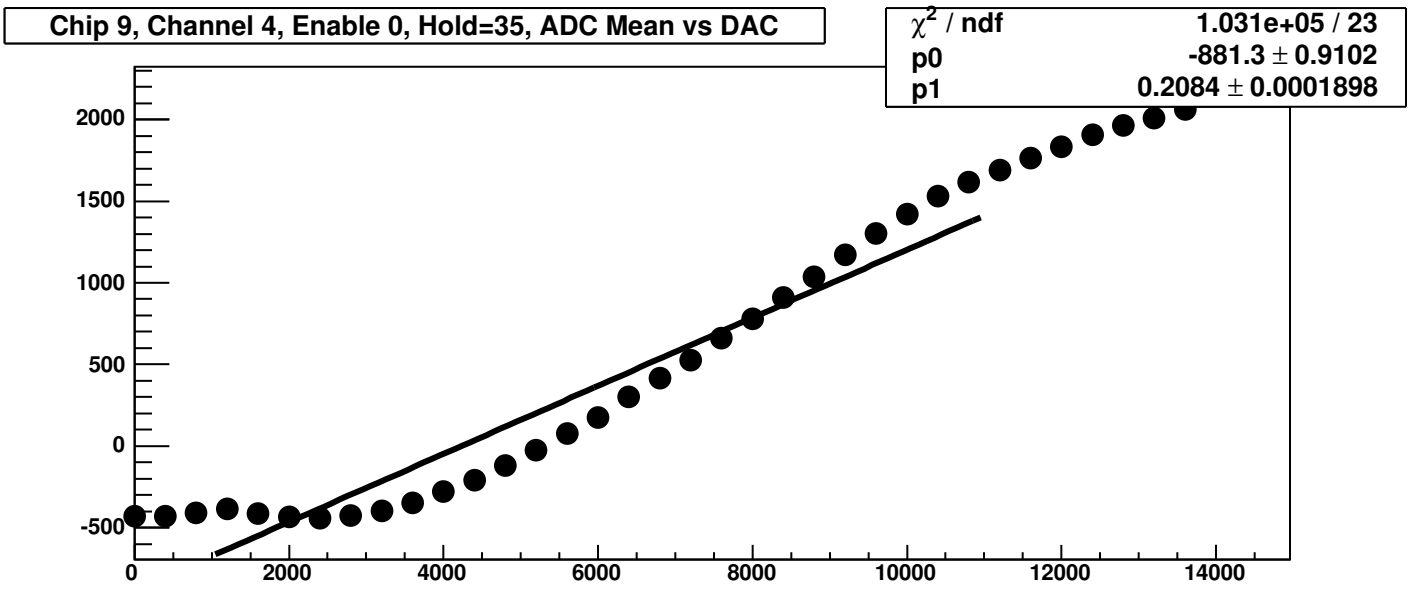

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

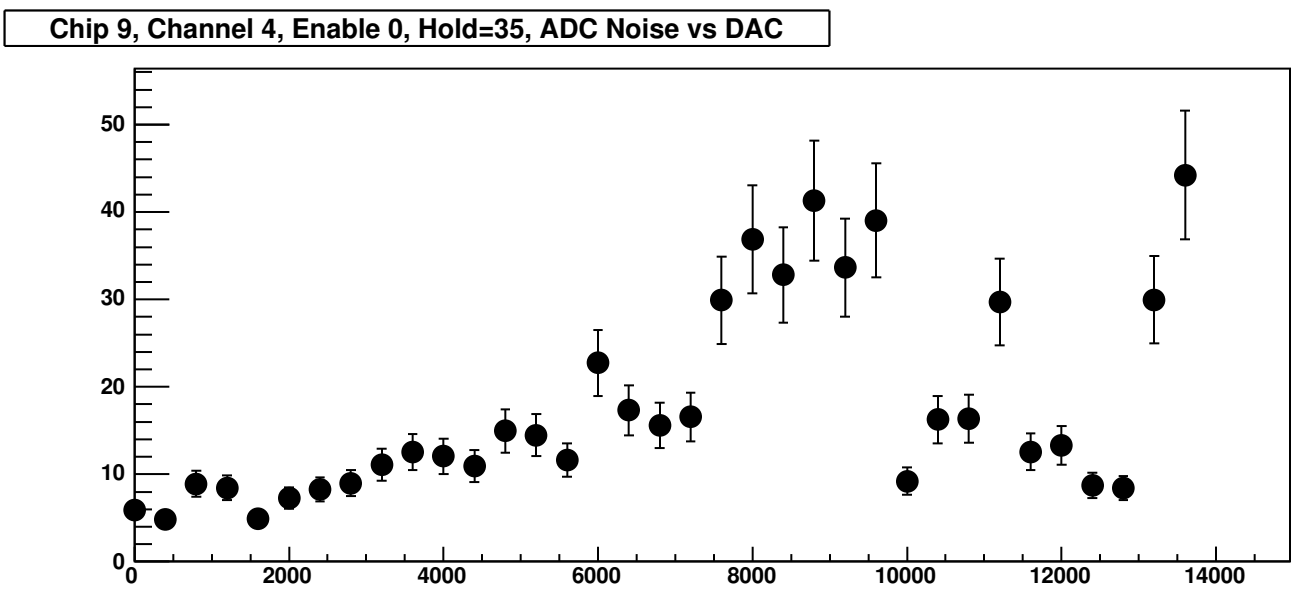

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

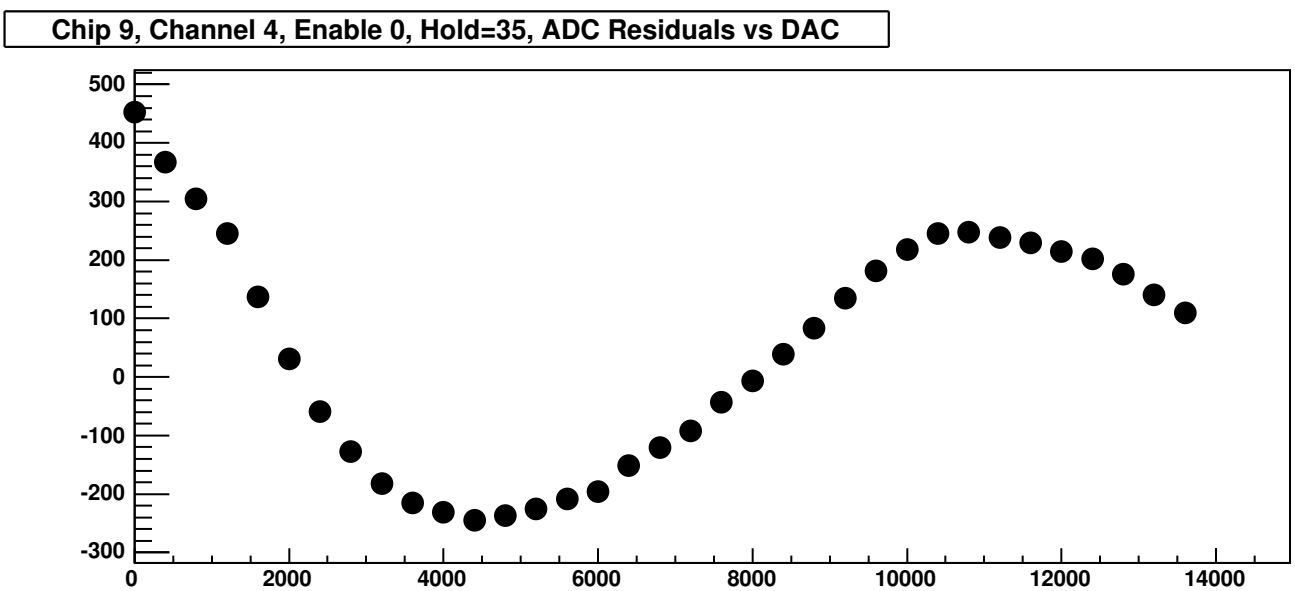

Chip 9, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2910 / 23
p0 -668.6 ± 1.034
p1 0.1688 ± 0.0001947
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

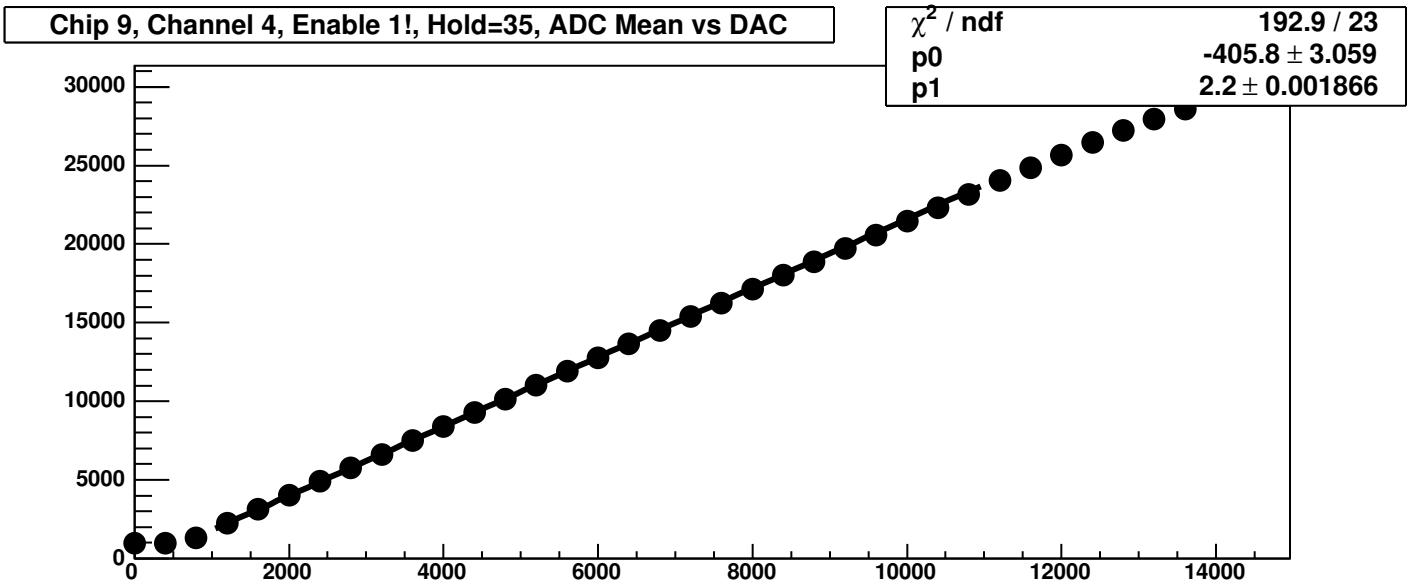

Chip 9, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

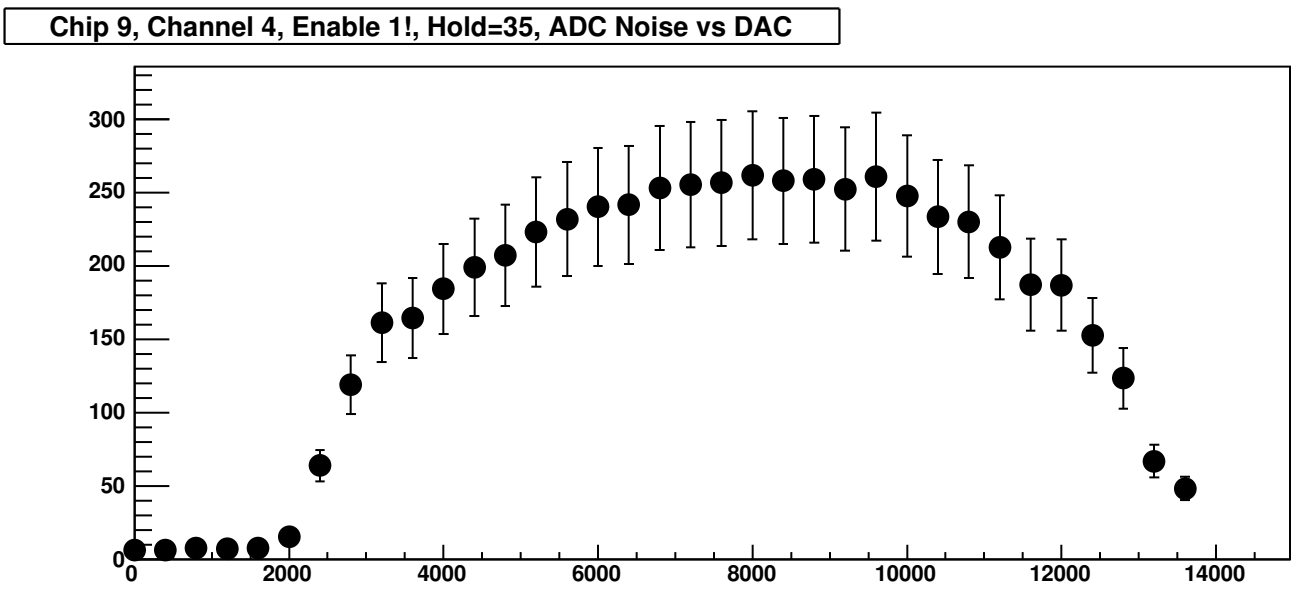

Chip 9, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

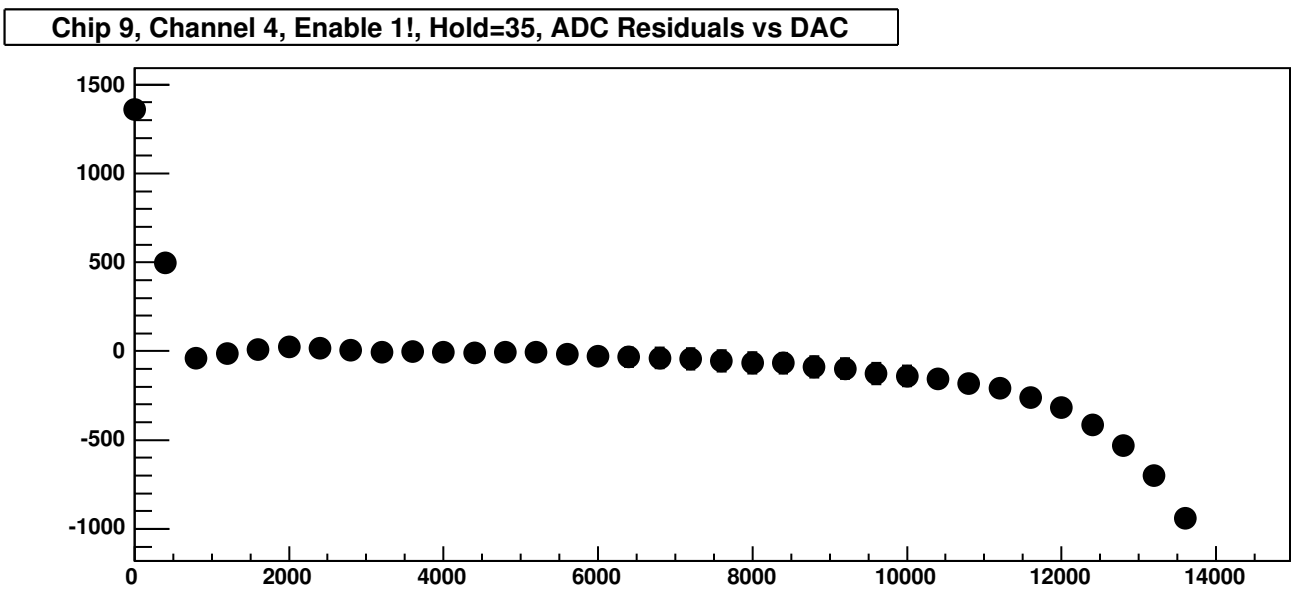

Chip 9, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.031e+05 / 23
p0 -881.3 ± 0.9102
p1 0.2084 ± 0.0001898
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

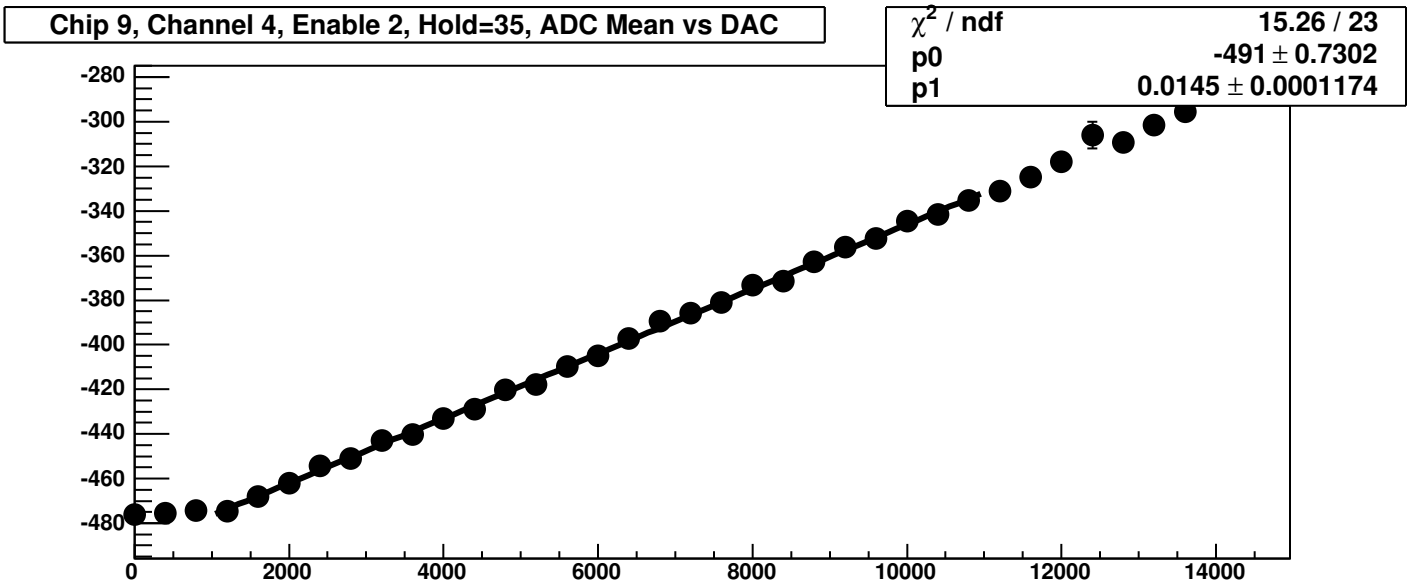

Chip 9, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

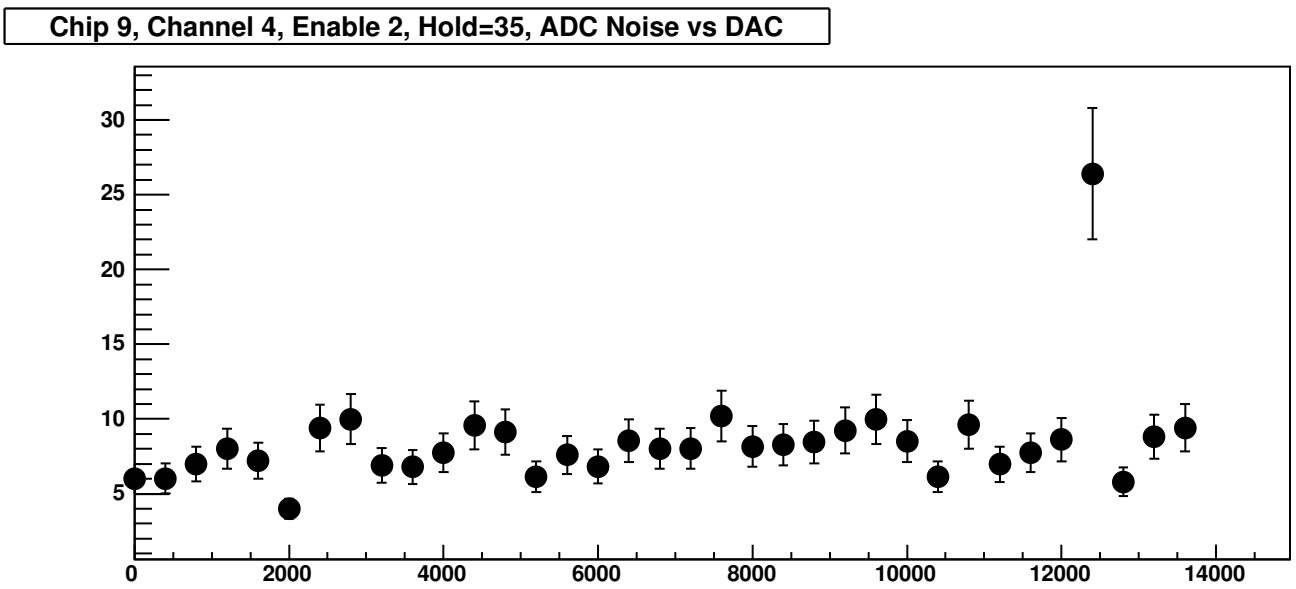

Chip 9, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

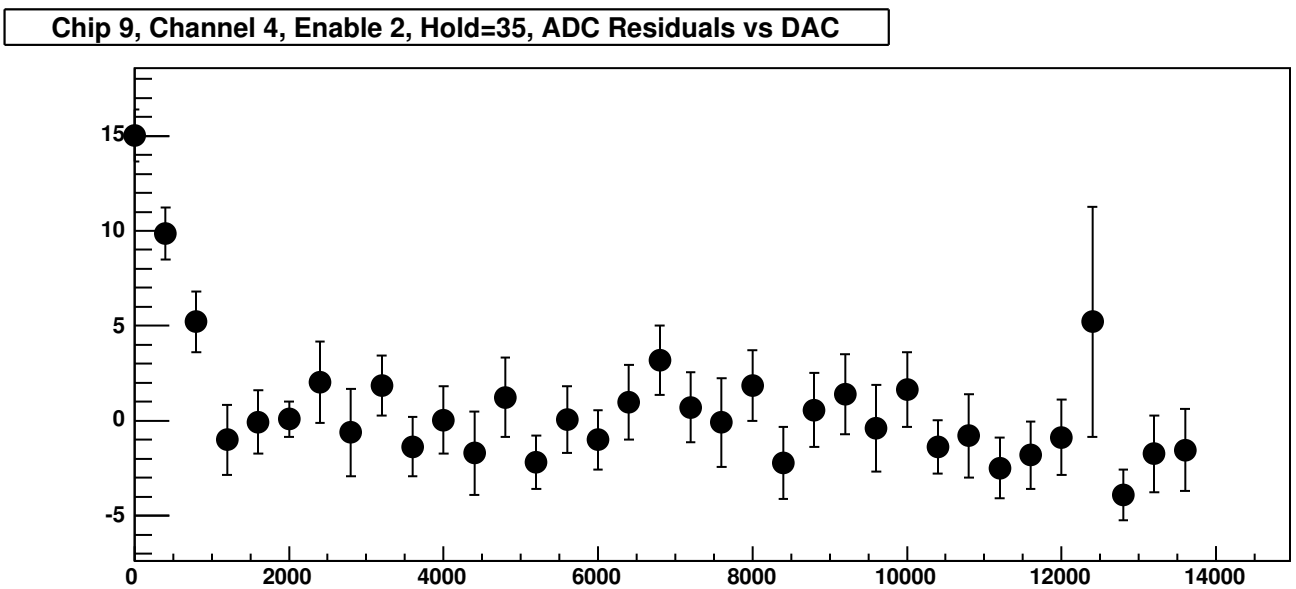

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 192.9 / 23
p0 -405.8 ± 3.059
p1 2.2 ± 0.001866
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

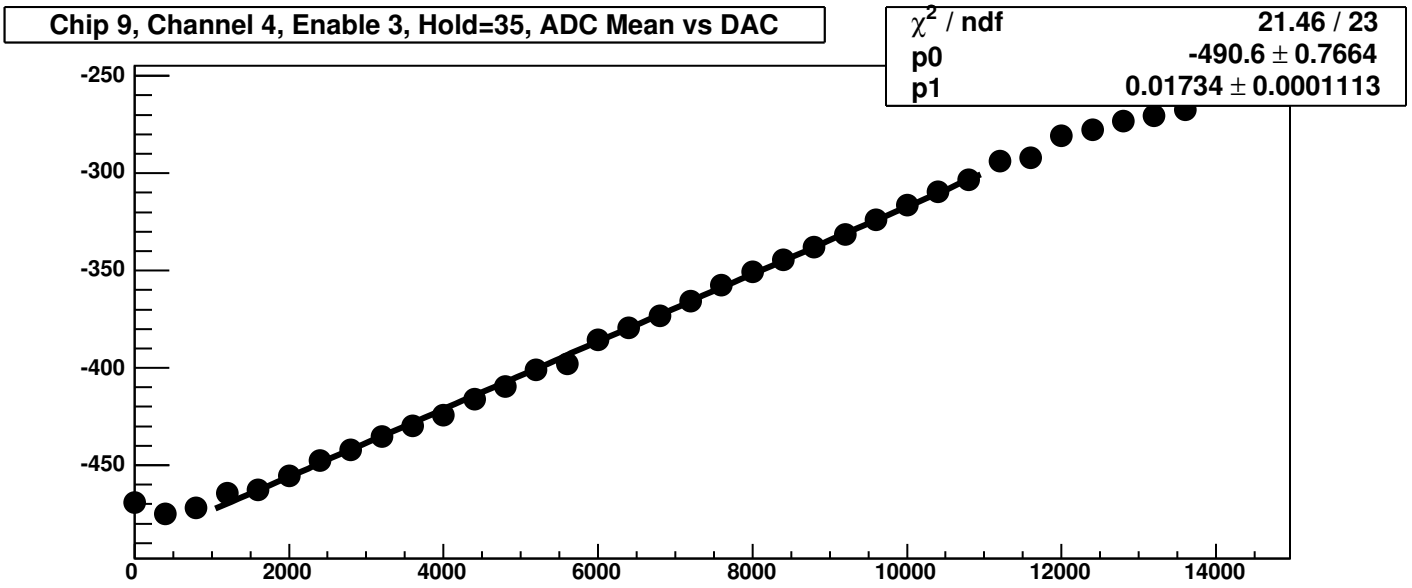

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

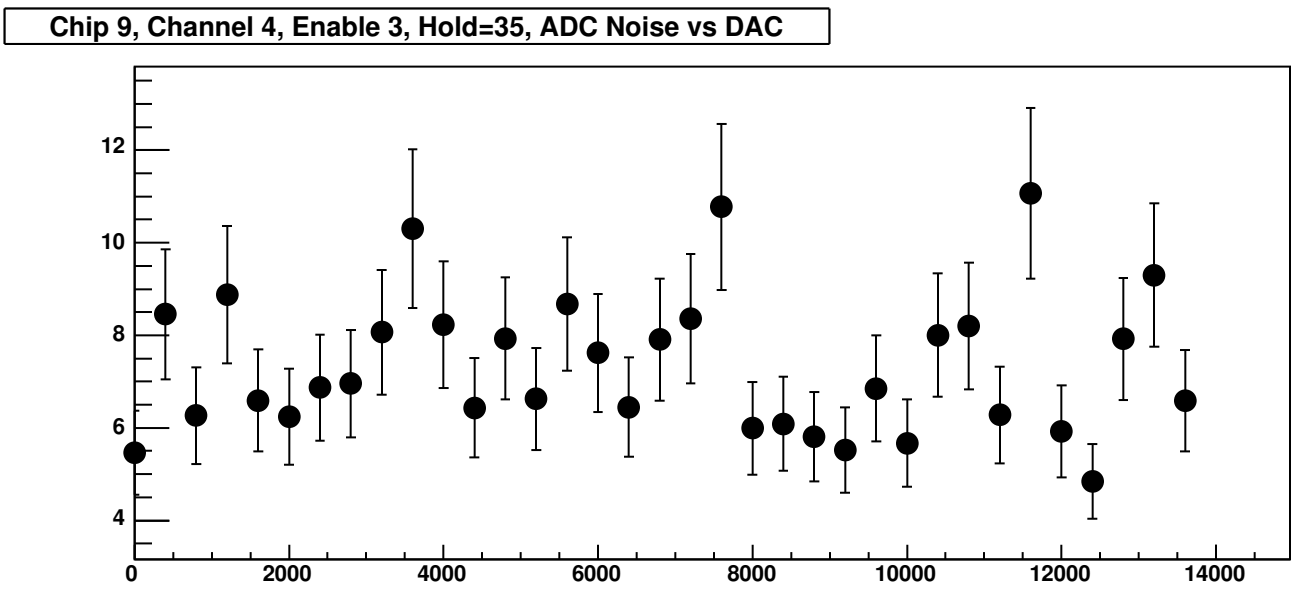

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

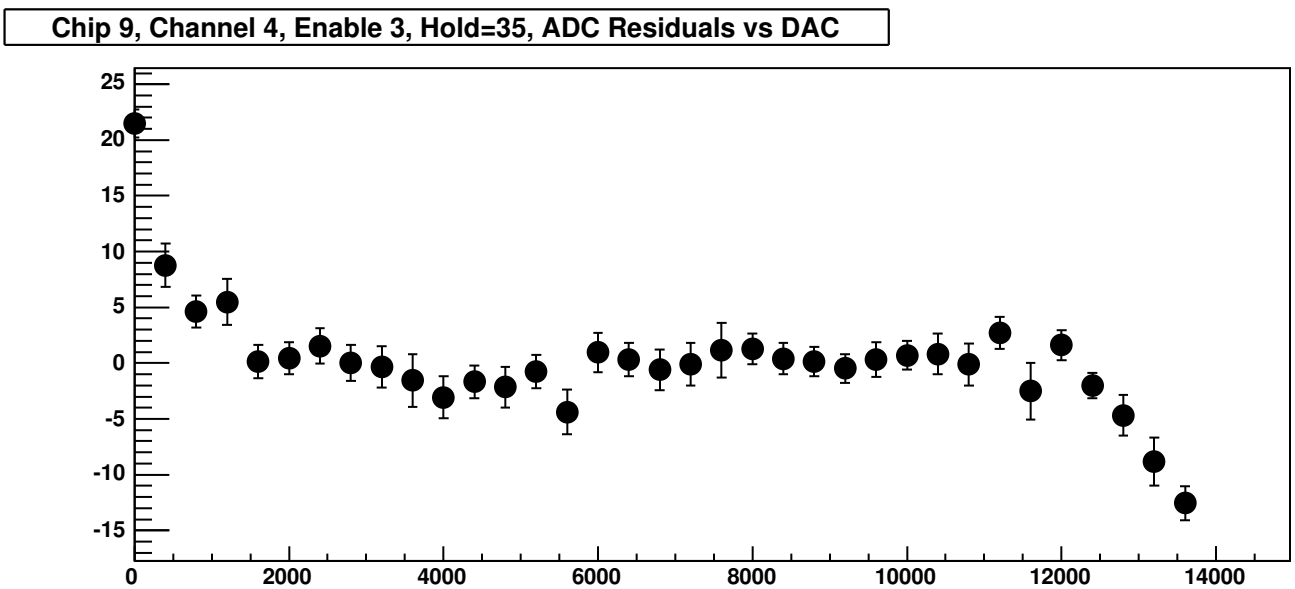

Chip 9, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.26 / 23
p0 -491 ± 0.7302
p1 0.0145 ± 0.0001174
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

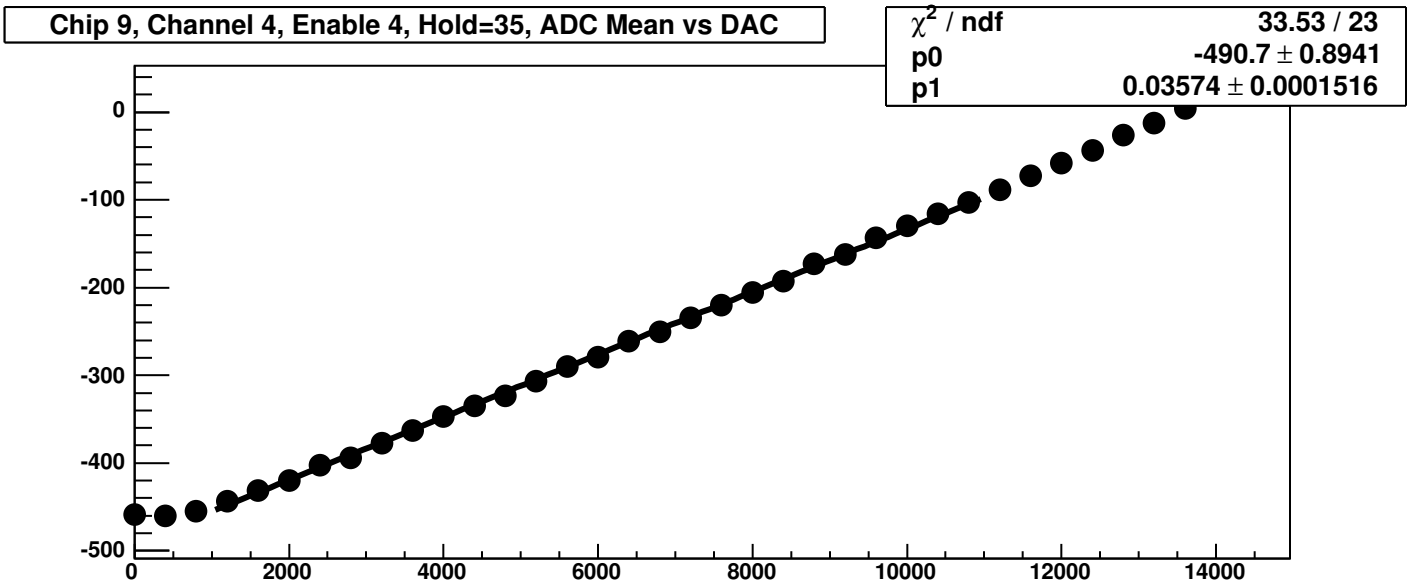

Chip 9, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

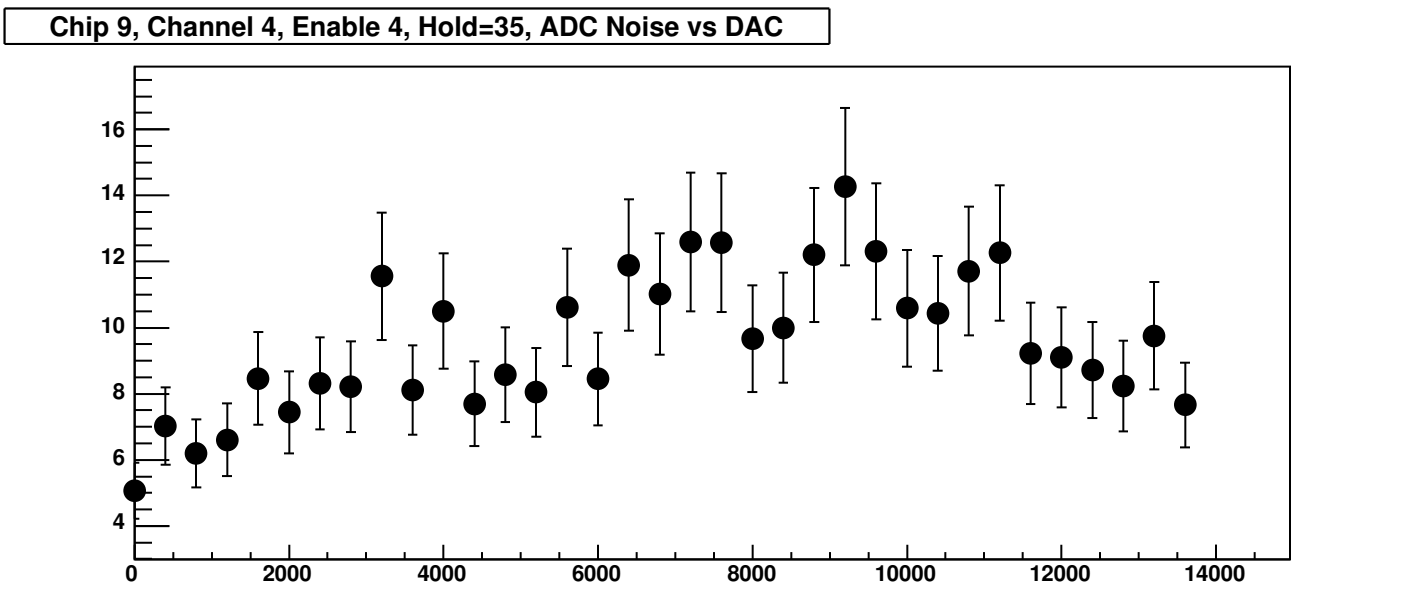

Chip 9, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

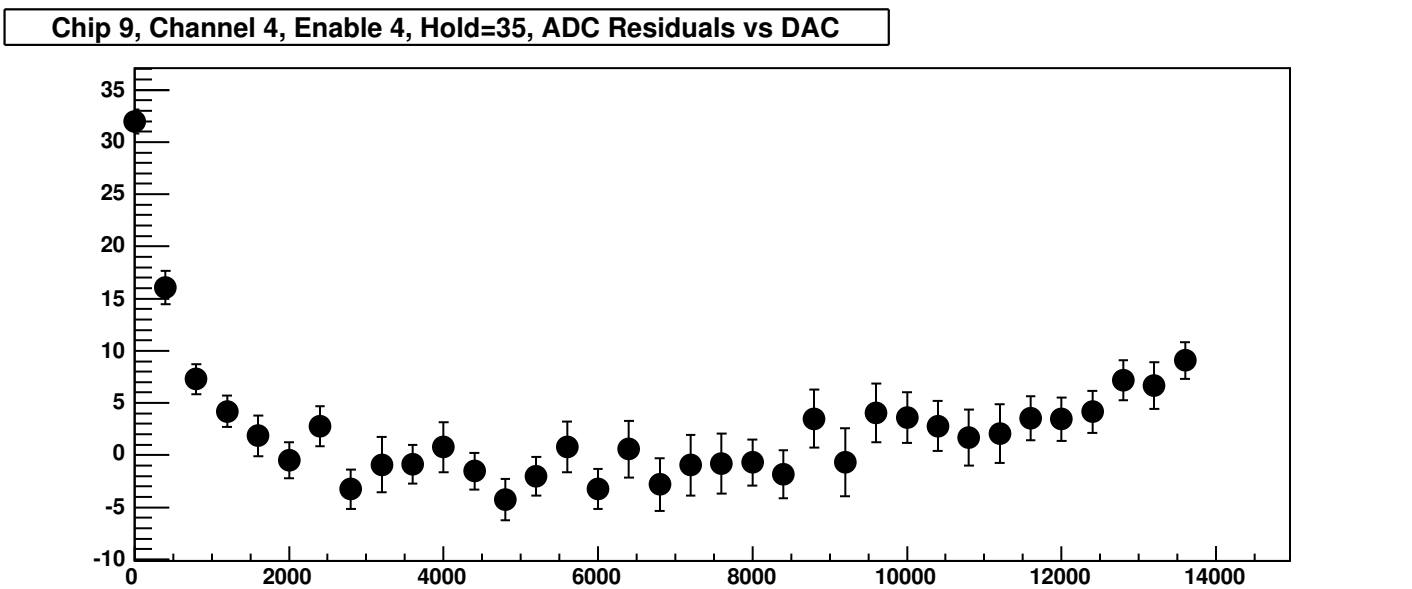

Chip 9, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.46 / 23
p0 -490.6 ± 0.7664
p1 0.01734 ± 0.0001113
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

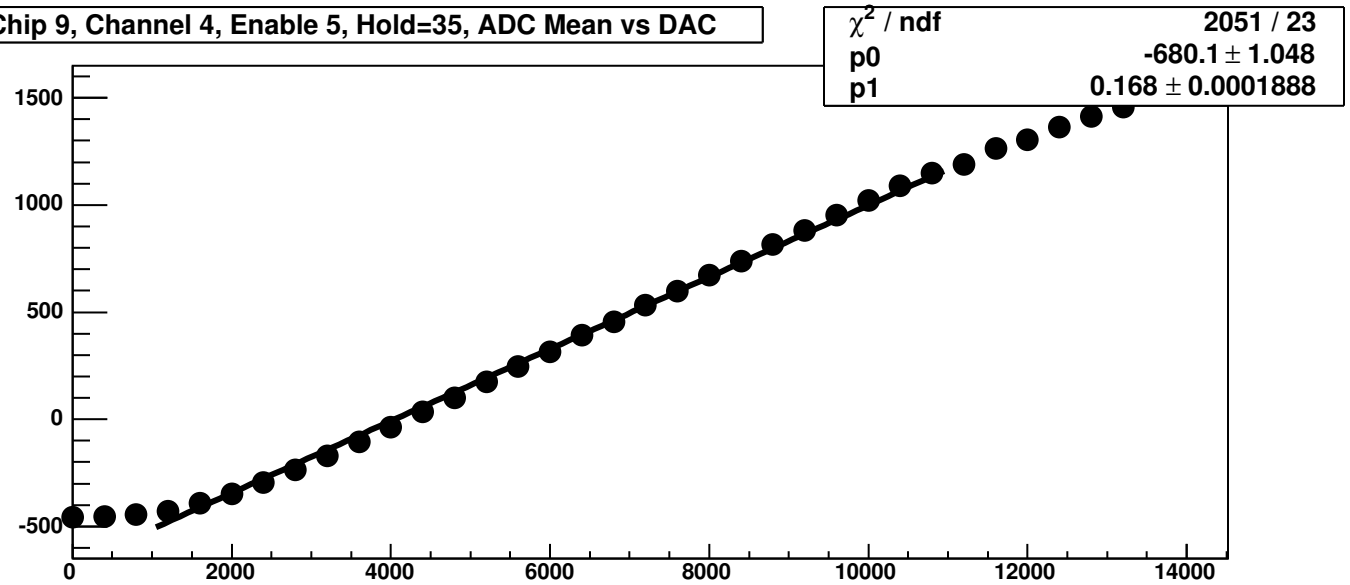

Chip 9, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.53 / 23
p0 -490.7 ± 0.8941
p1 0.03574 ± 0.0001516
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

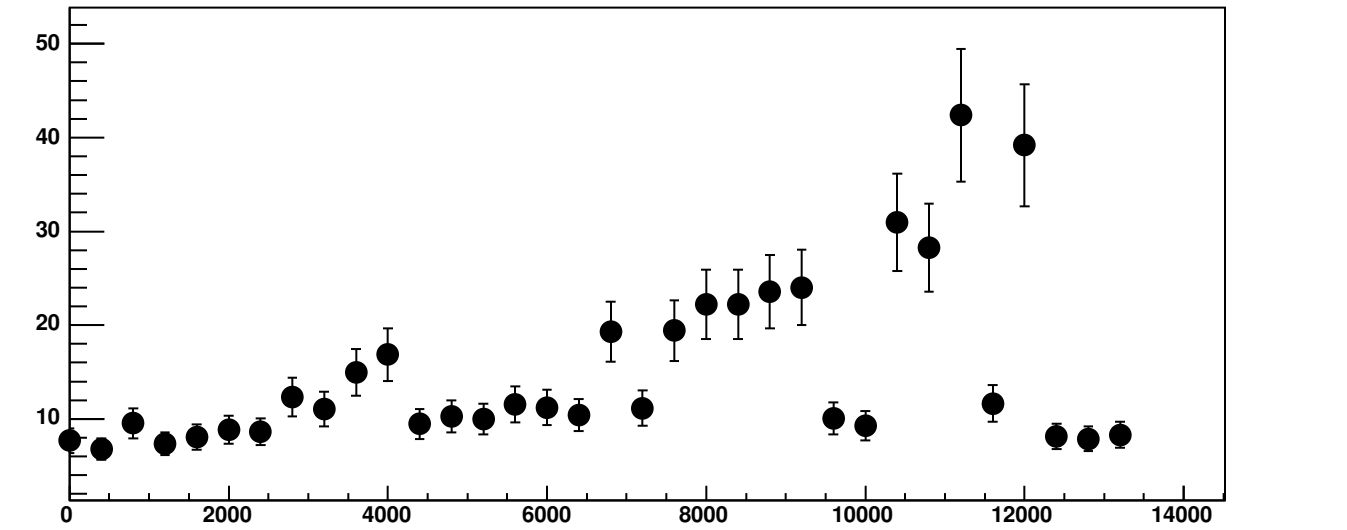

Chip 9, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

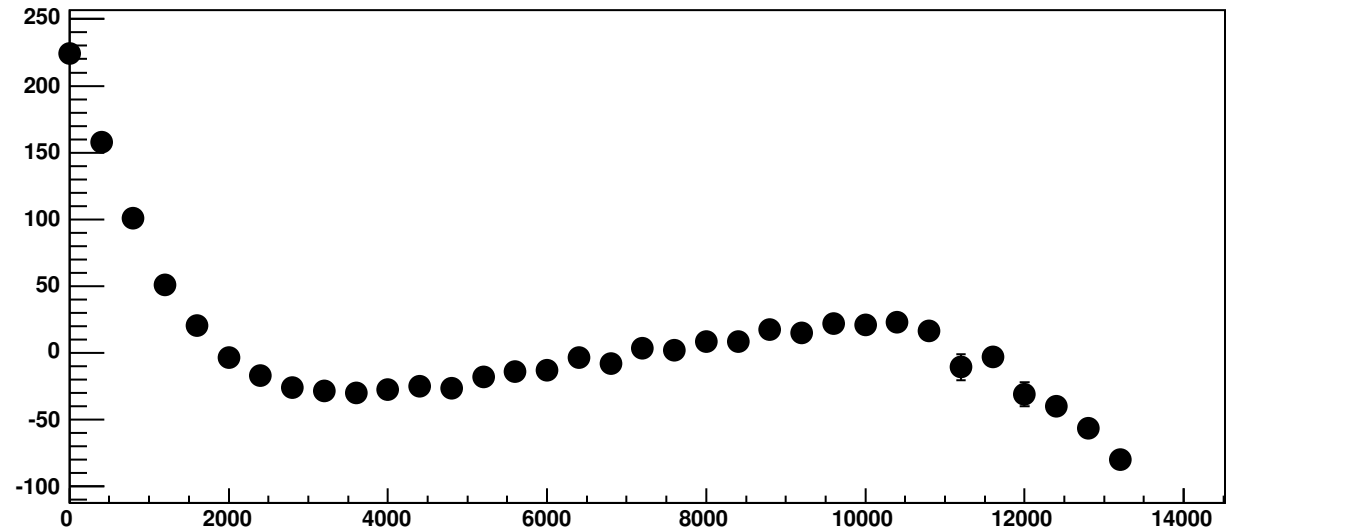
Chip 9, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

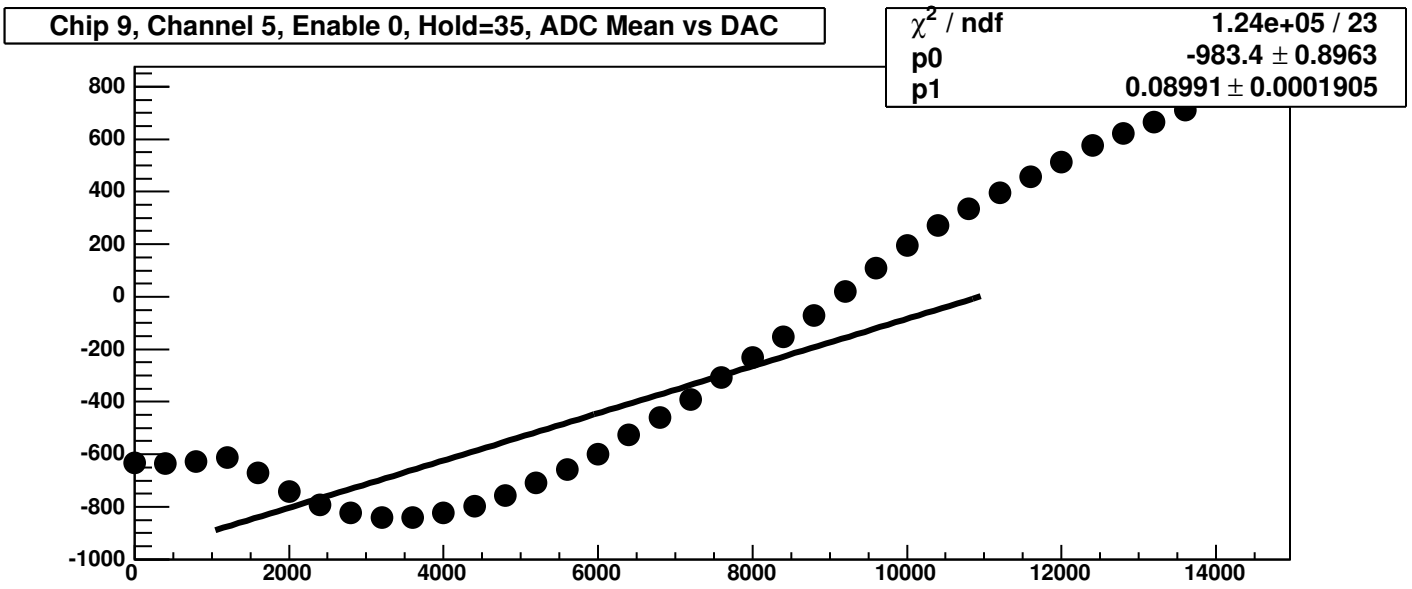


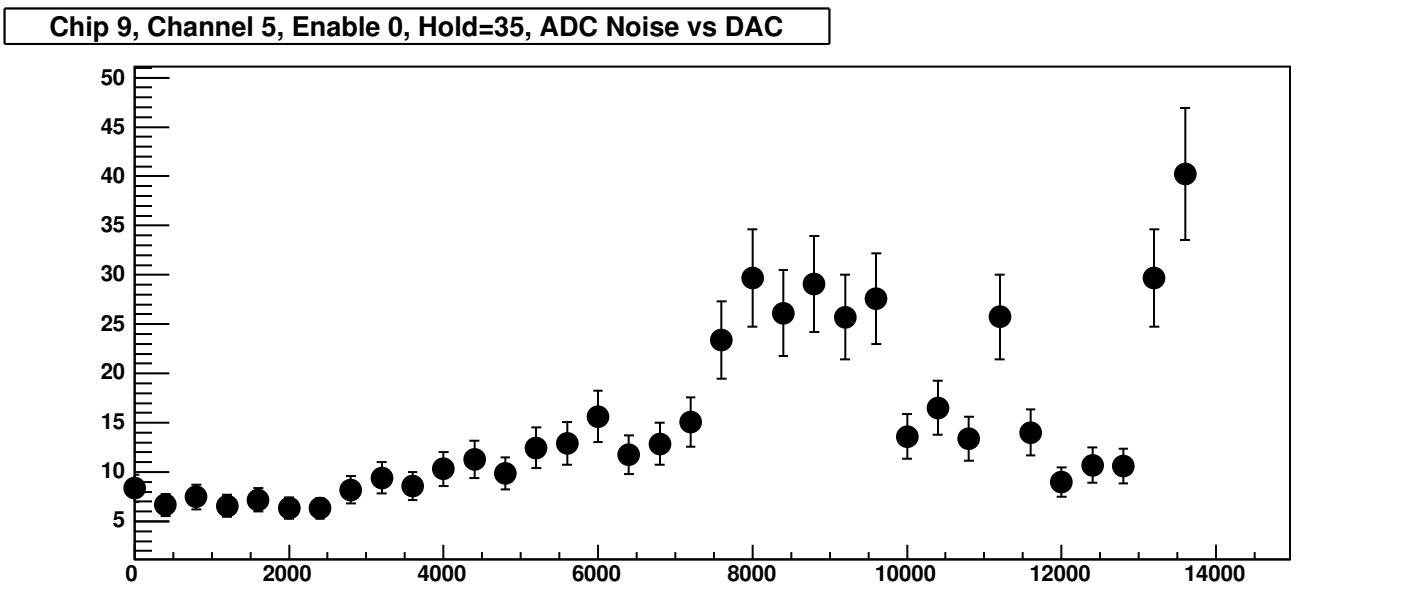
Chip 9, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

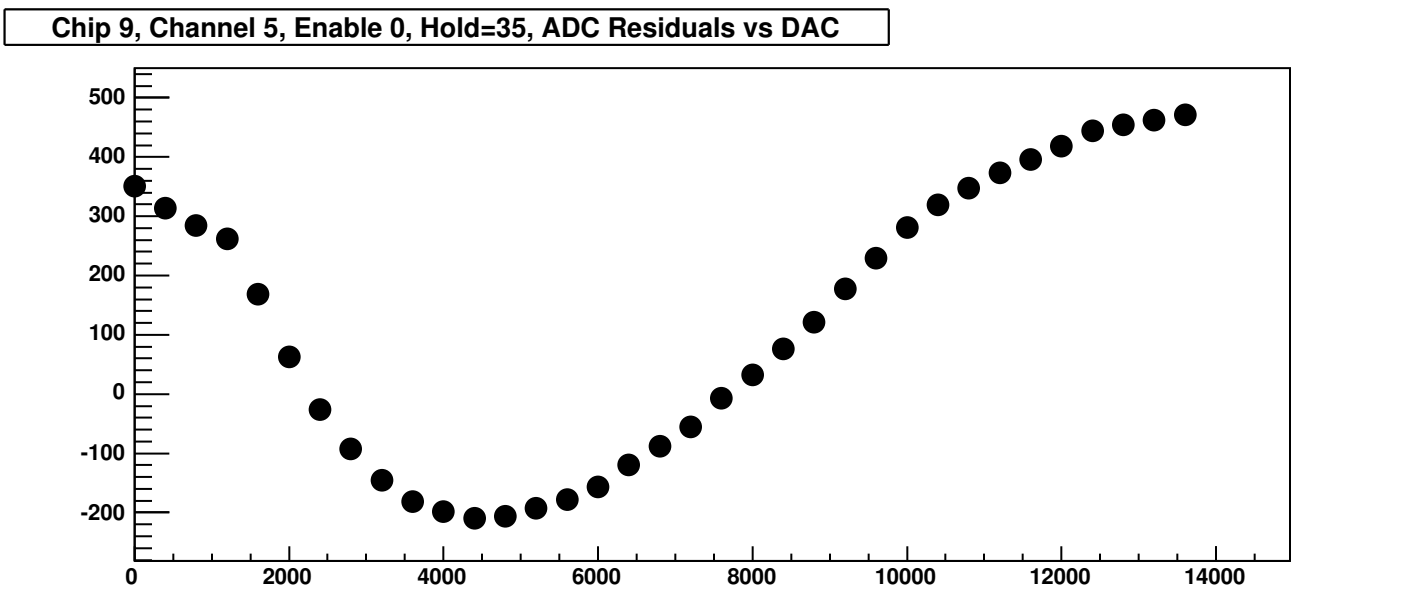


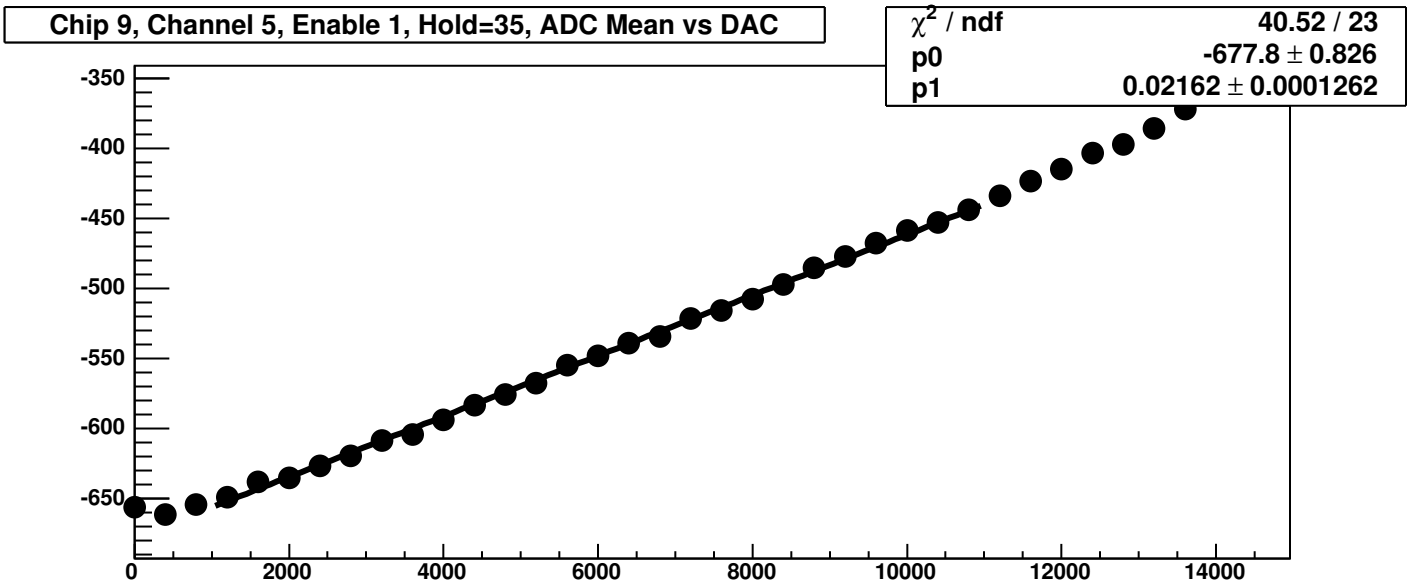
Chip 9, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

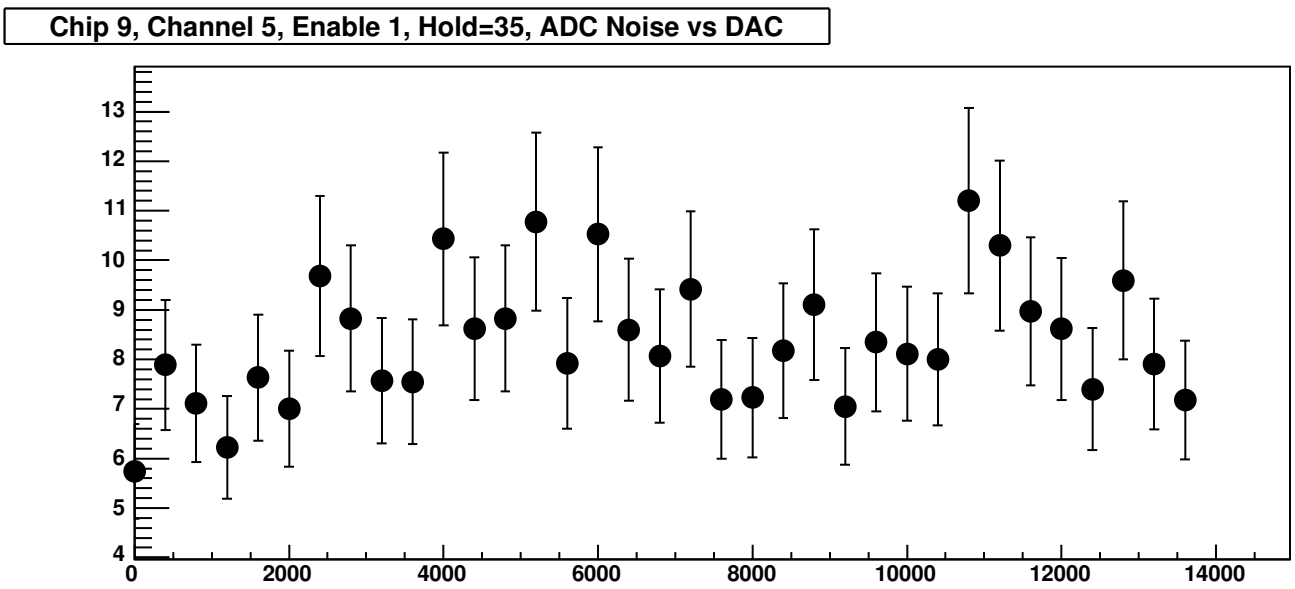


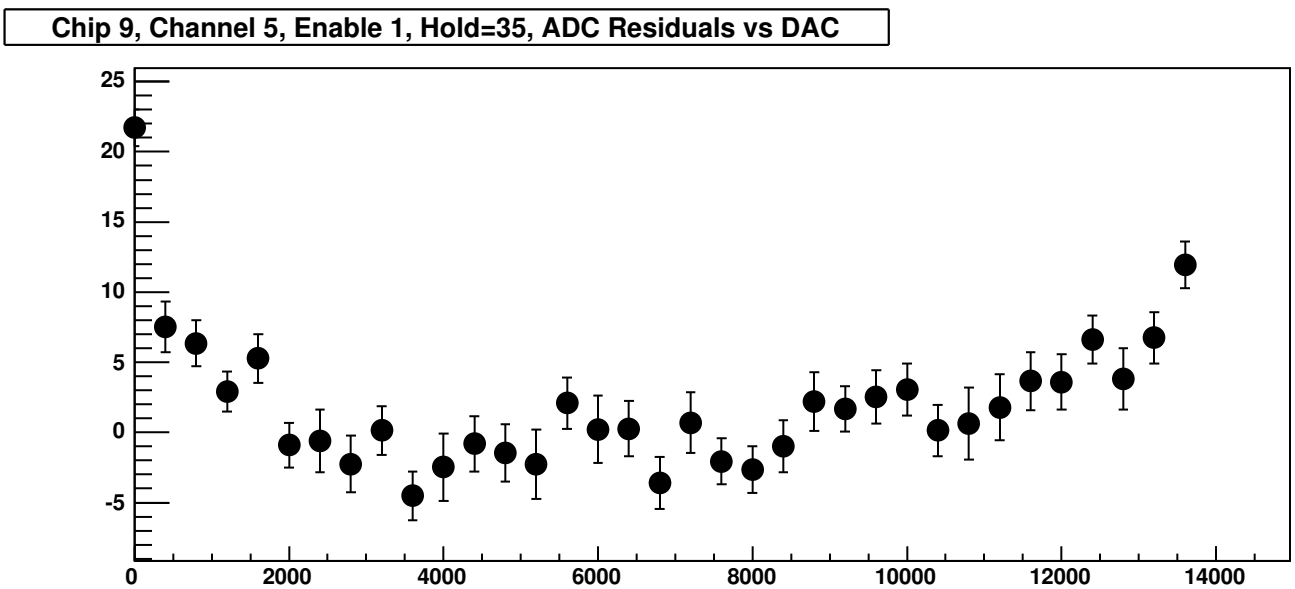

Chip 9, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.24e+05 / 23
p0 -983.4 ± 0.8963
p1 0.08991 ± 0.0001905
800
600
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

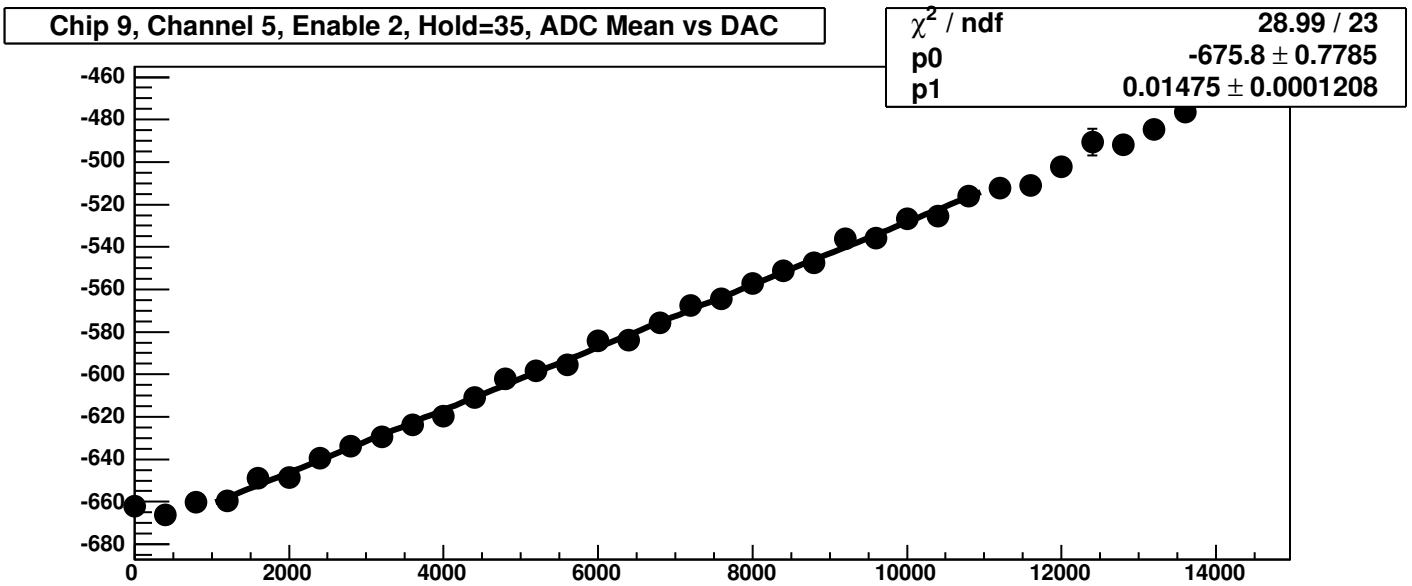

Chip 9, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

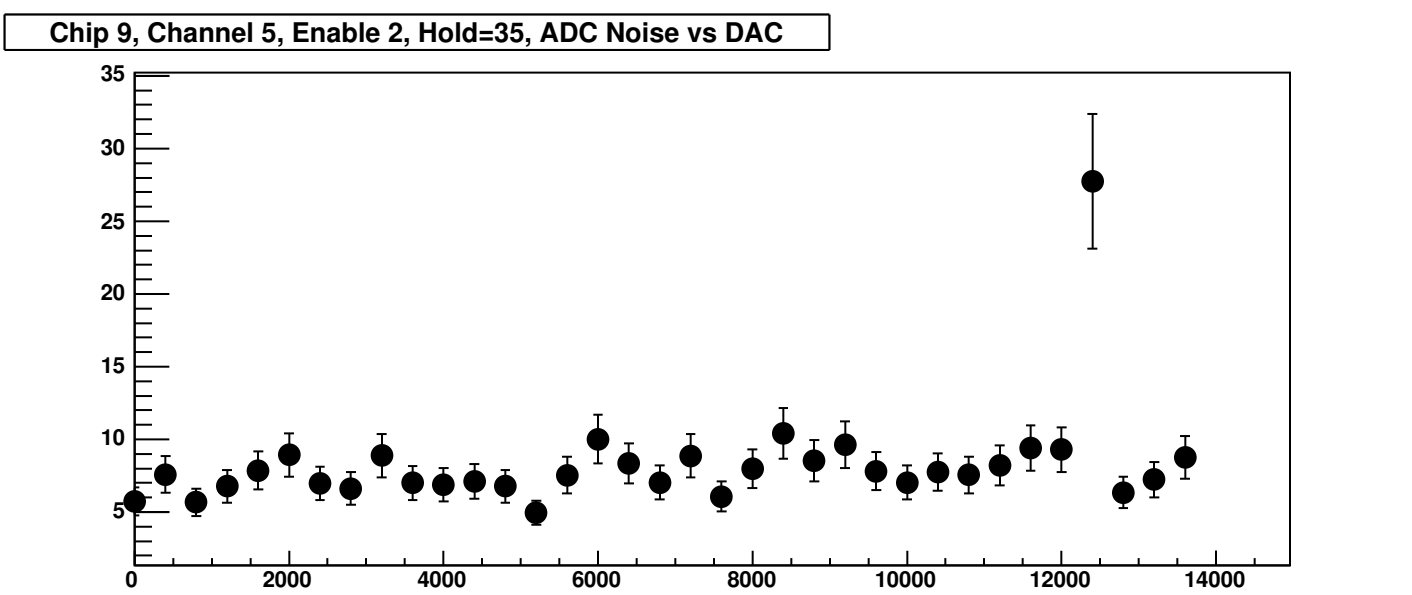

Chip 9, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

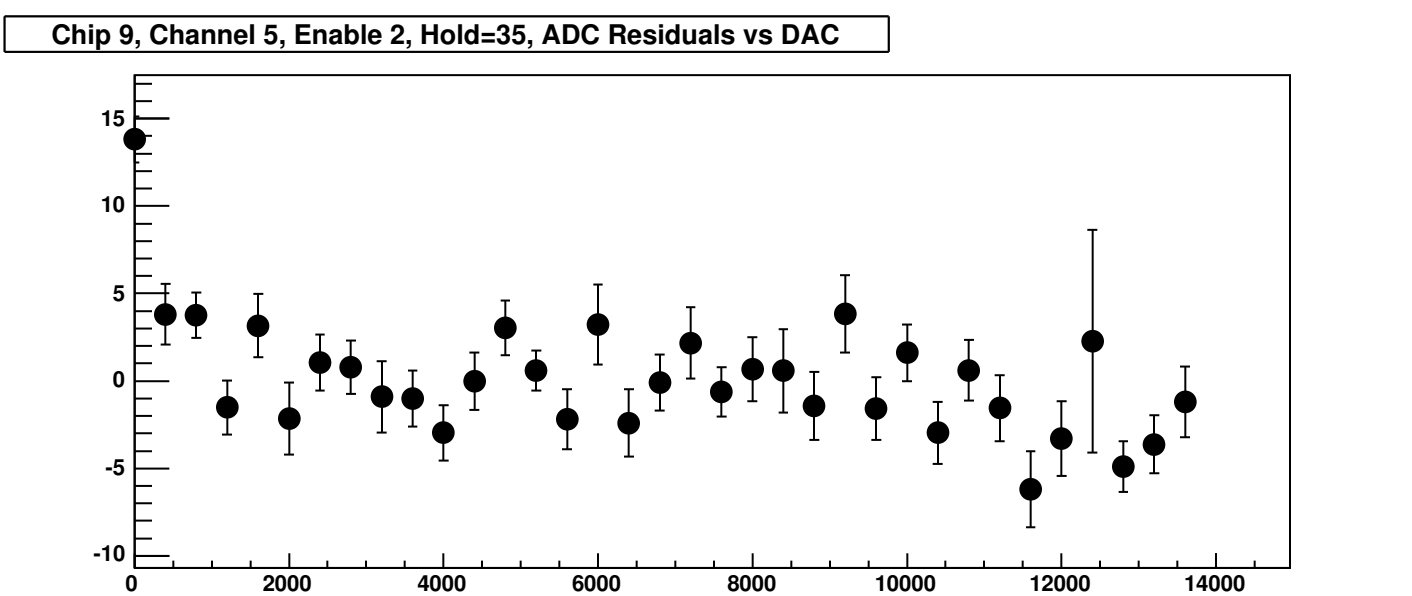

Chip 9, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.52 / 23
p0 -677.8 ± 0.826
p1 0.02162 ± 0.0001262
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

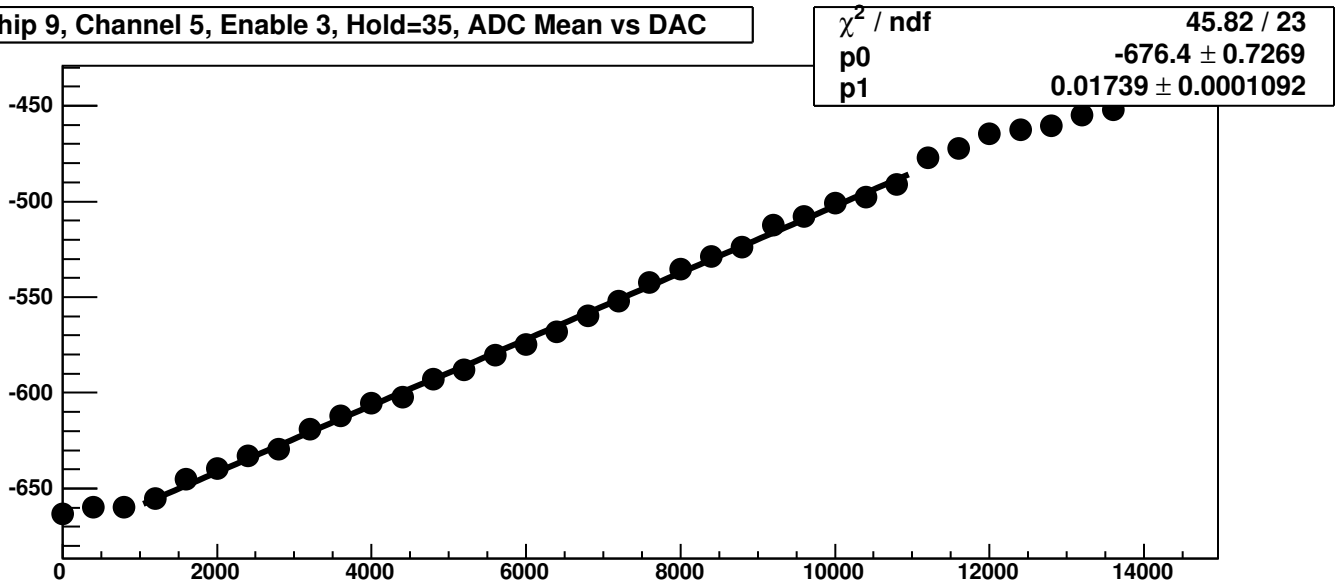

Chip 9, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.99 / 23
p0 -675.8 ± 0.7785
p1 0.01475 ± 0.0001208
-460
-480
-500
-520
-540
-560
-580
-600
-620
-640
-660
-680
0
2000
4000
6000
8000
10000
12000
14000

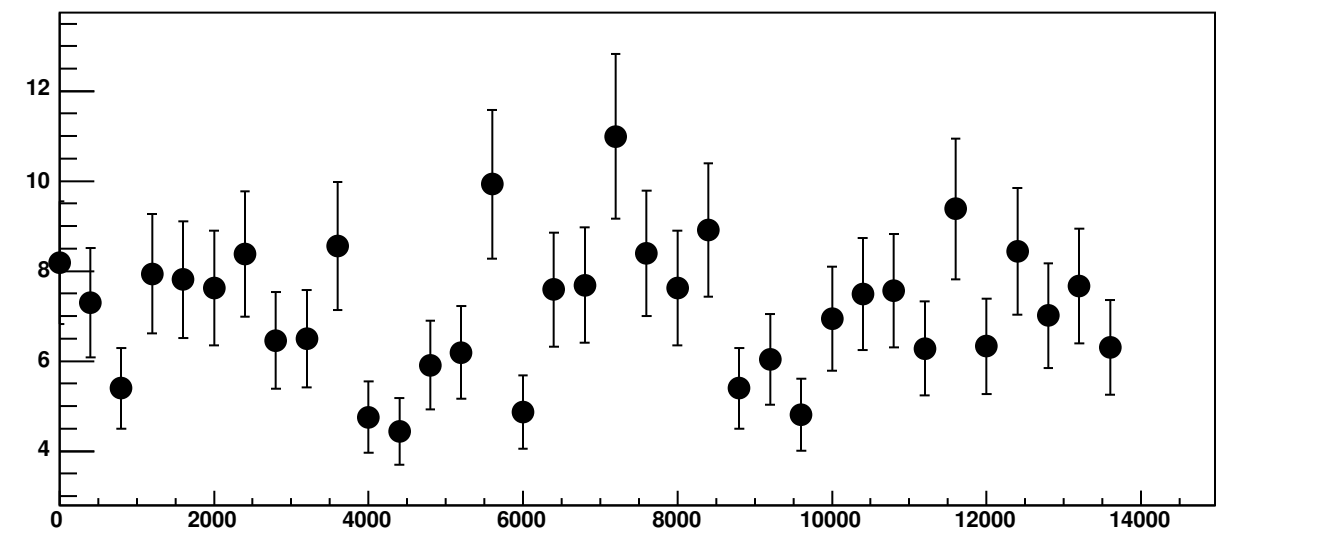

Chip 9, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

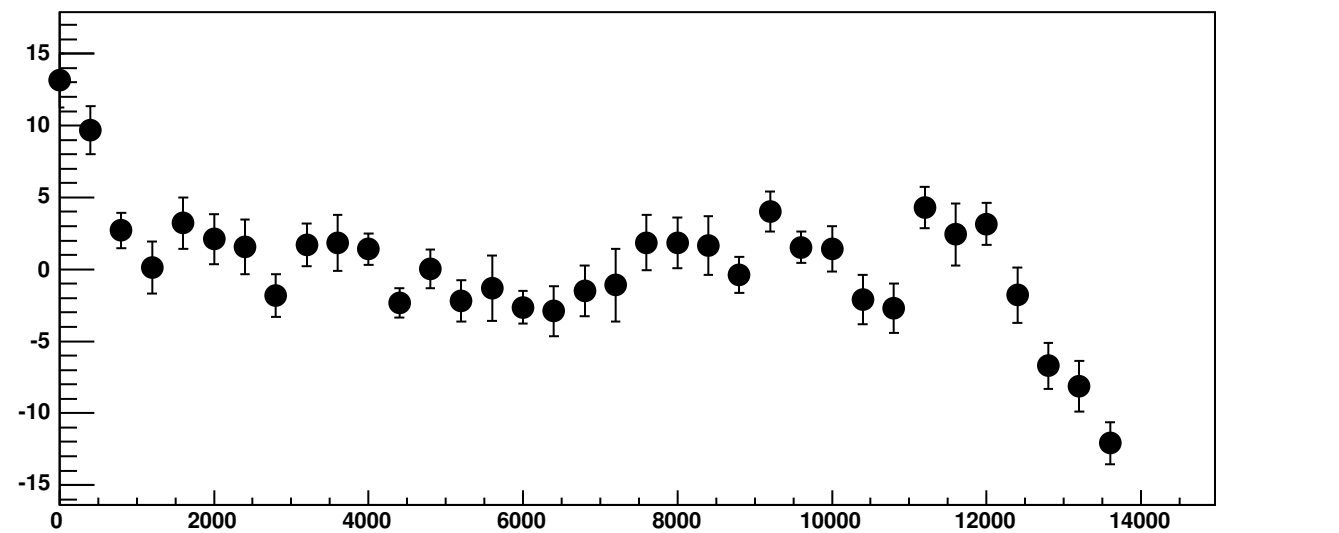
Chip 9, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

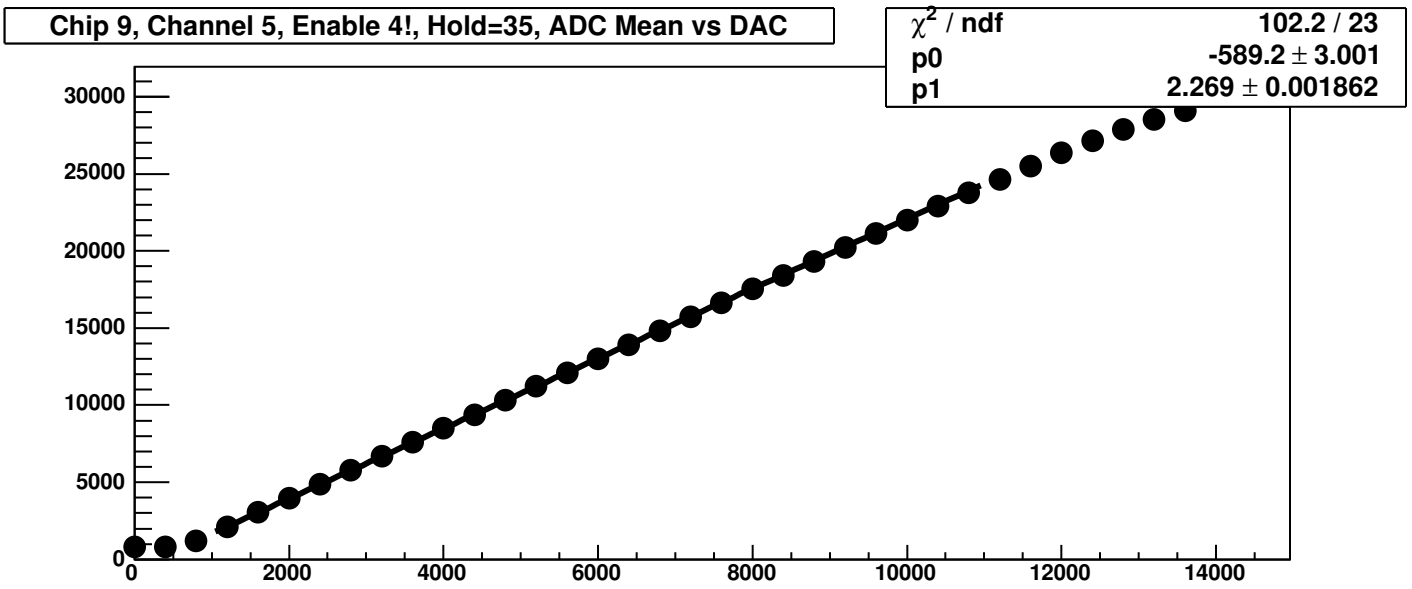


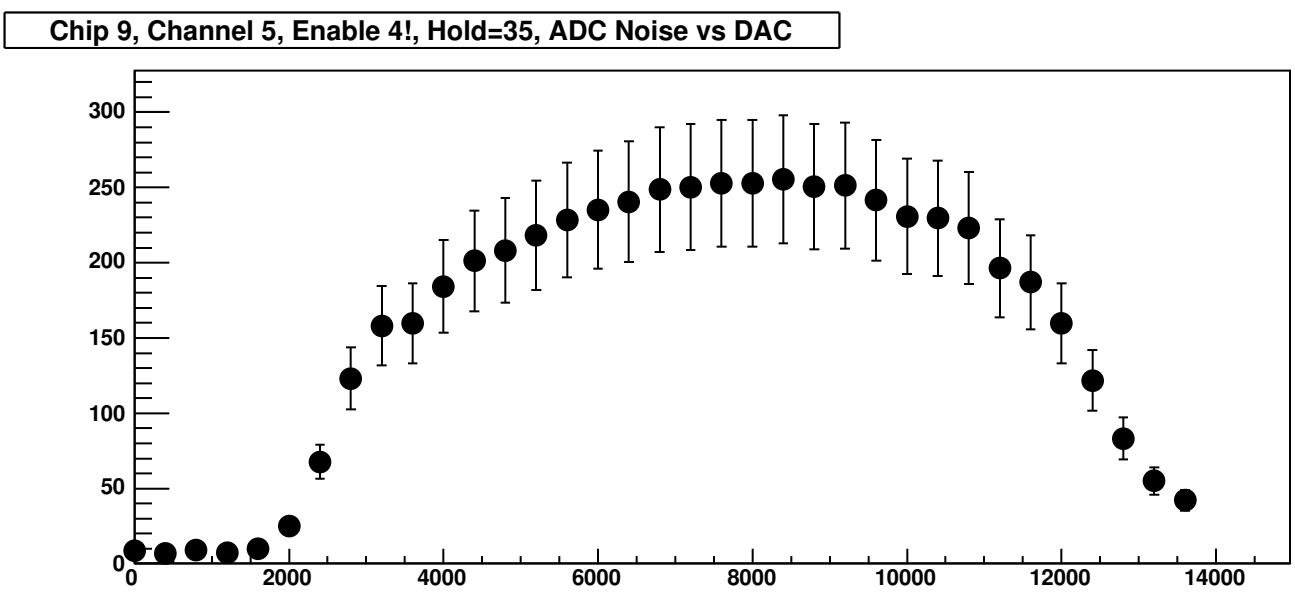
Chip 9, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

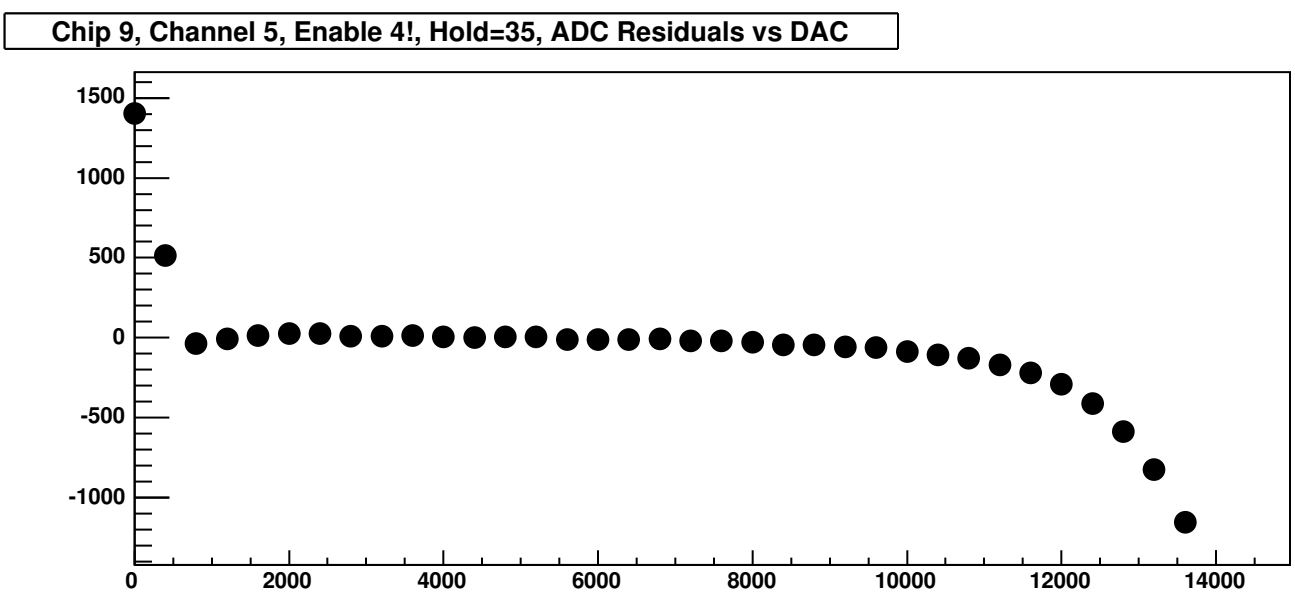


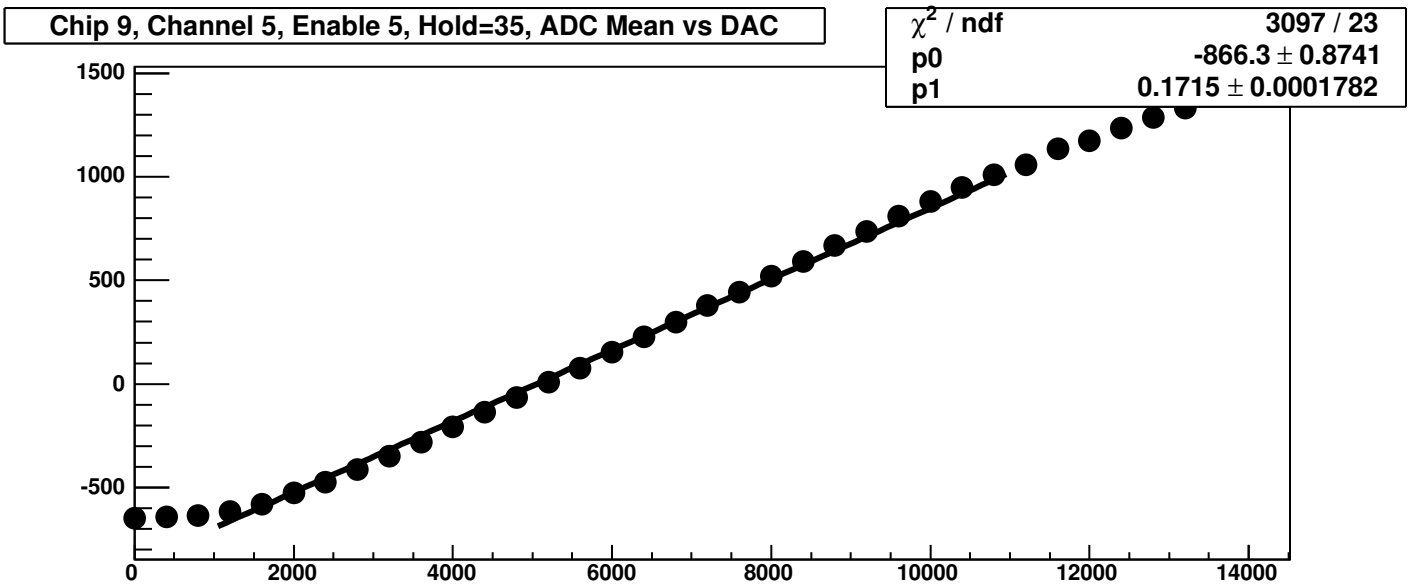
Chip 9, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

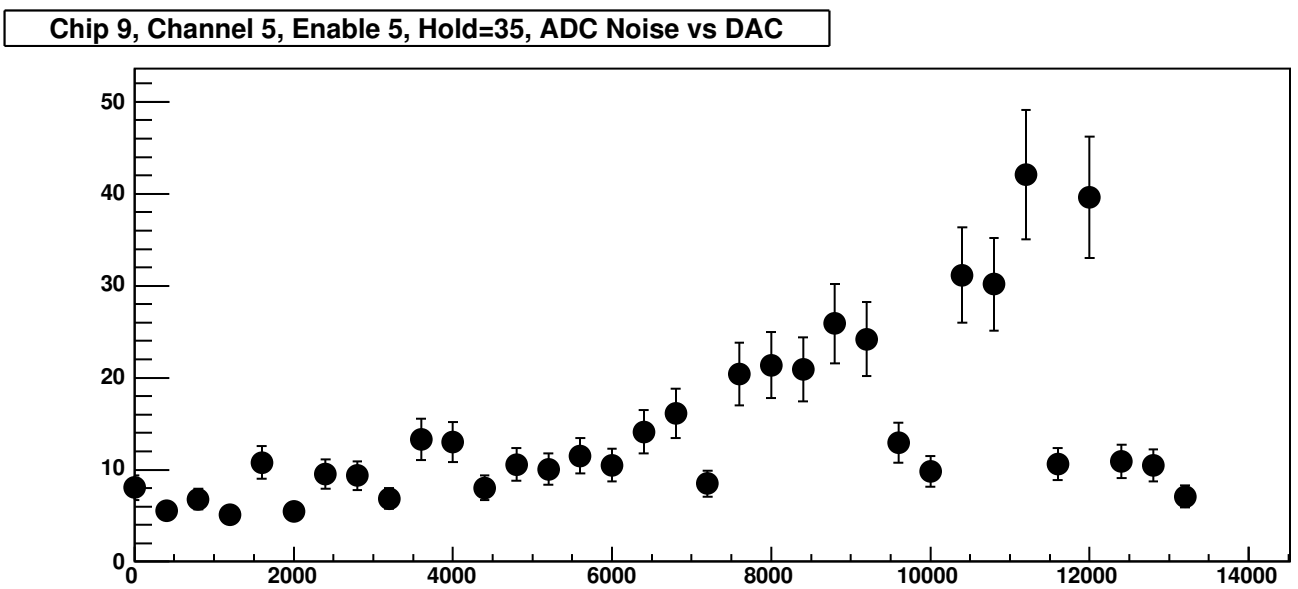


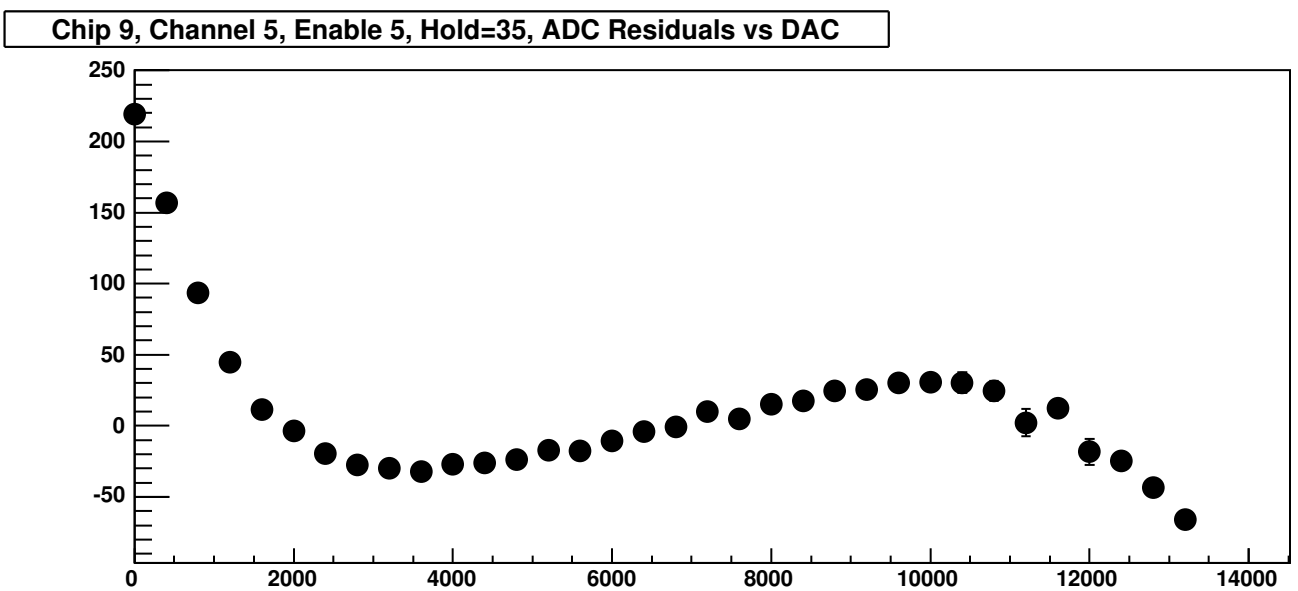

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 102.2 / 23
p0 -589.2 ± 3.001
p1 2.269 ± 0.001862
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

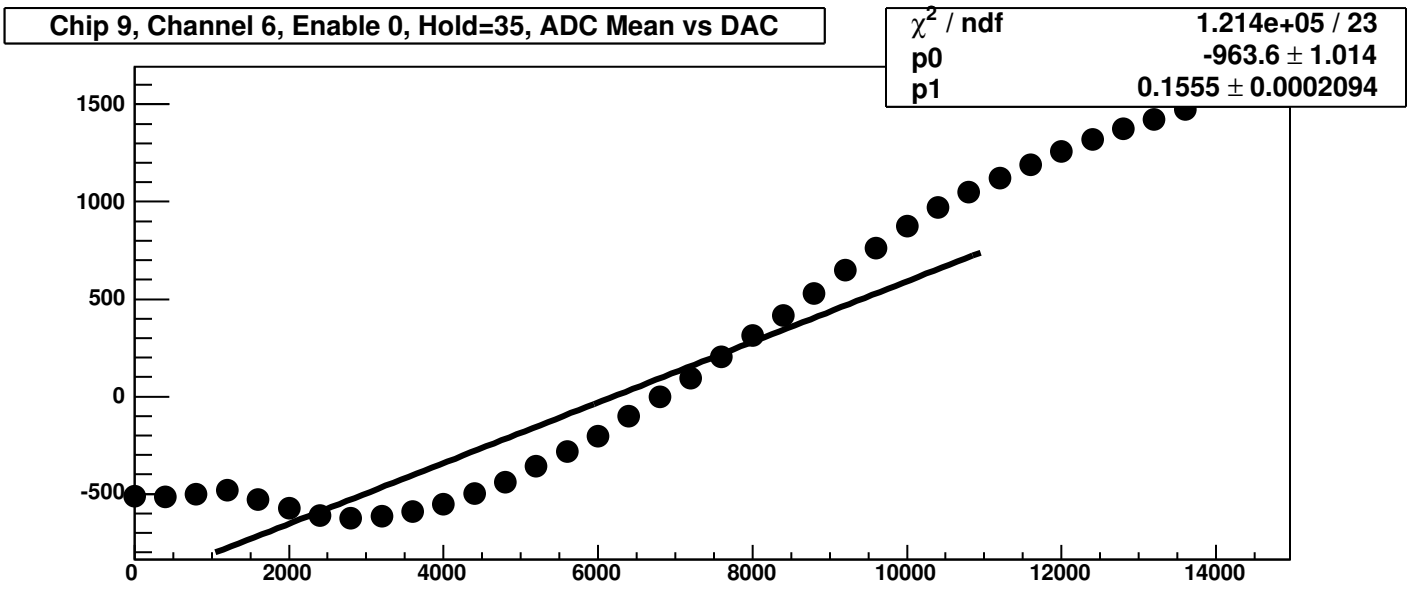

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

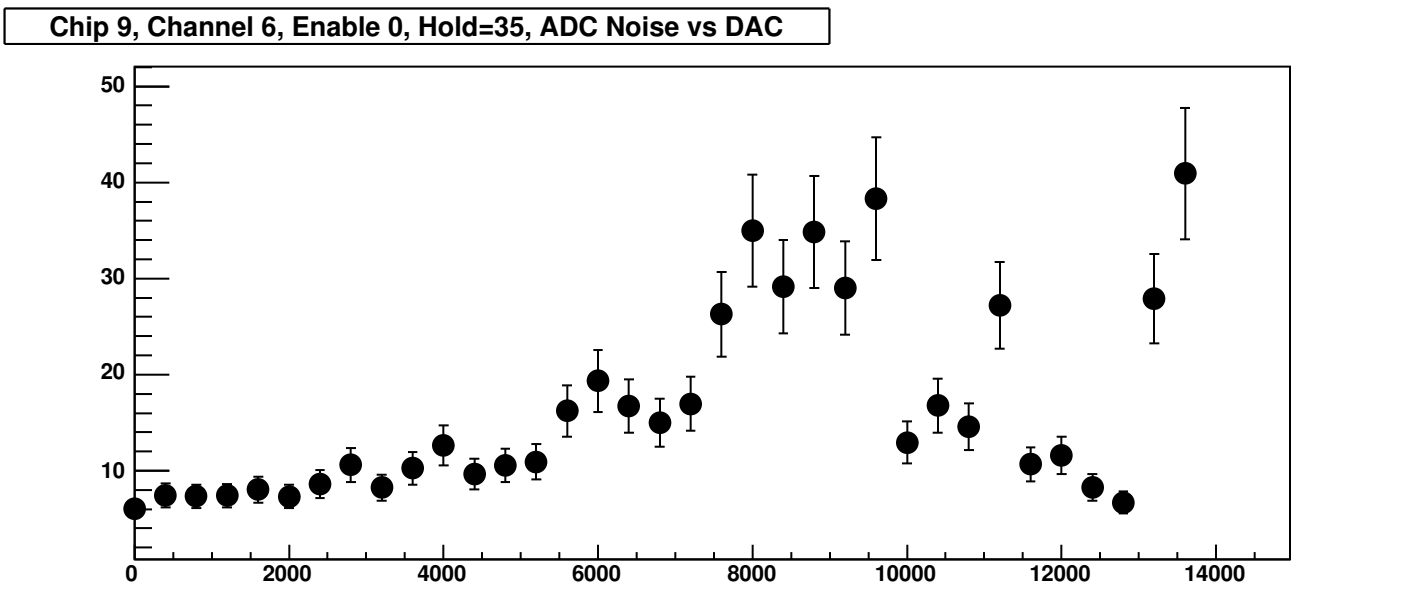

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

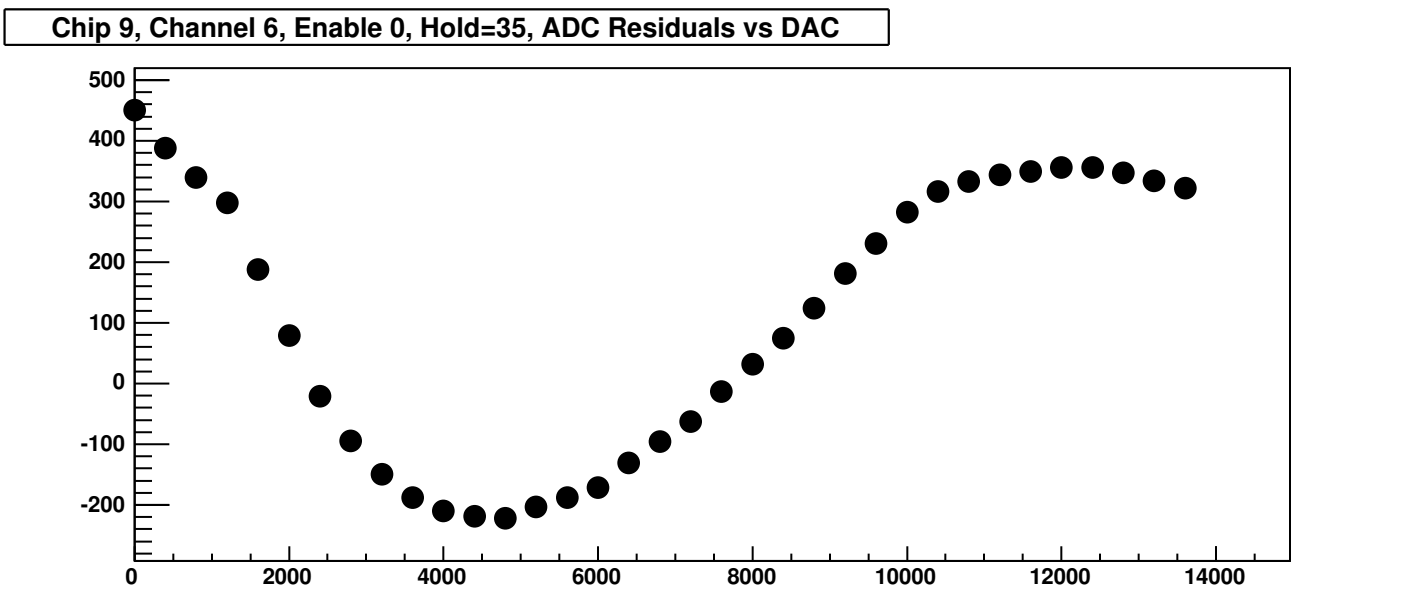

Chip 9, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3097 / 23
p0 -866.3 ± 0.8741
p1 0.1715 ± 0.0001782
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

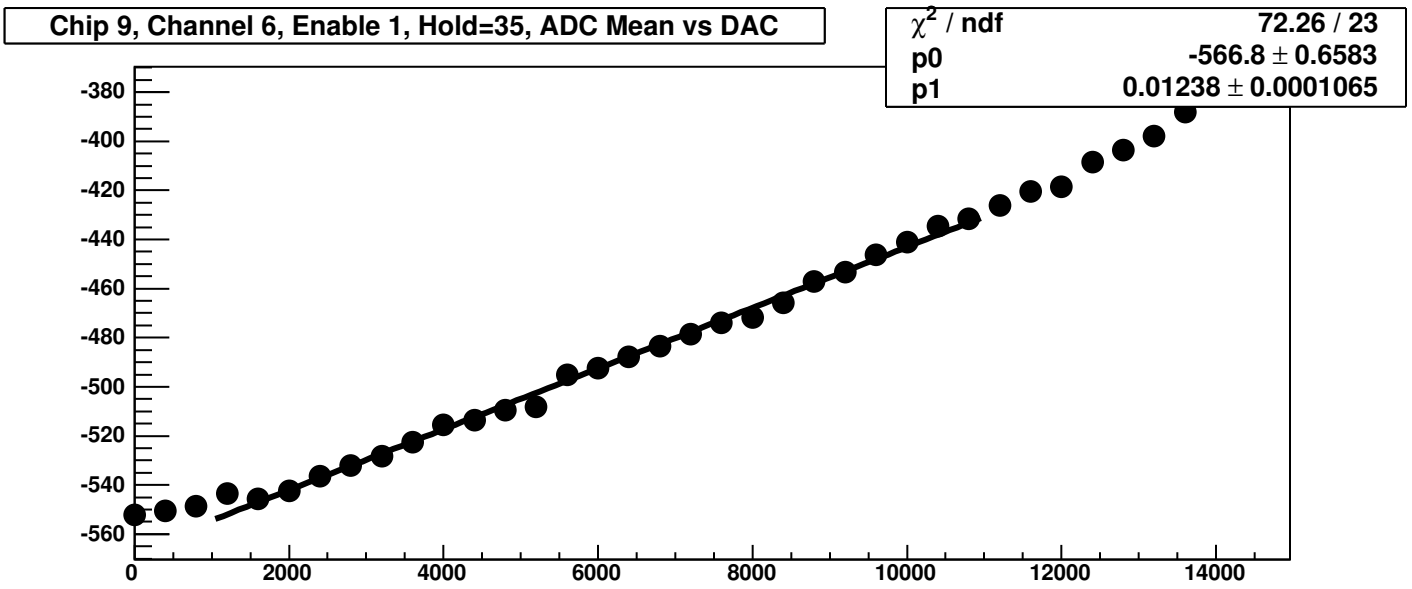

Chip 9, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

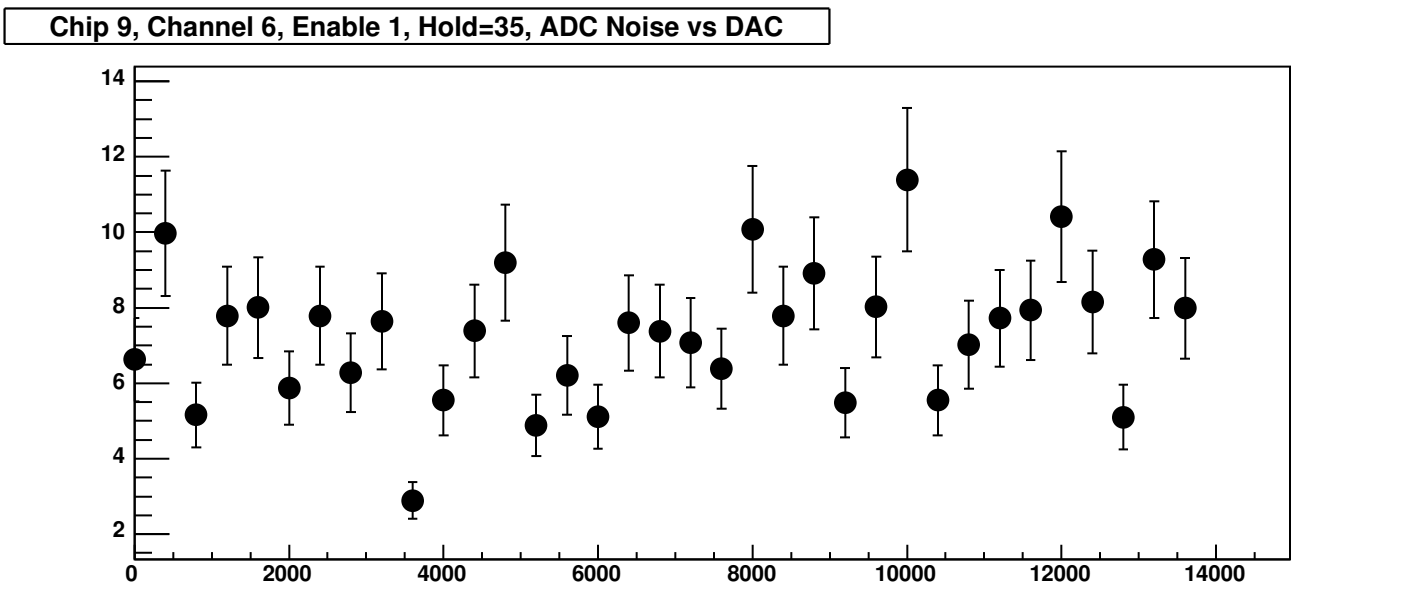

Chip 9, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

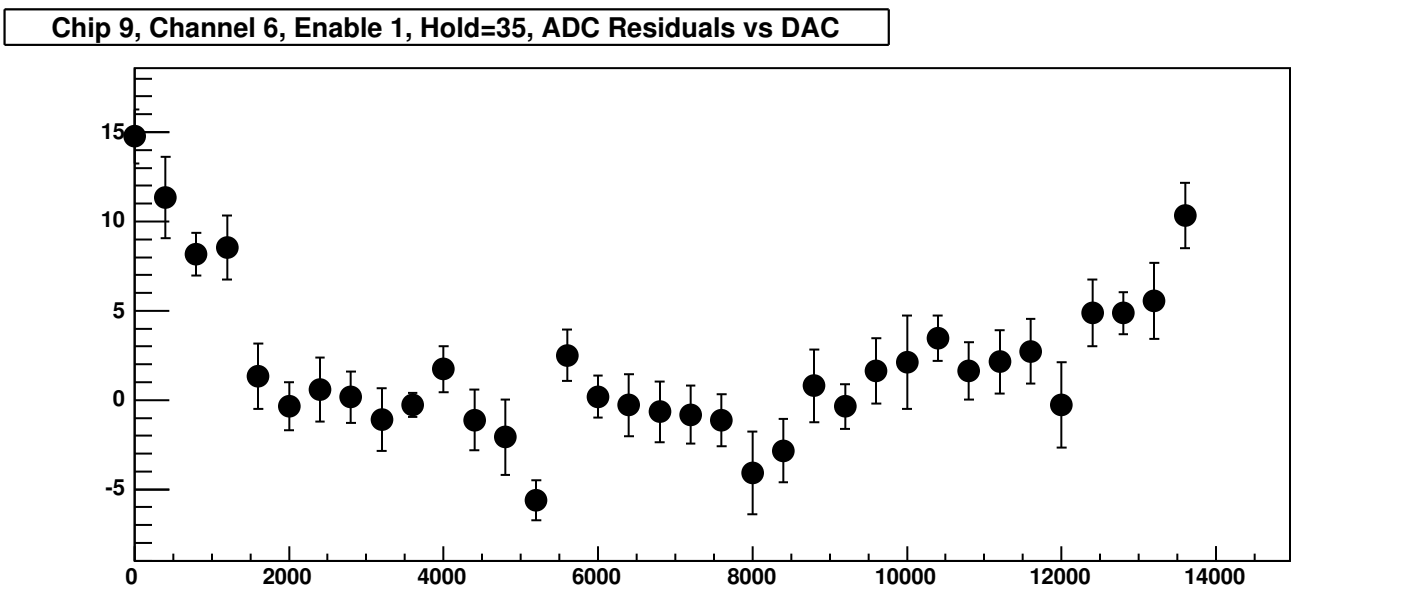

Chip 9, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.214e+05 / 23
p0 -963.6 ± 1.014
p1 0.1555 ± 0.0002094
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

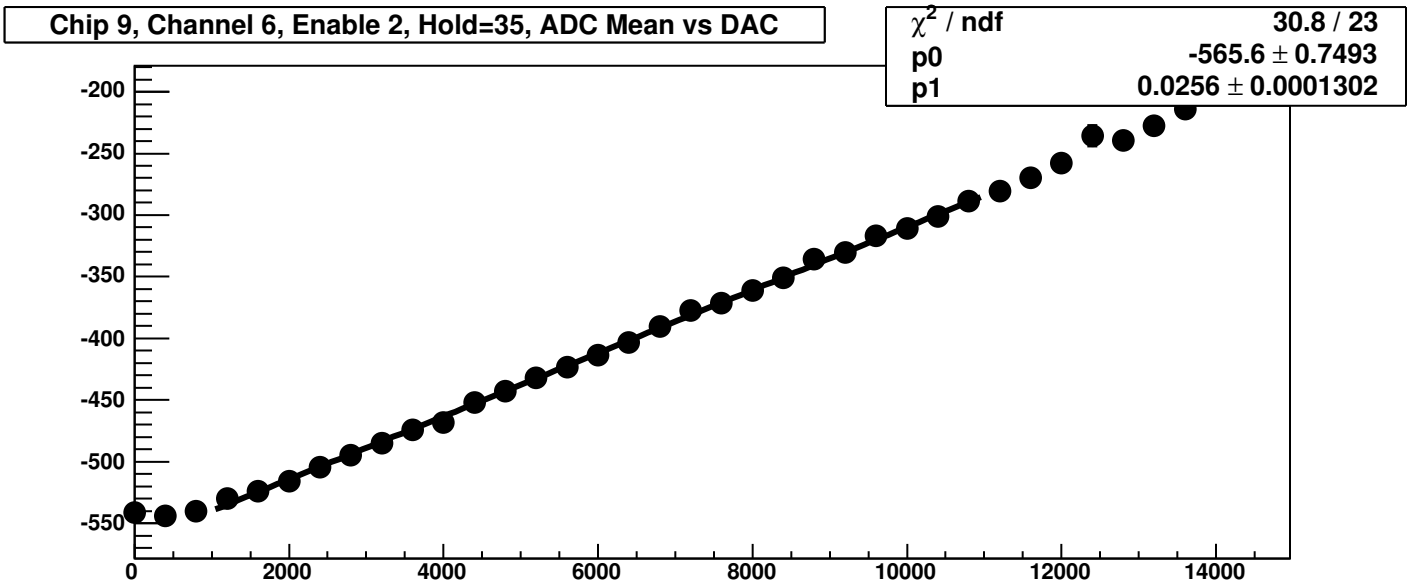

Chip 9, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

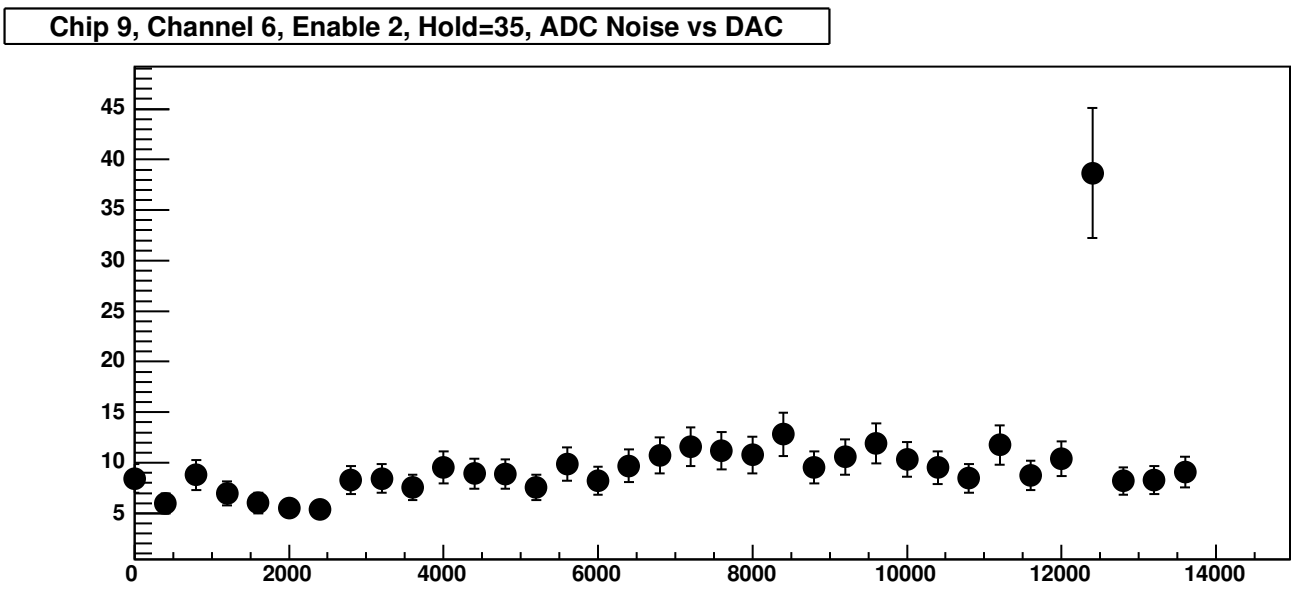

Chip 9, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

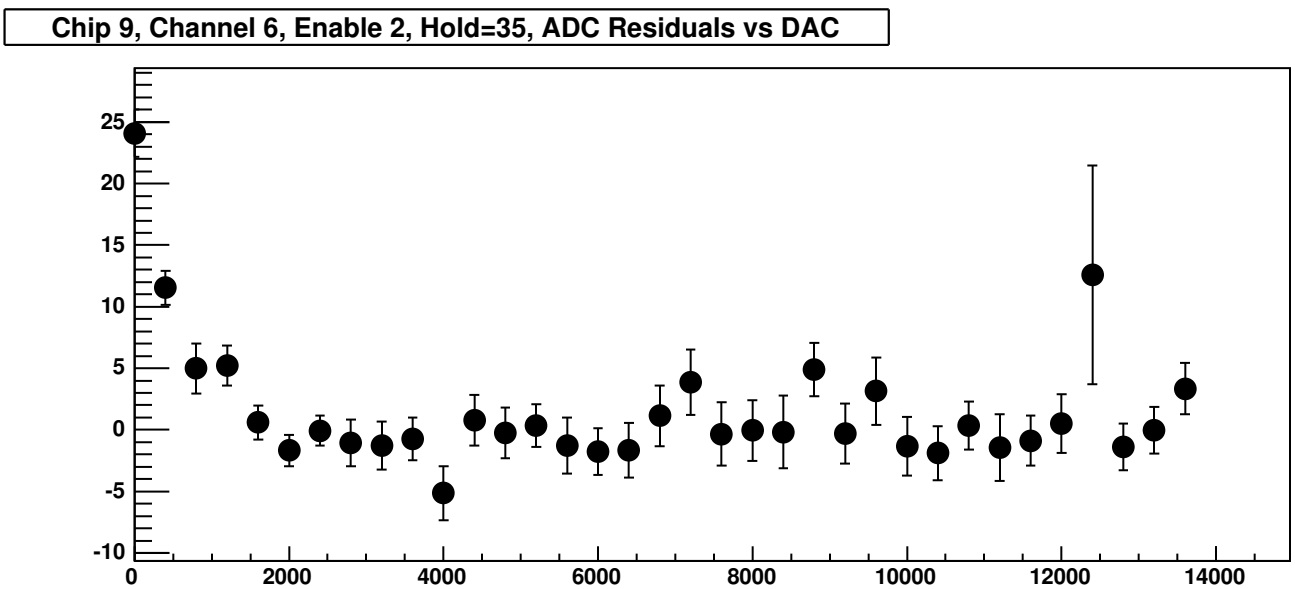

Chip 9, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 72.26 / 23
p0 -566.8 ± 0.6583
p1 0.01238 ± 0.0001065
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

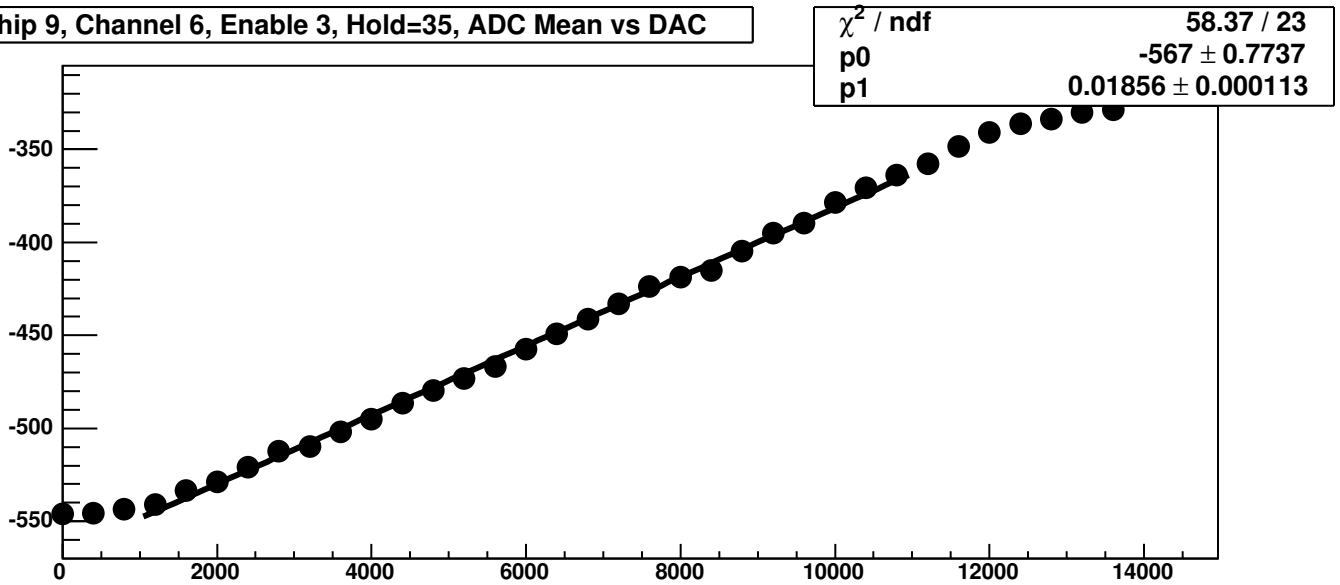

Chip 9, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.8 / 23
p0 -565.6 ± 0.7493
p1 0.0256 ± 0.0001302
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

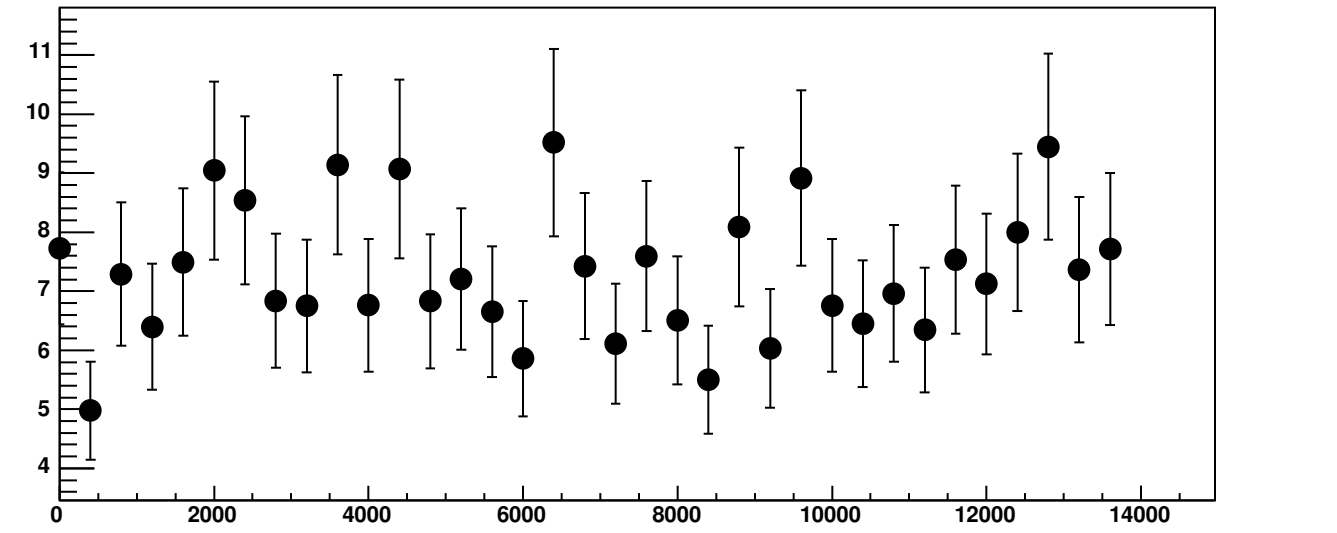

Chip 9, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

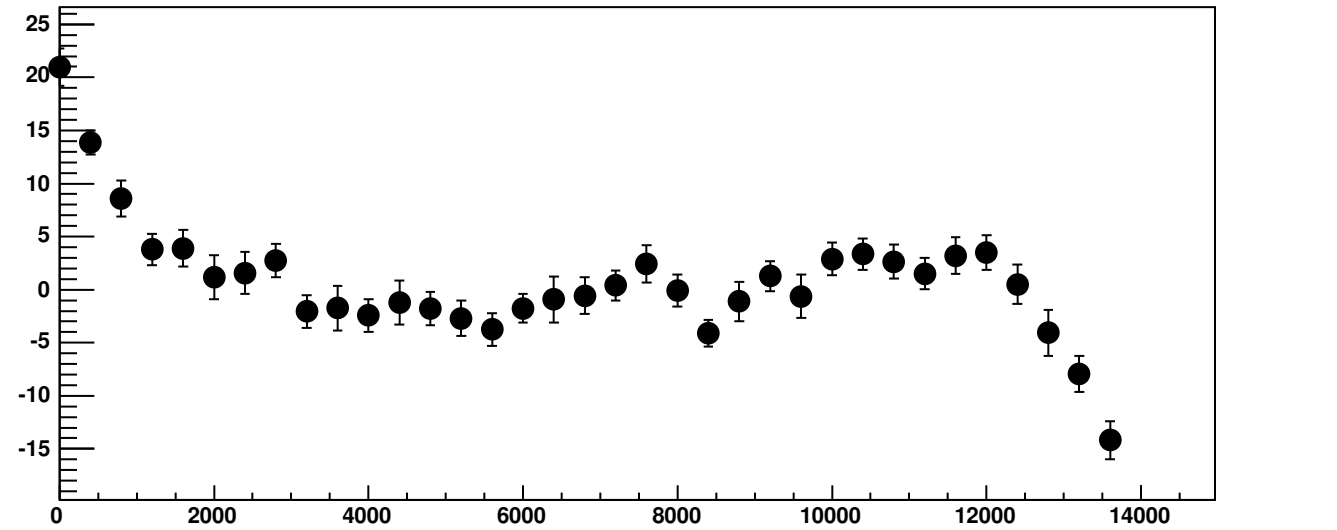
Chip 9, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

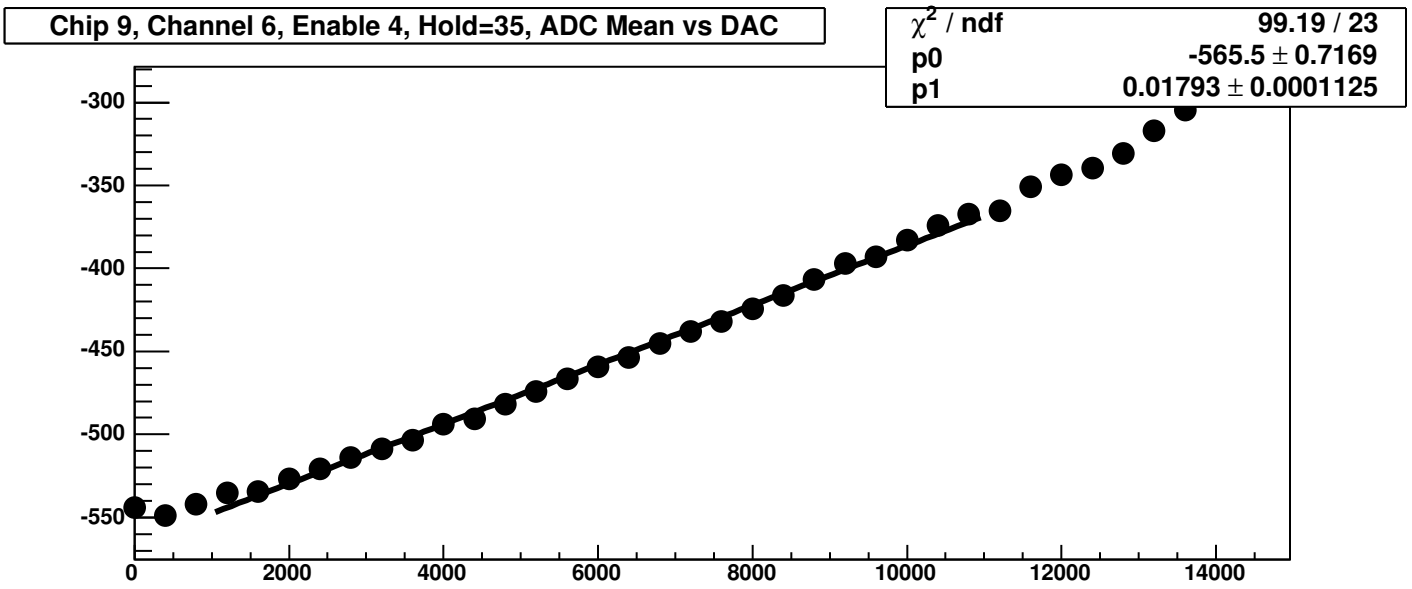


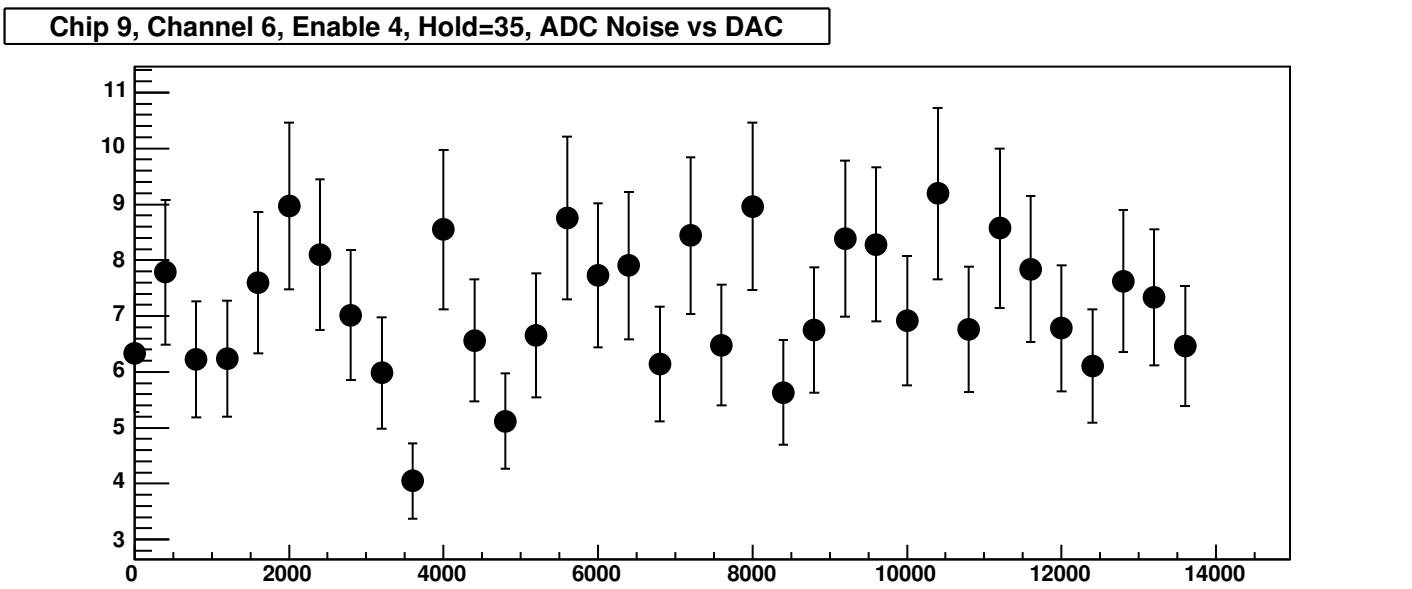
Chip 9, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

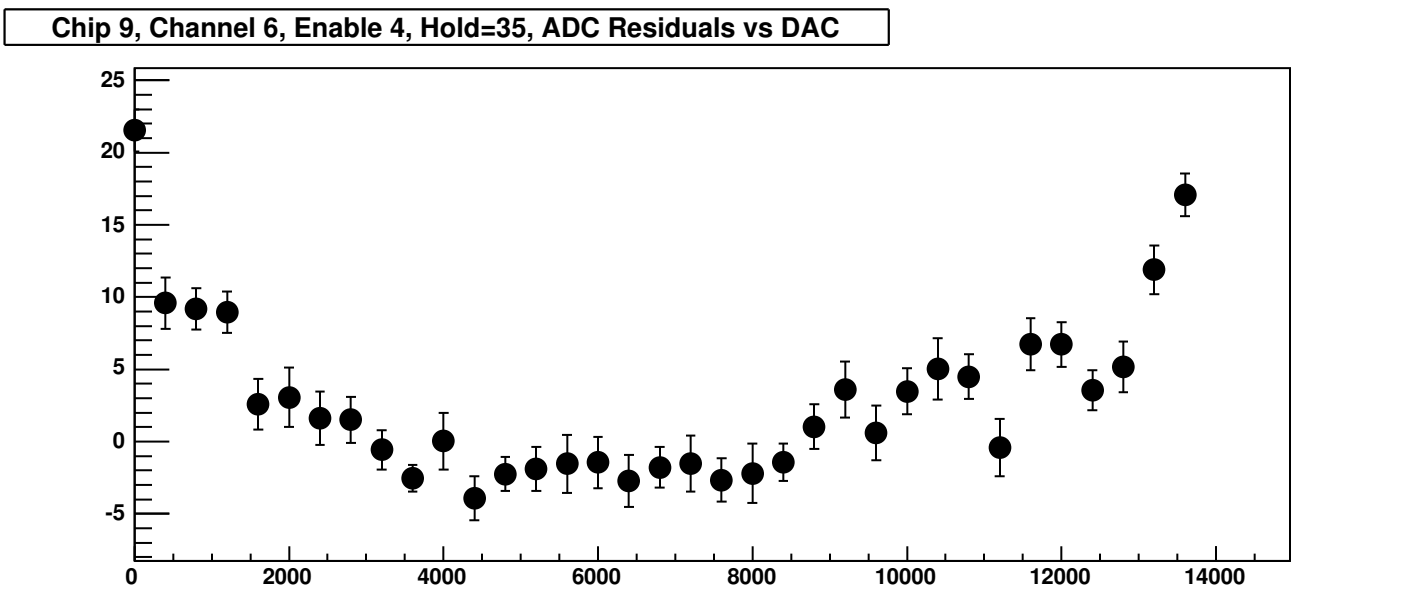


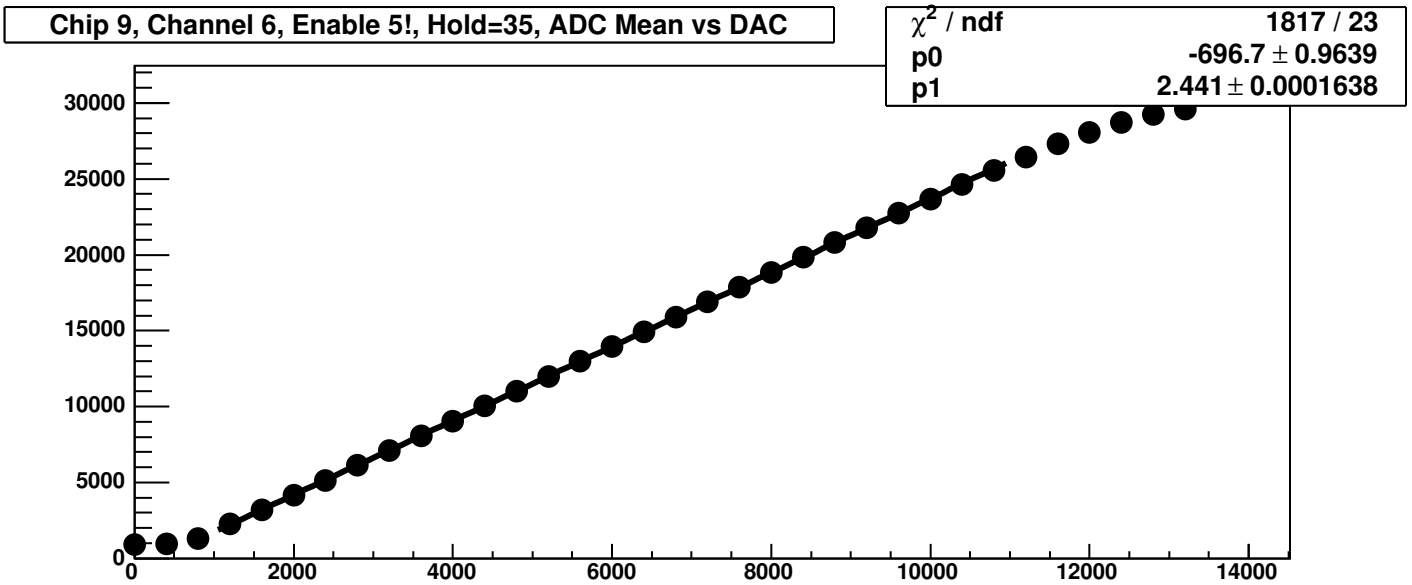
Chip 9, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

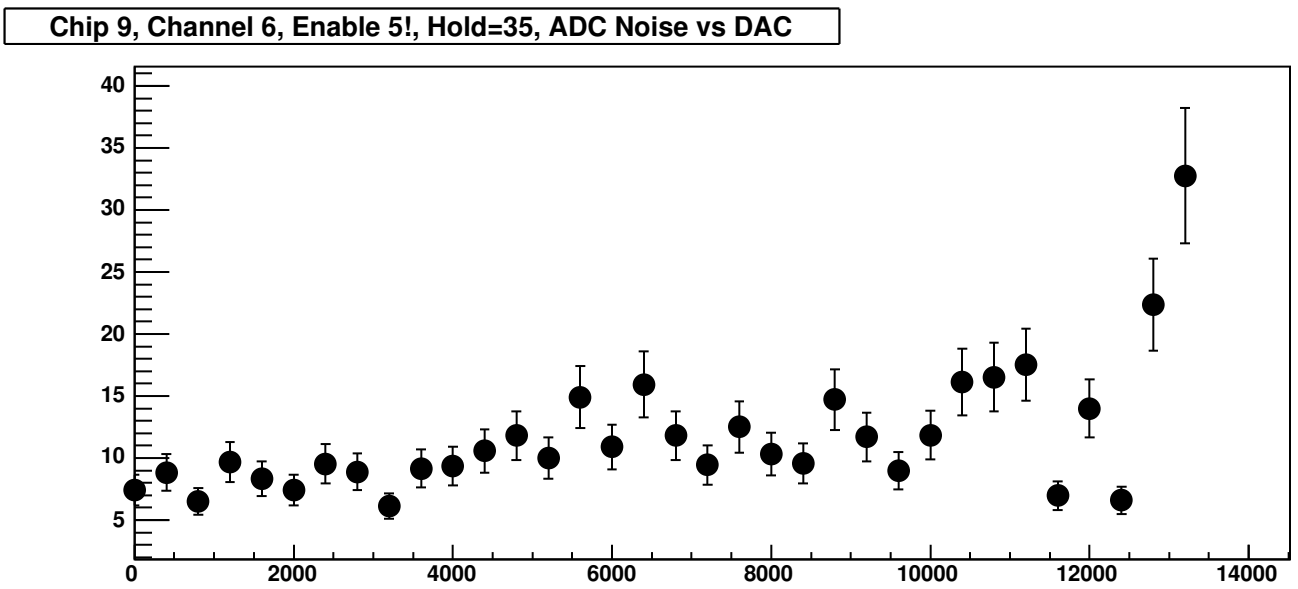


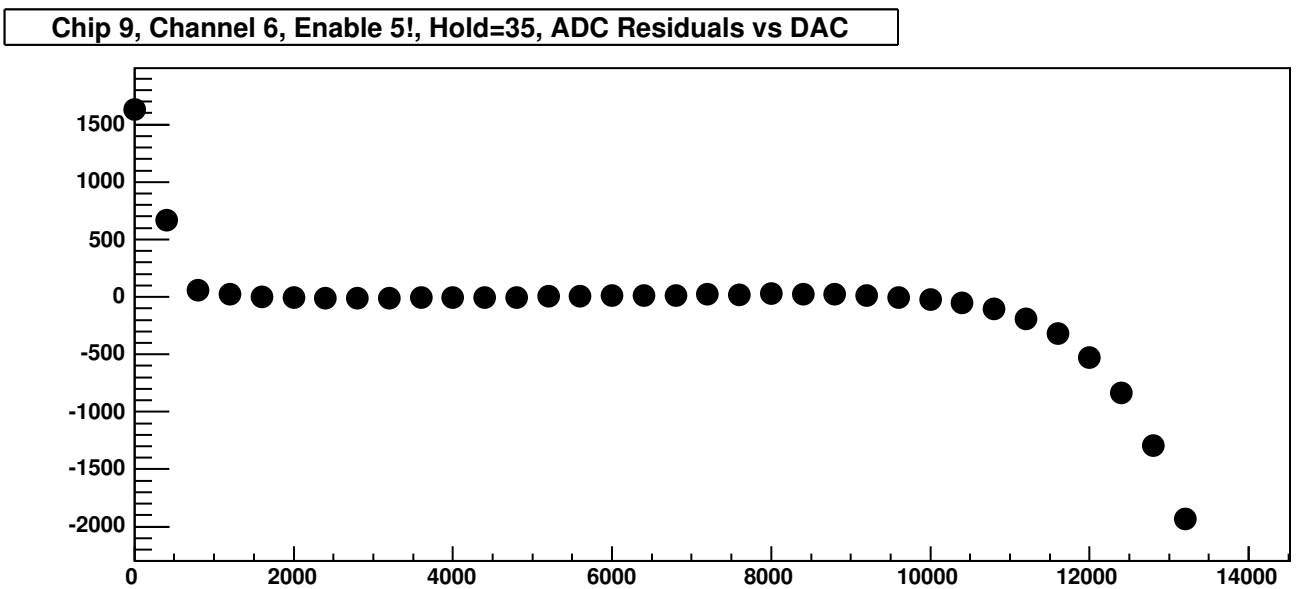

Chip 9, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 99.19 / 23
p0 -565.5 ± 0.7169
p1 0.01793 ± 0.0001125
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

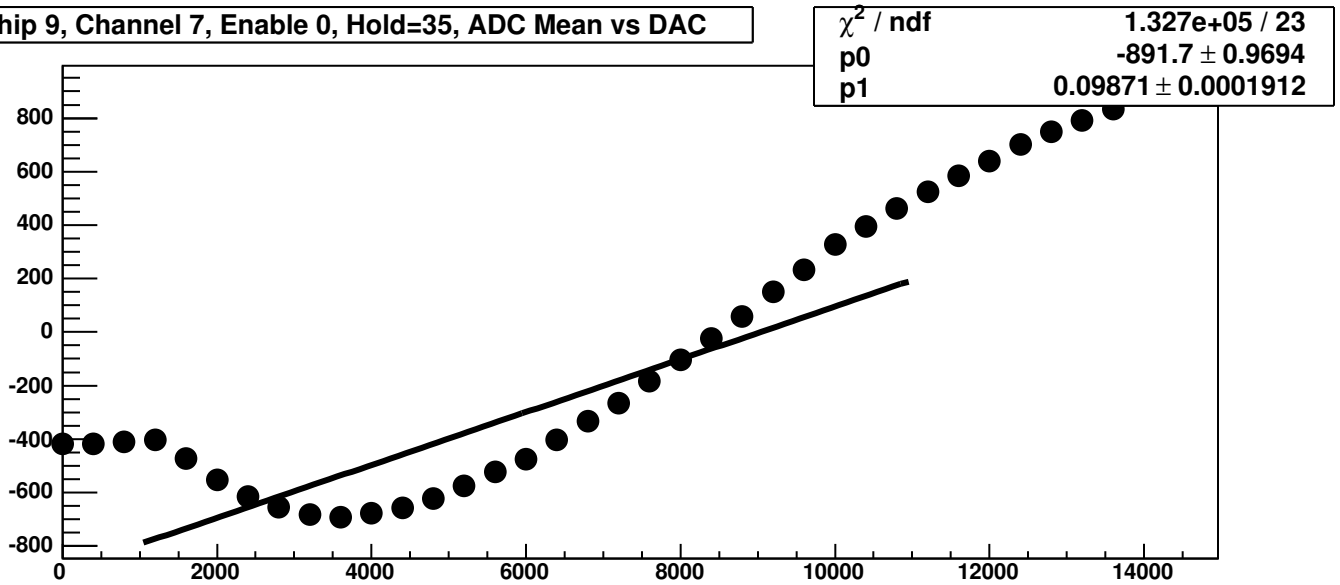

Chip 9, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1817 / 23
p0 -696.7 ± 0.9639
p1 2.441 ± 0.0001638
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

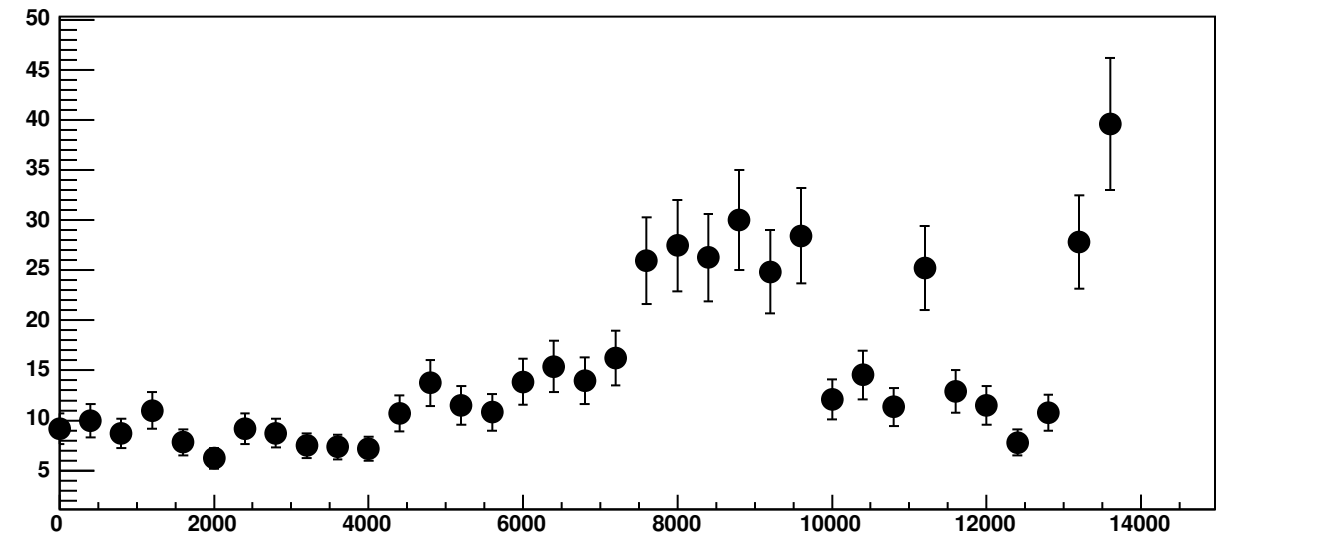

Chip 9, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

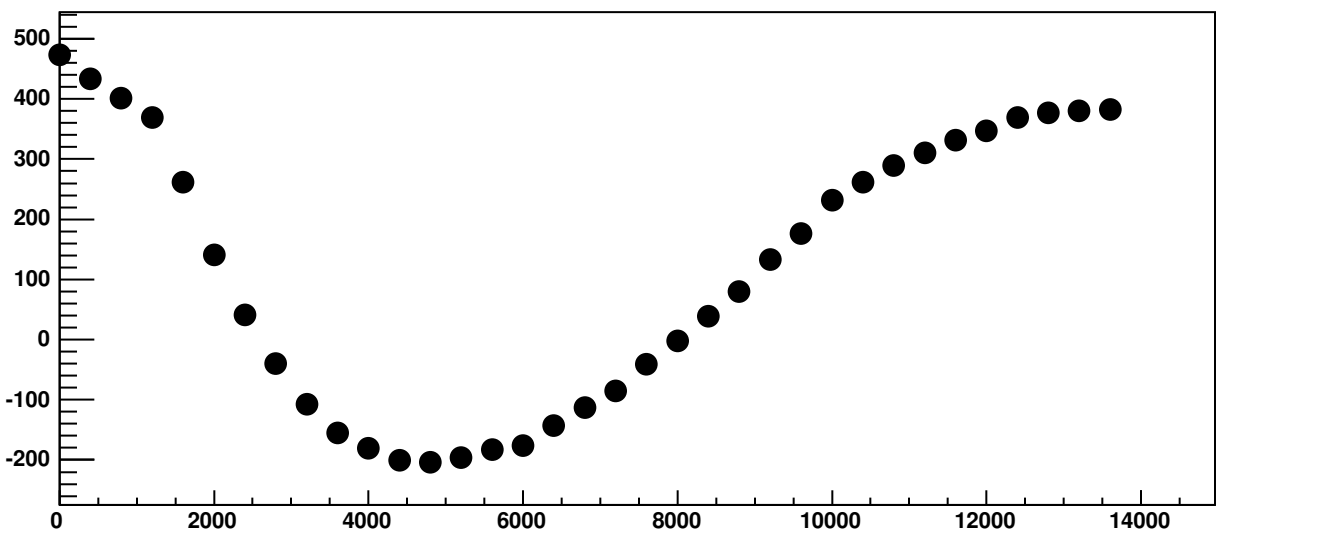
Chip 9, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

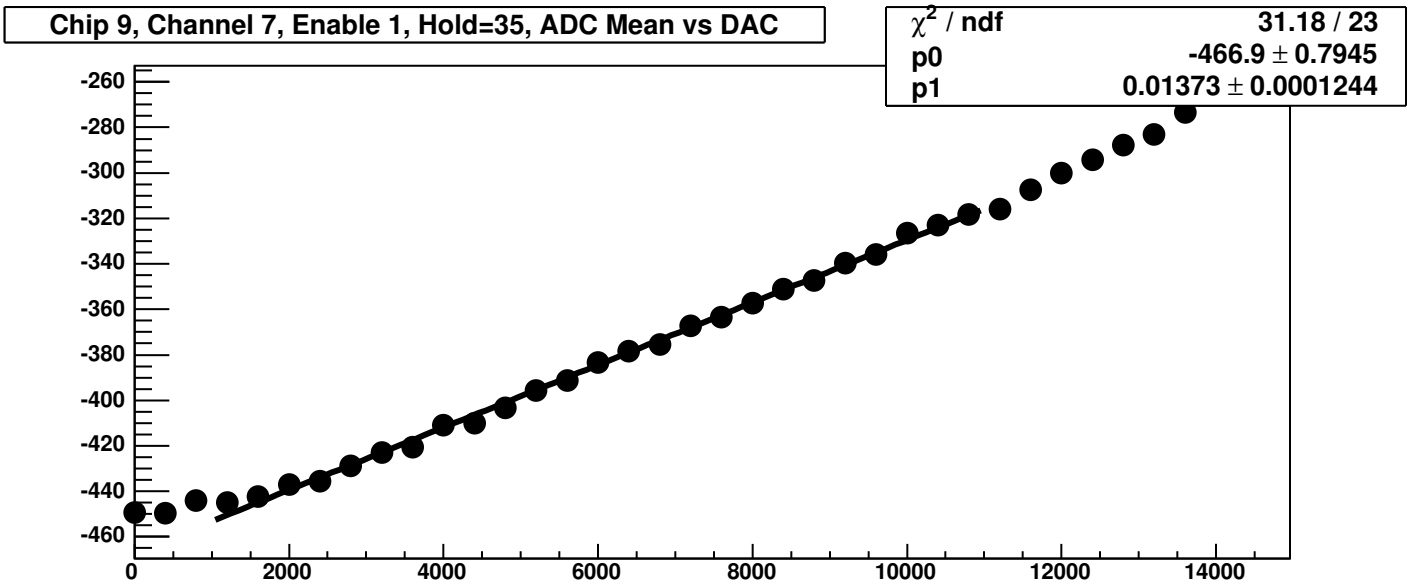


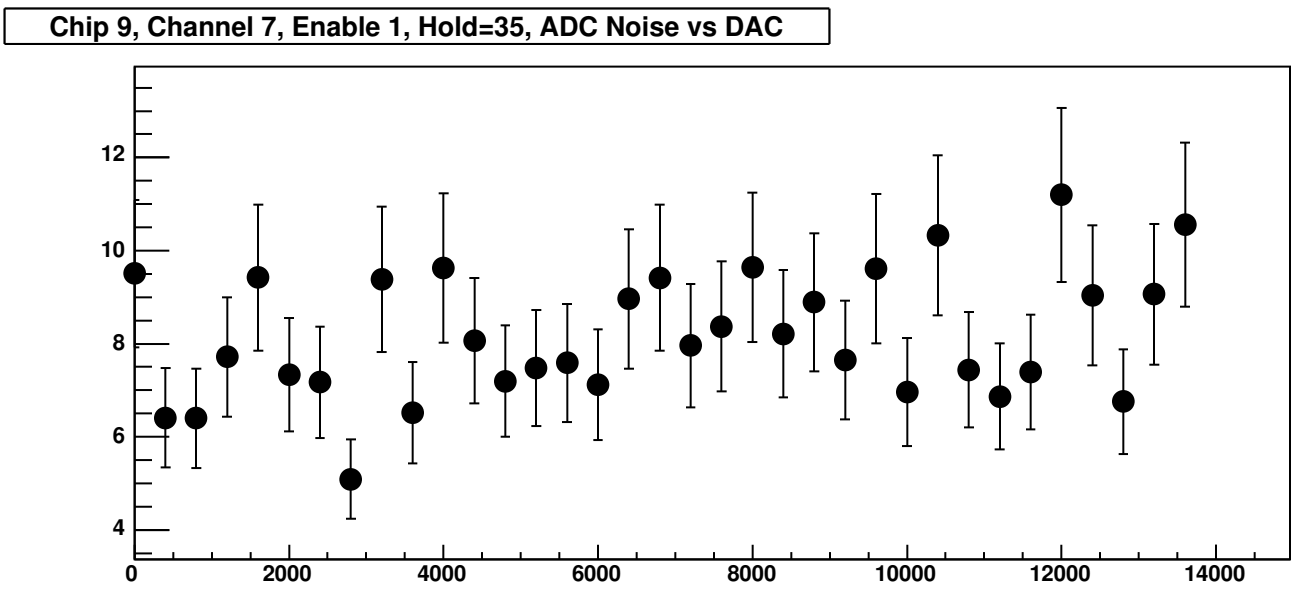
Chip 9, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

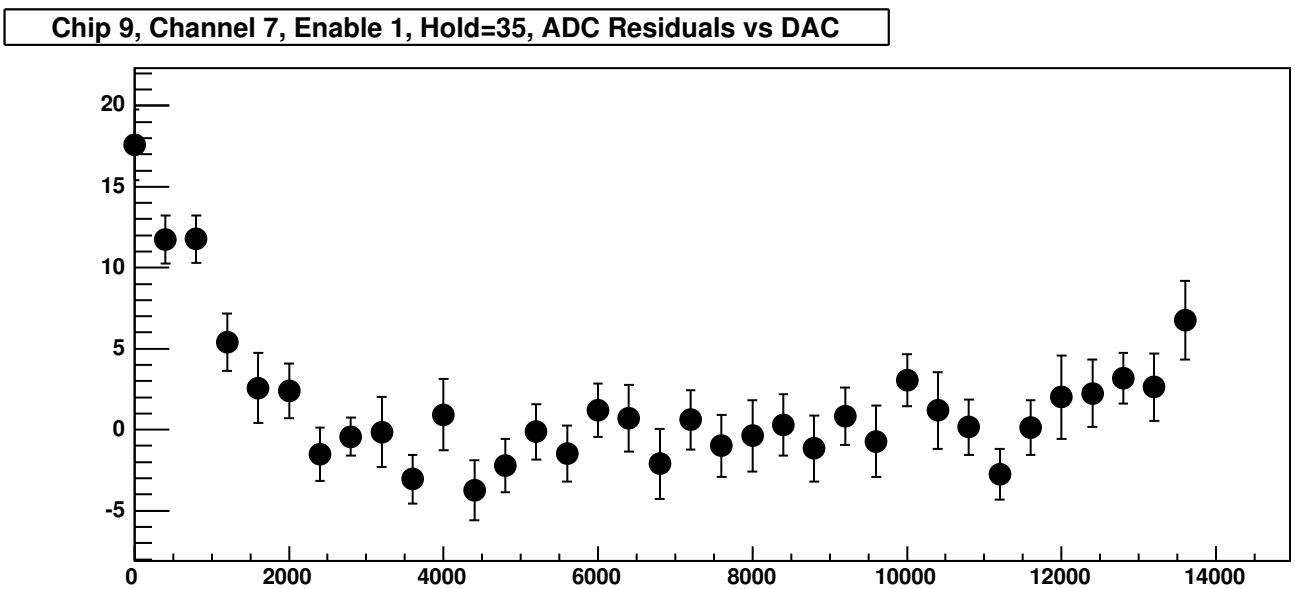


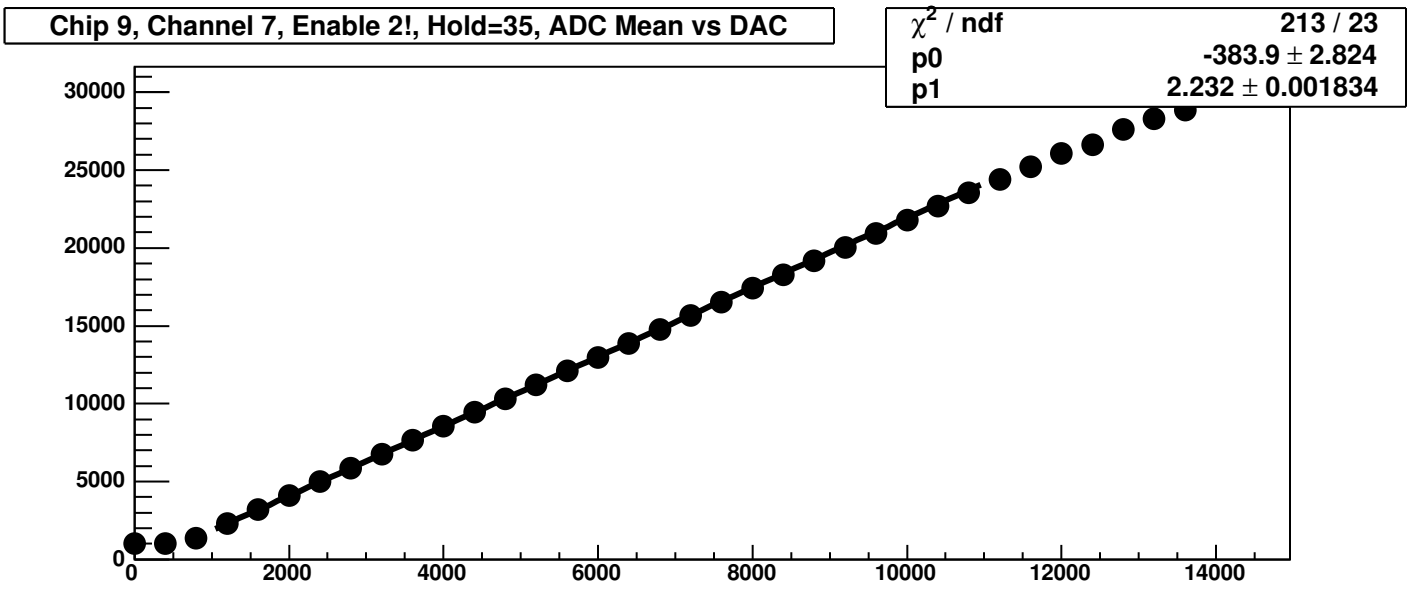
Chip 9, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

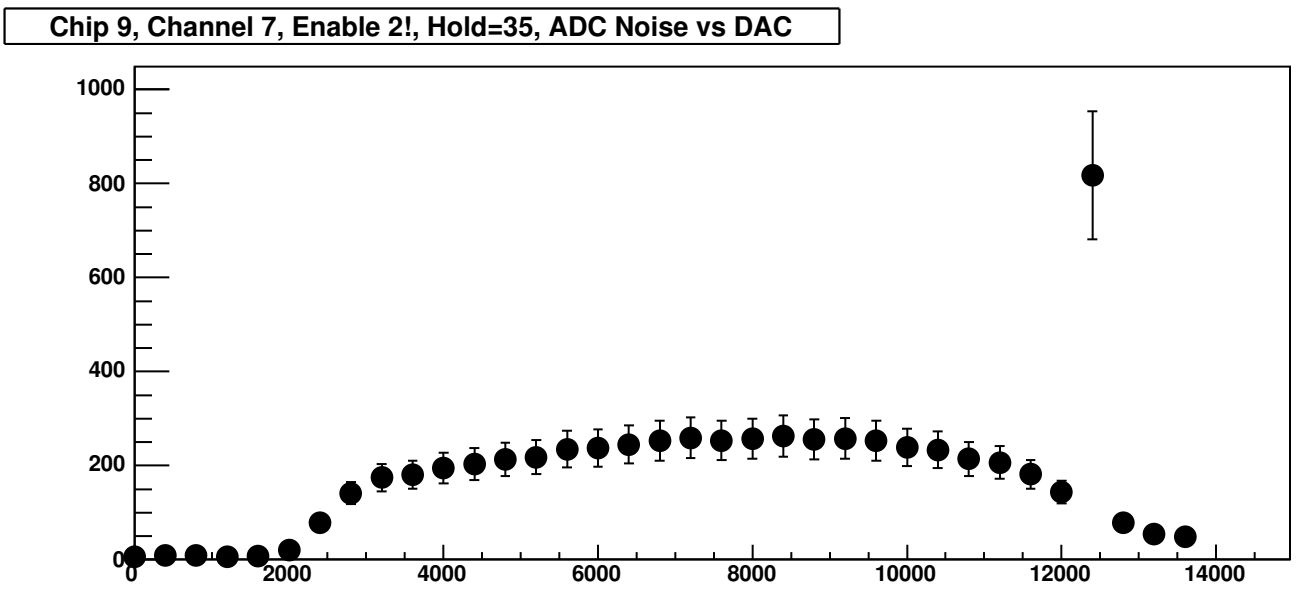


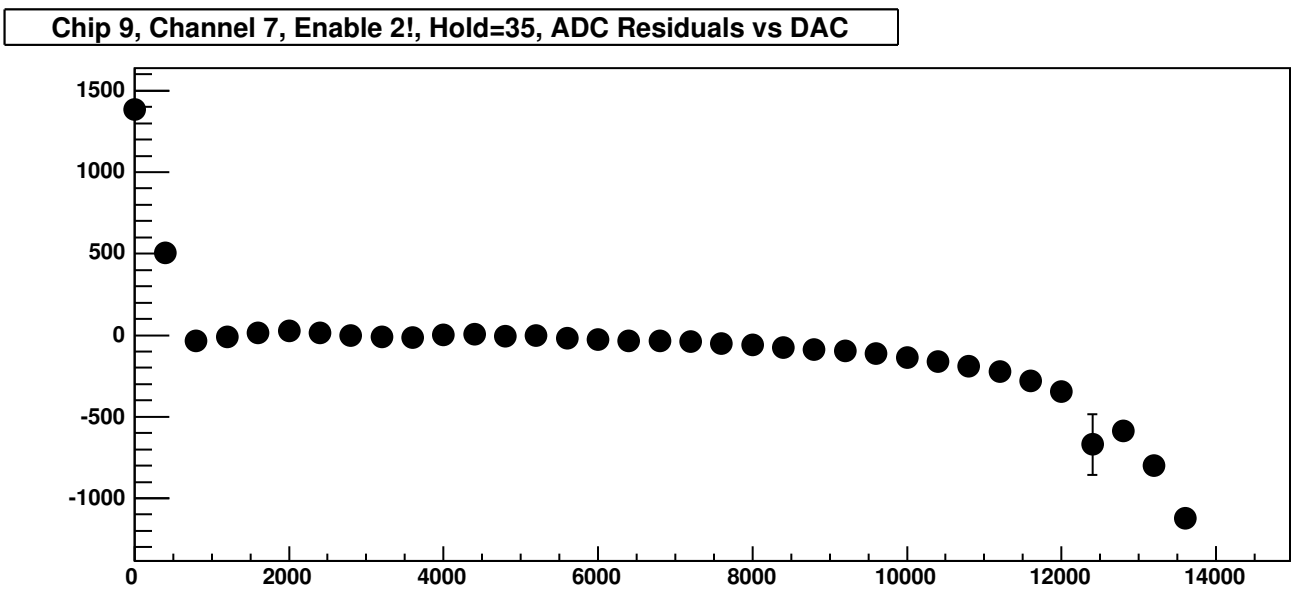

Chip 9, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.18 / 23
p0 -466.9 ± 0.7945
p1 0.01373 ± 0.0001244
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
2000
4000
6000
8000
10000
12000
14000

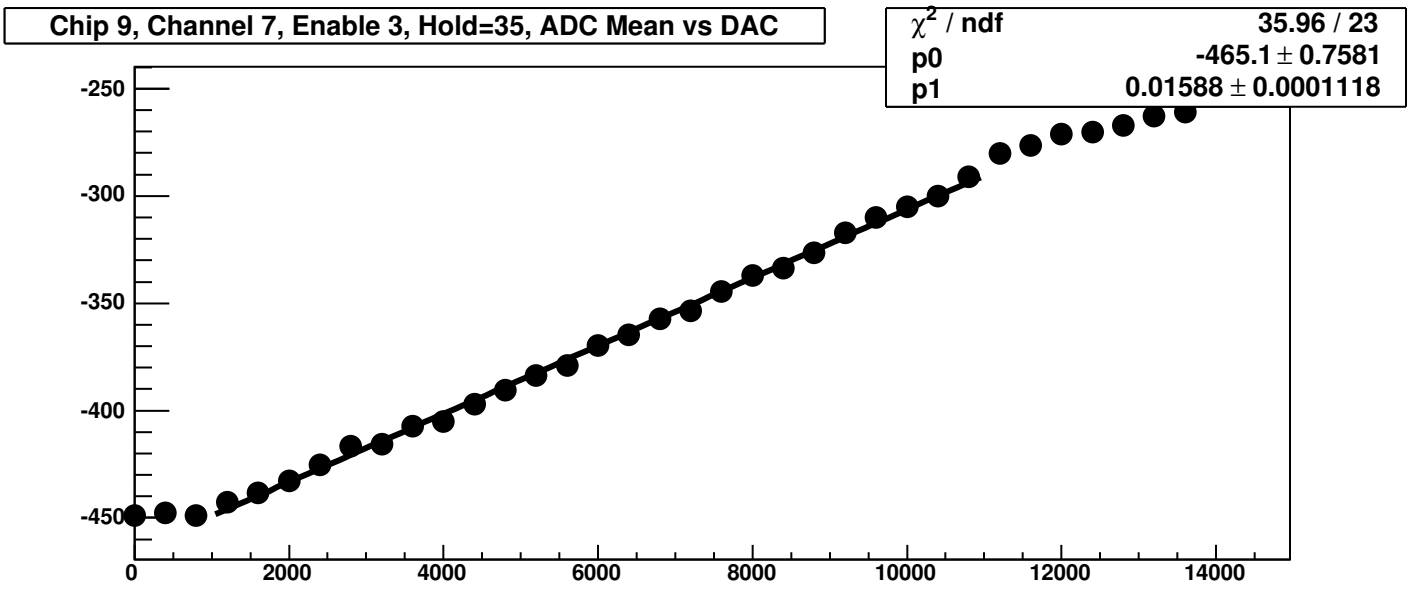

Chip 9, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

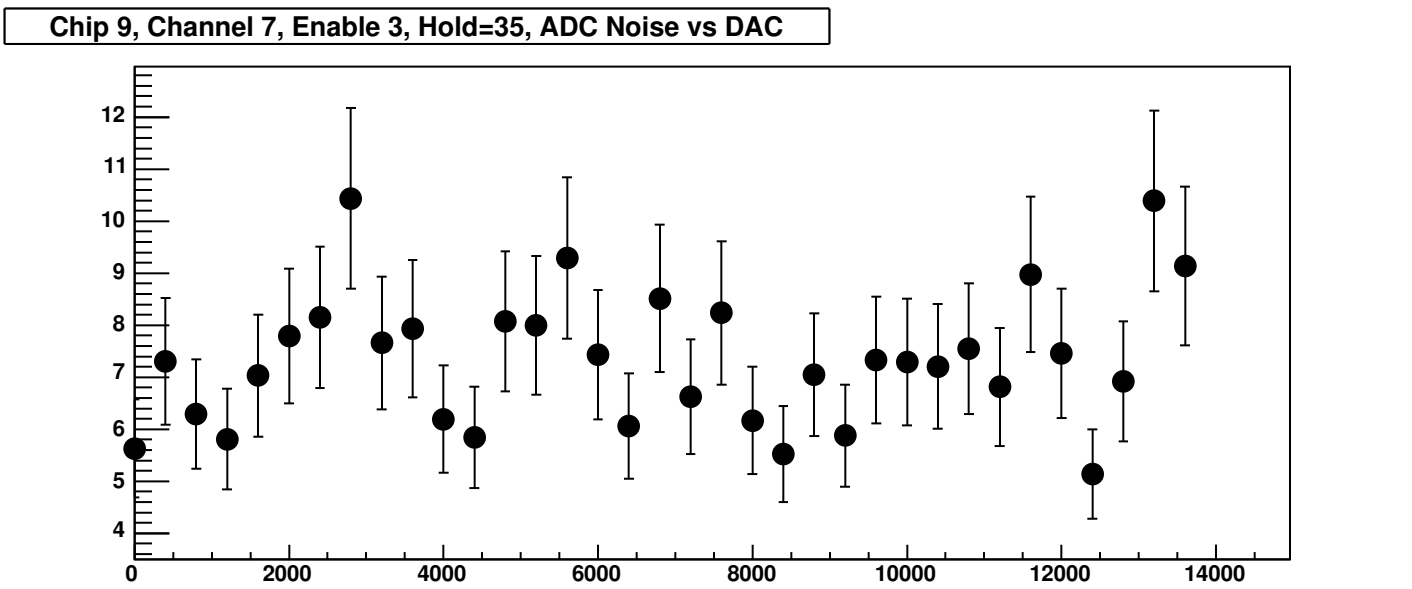

Chip 9, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

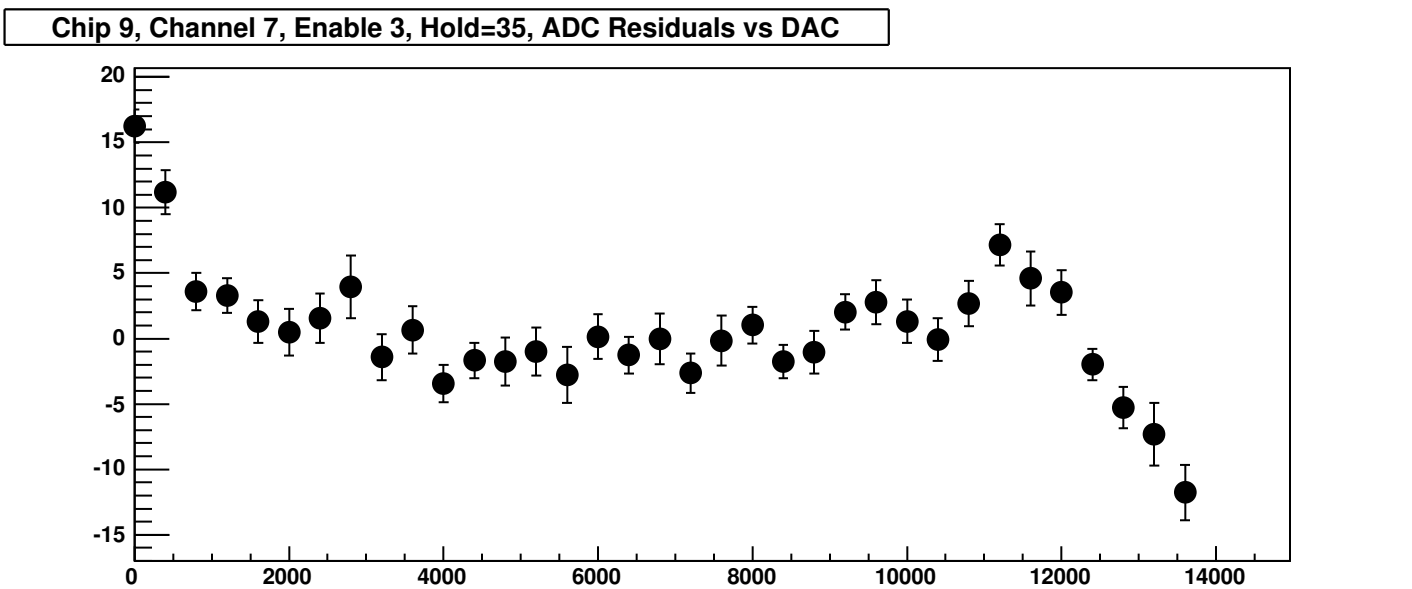

Chip 9, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 213 / 23
p0 -383.9 ± 2.824
p1 2.232 ± 0.001834
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

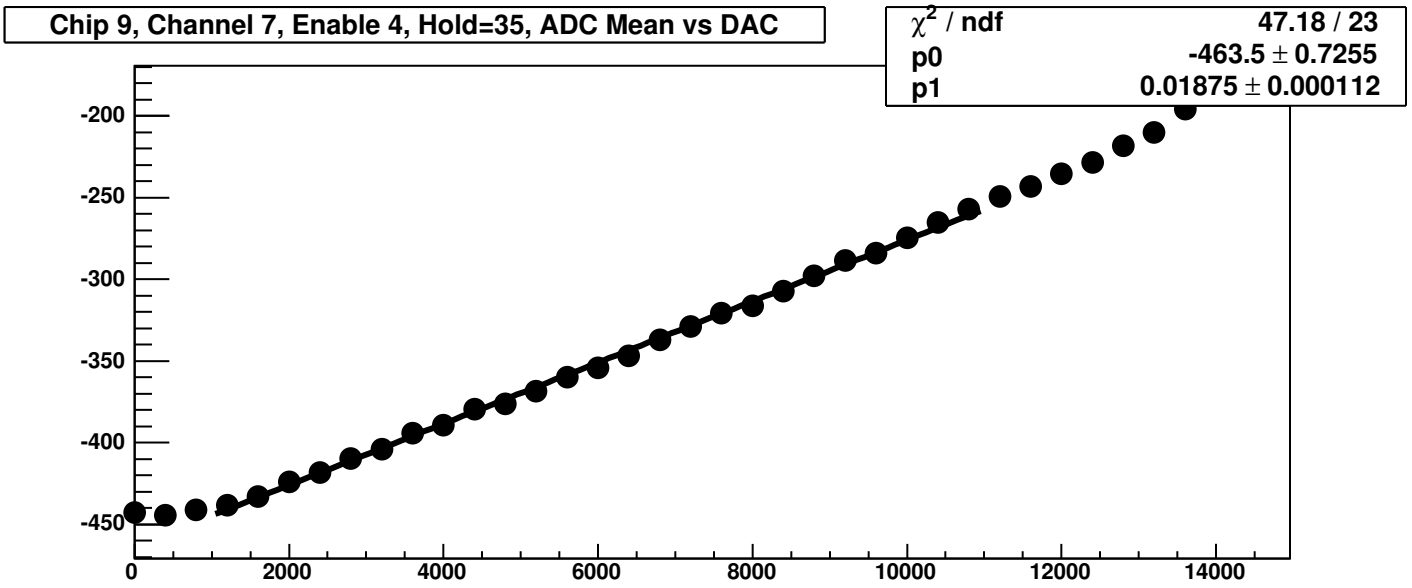

Chip 9, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

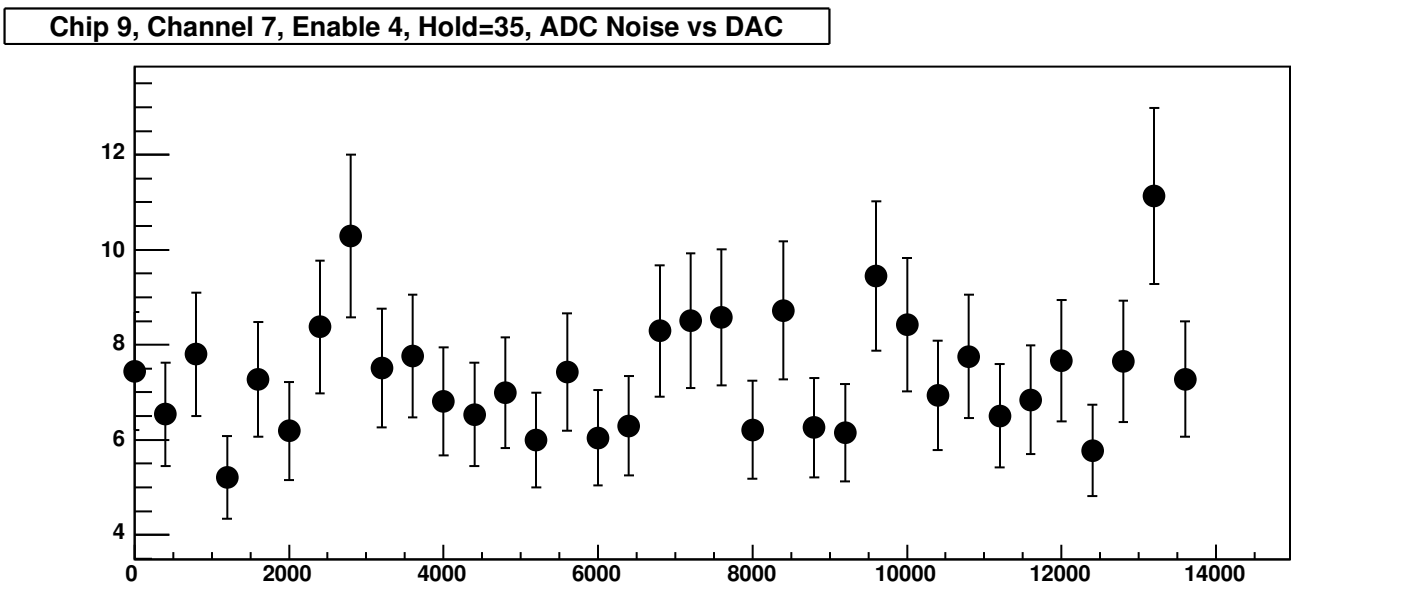

Chip 9, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

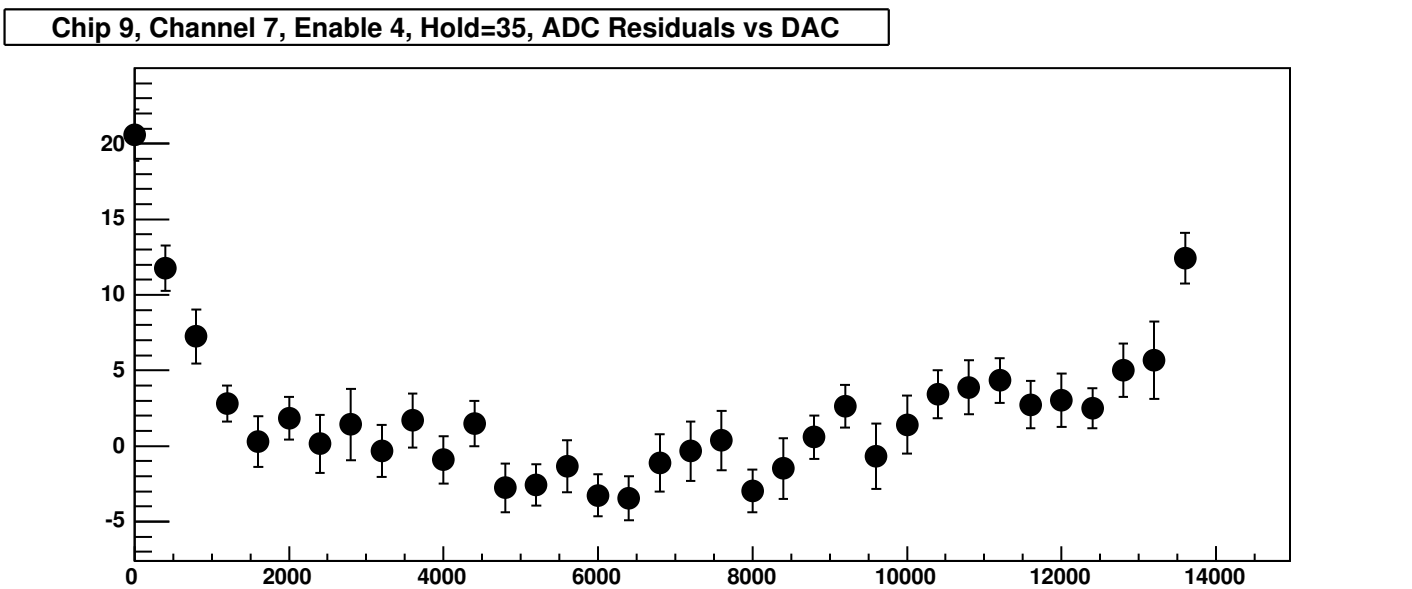

Chip 9, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.96 / 23
p0 -465.1 ± 0.7581
p1 0.01588 ± 0.0001118
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

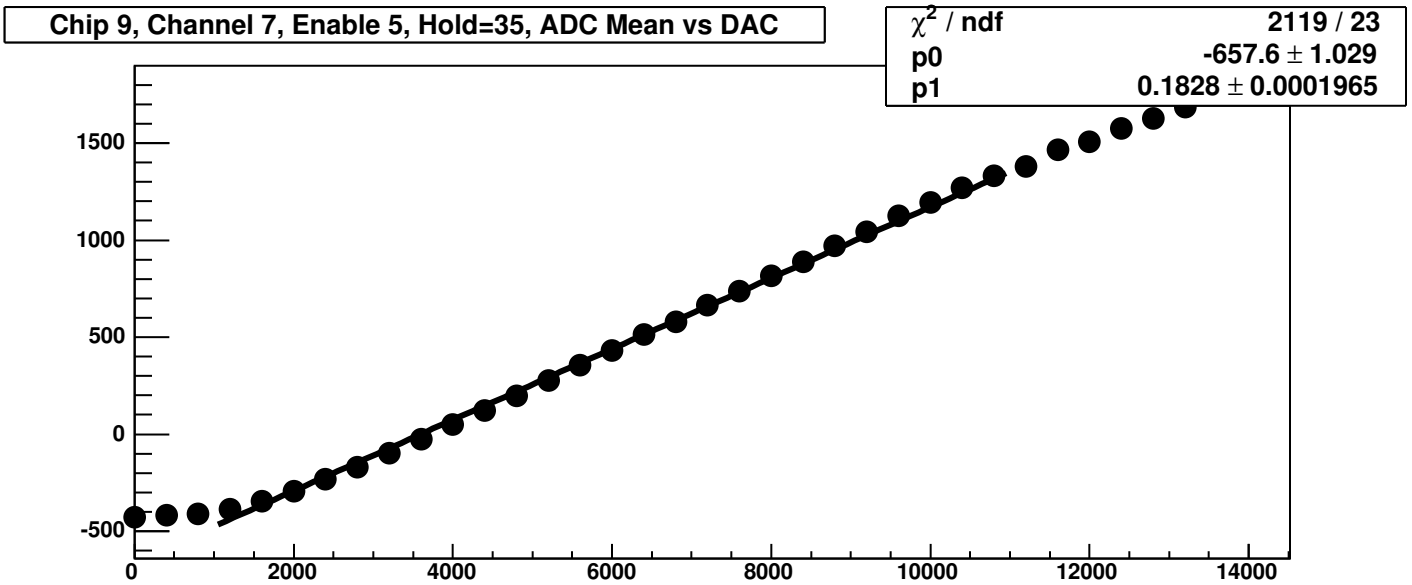

Chip 9, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

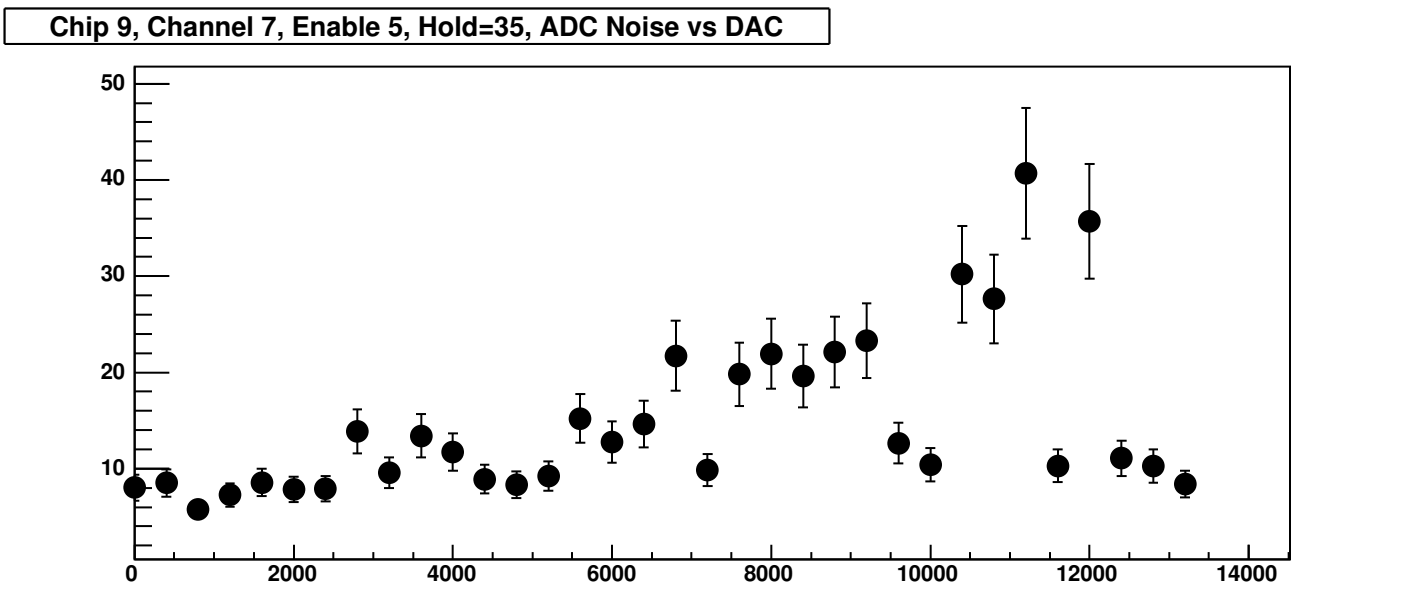

Chip 9, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

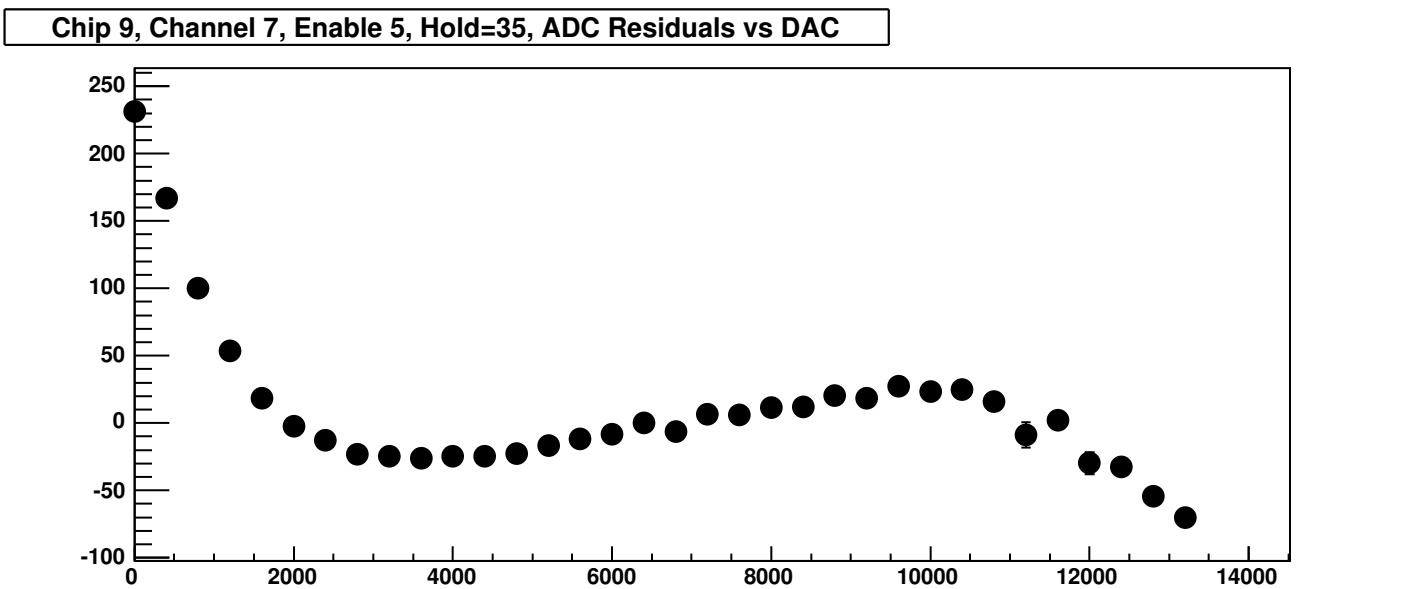

Chip 9, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.18 / 23
p0 -463.5 ± 0.7255
p1 0.01875 ± 0.000112
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

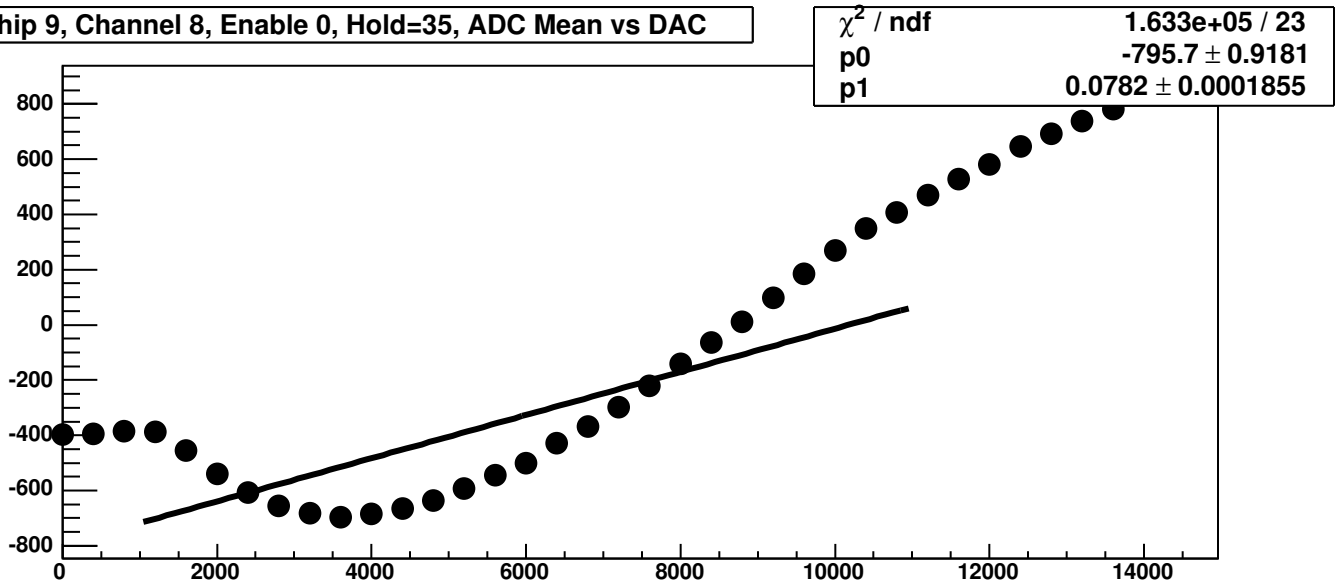

Chip 9, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2119 / 23
p0 -657.6 ± 1.029
p1 0.1828 ± 0.0001965
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

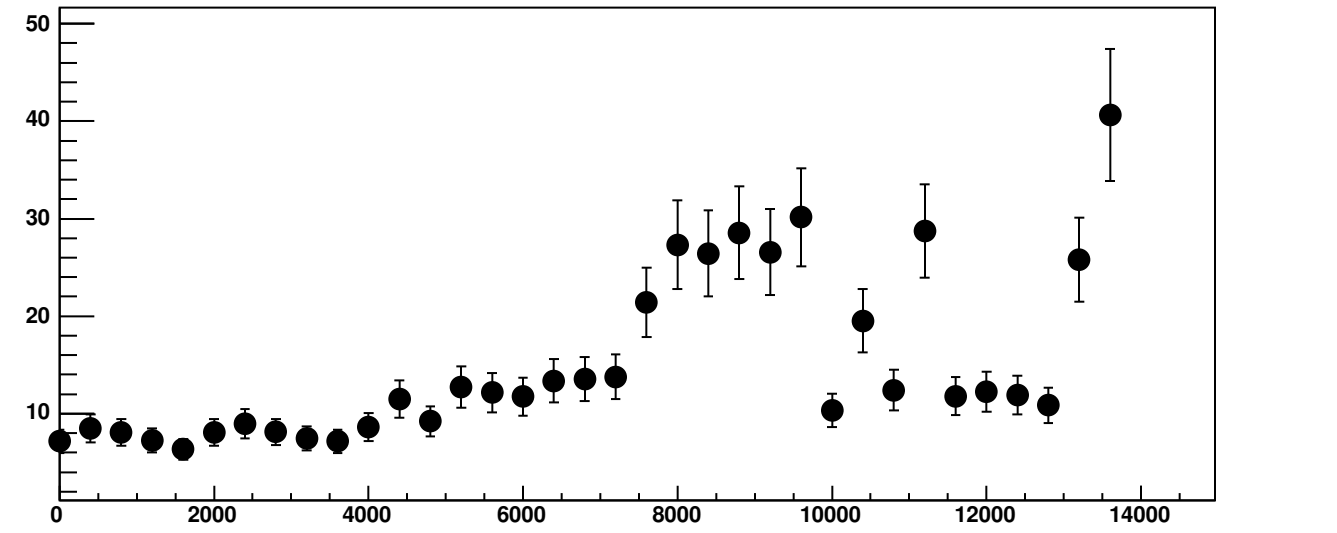

Chip 9, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

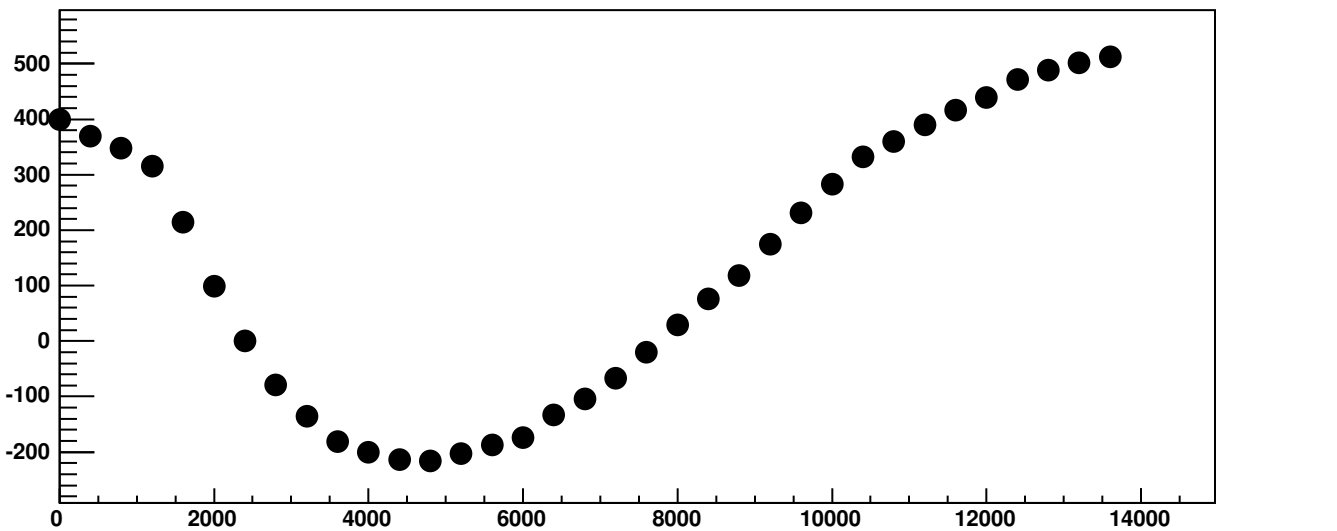
Chip 9, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

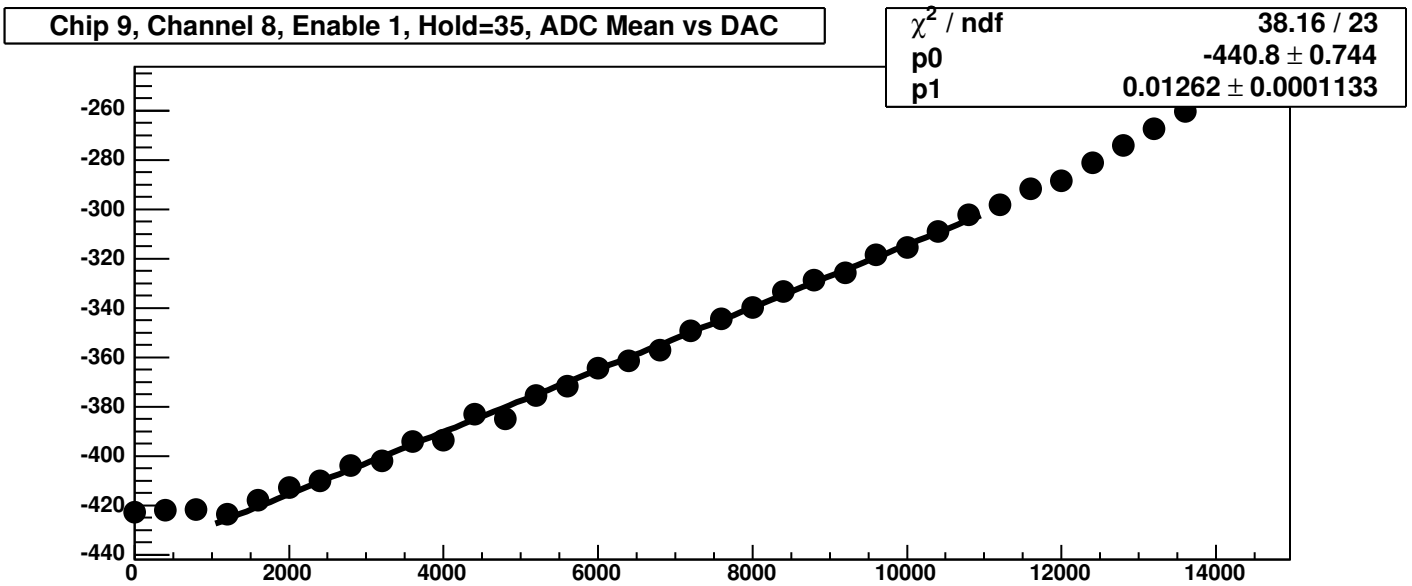


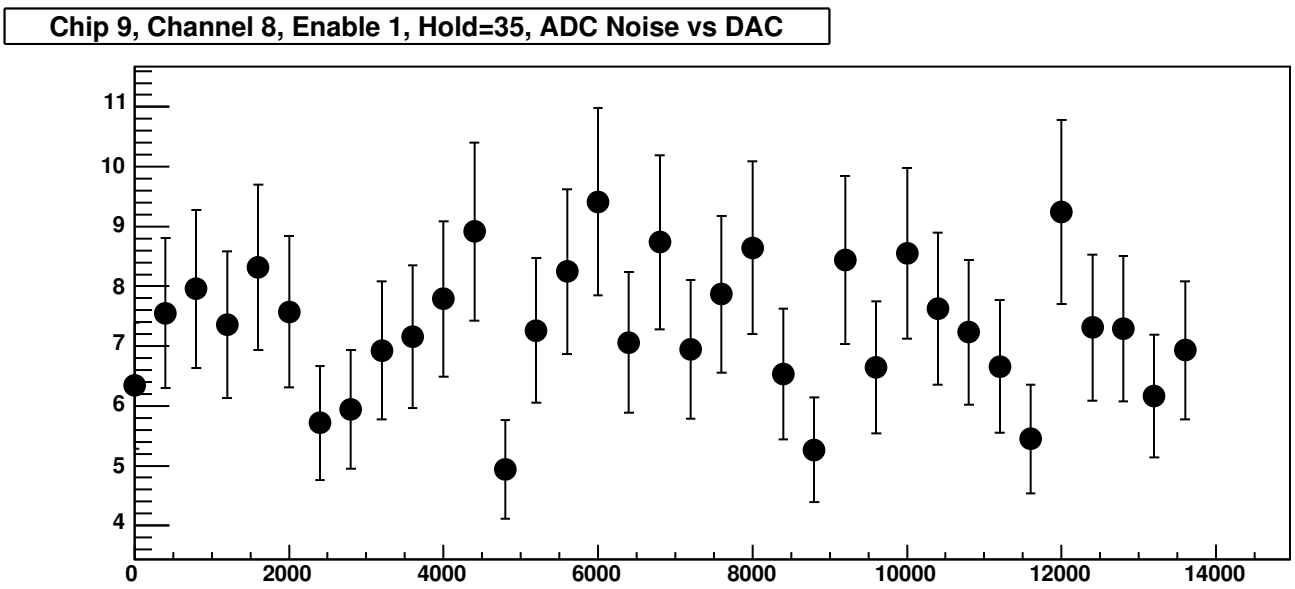
Chip 9, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC

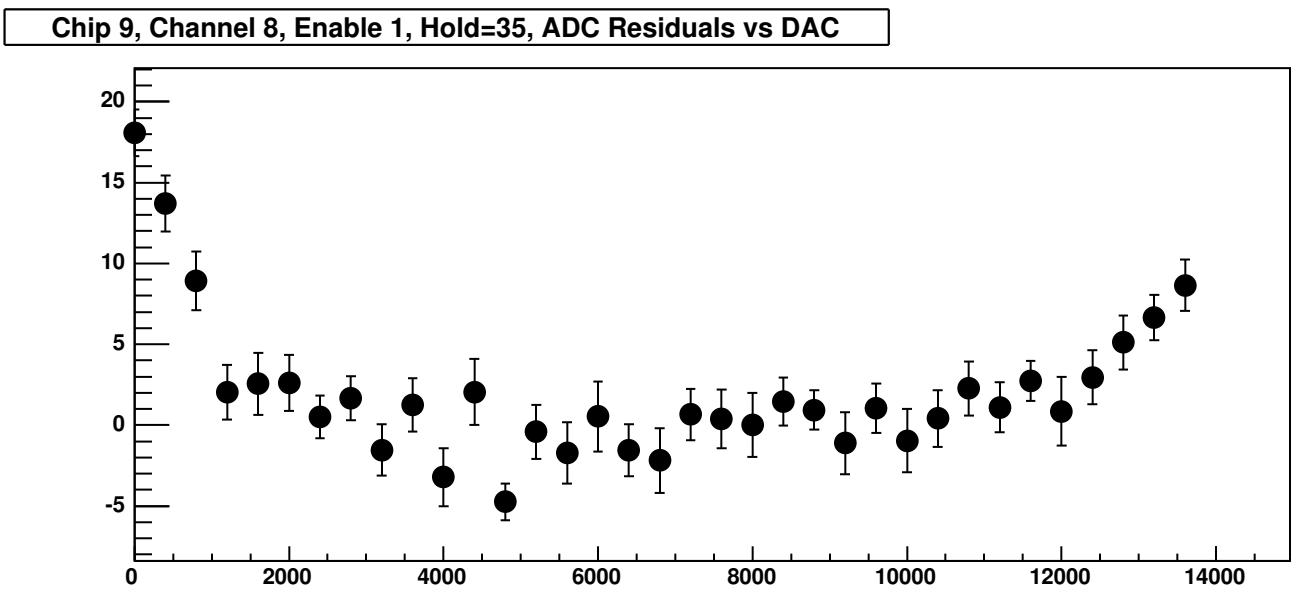


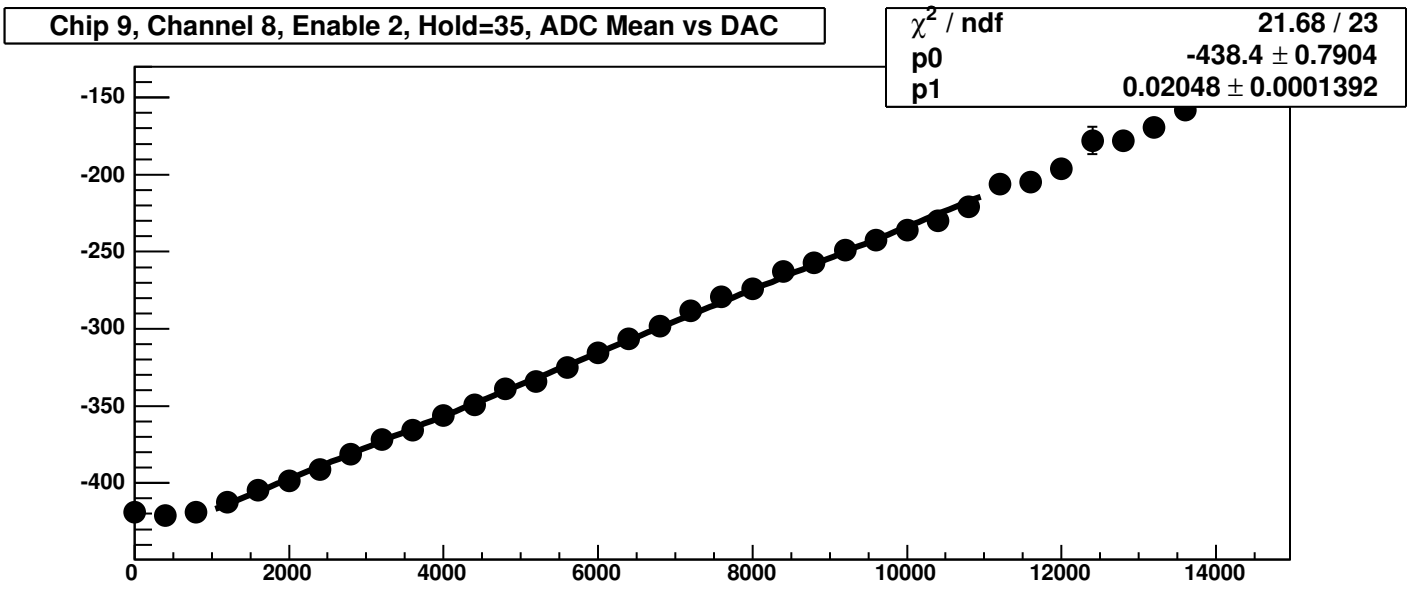
Chip 9, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

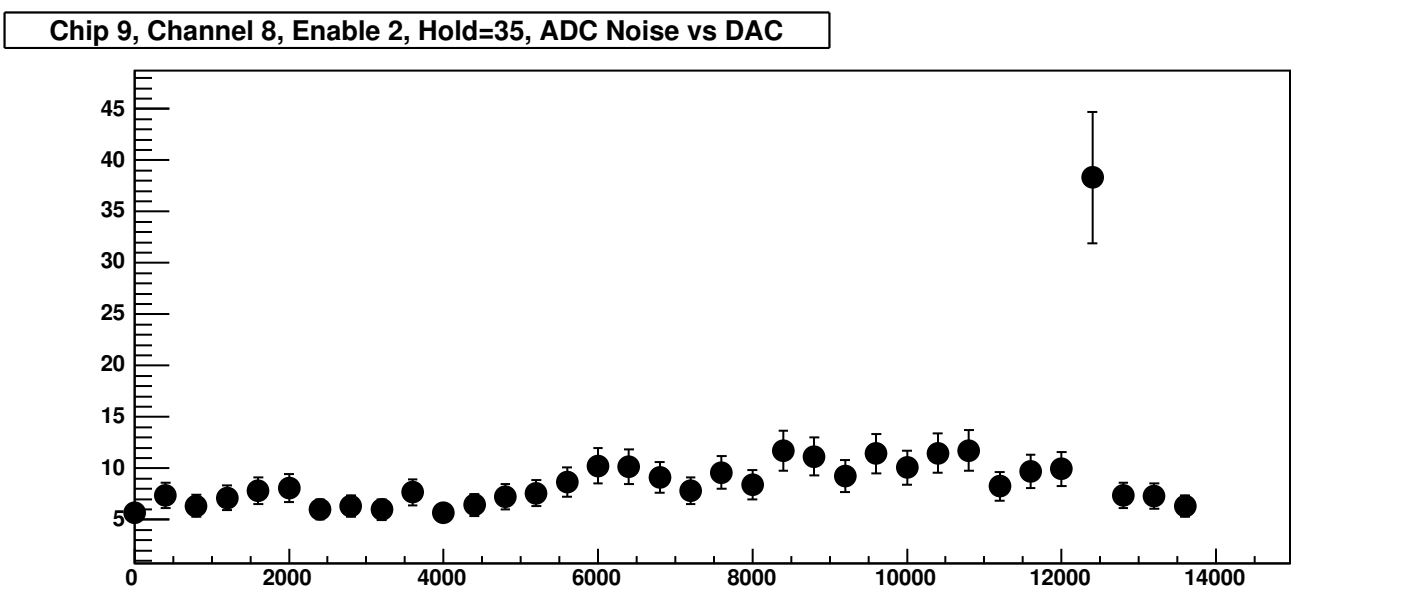


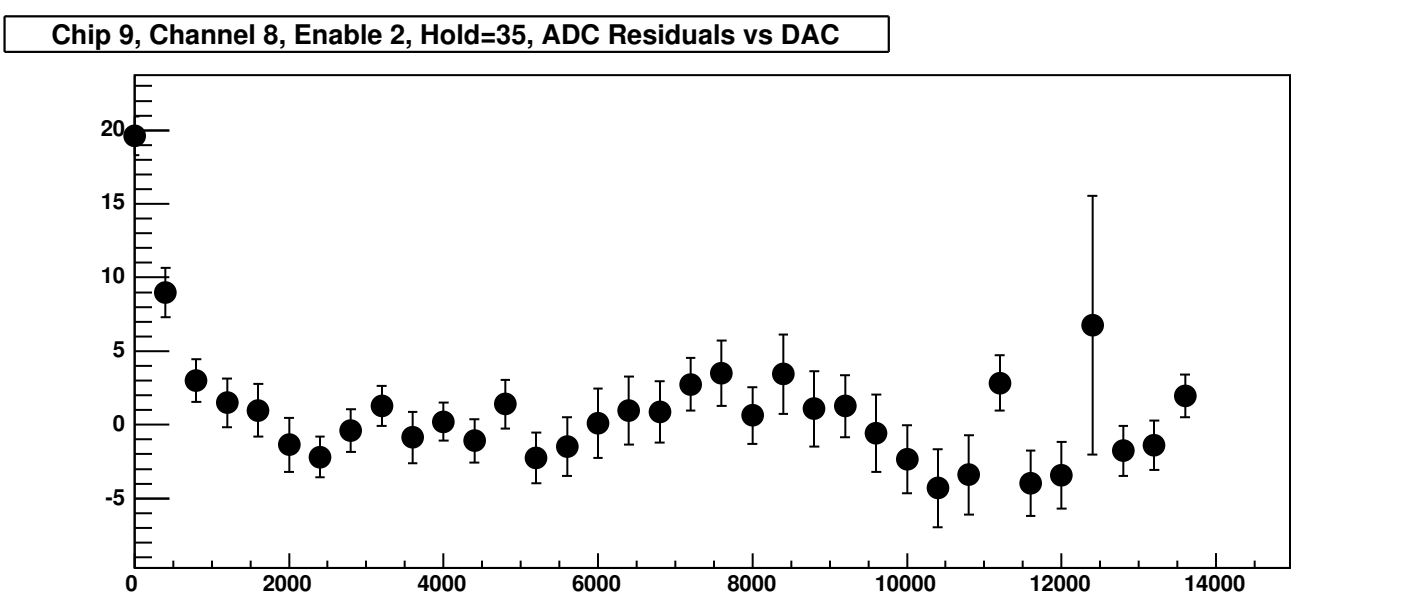

Chip 9, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.16 / 23
p0 -440.8 ± 0.744
p1 0.01262 ± 0.0001133
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

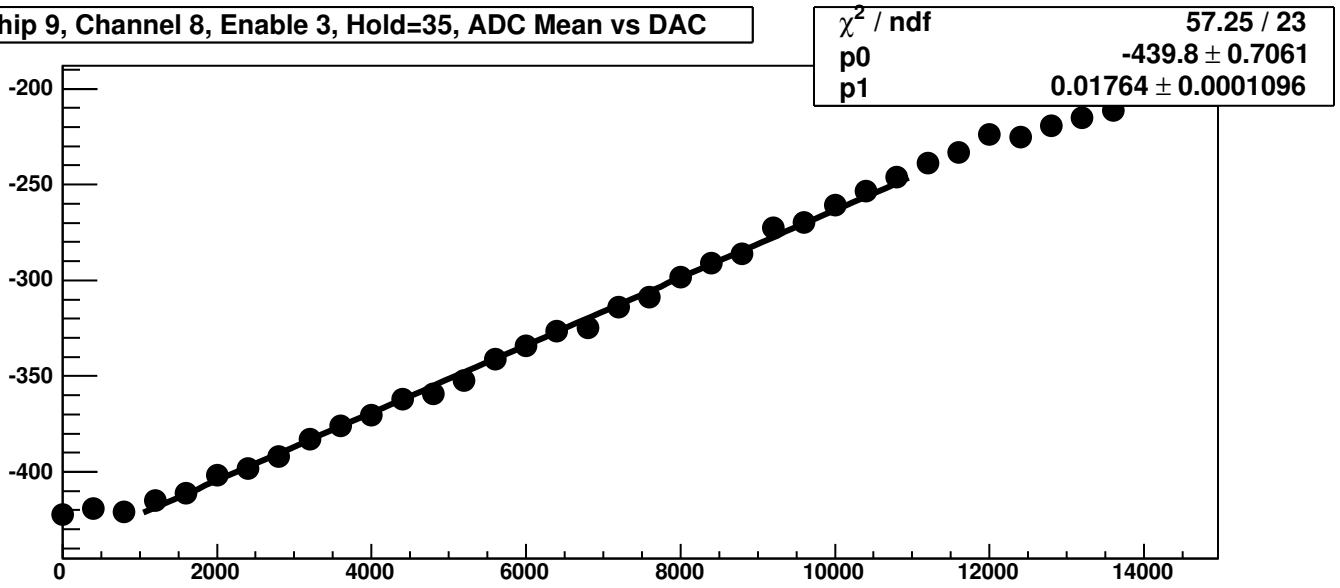

Chip 9, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.68 / 23
p0 -438.4 ± 0.7904
p1 0.02048 ± 0.0001392
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

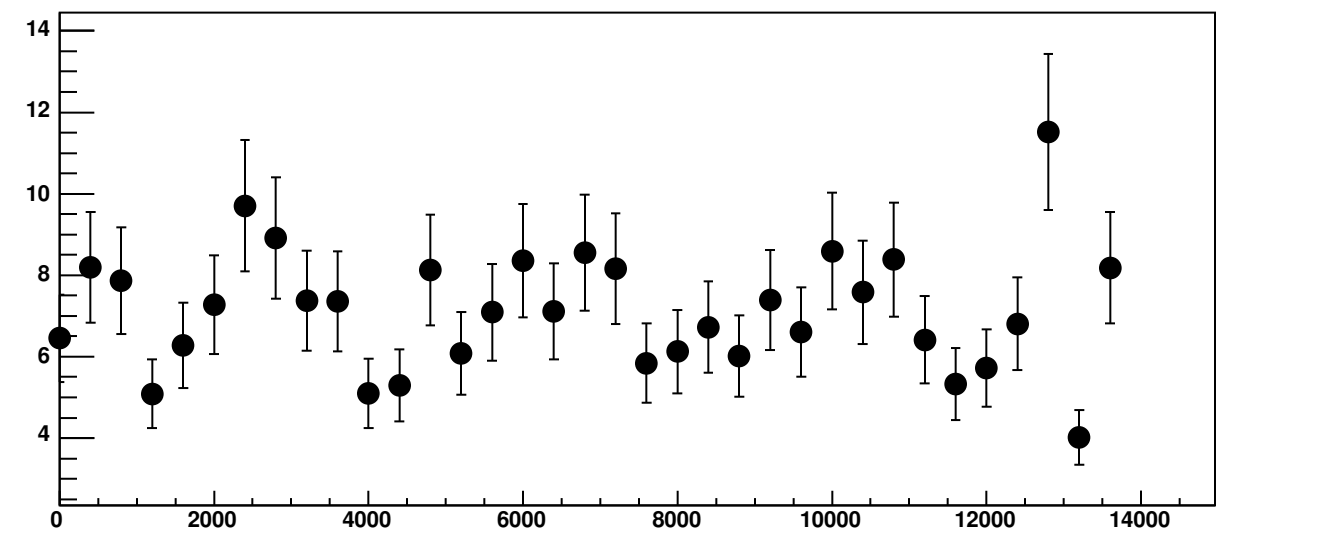

Chip 9, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

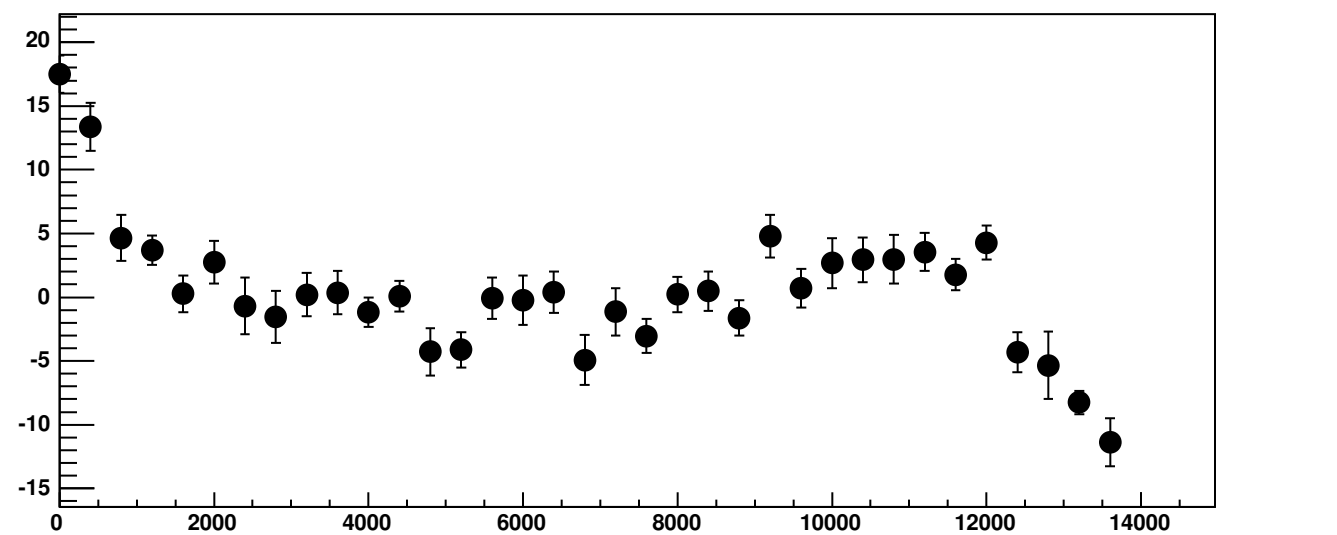
Chip 9, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



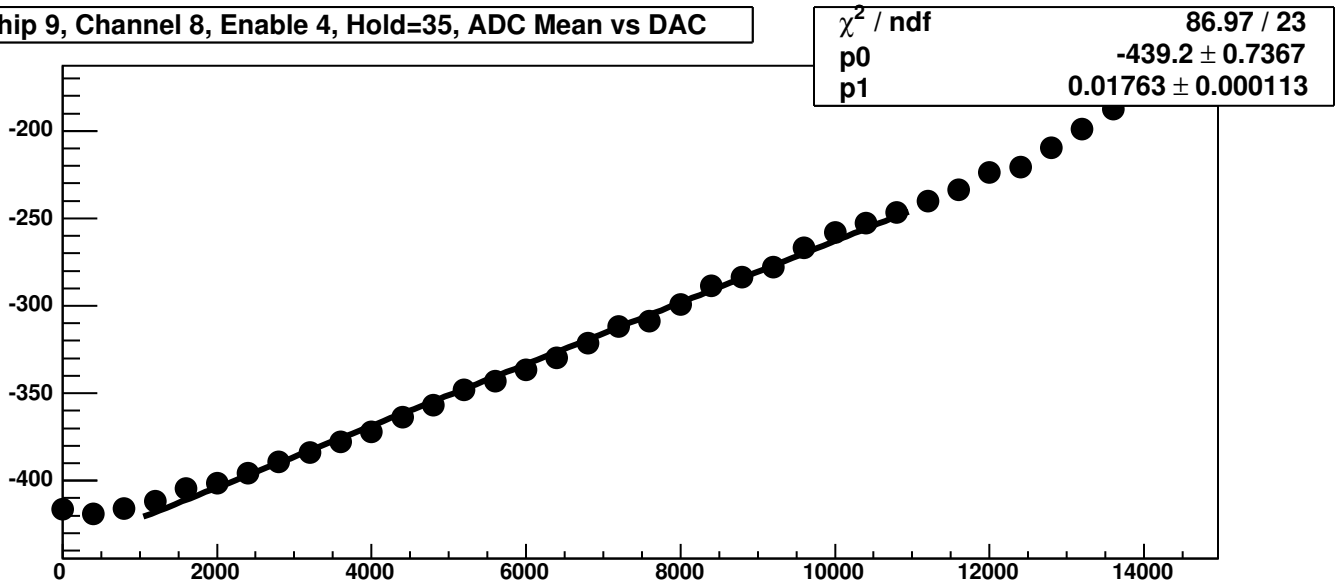
Chip 9, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



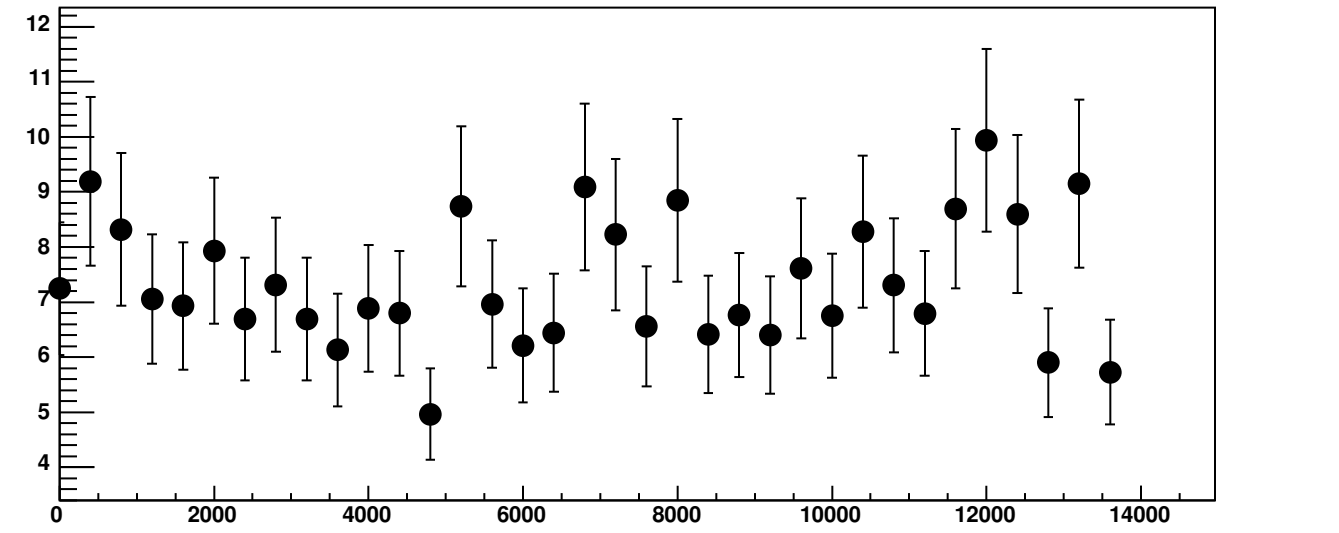
Chip 9, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



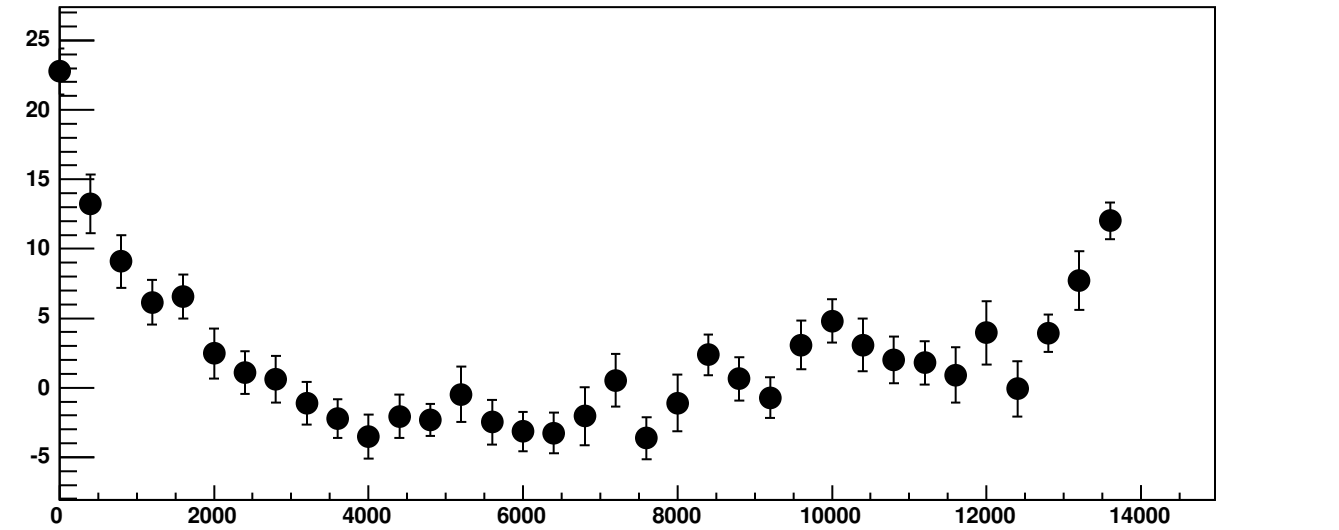
Chip 9, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

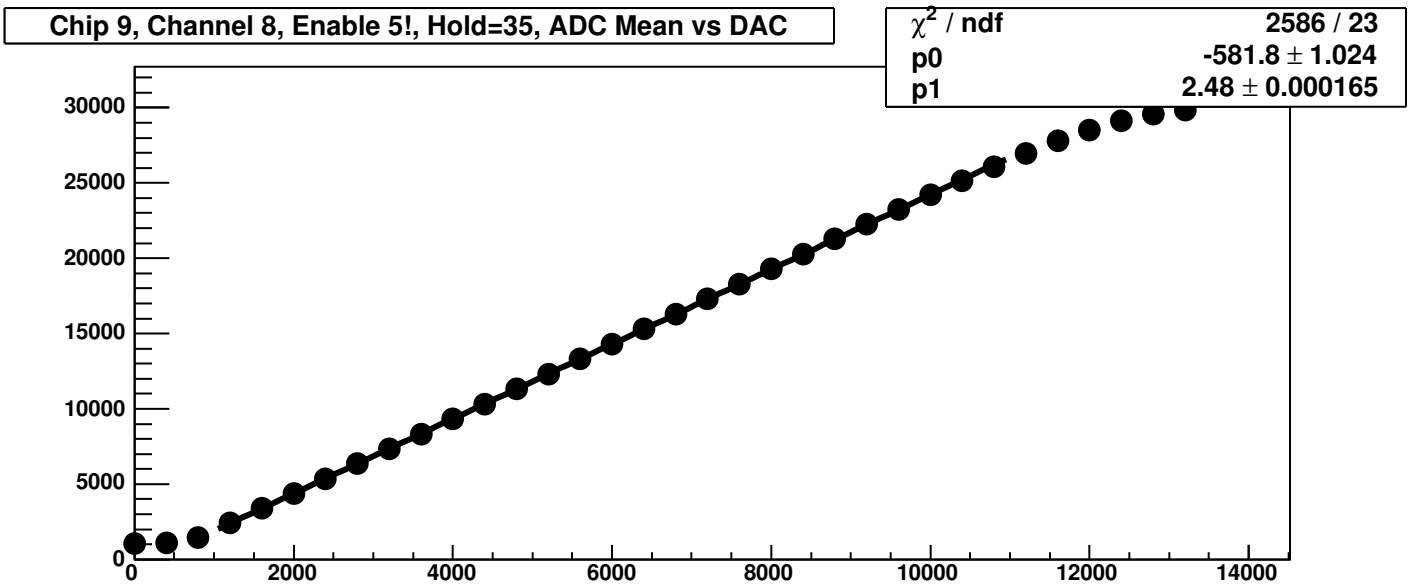


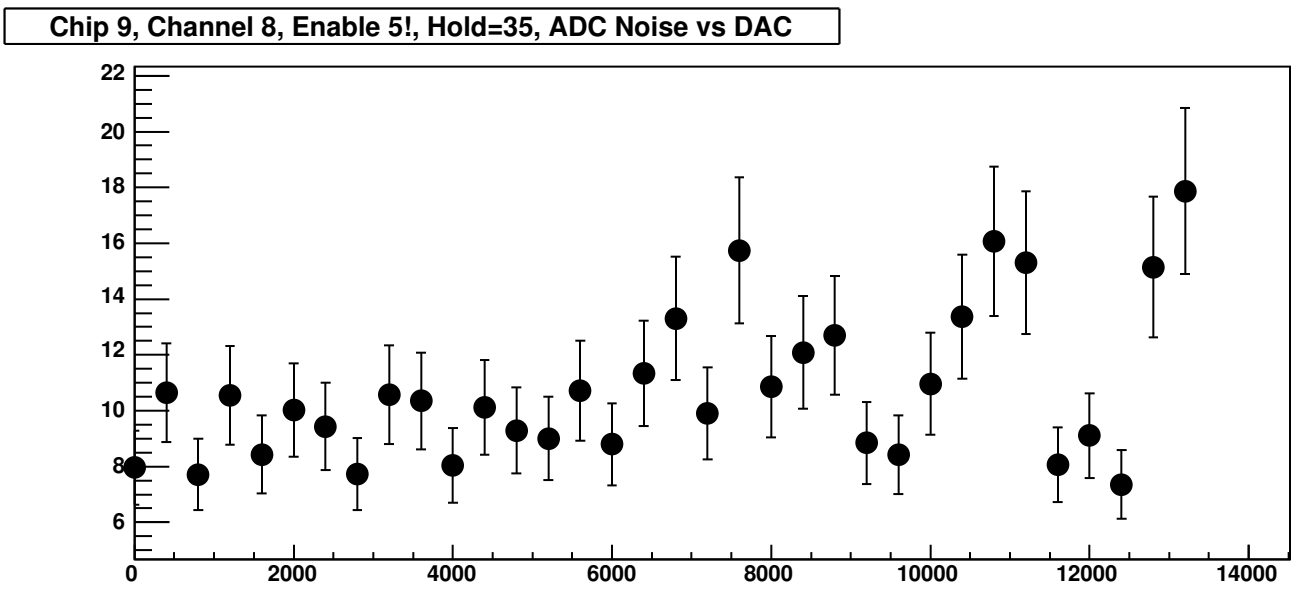
Chip 9, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

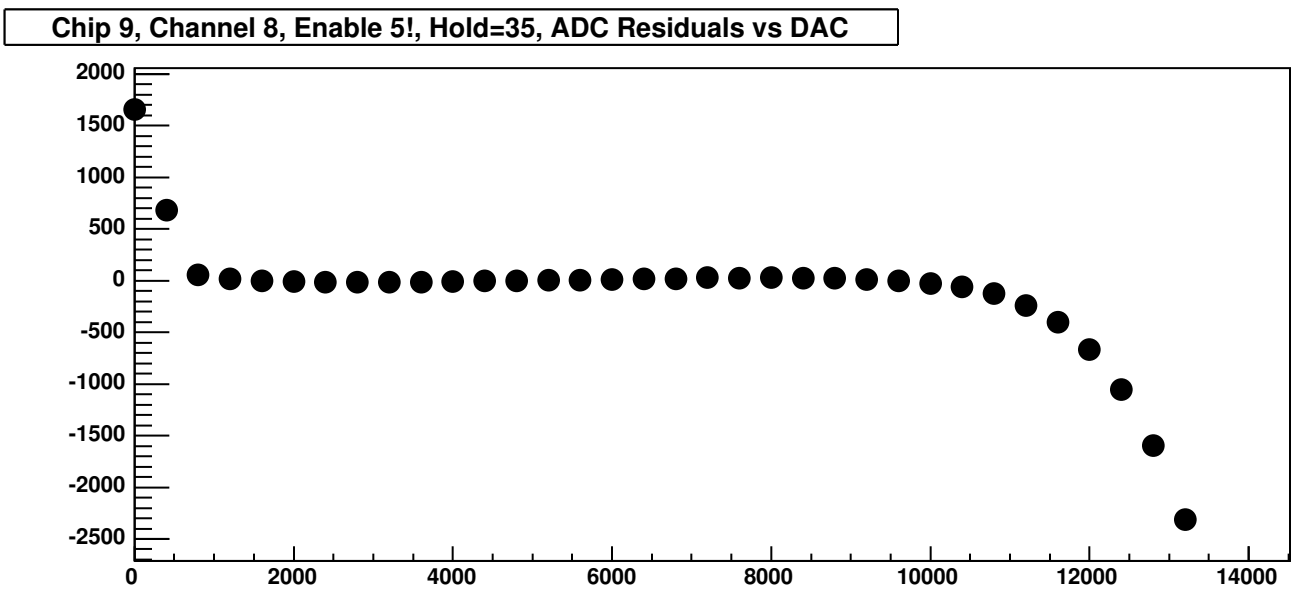


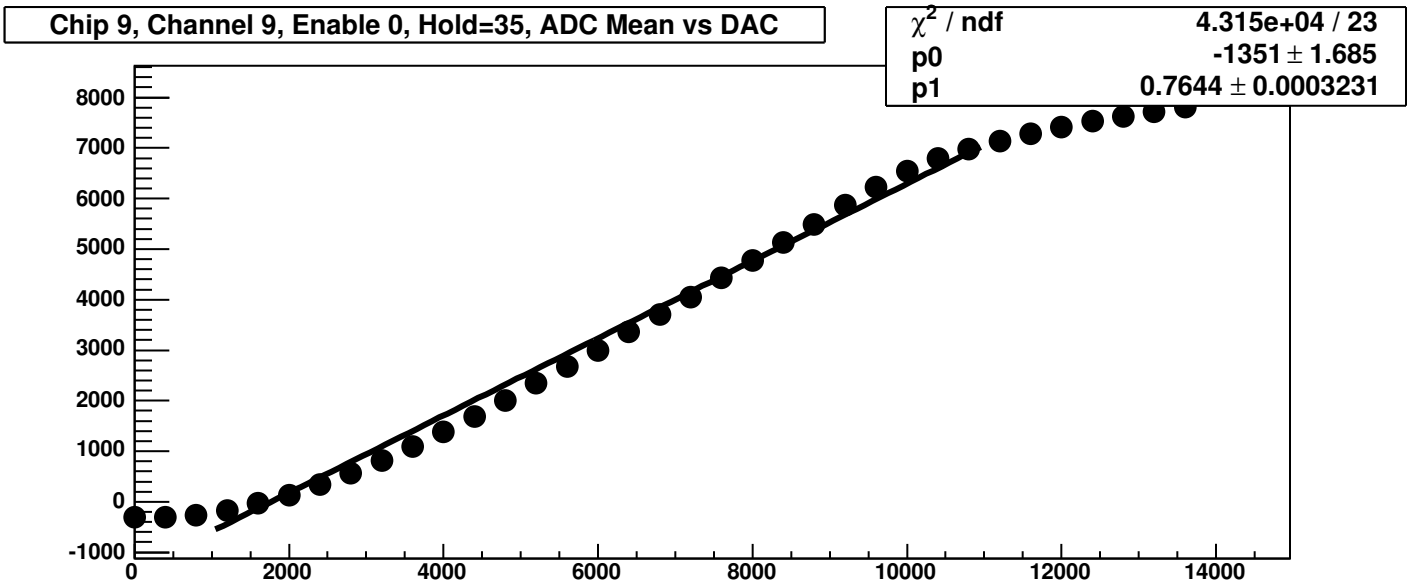
Chip 9, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

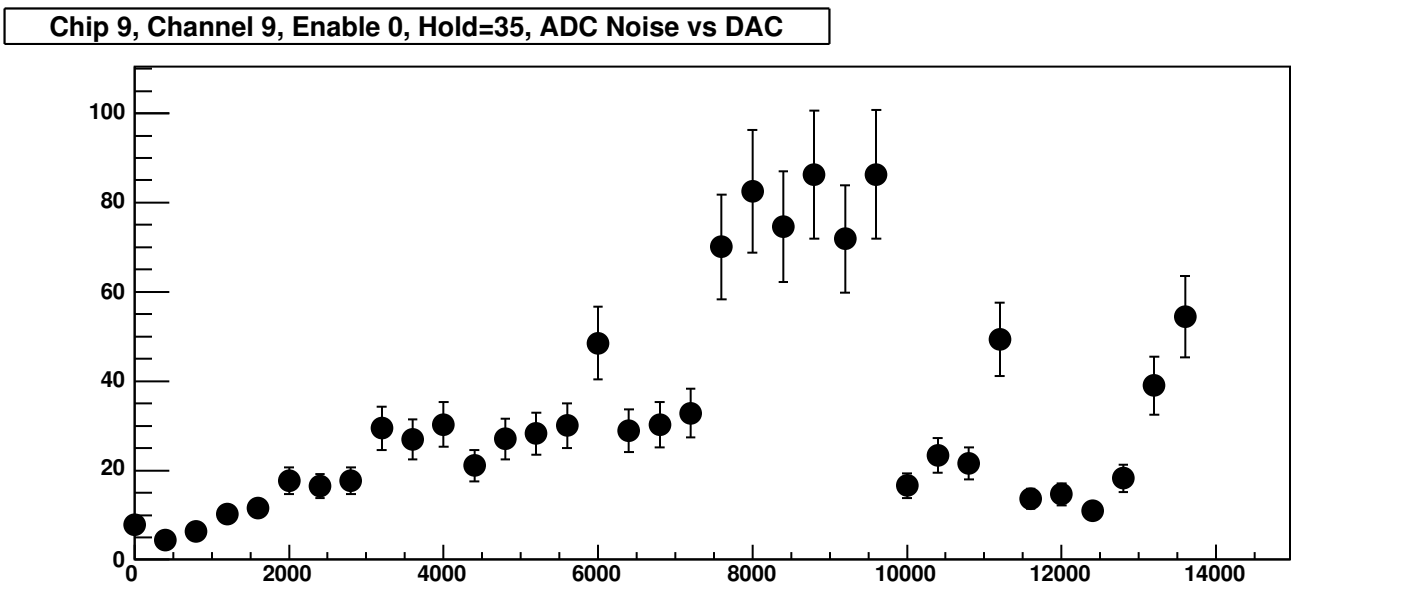


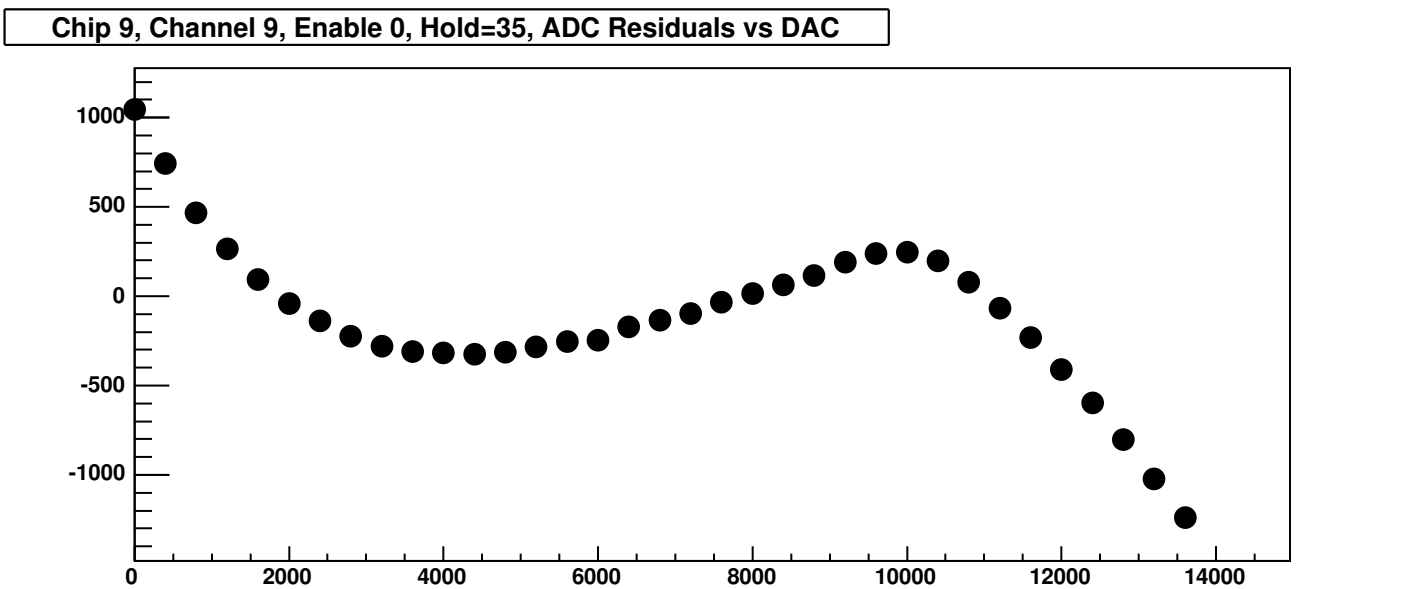

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2586 / 23
p0 -581.8 ± 1.024
p1 2.48 ± 0.000165
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

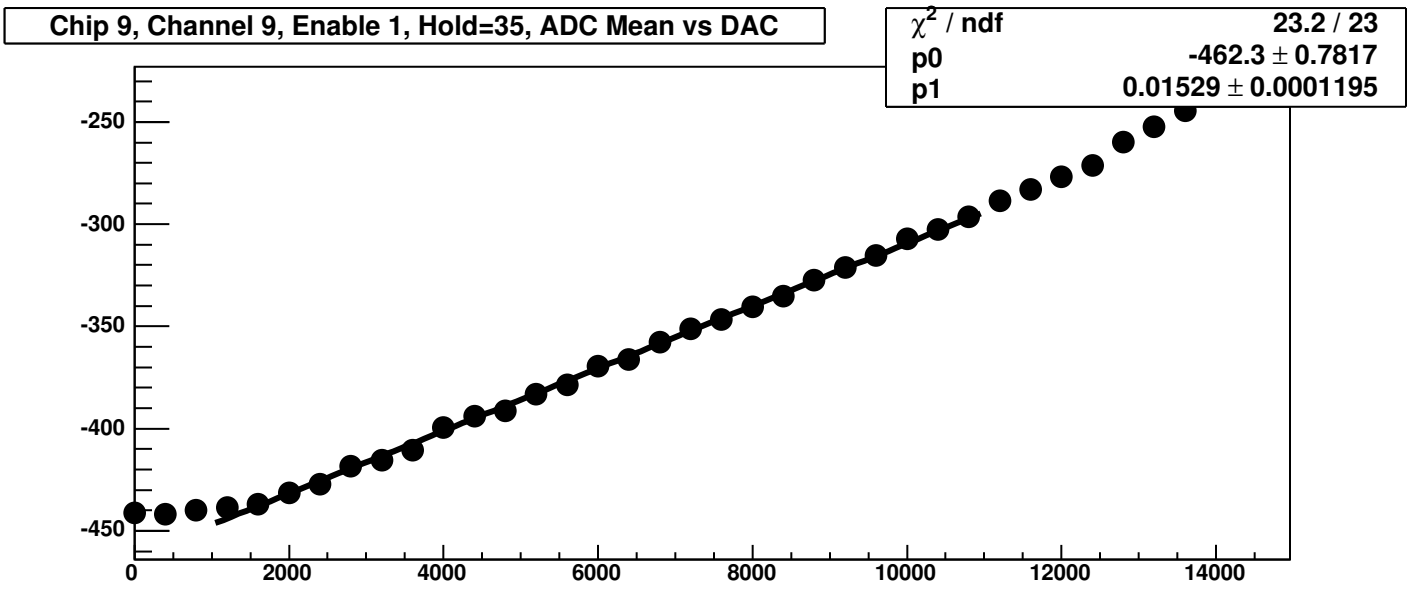

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

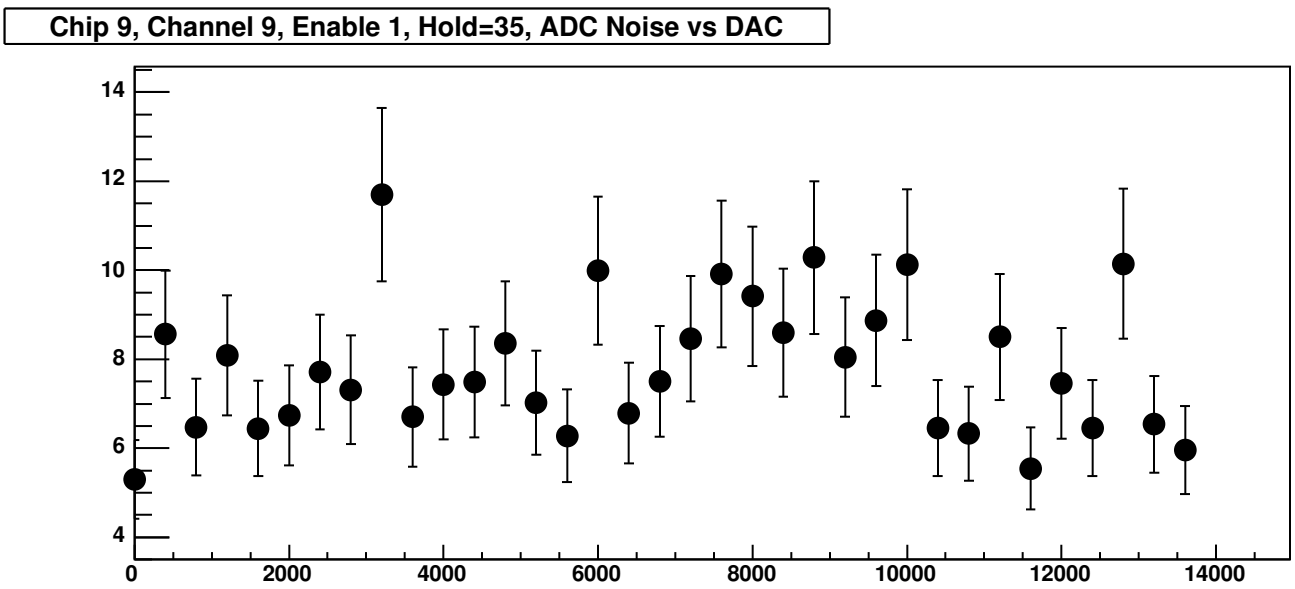

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

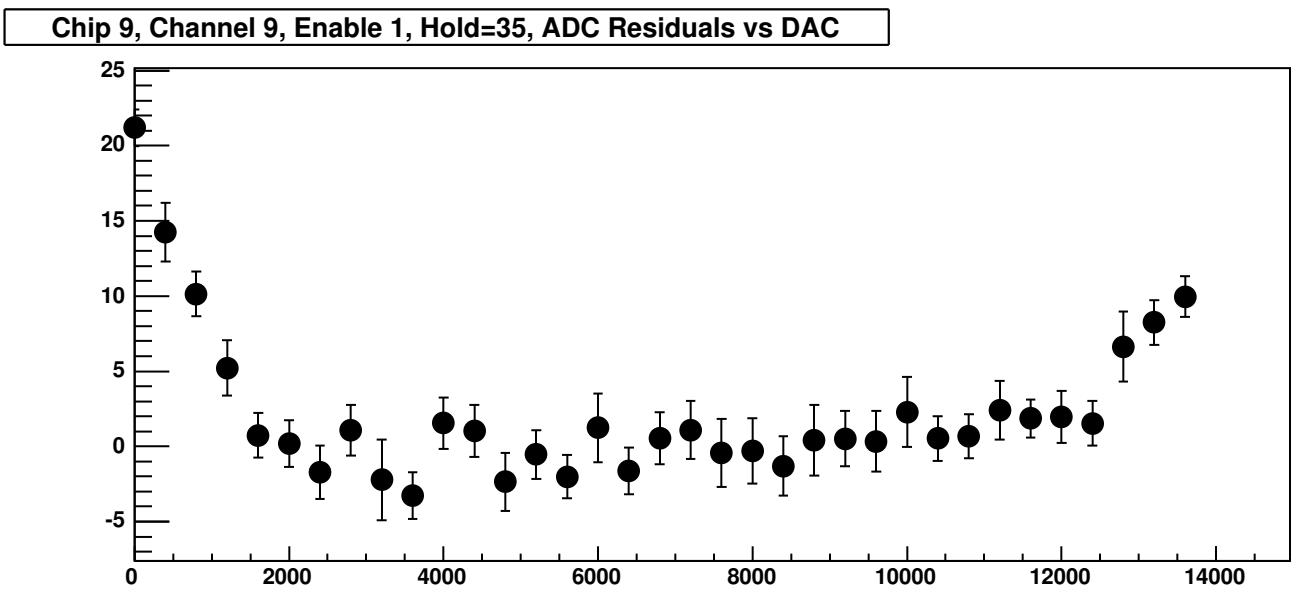

Chip 9, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.315e+04 / 23
p0 -1351 ± 1.685
p1 0.7644 ± 0.0003231
8000
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

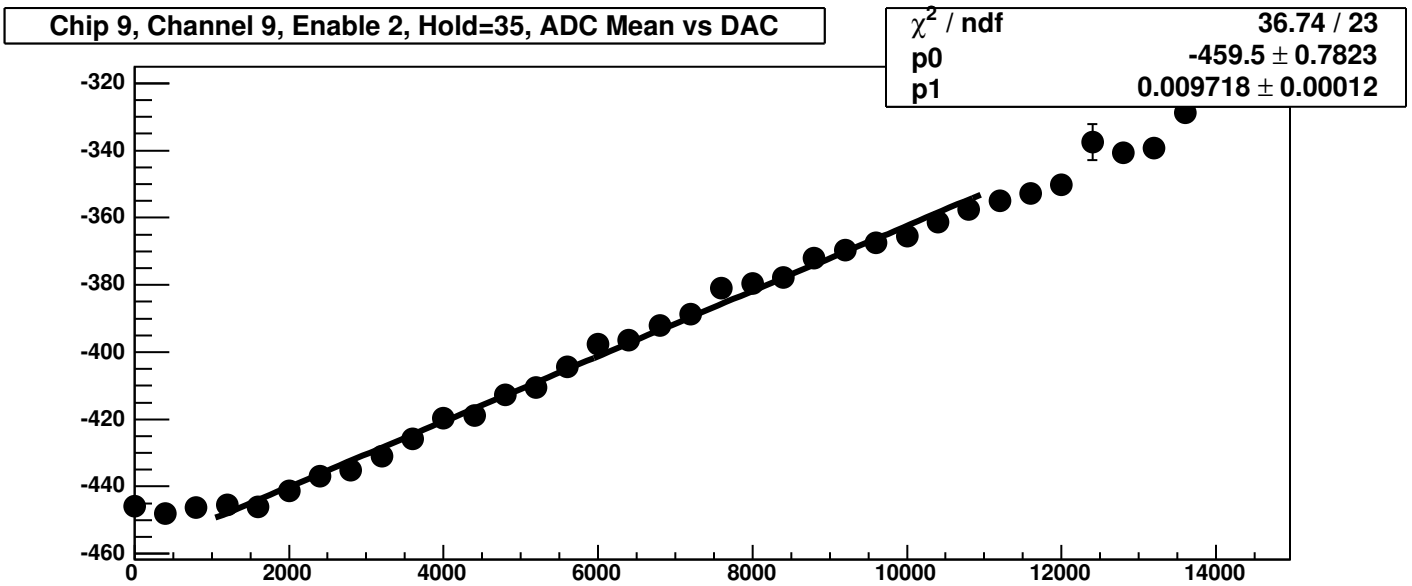

Chip 9, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

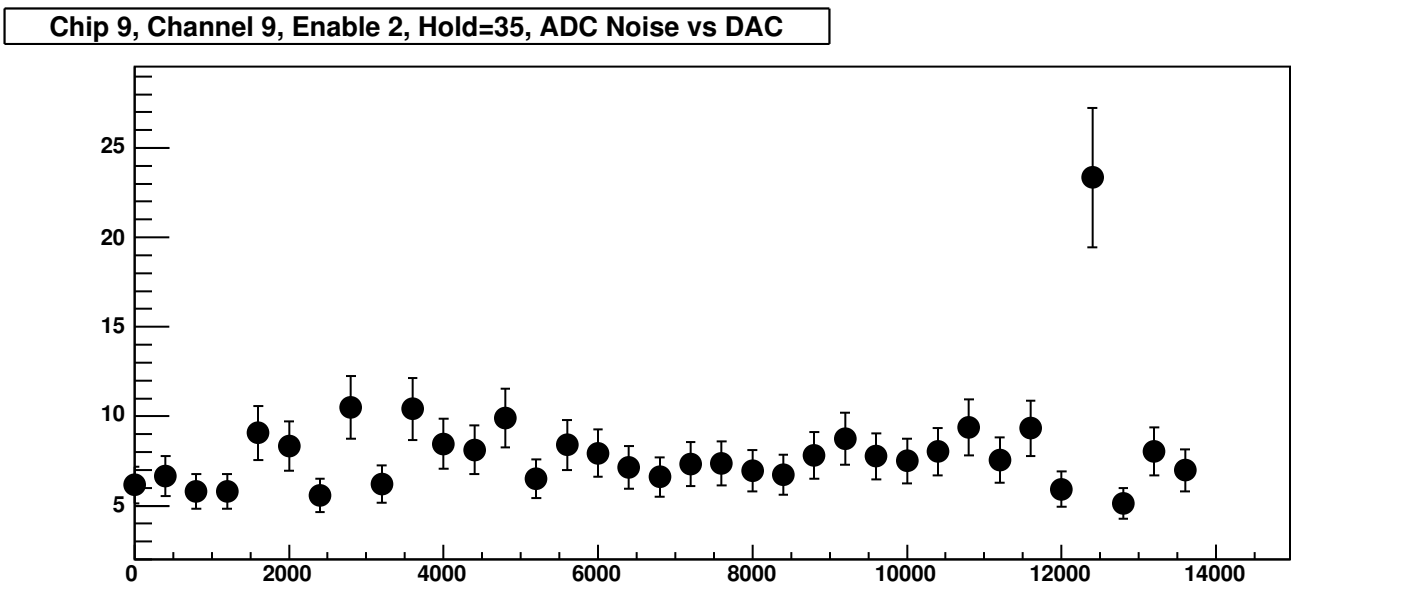

Chip 9, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

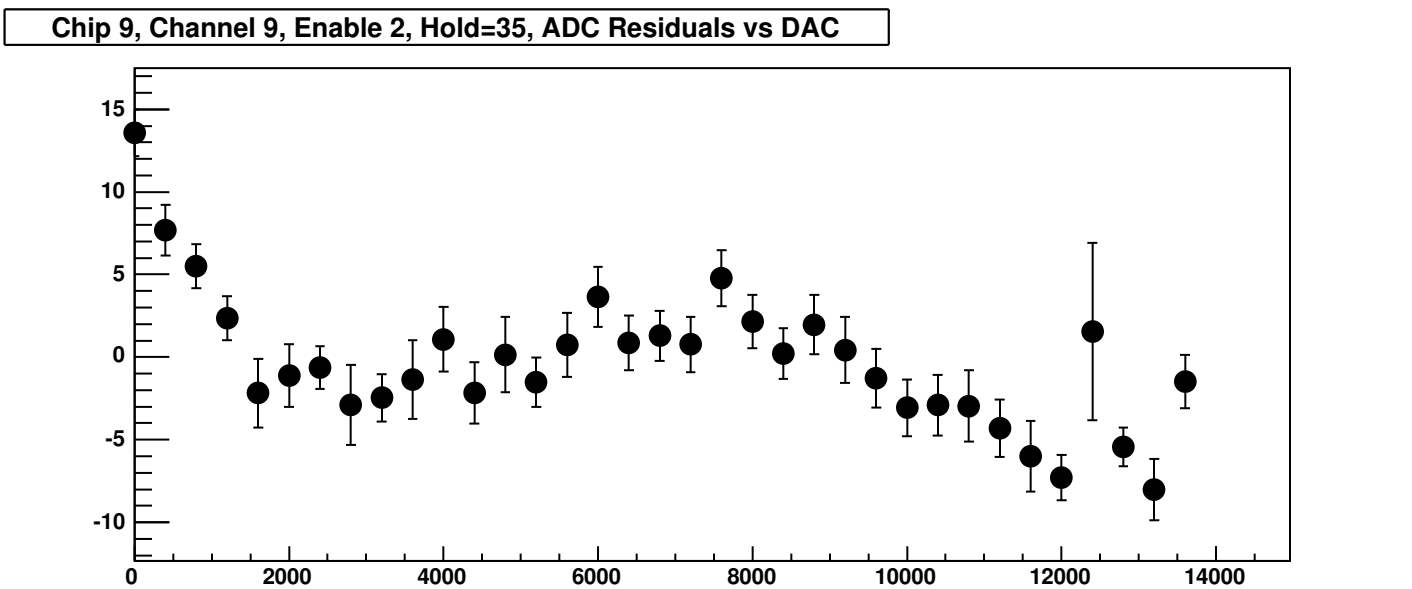

Chip 9, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.2 / 23
p0 -462.3 ± 0.7817
p1 0.01529 ± 0.0001195
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

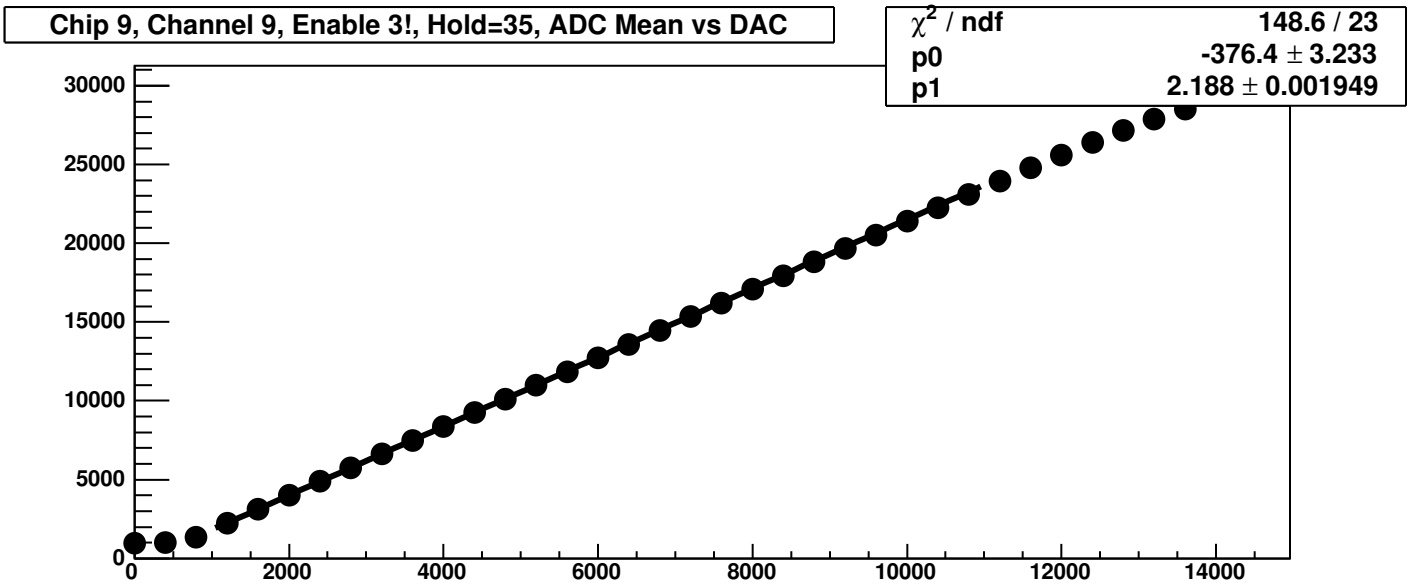

Chip 9, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

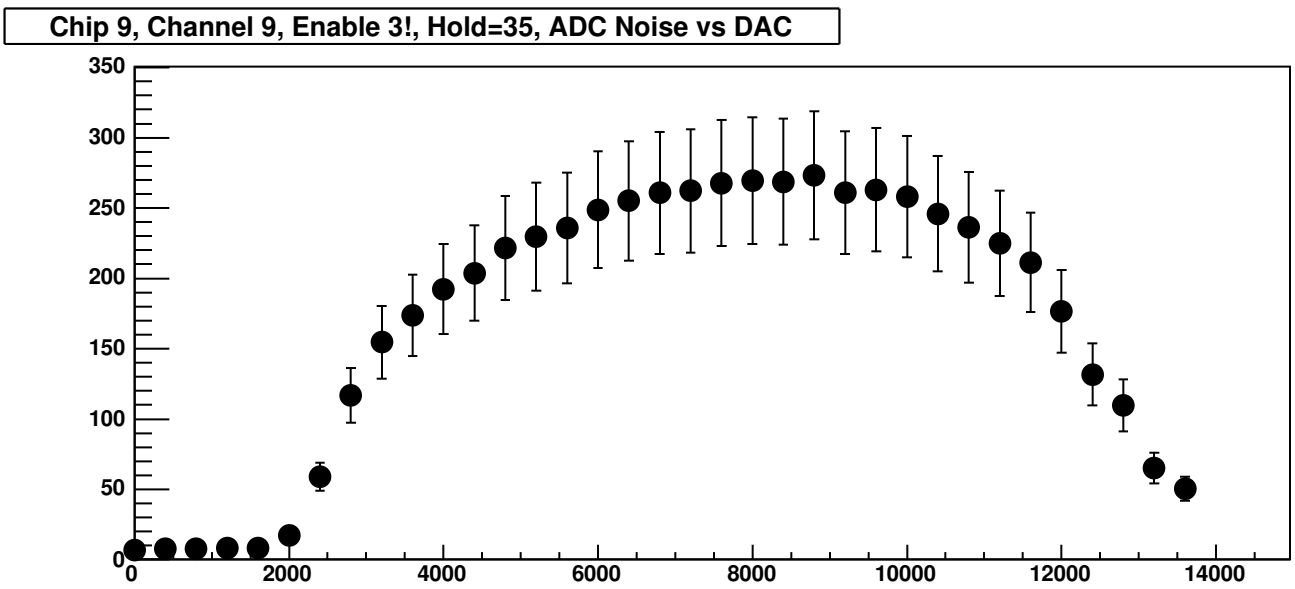

Chip 9, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

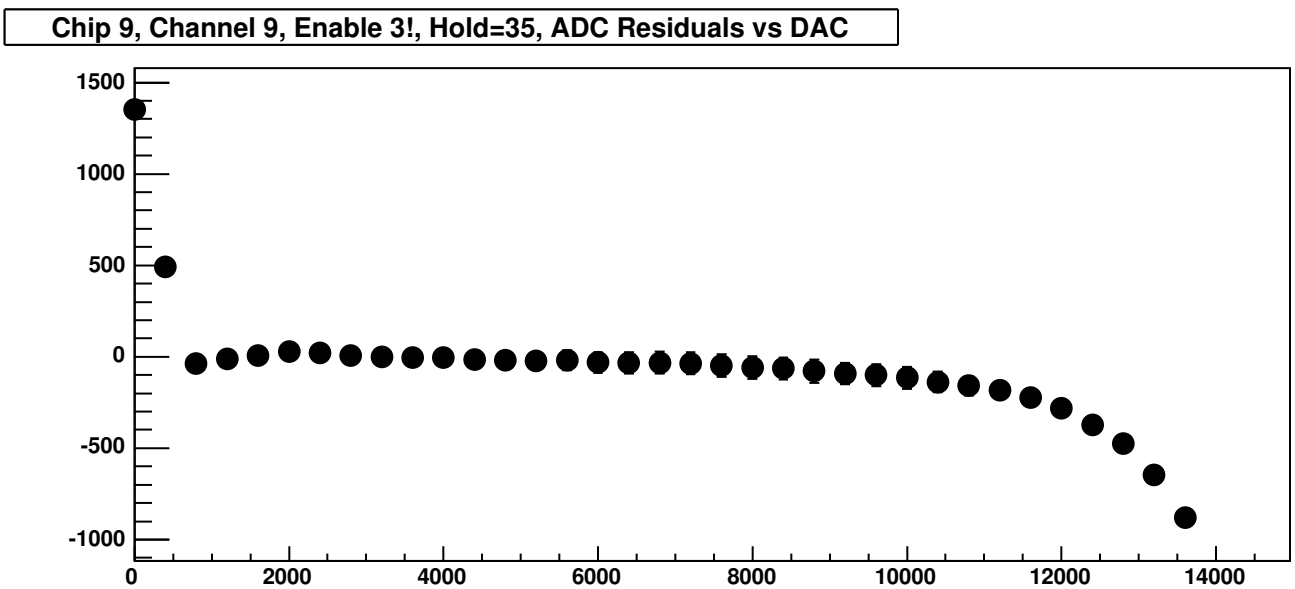

Chip 9, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.74 / 23
p0 -459.5 ± 0.7823
p1 0.009718 ± 0.00012
-320
-340
-360
-380
-400
-420
-440
-460
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

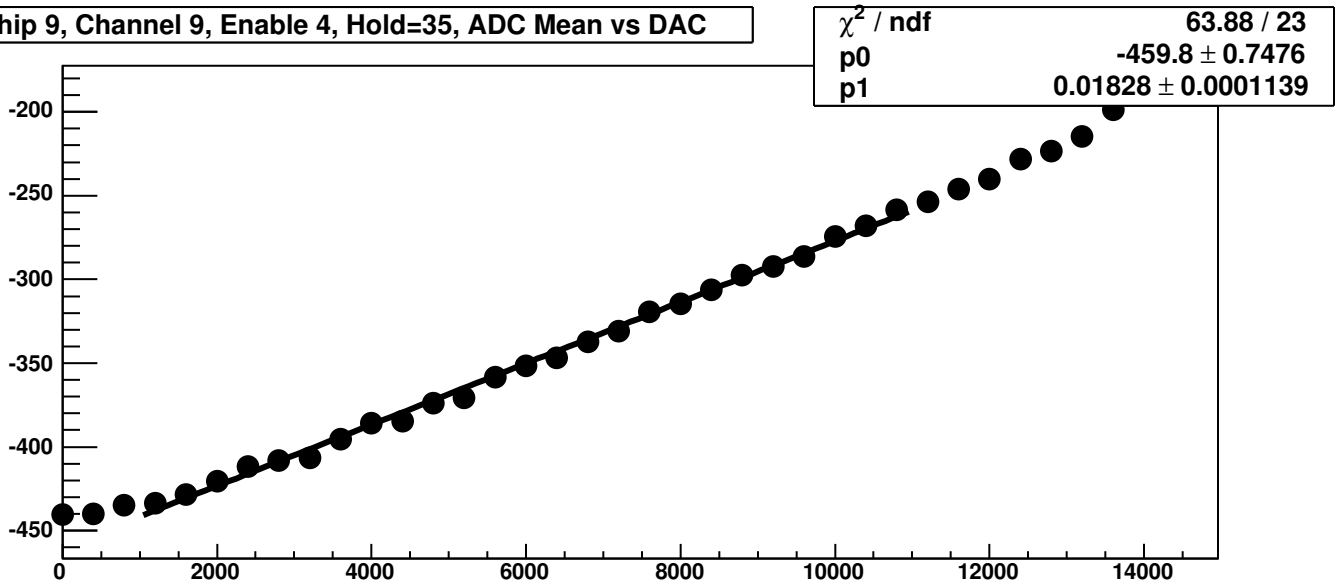

Chip 9, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 148.6 / 23
p0 -376.4 ± 3.233
p1 2.188 ± 0.001949
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

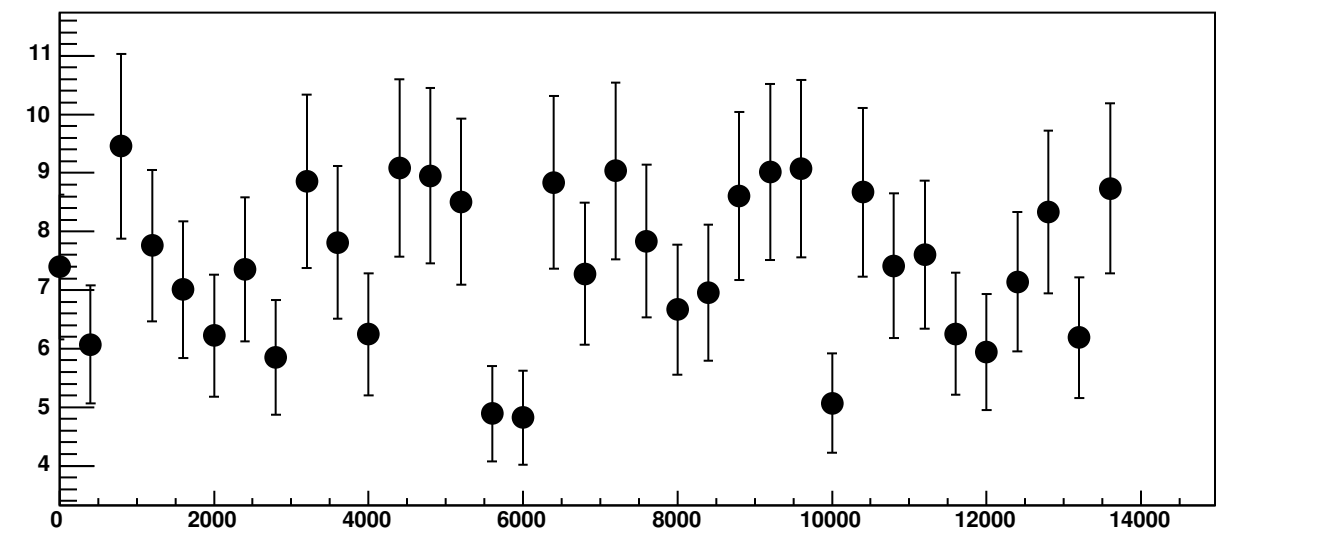

Chip 9, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

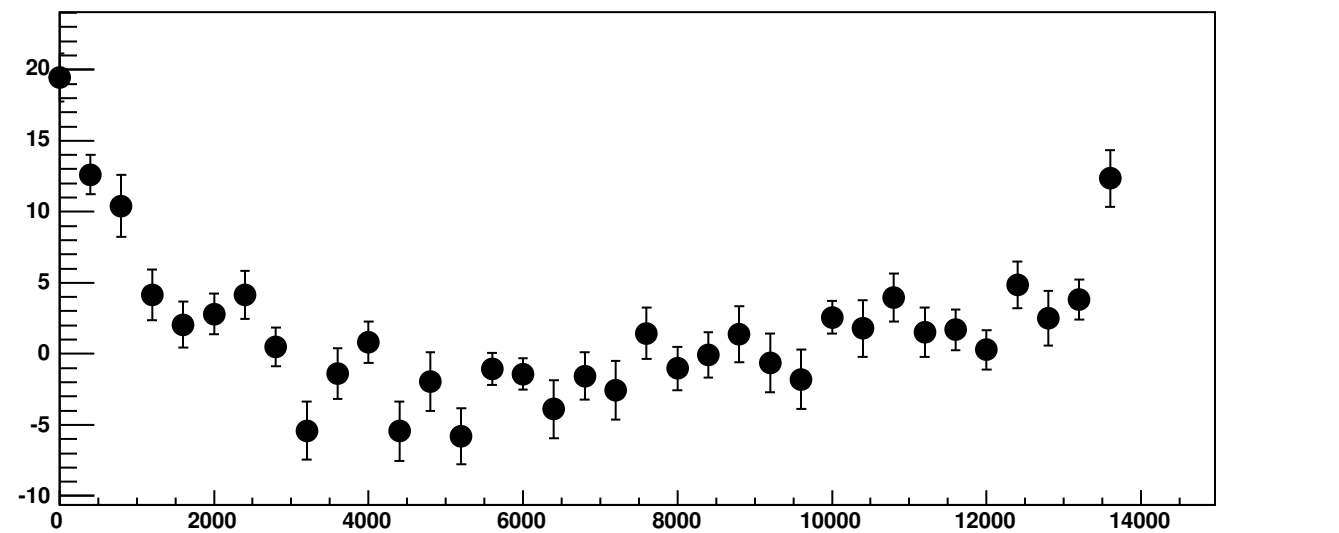
Chip 9, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

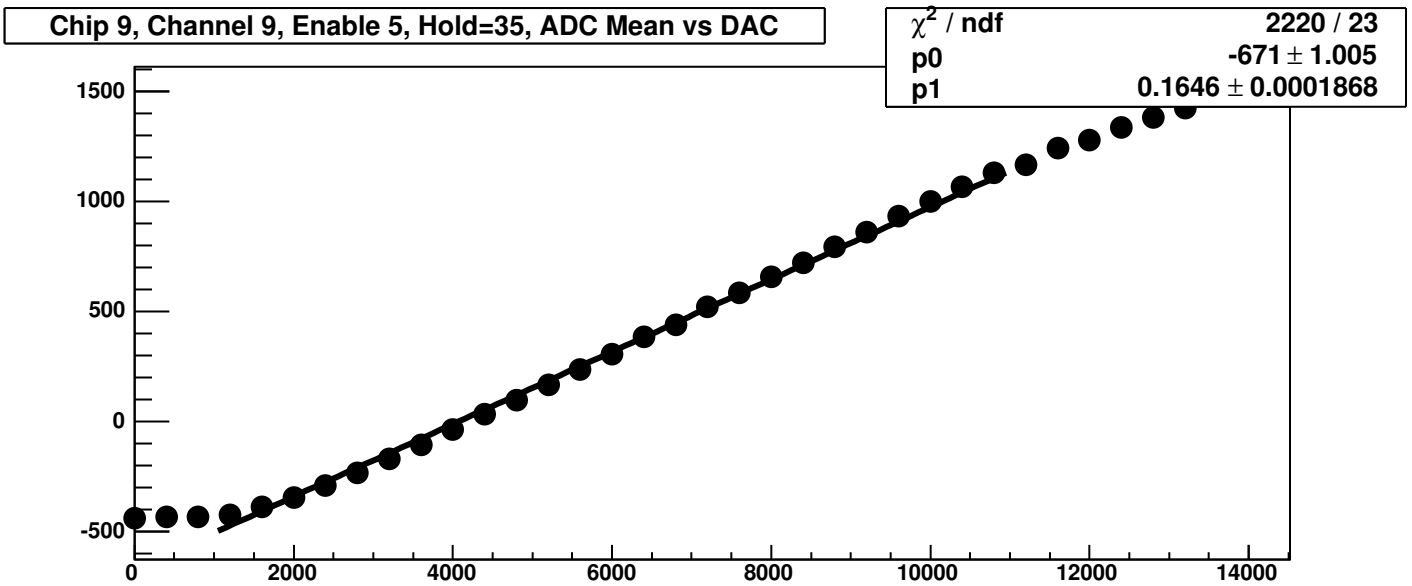


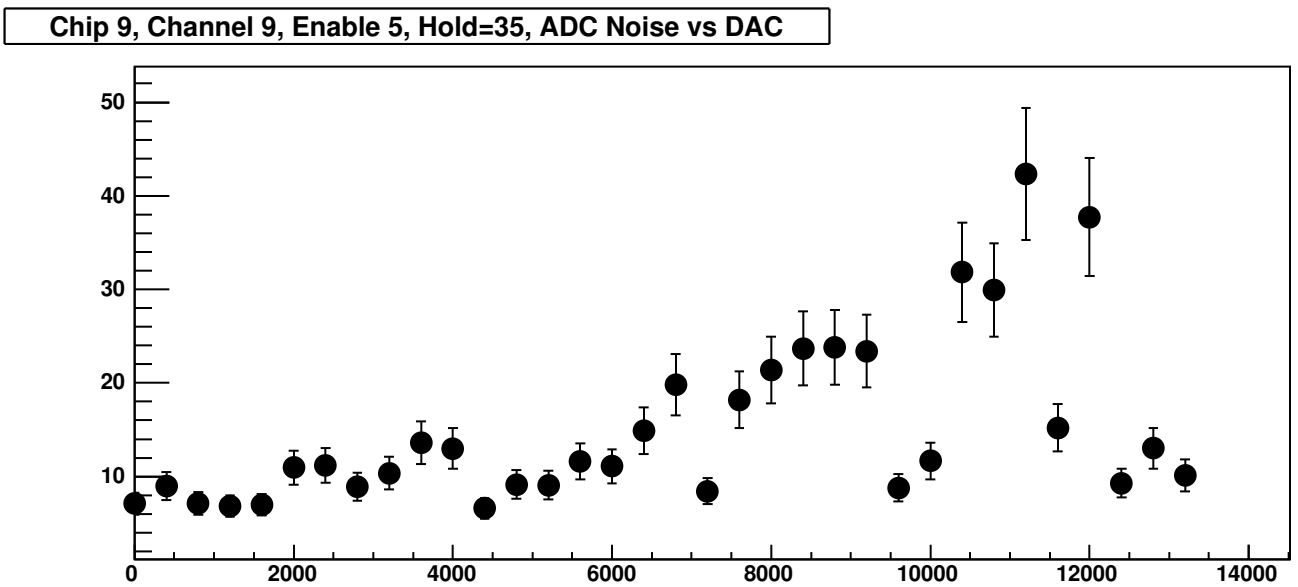
Chip 9, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

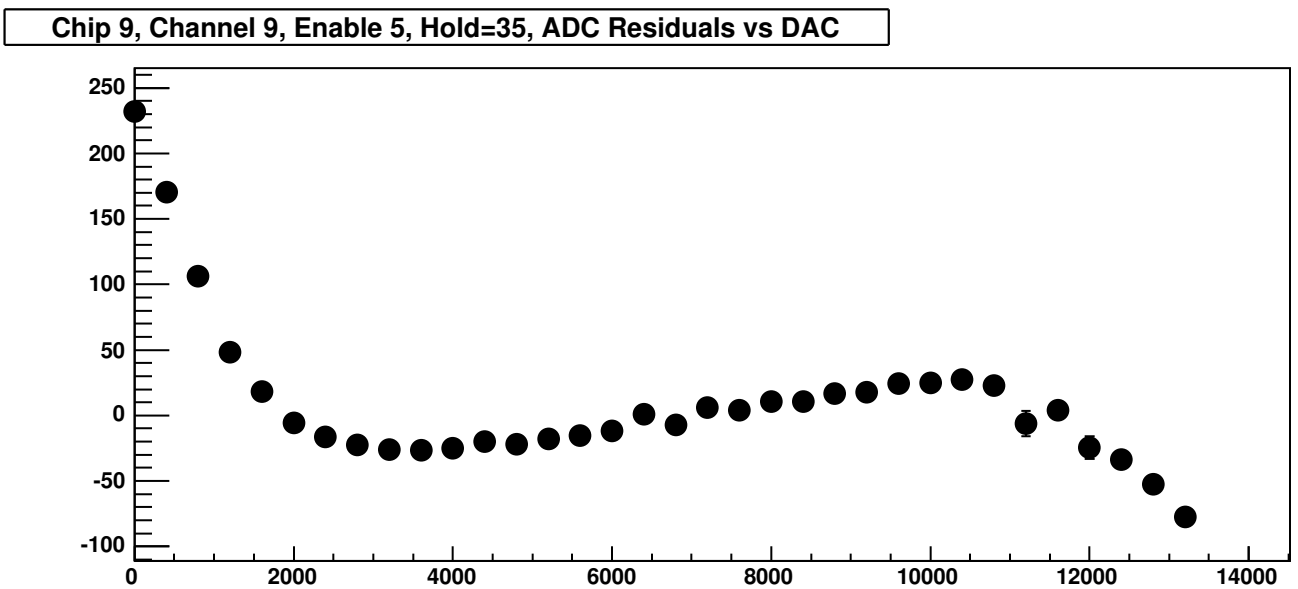


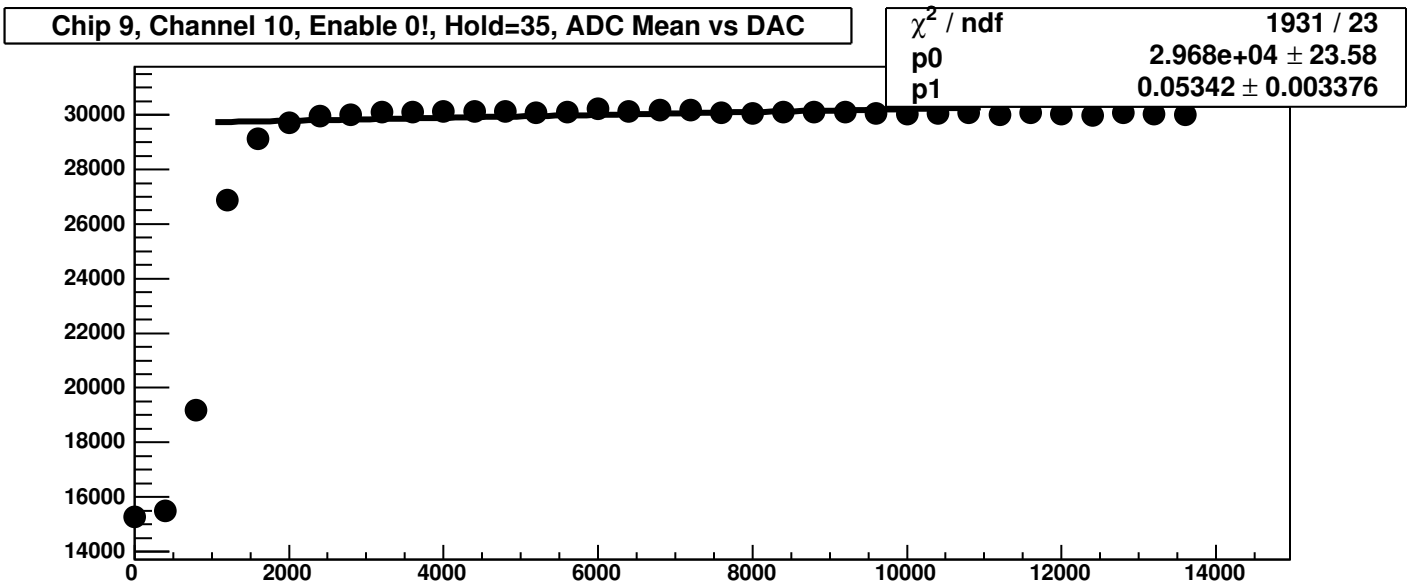
Chip 9, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

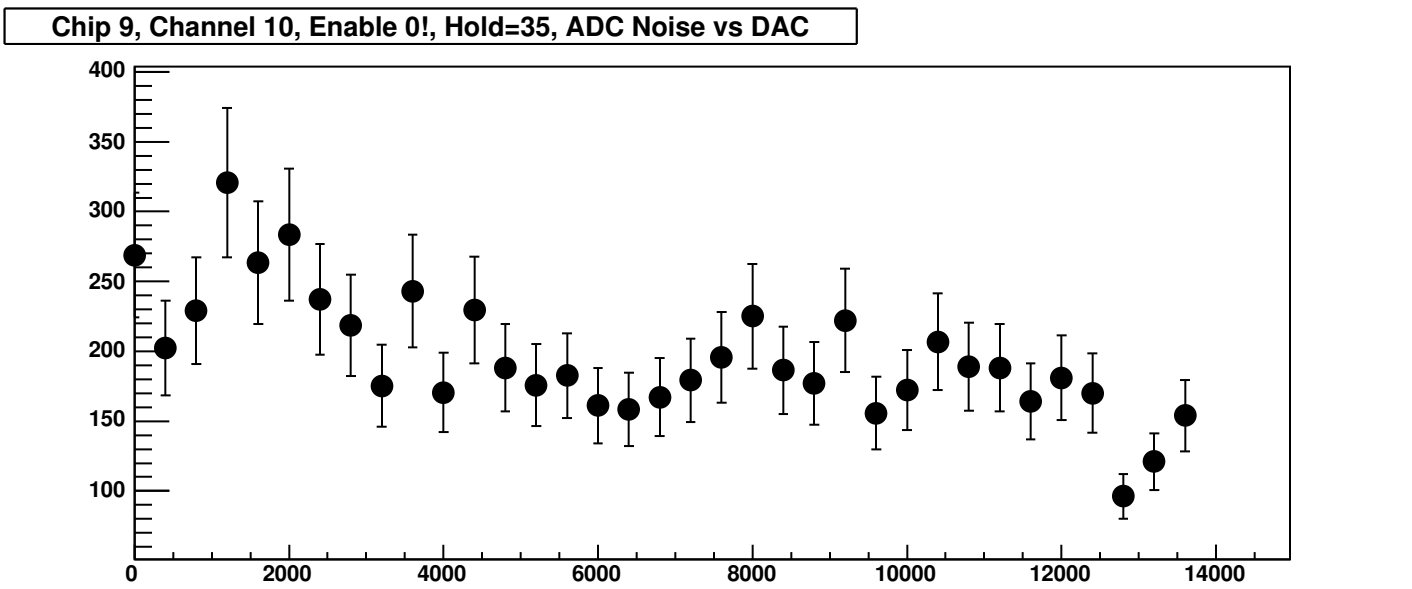


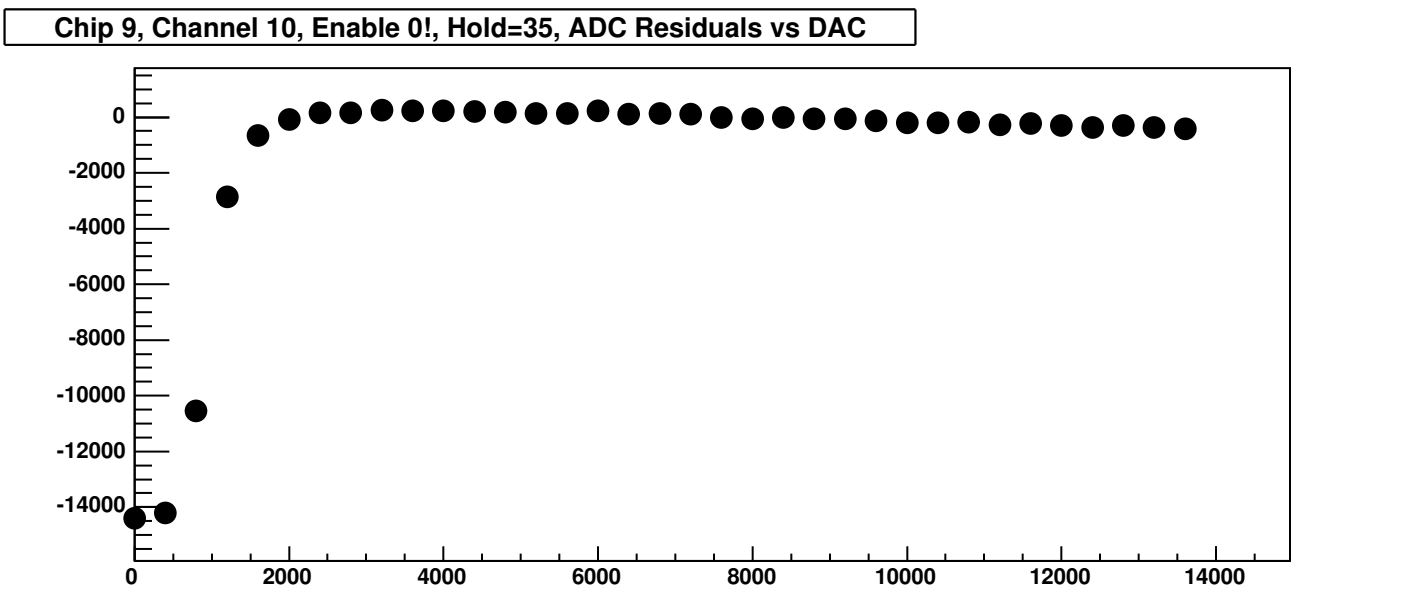

Chip 9, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2220 / 23
p0 -671 ± 1.005
p1 0.1646 ± 0.0001868
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

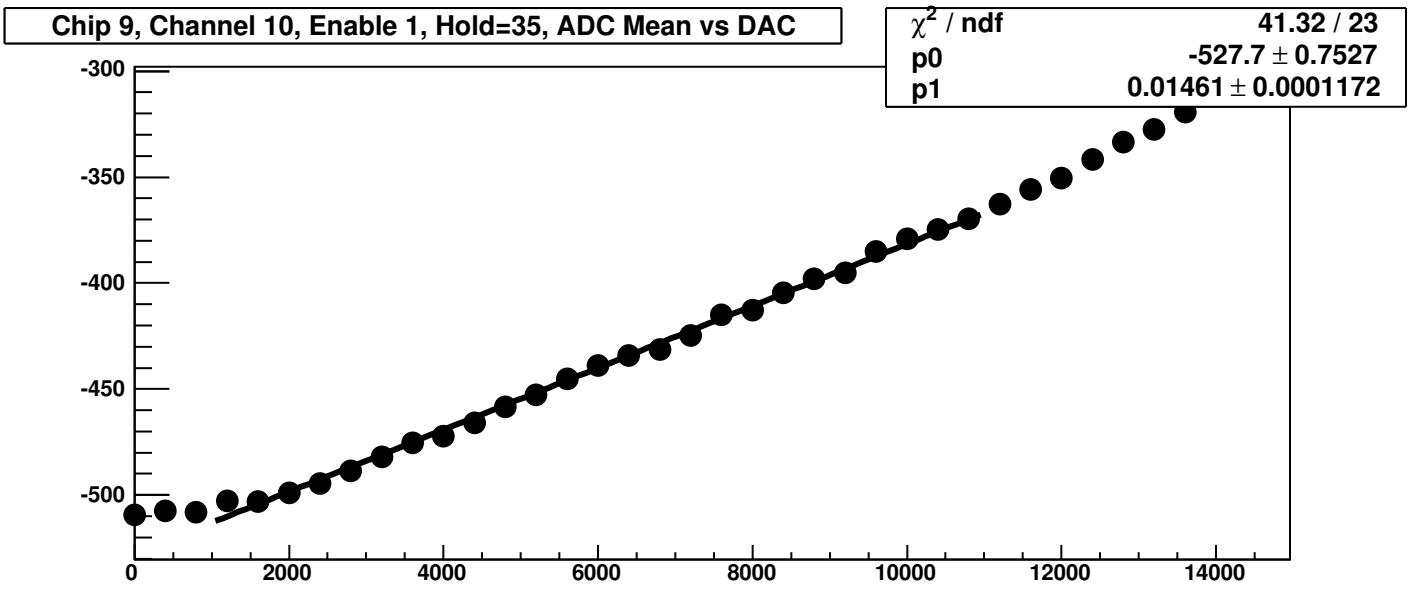

Chip 9, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

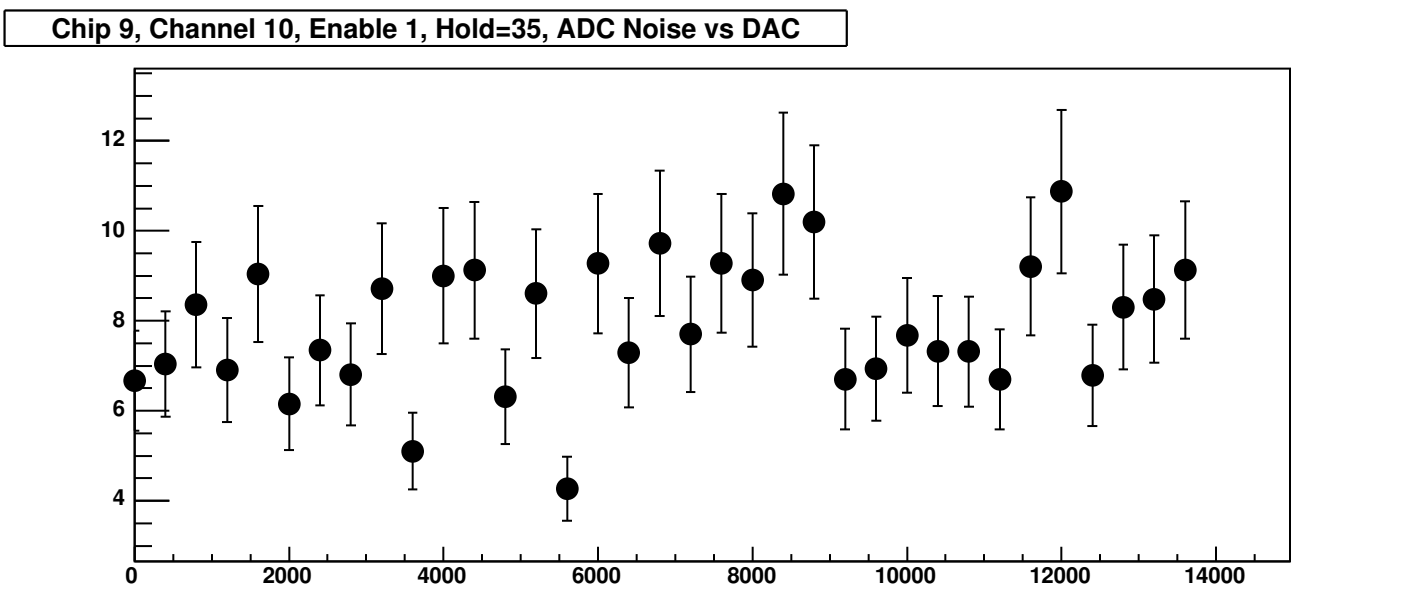

Chip 9, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

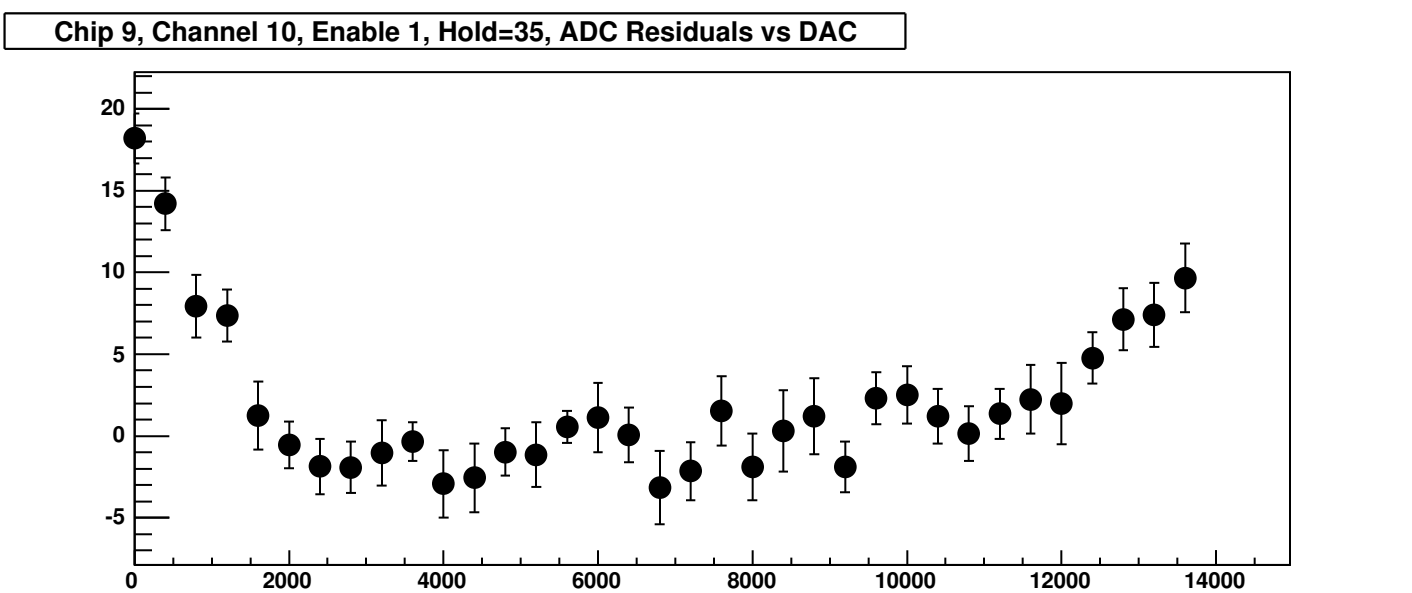

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1931 / 23
p0 2.968e+04 ± 23.58
p1 0.05342 ± 0.003376
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

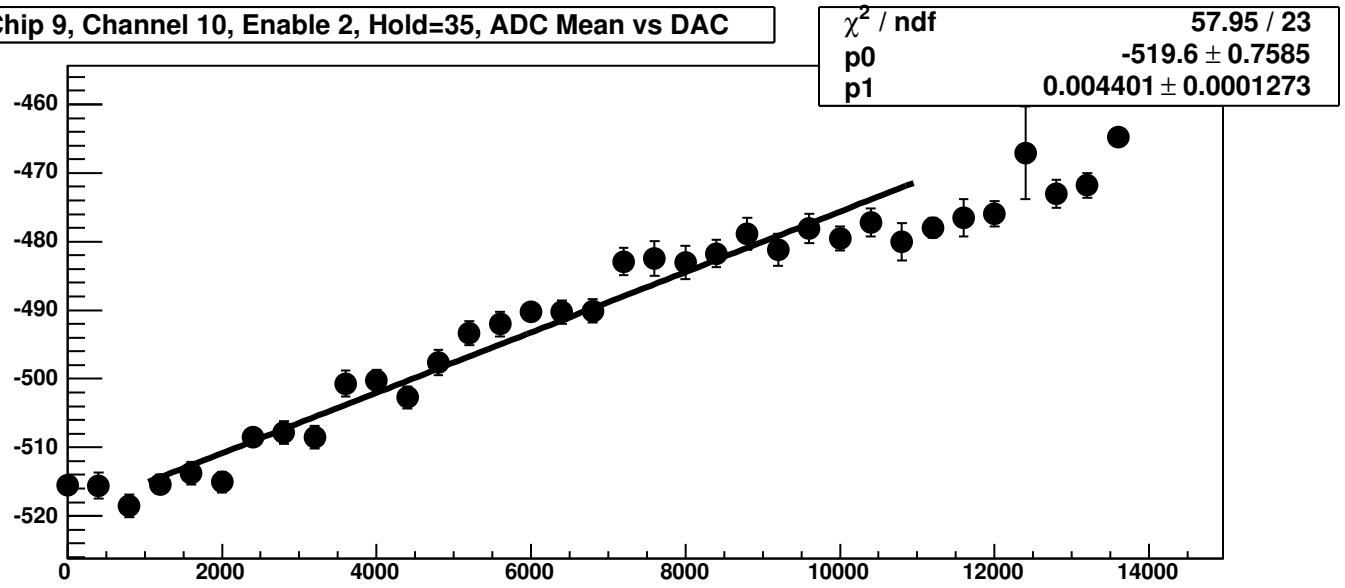

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.32 / 23
p0 -527.7 ± 0.7527
p1 0.01461 ± 0.0001172
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

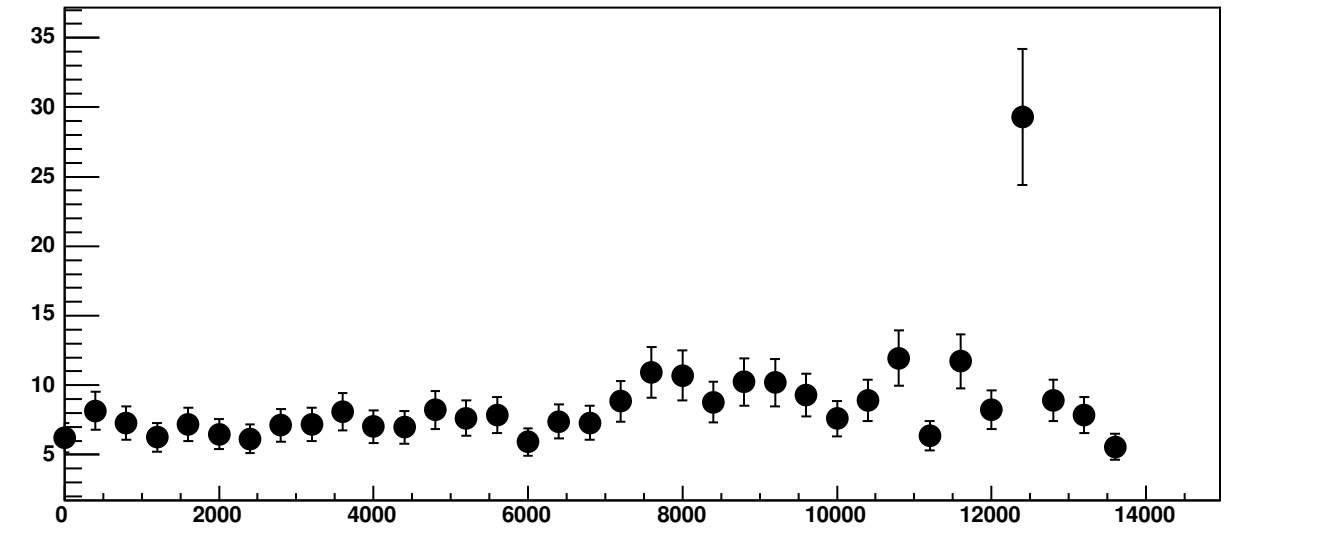

Chip 9, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

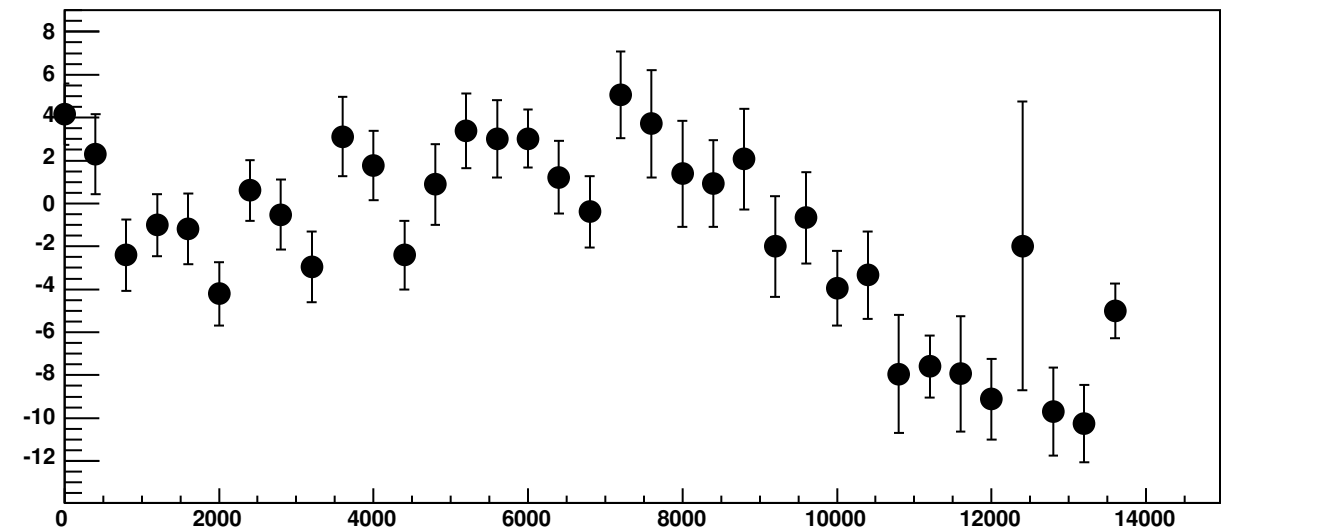
Chip 9, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC



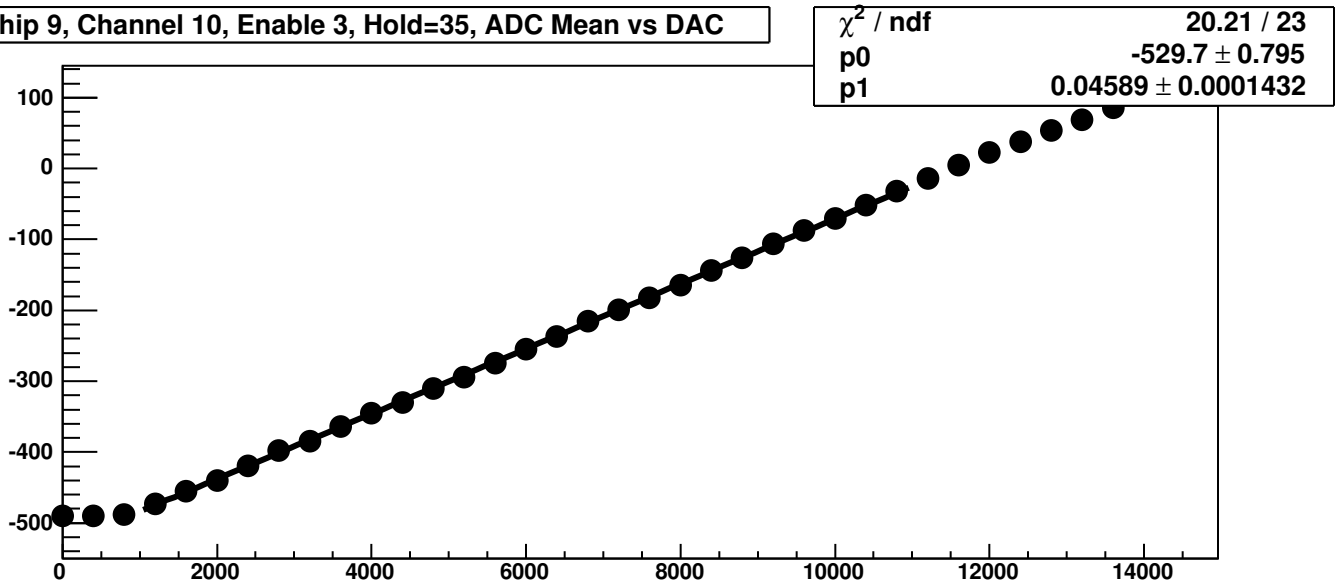
Chip 9, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC



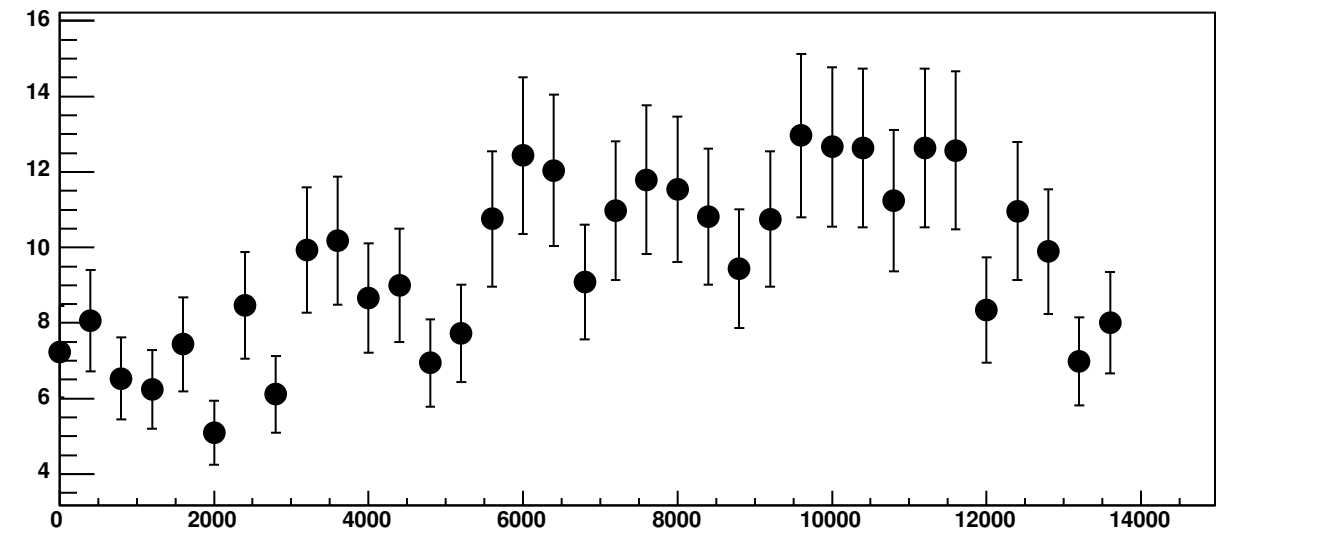
Chip 9, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC



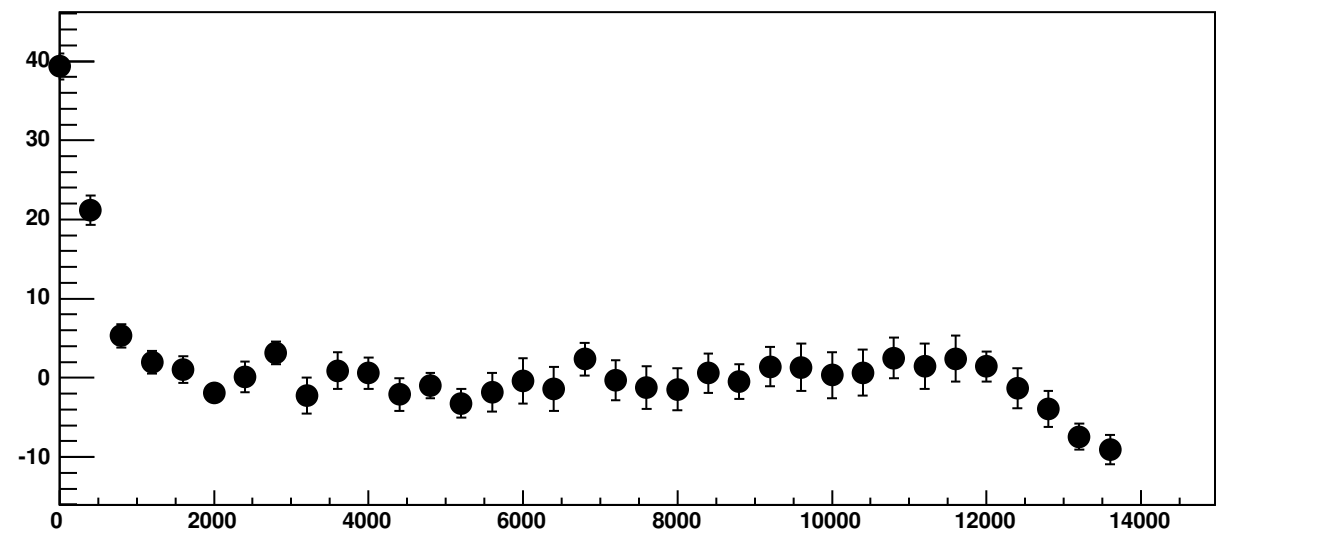
Chip 9, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

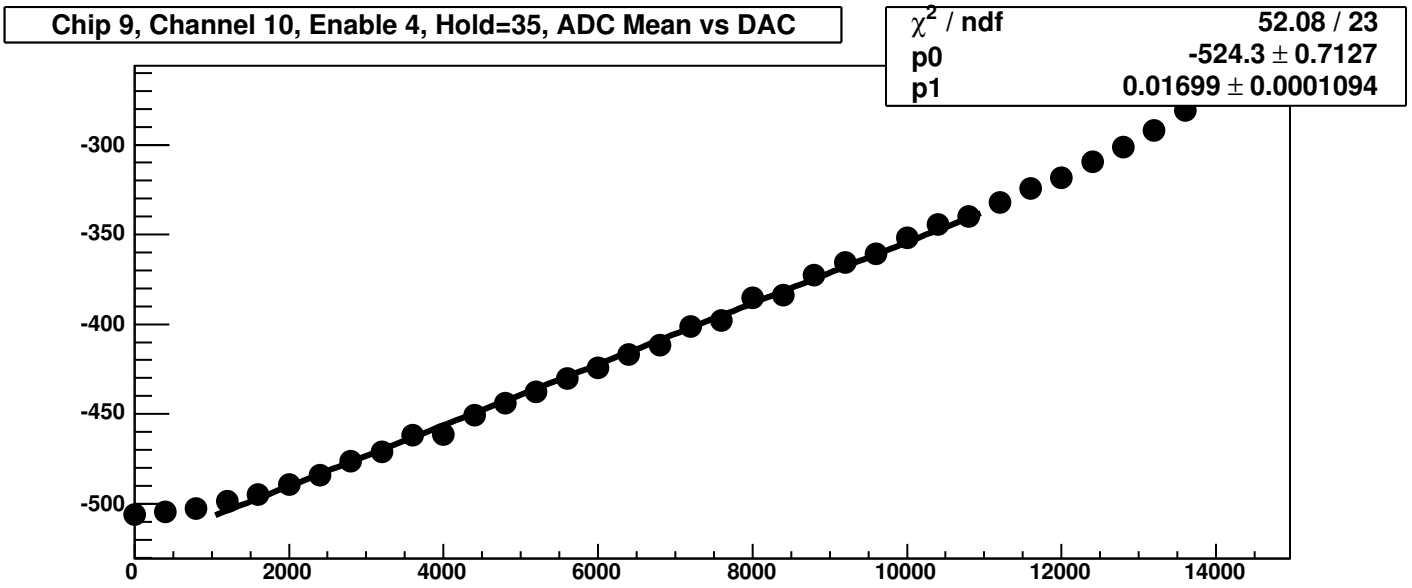


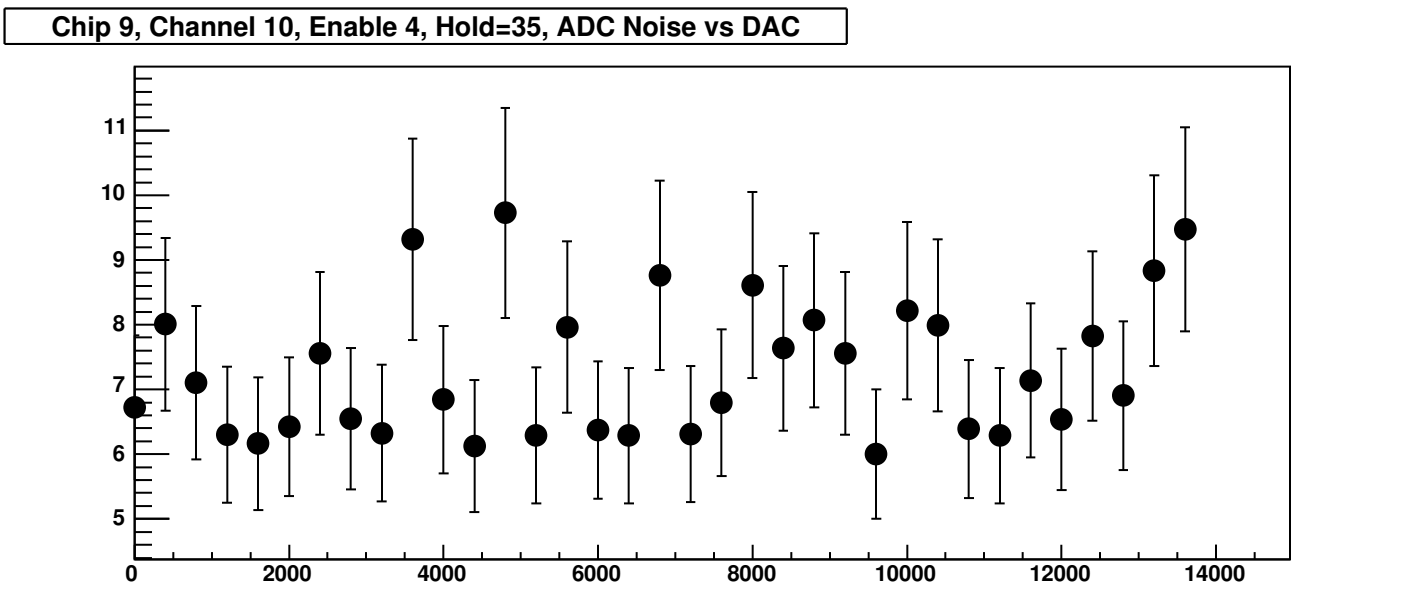
Chip 9, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC

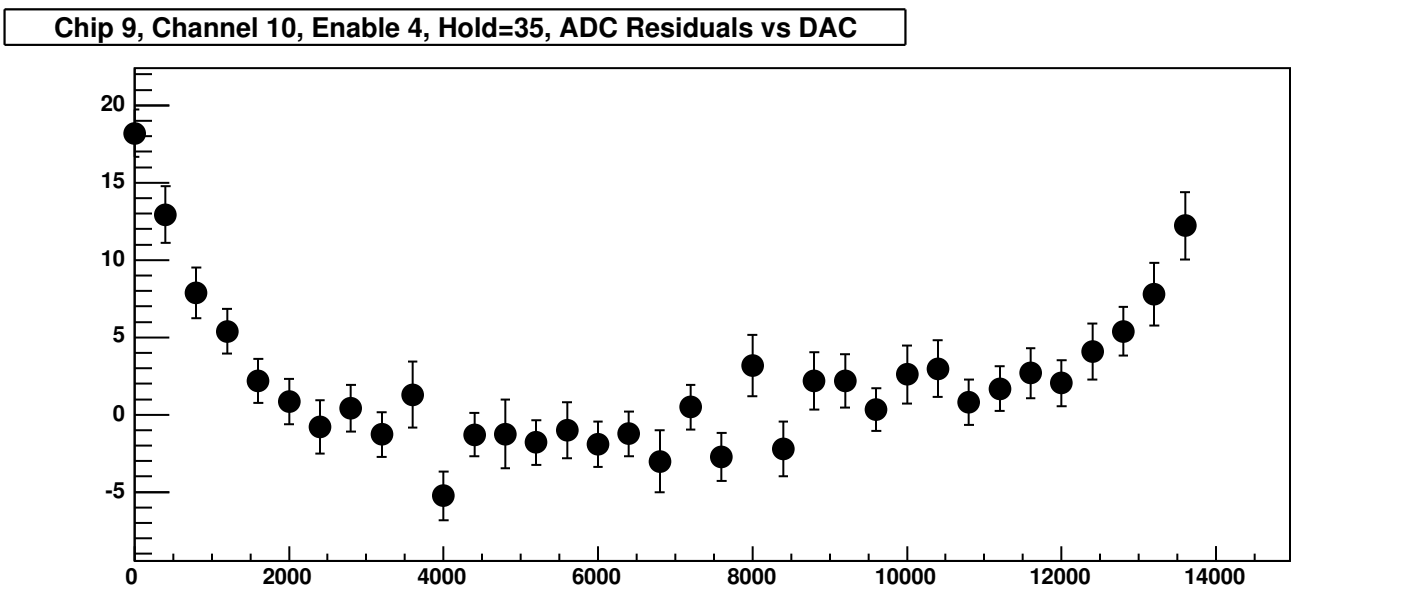


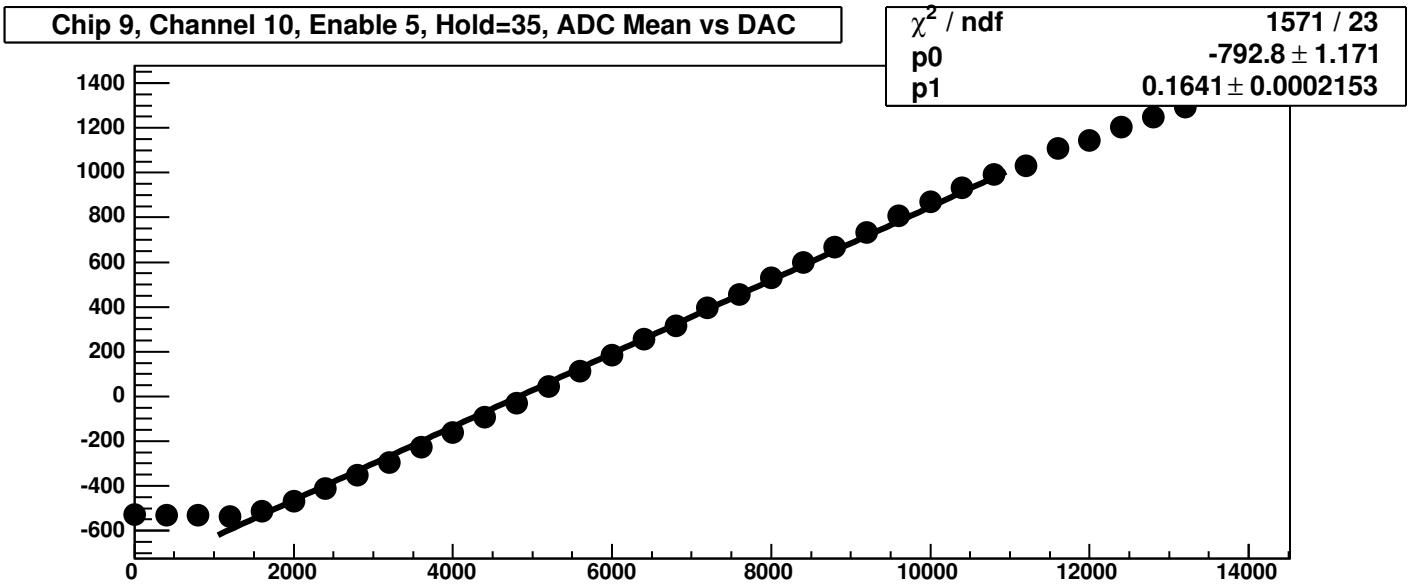
Chip 9, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

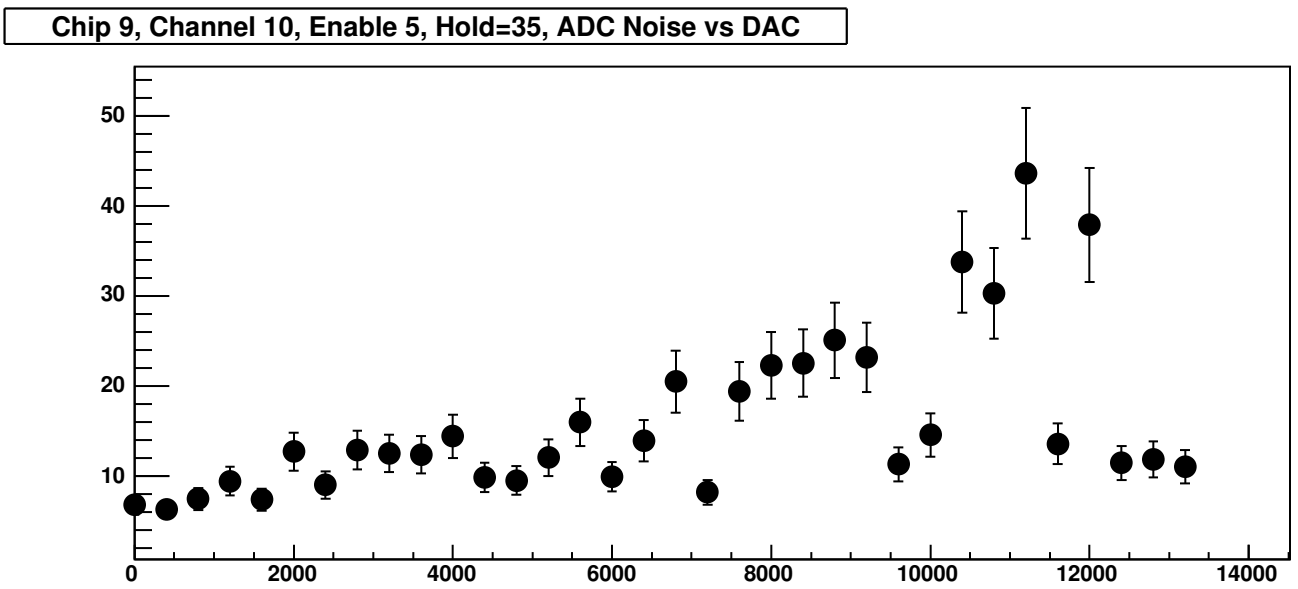


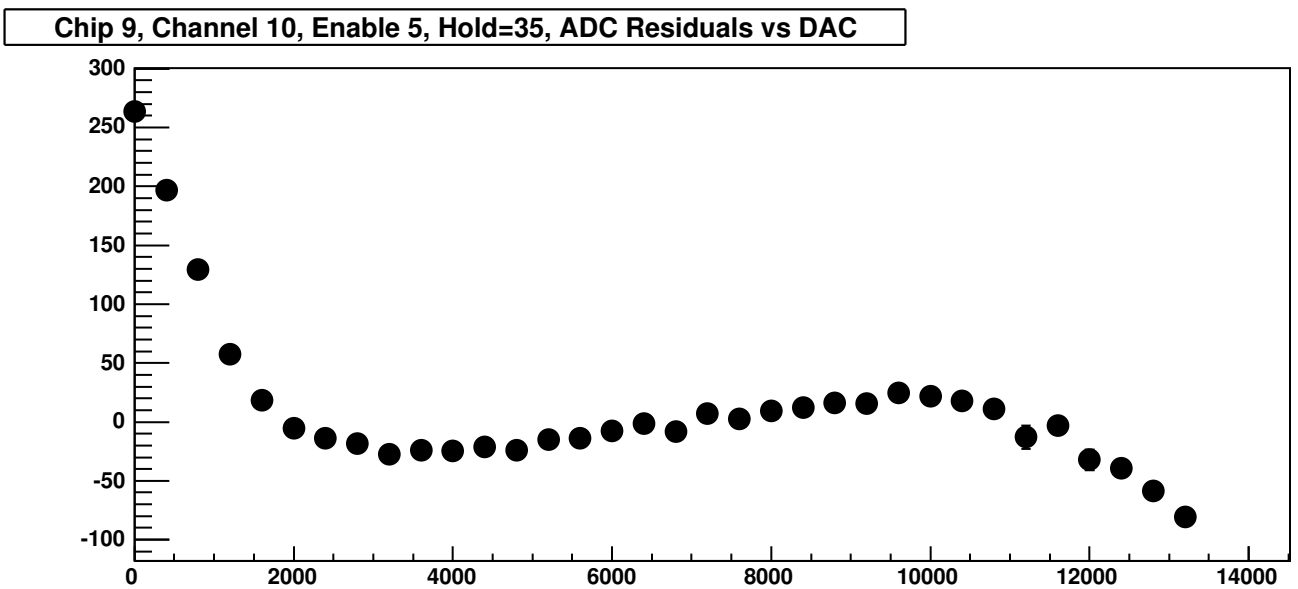

Chip 9, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 52.08 / 23
p0 -524.3 ± 0.7127
p1 0.01699 ± 0.0001094
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

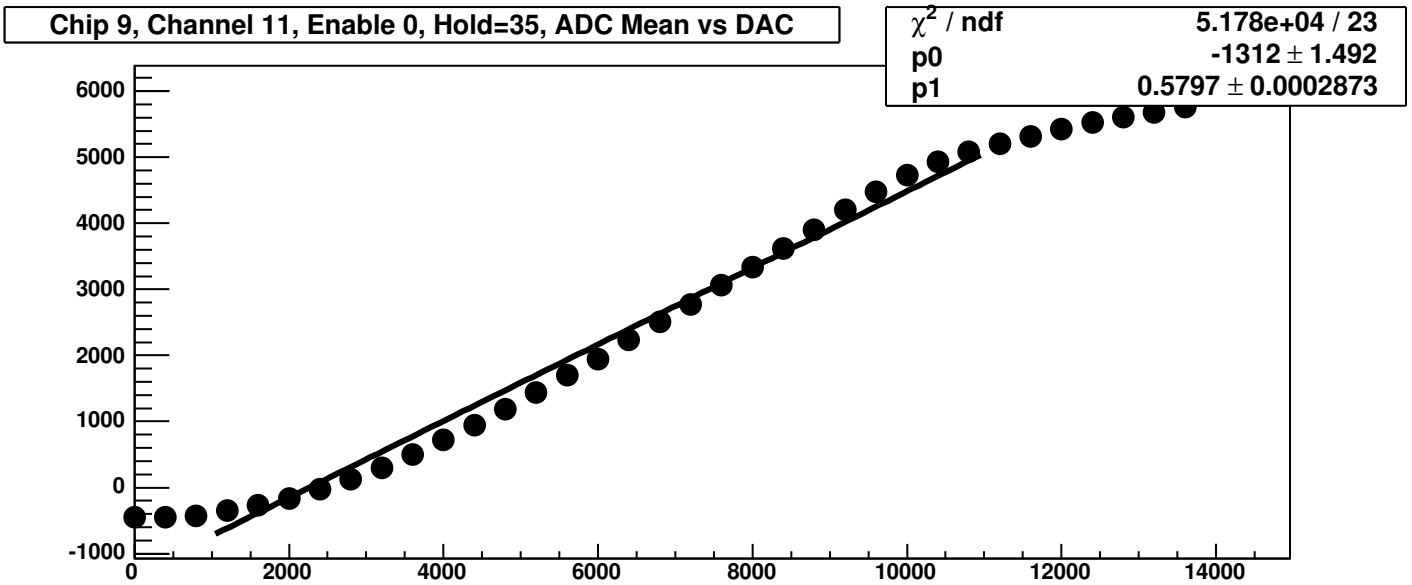

Chip 9, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

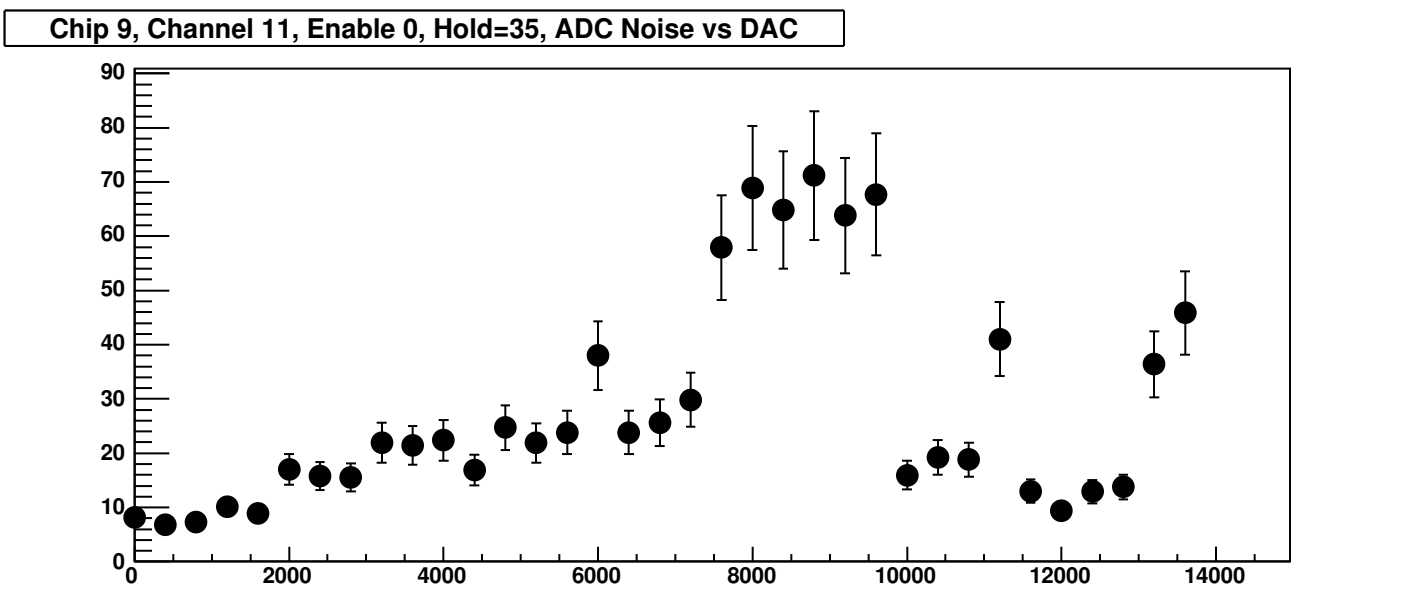

Chip 9, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

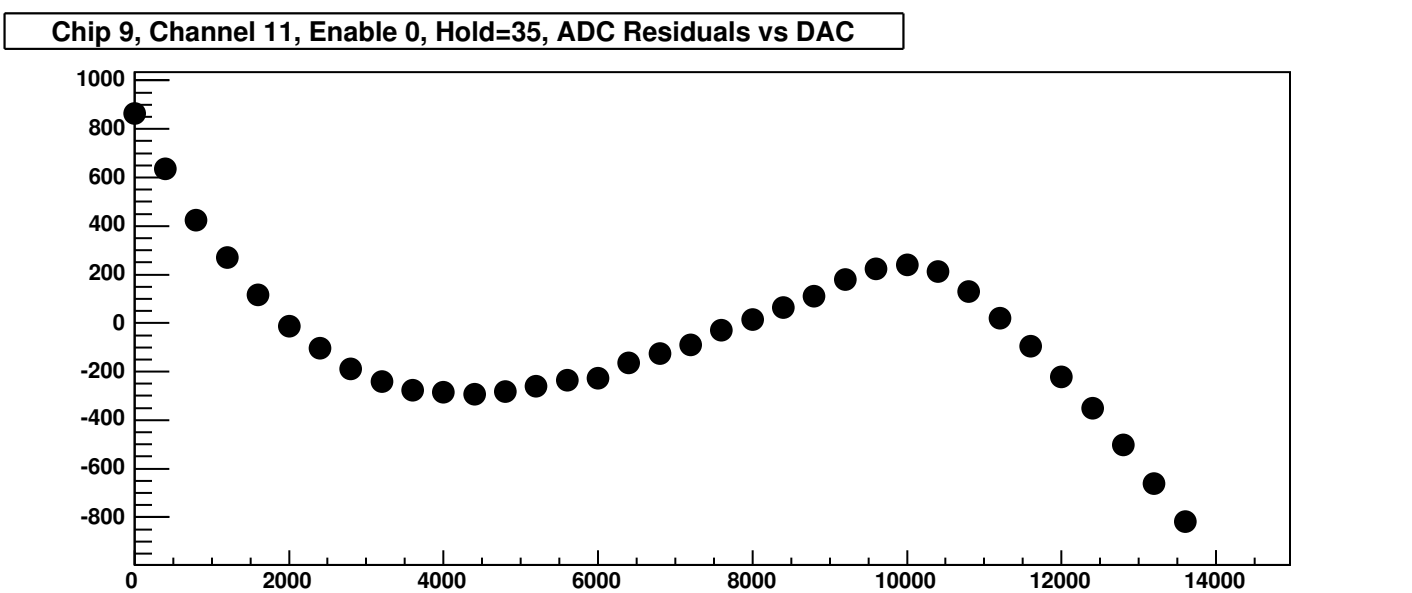

Chip 9, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1571 / 23
p0 -792.8 ± 1.171
p1 0.1641 ± 0.0002153
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

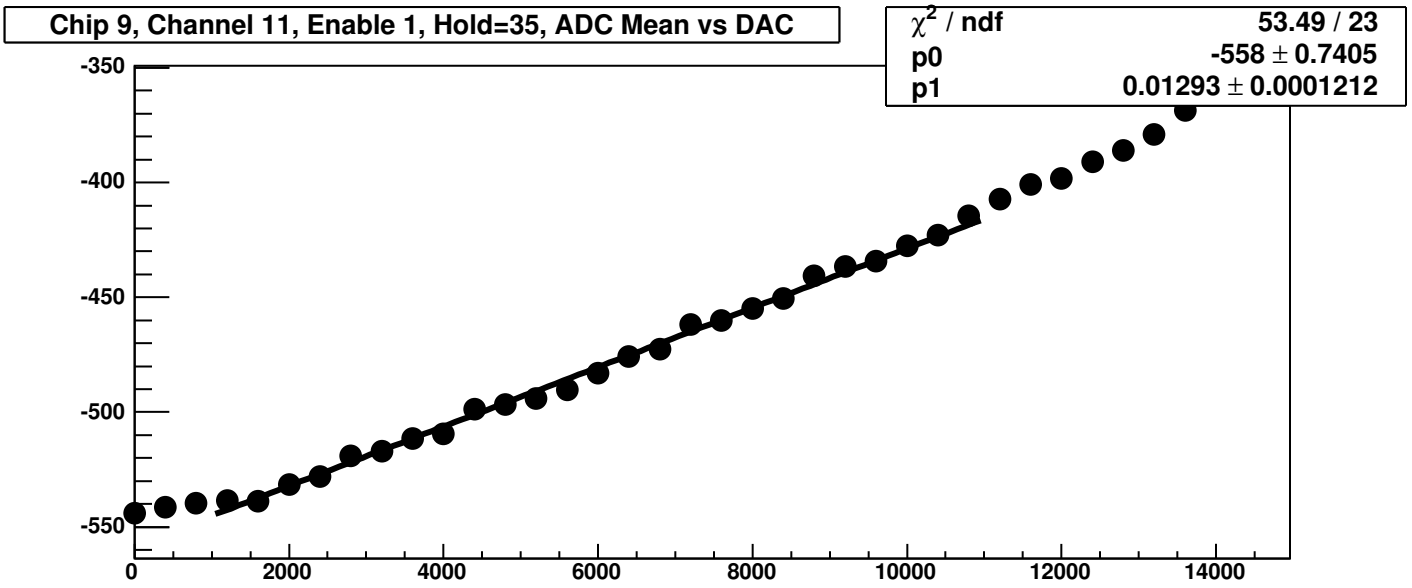

Chip 9, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

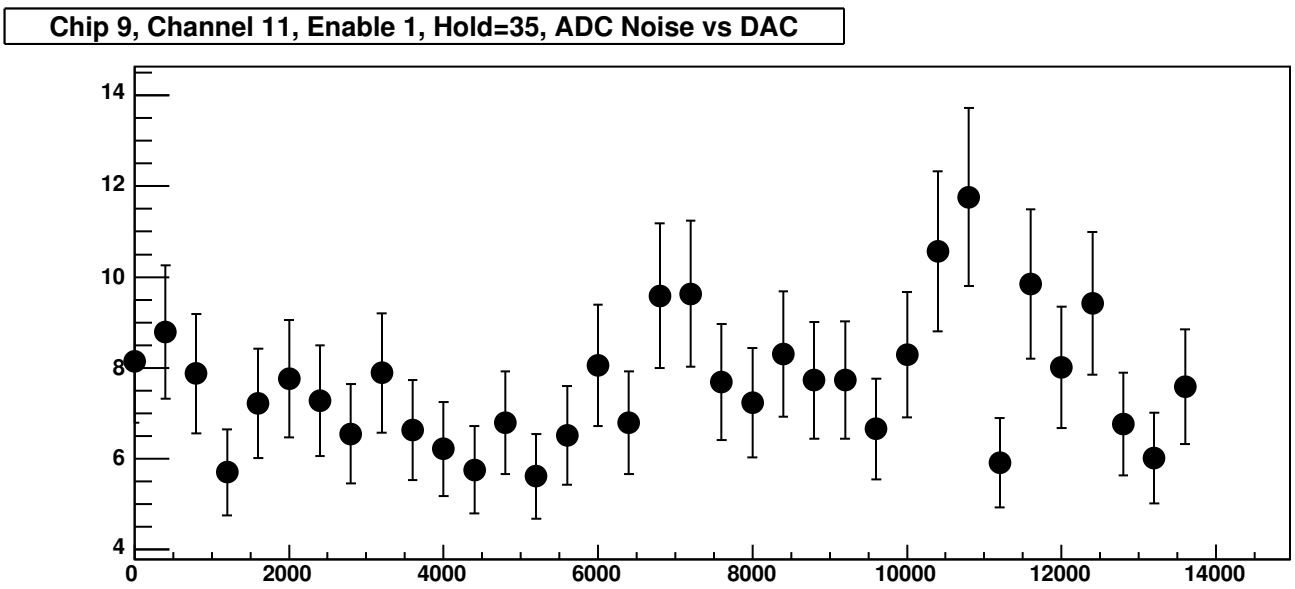

Chip 9, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

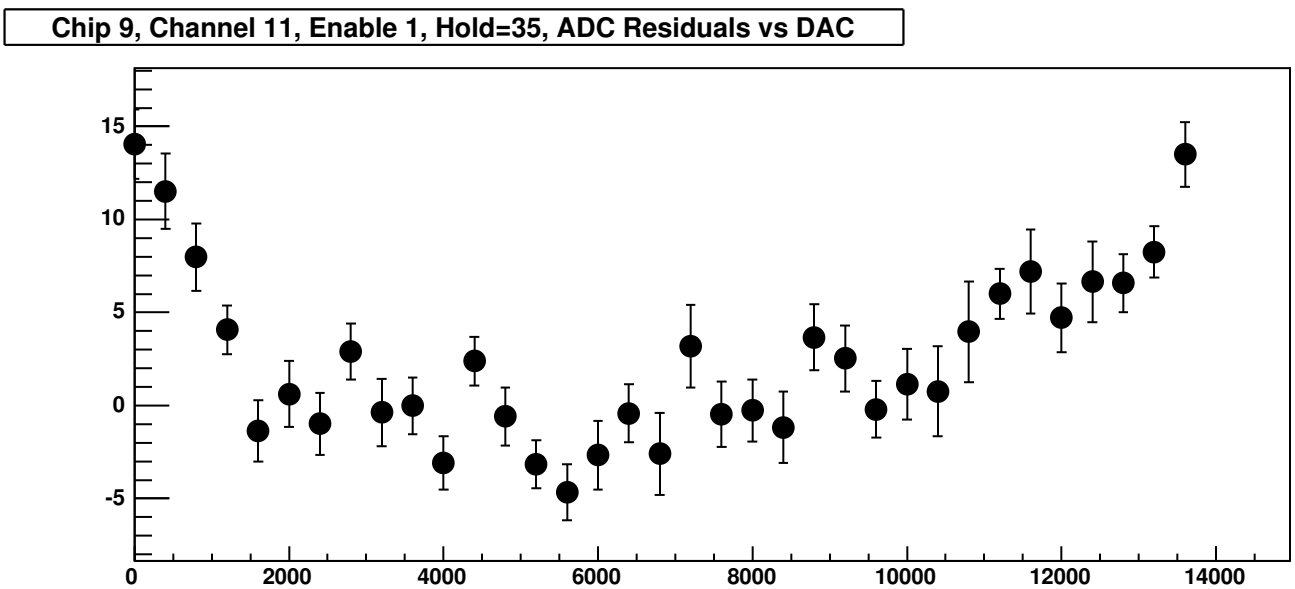

Chip 9, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.178e+04 / 23
p0 -1312 ± 1.492
p1 0.5797 ± 0.0002873
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

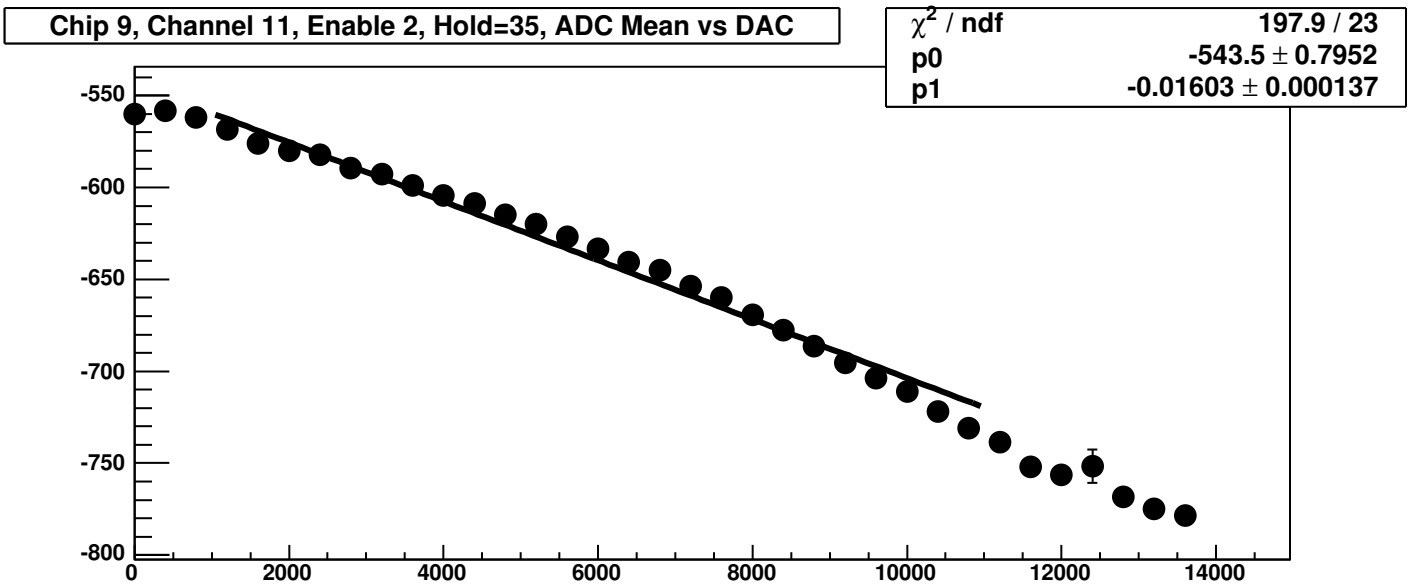

Chip 9, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

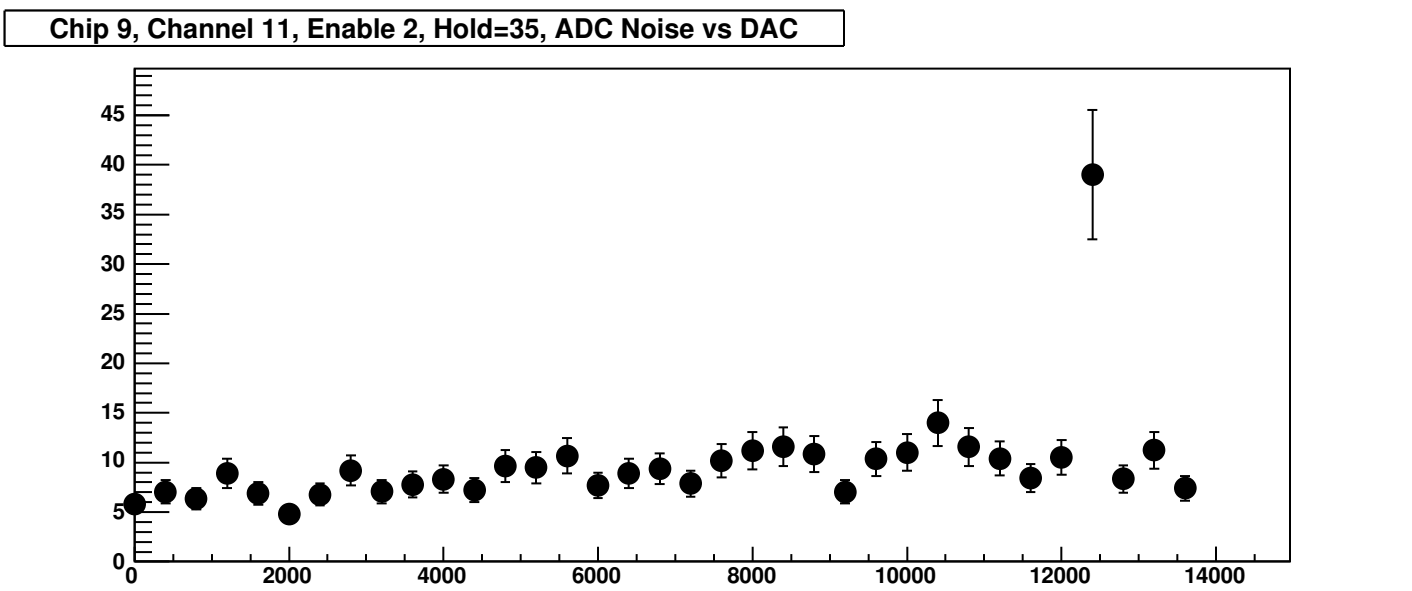

Chip 9, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

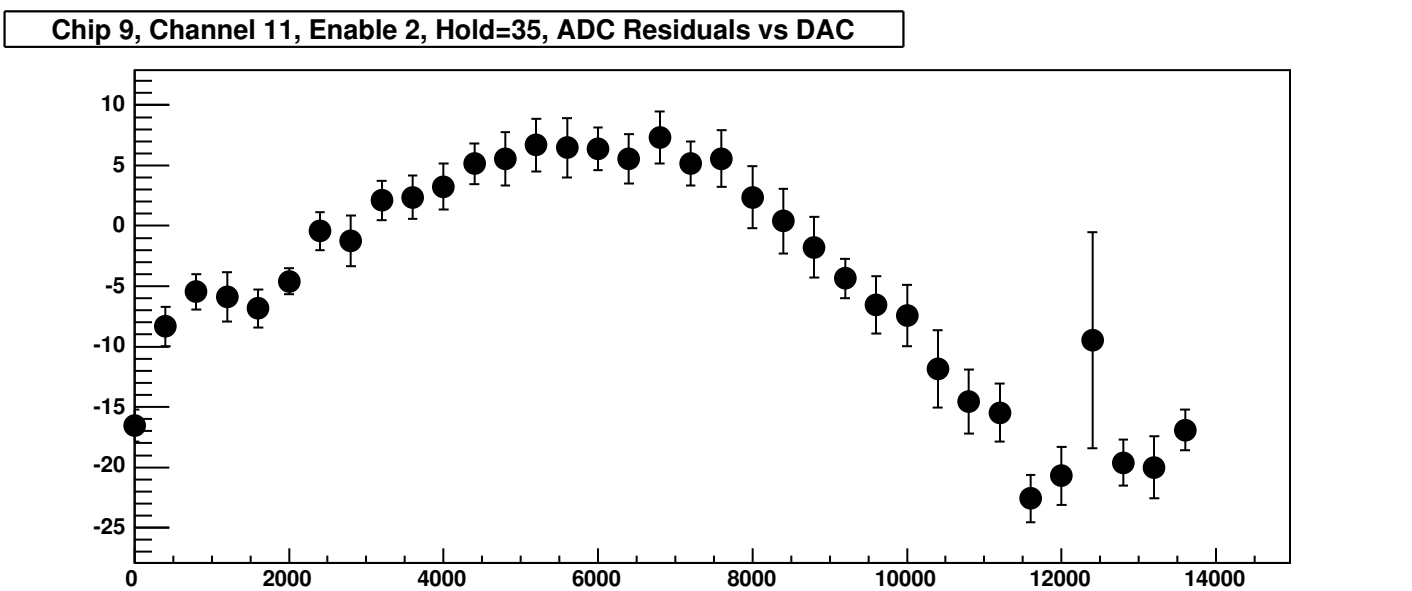

Chip 9, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 53.49 / 23
p0 -558 ± 0.7405
p1 0.01293 ± 0.0001212
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

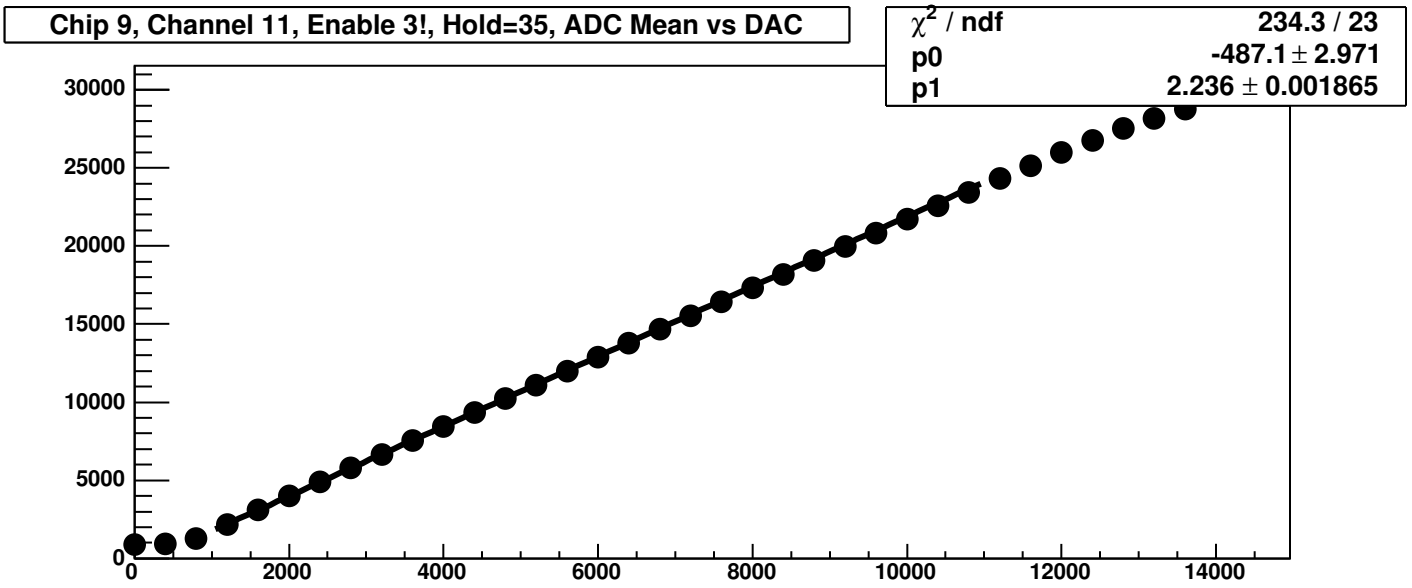

Chip 9, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

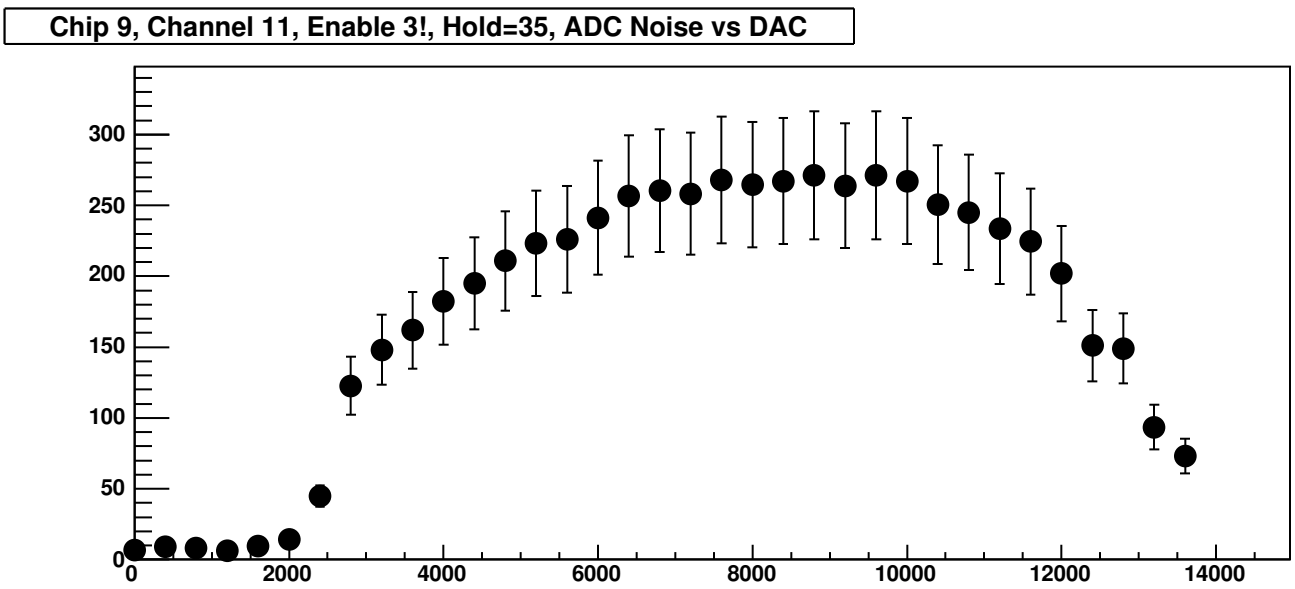

Chip 9, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

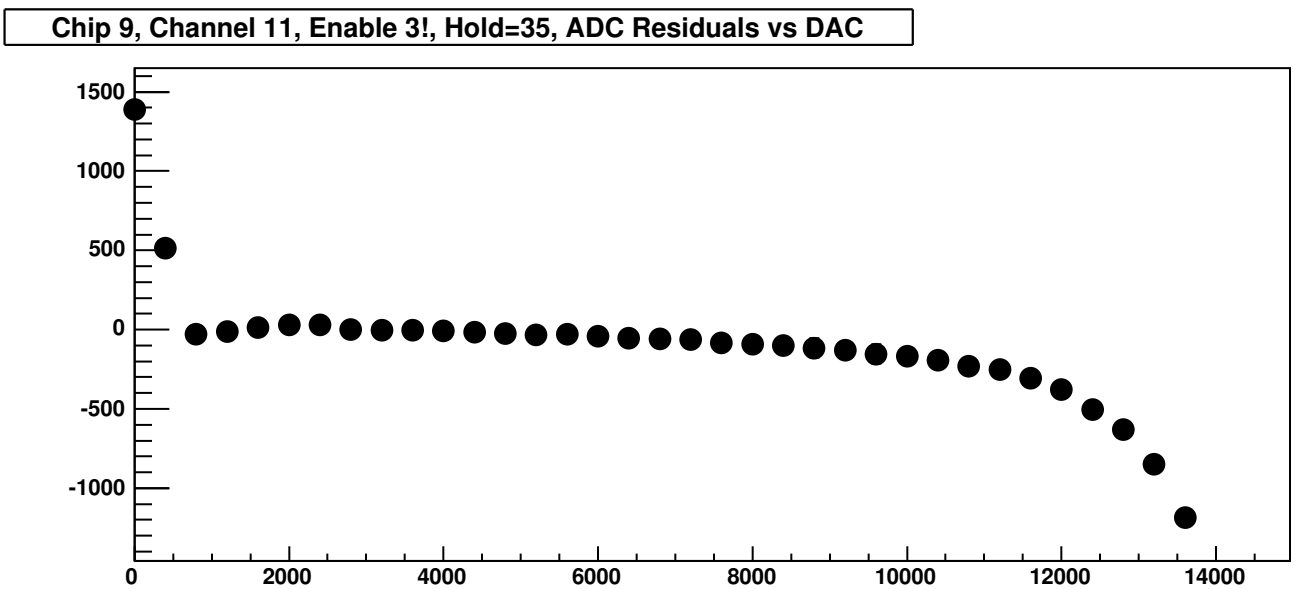

Chip 9, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 197.9 / 23
p0 -543.5 ± 0.7952
p1 -0.01603 ± 0.000137
-550
-600
-650
-700
-750
-800
0
2000
4000
6000
8000
10000
12000
14000

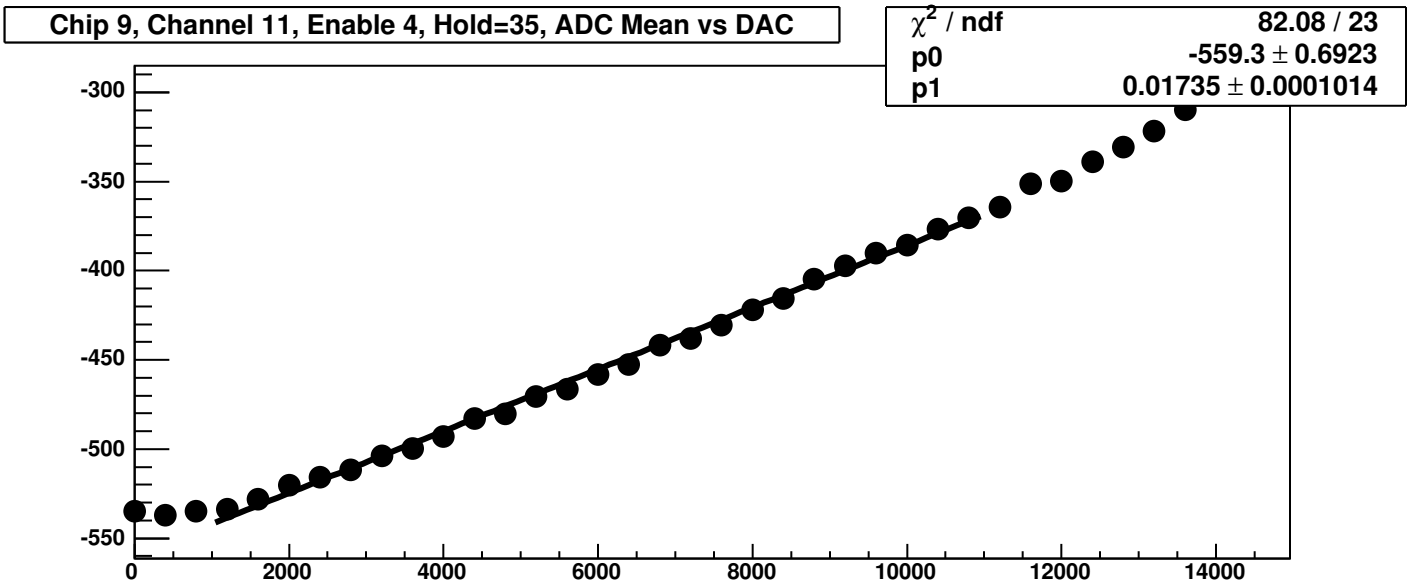

Chip 9, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

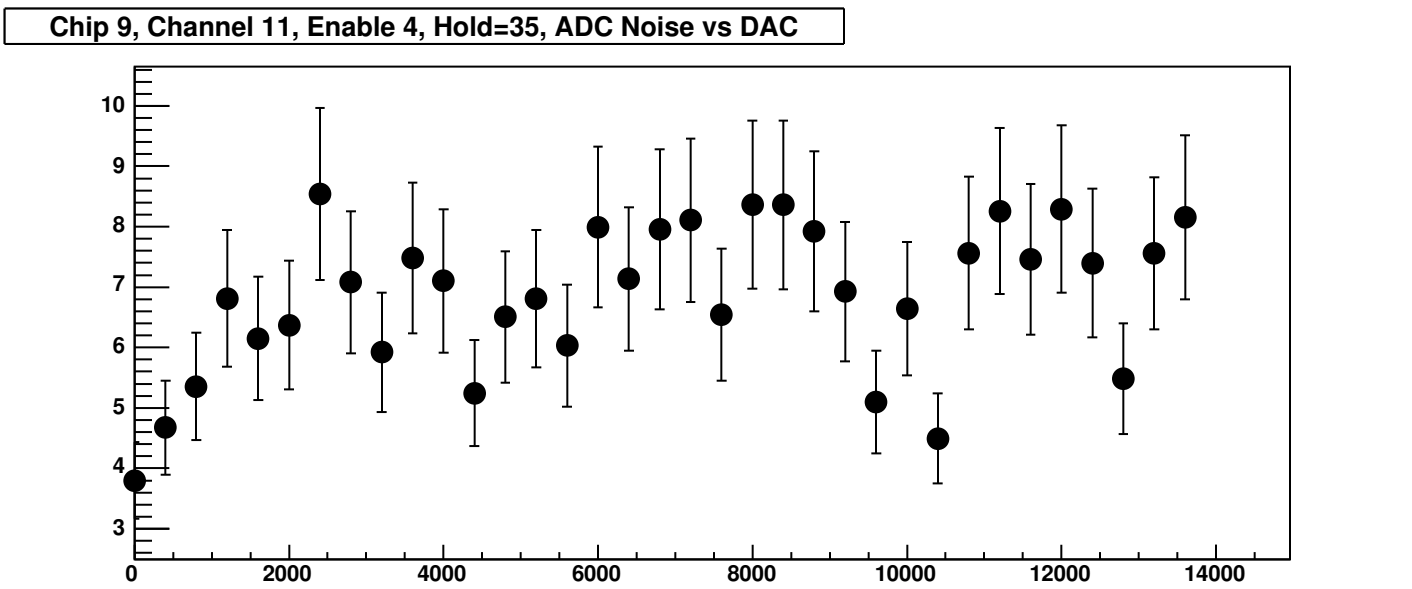

Chip 9, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

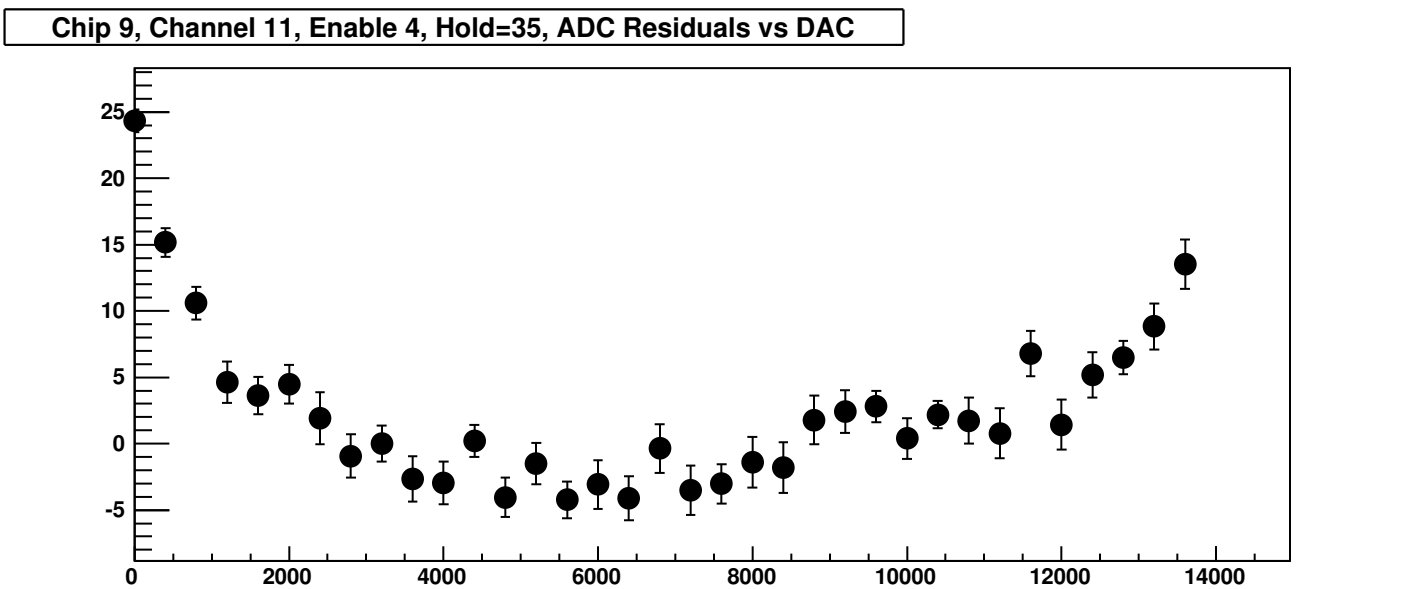

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 234.3 / 23
p0 -487.1 ± 2.971
p1 2.236 ± 0.001865
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

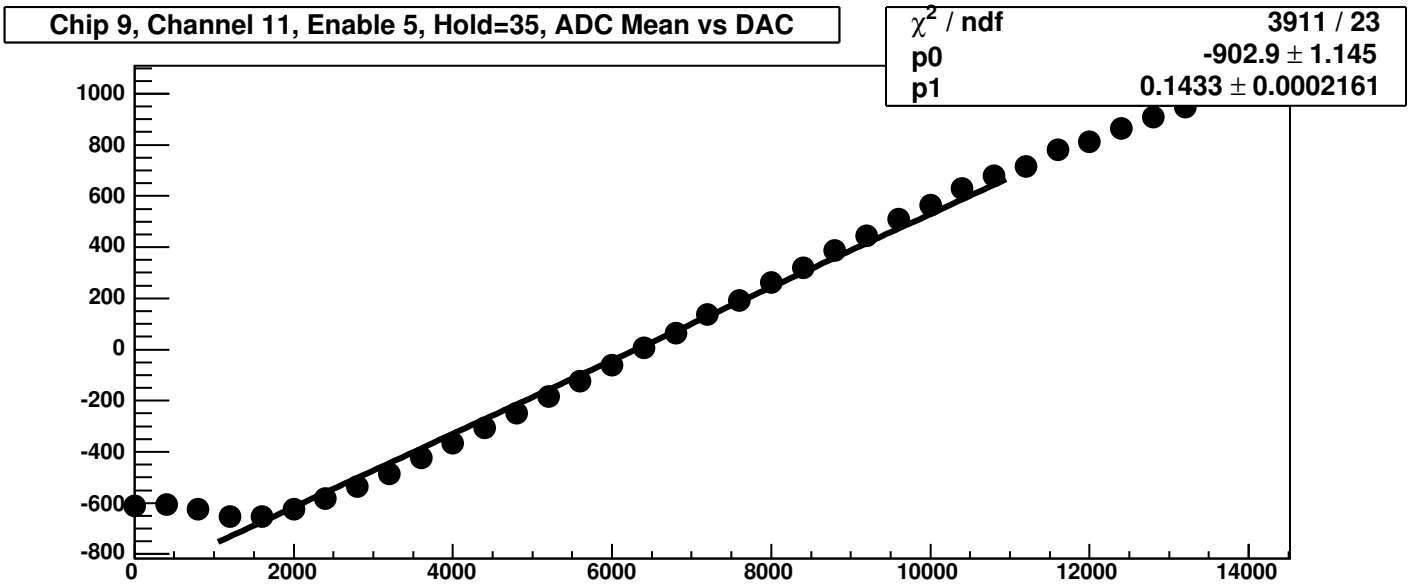

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

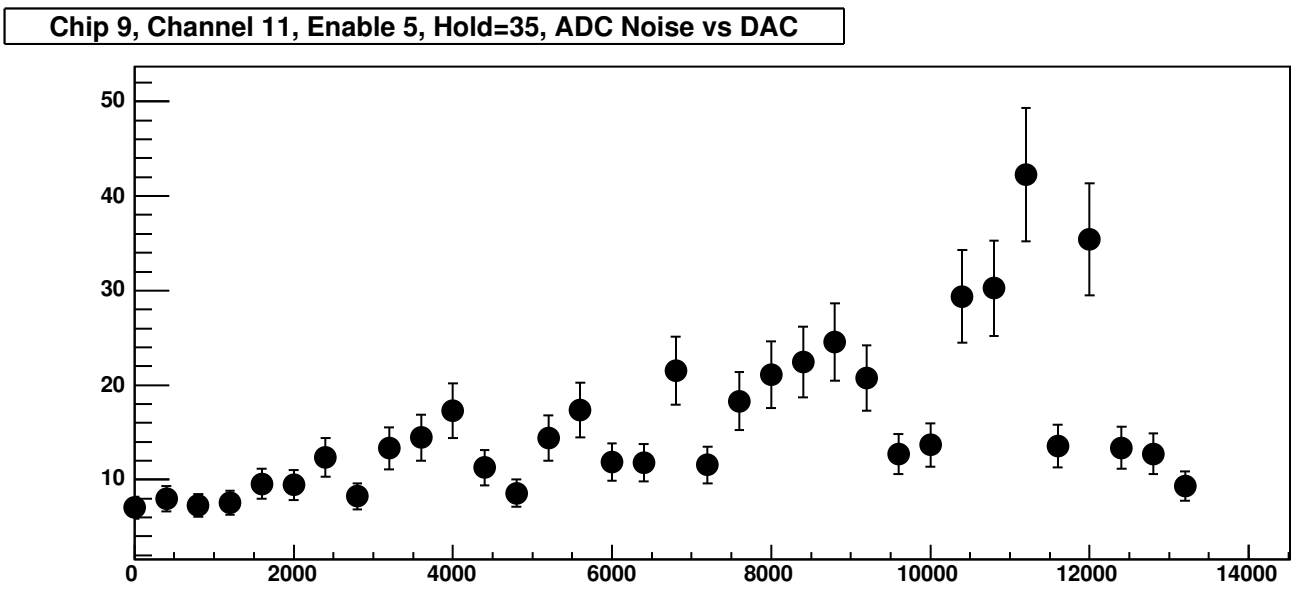

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

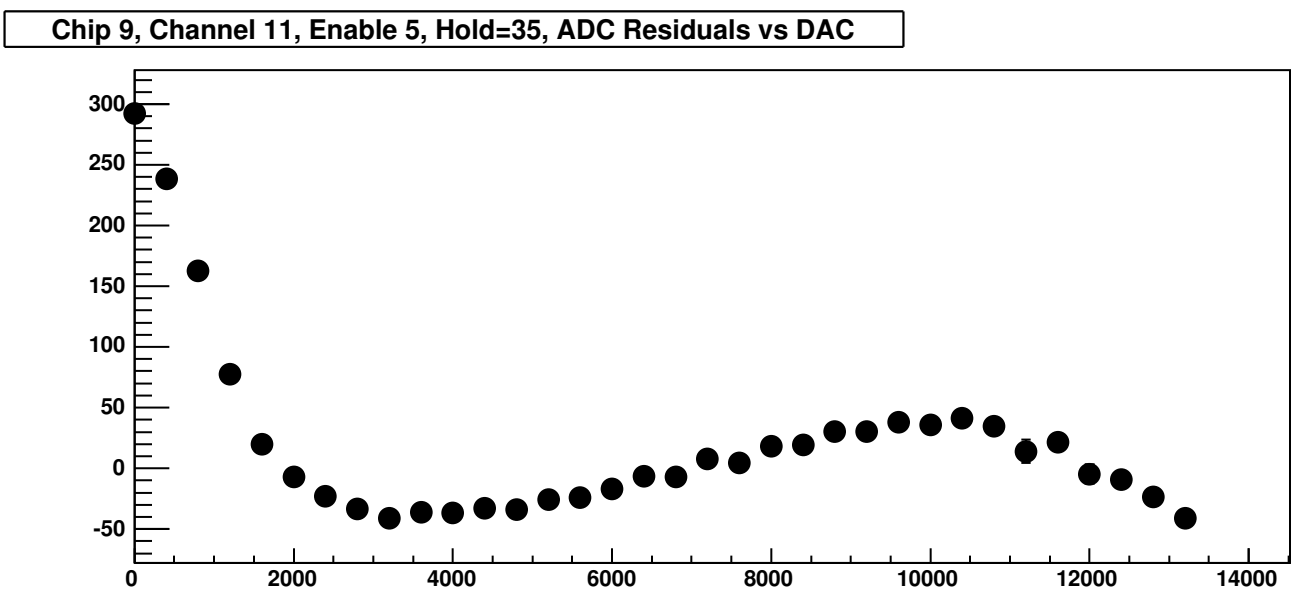

Chip 9, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 82.08 / 23
p0 -559.3 ± 0.6923
p1 0.01735 ± 0.0001014
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

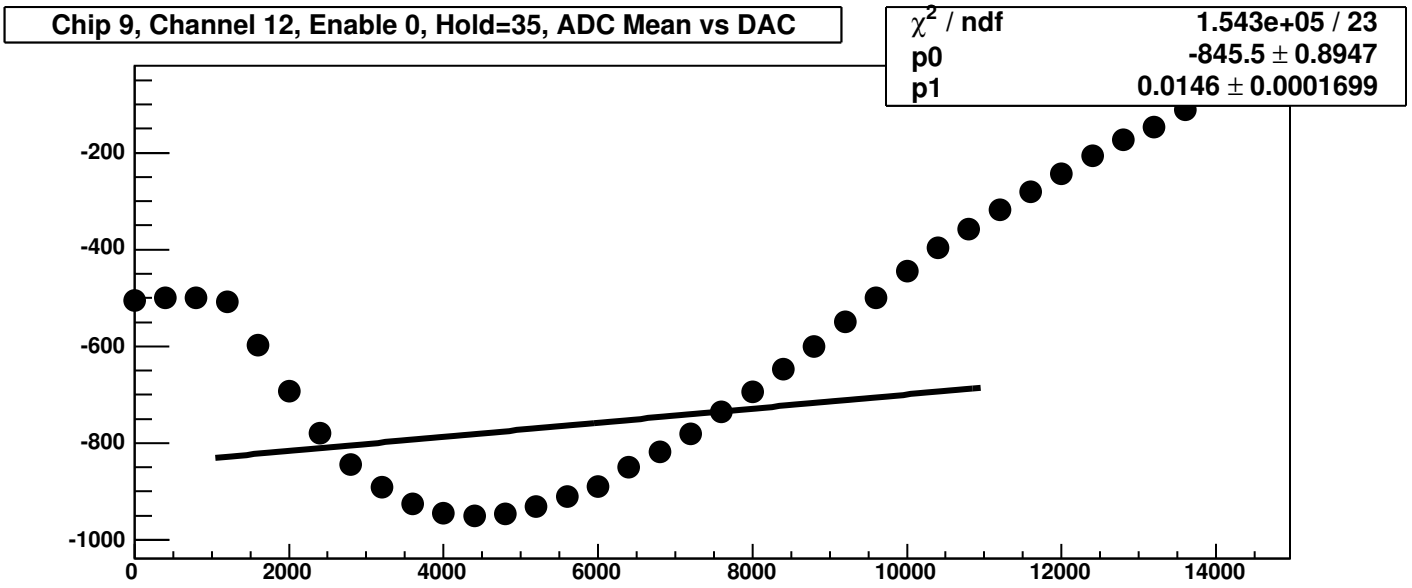

Chip 9, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

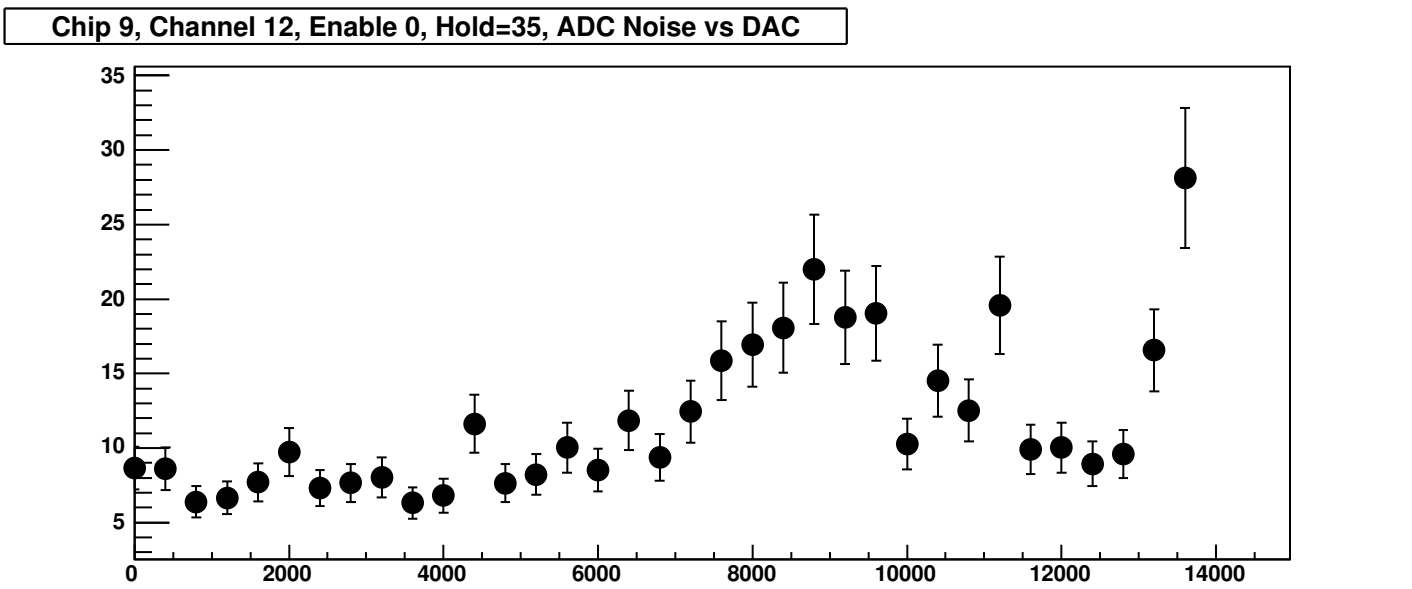

Chip 9, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

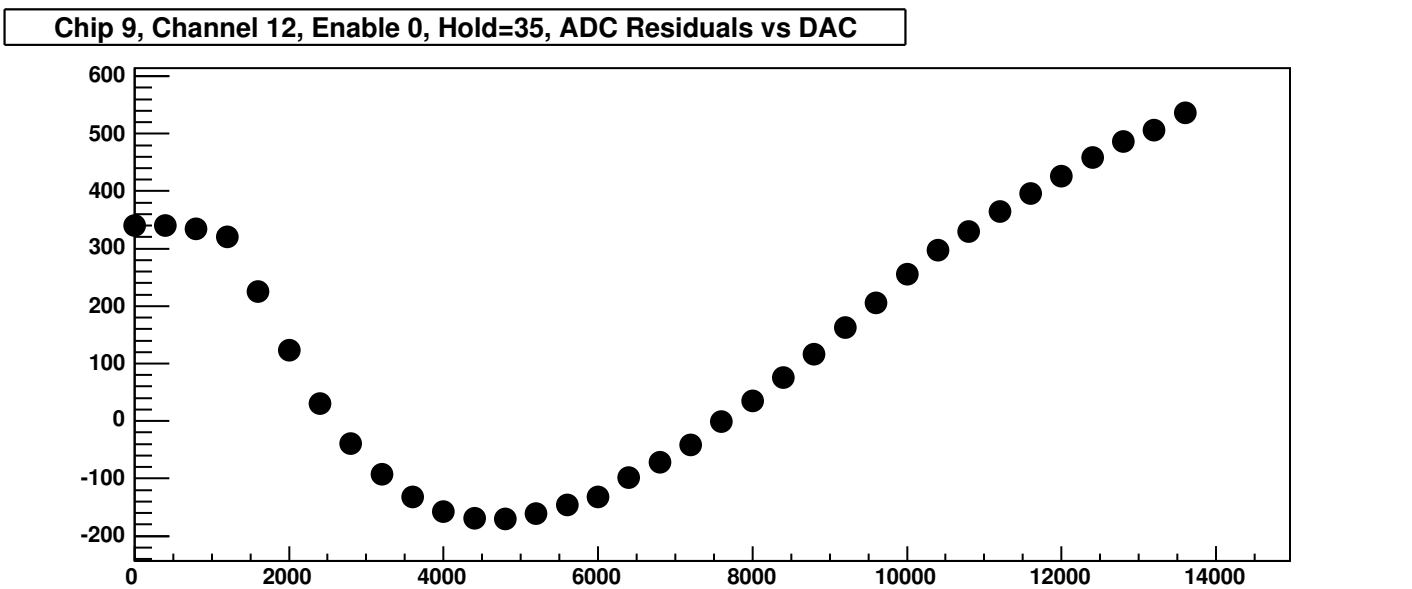

Chip 9, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3911 / 23
p0 -902.9 ± 1.145
p1 0.1433 ± 0.0002161
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

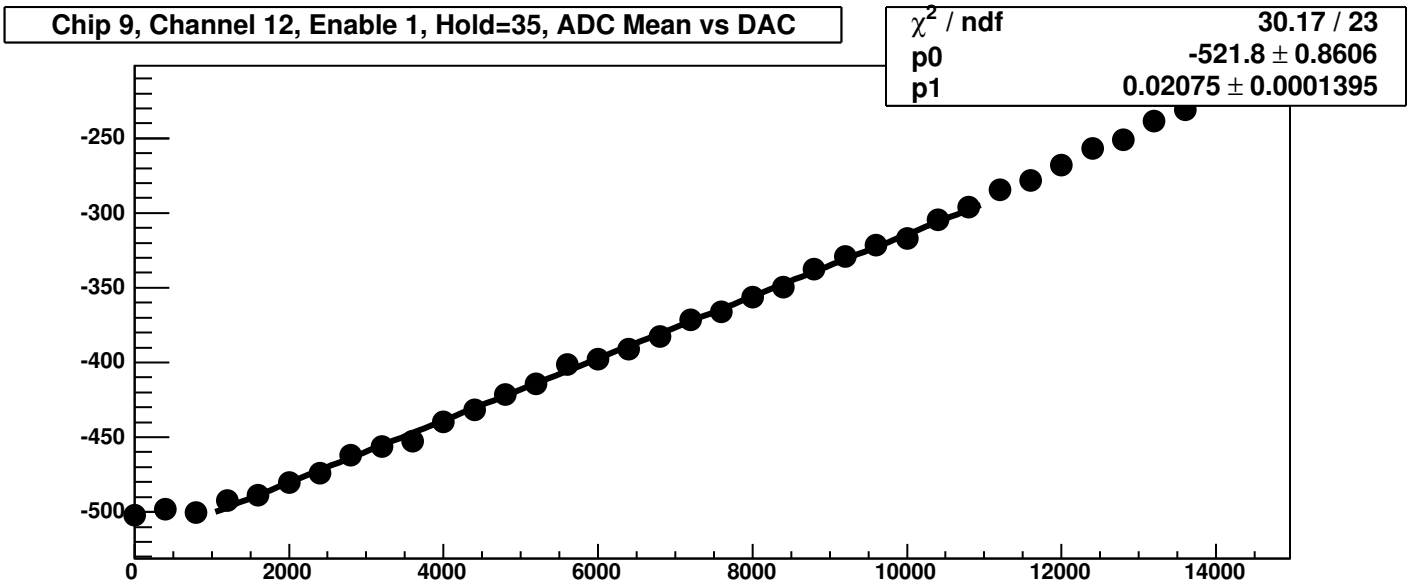

Chip 9, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

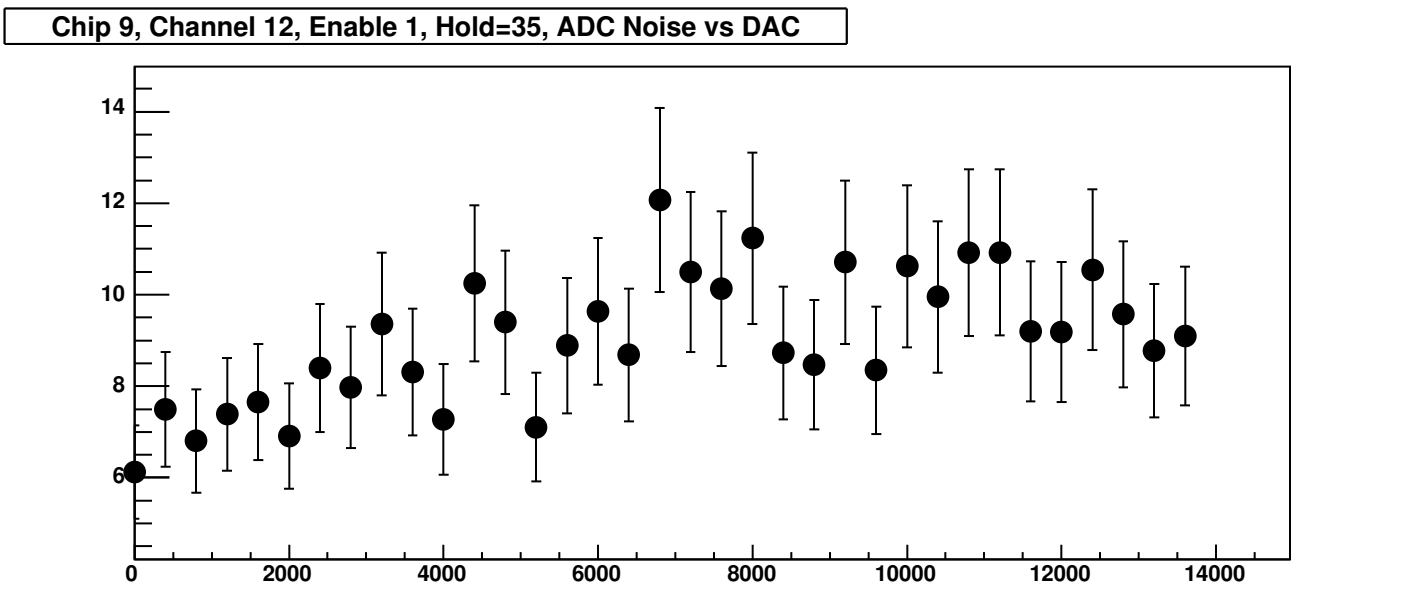

Chip 9, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

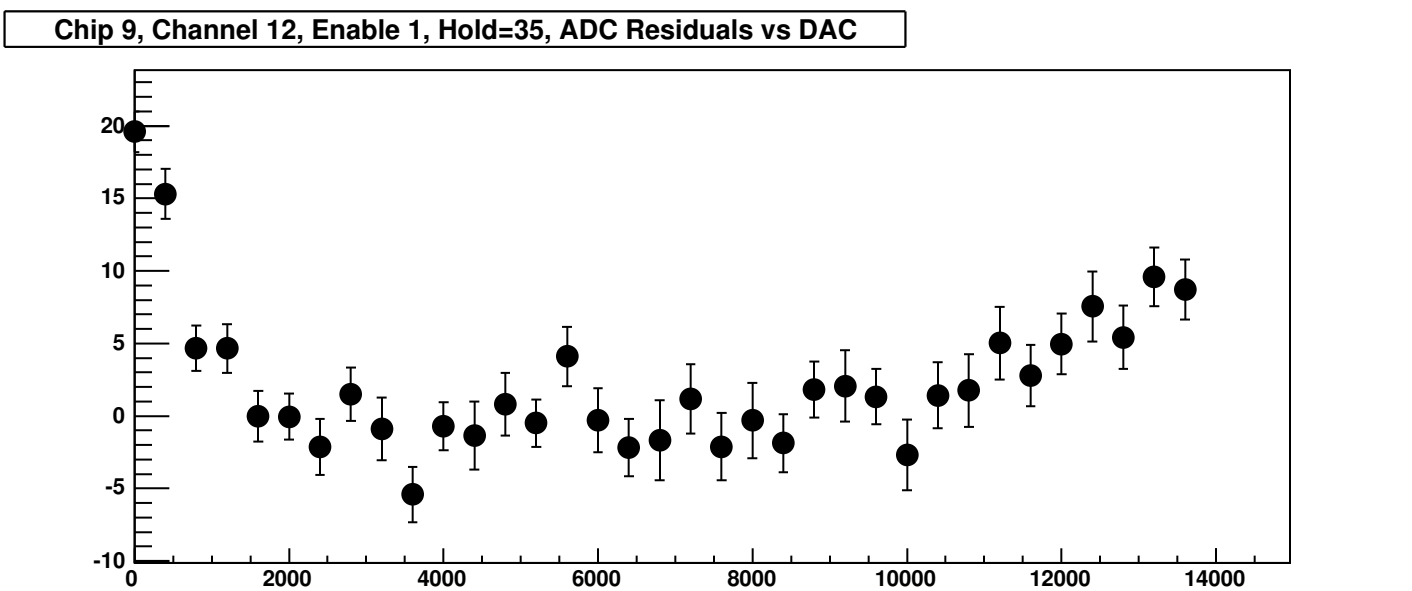

Chip 9, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.543e+05 / 23
p0 -845.5 ± 0.8947
p1 0.0146 ± 0.0001699
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

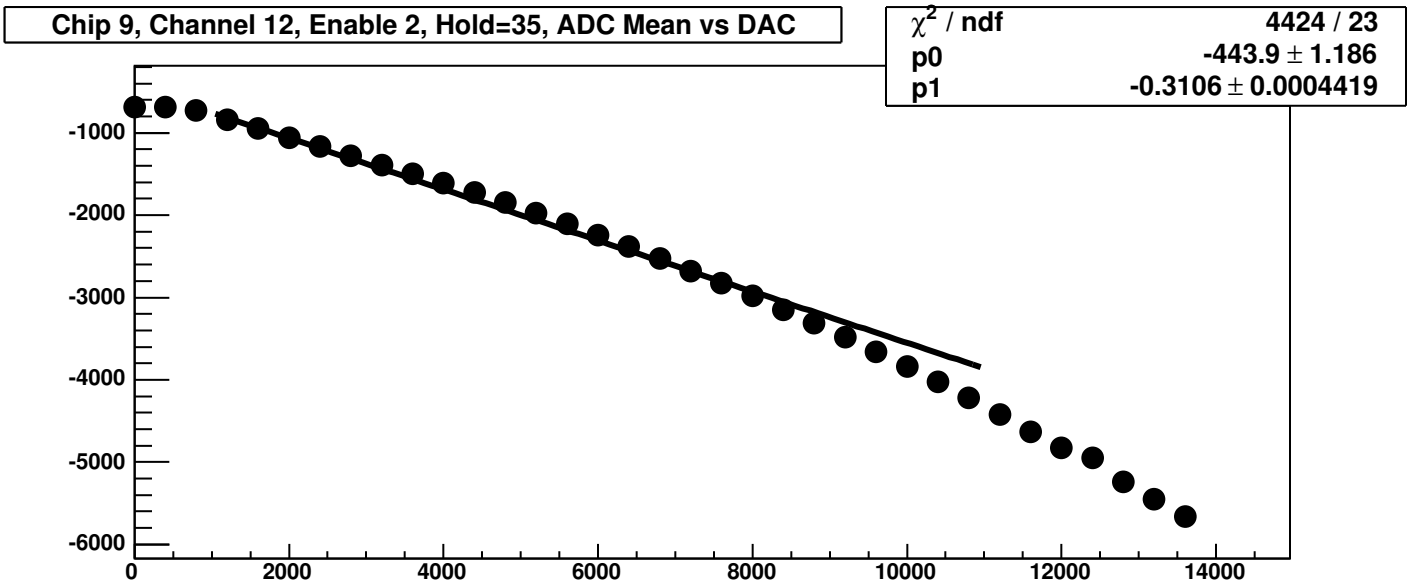

Chip 9, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

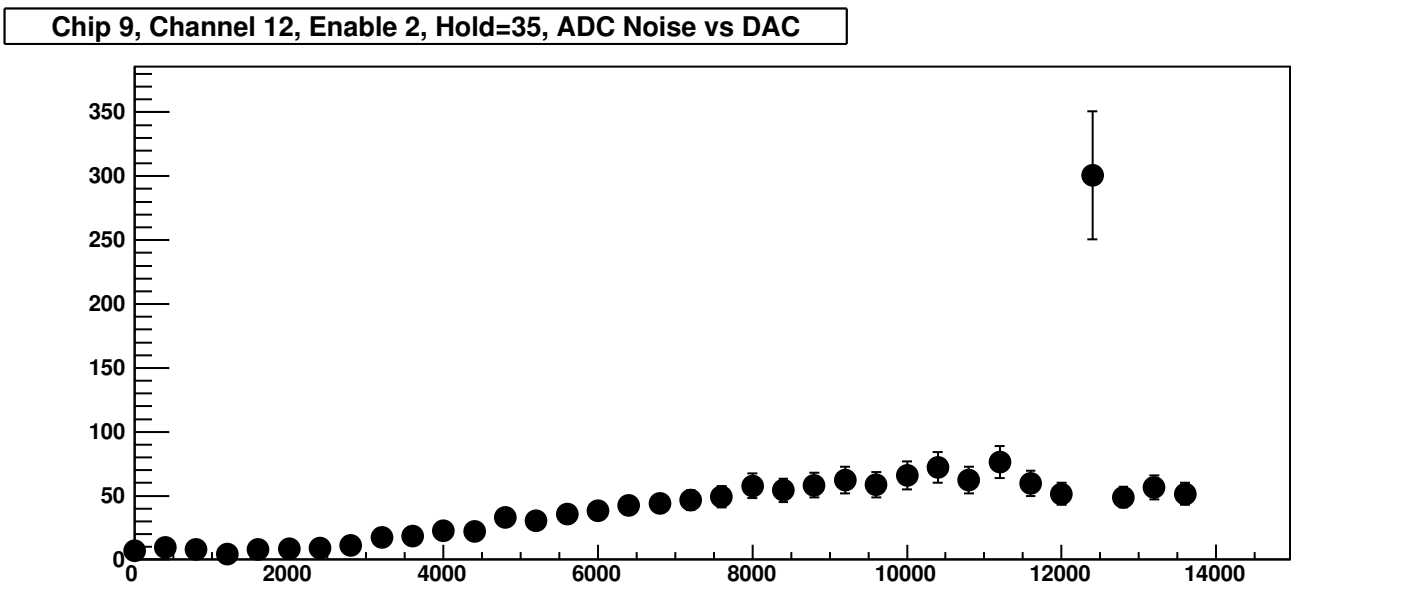

Chip 9, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

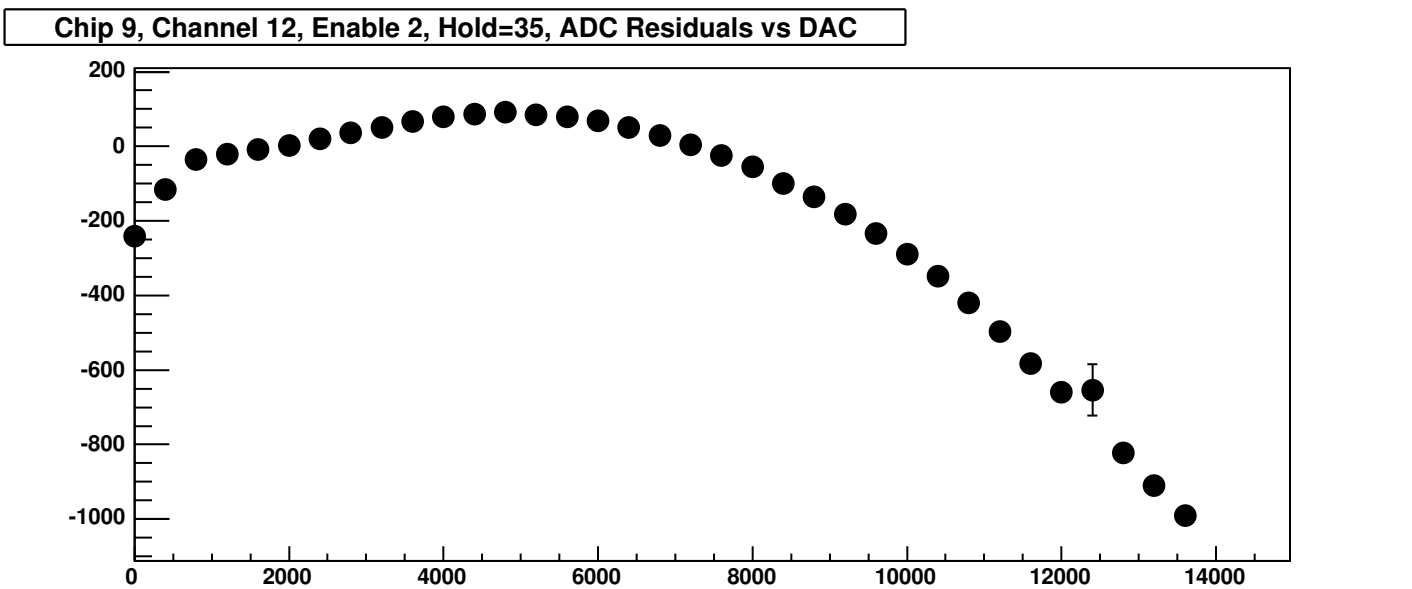

Chip 9, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.17 / 23
p0 -521.8 ± 0.8606
p1 0.02075 ± 0.0001395
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

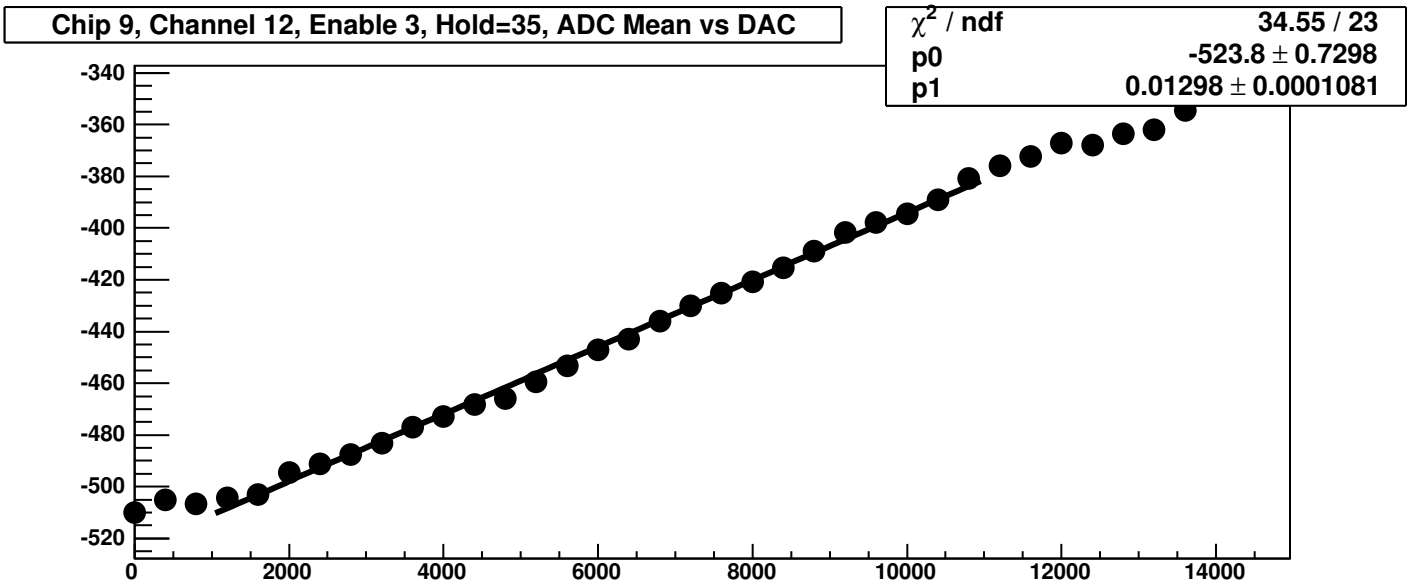

Chip 9, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

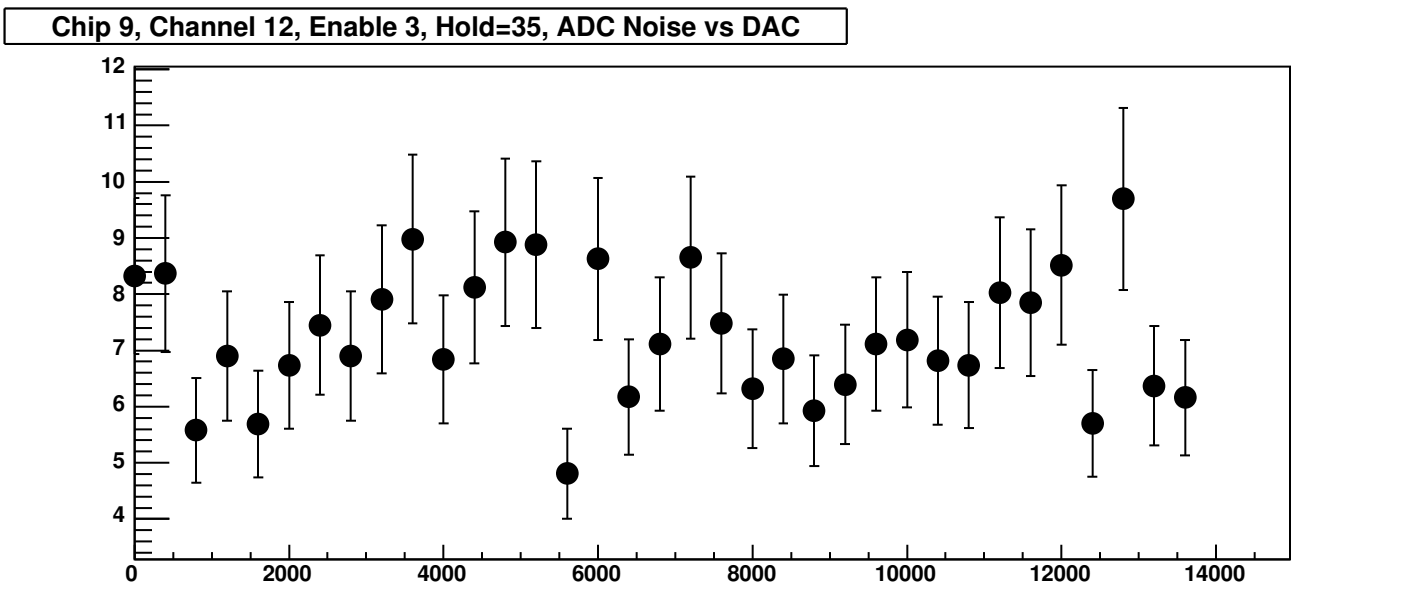

Chip 9, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

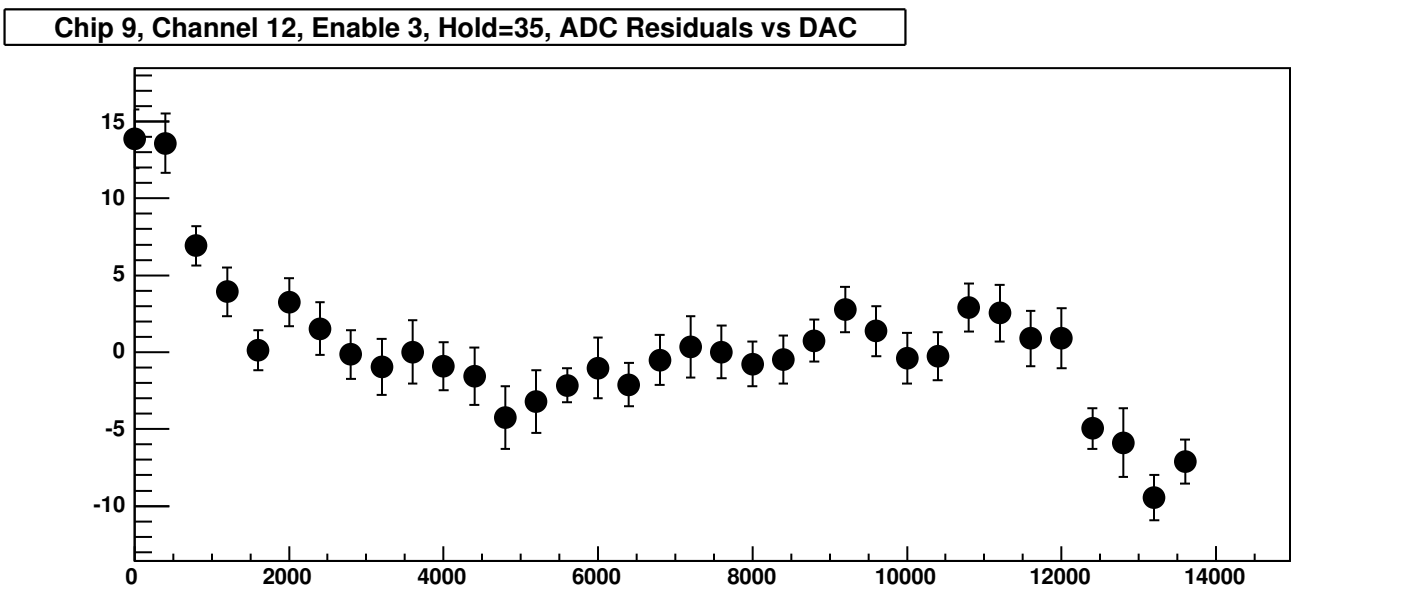

Chip 9, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4424 / 23
p0 -443.9 ± 1.186
p1 -0.3106 ± 0.0004419
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

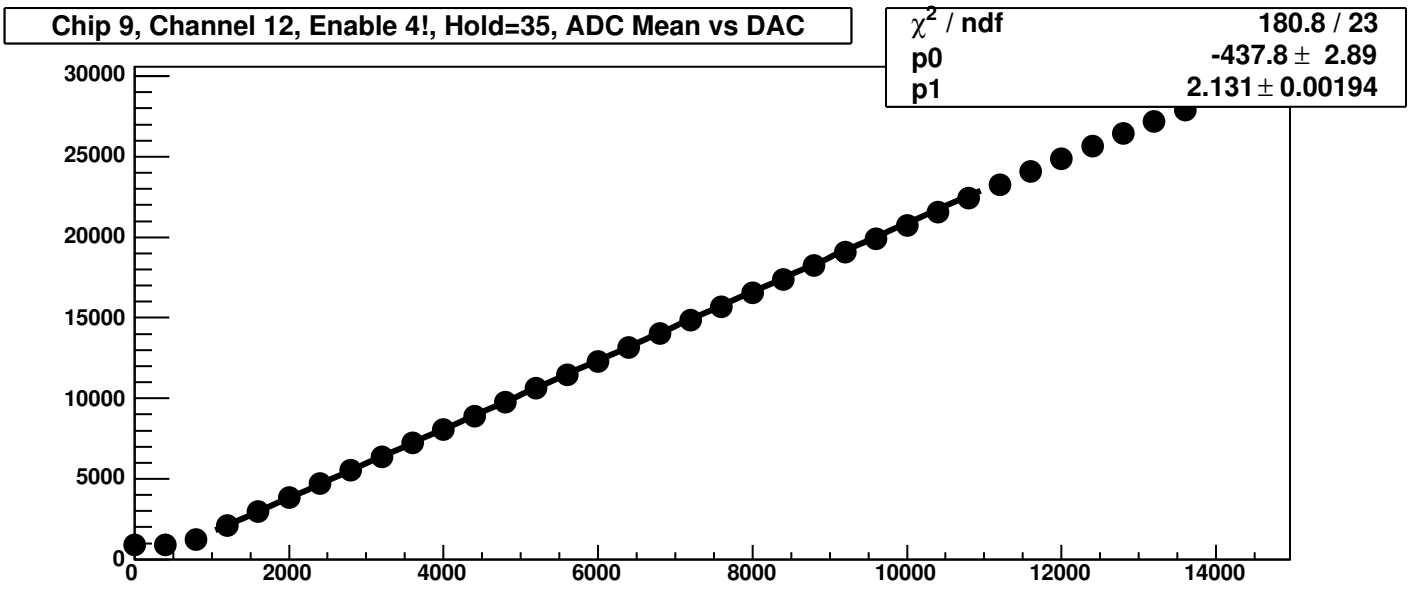

Chip 9, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

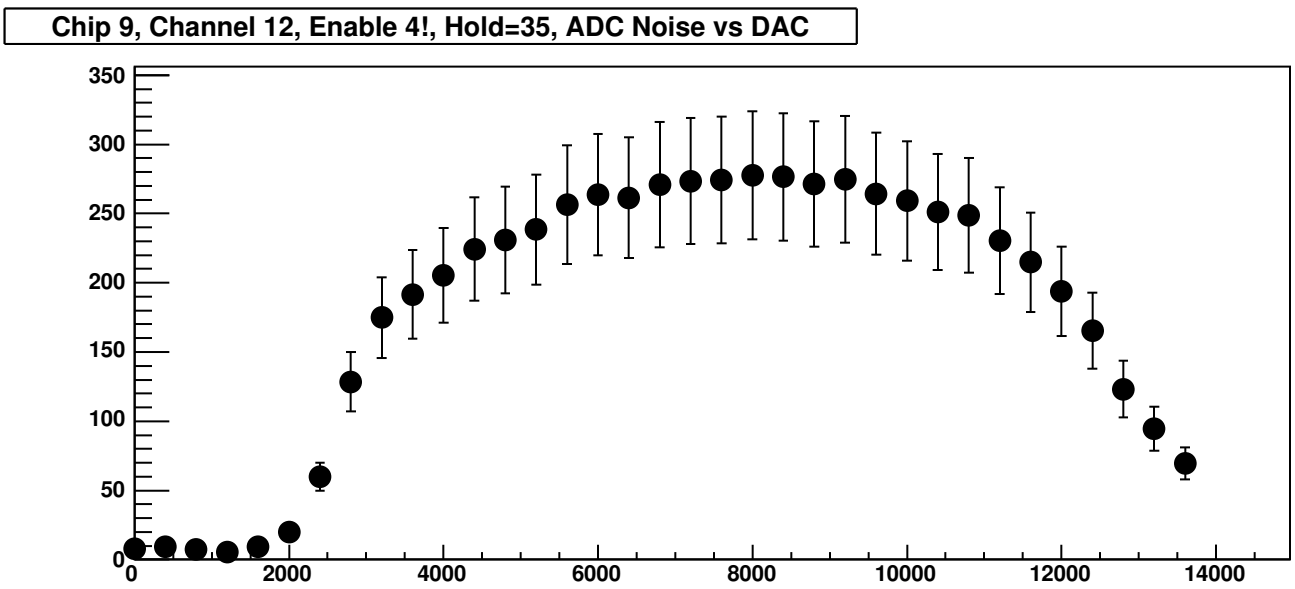

Chip 9, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

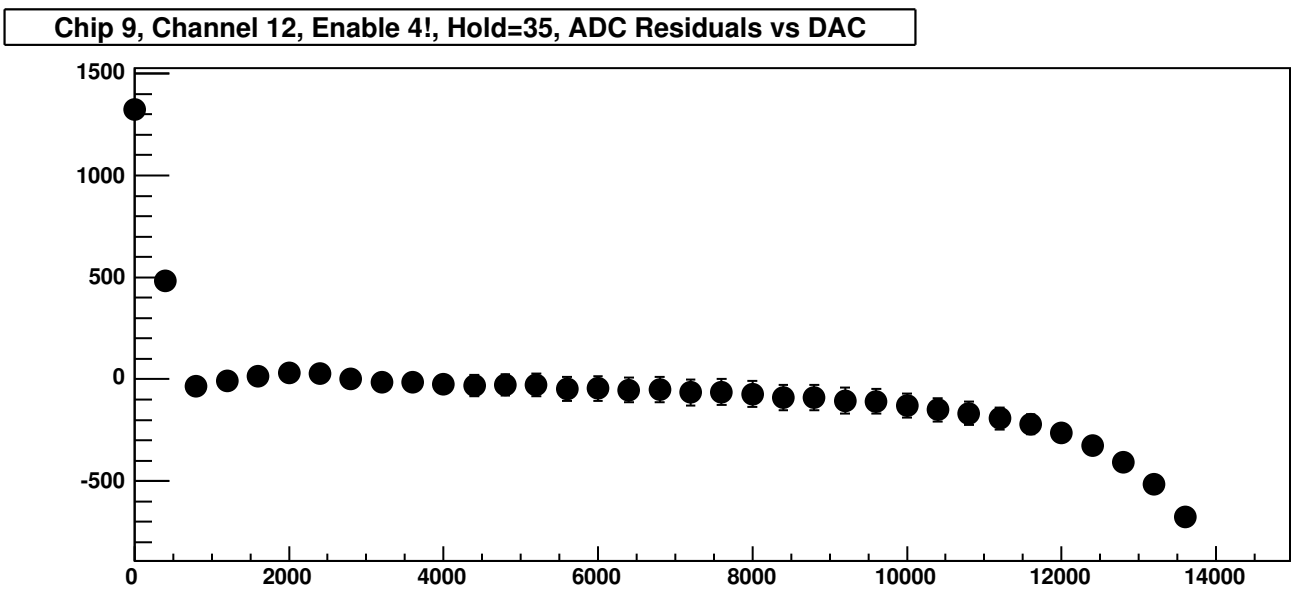

Chip 9, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.55 / 23
p0 -523.8 ± 0.7298
p1 0.01298 ± 0.0001081
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

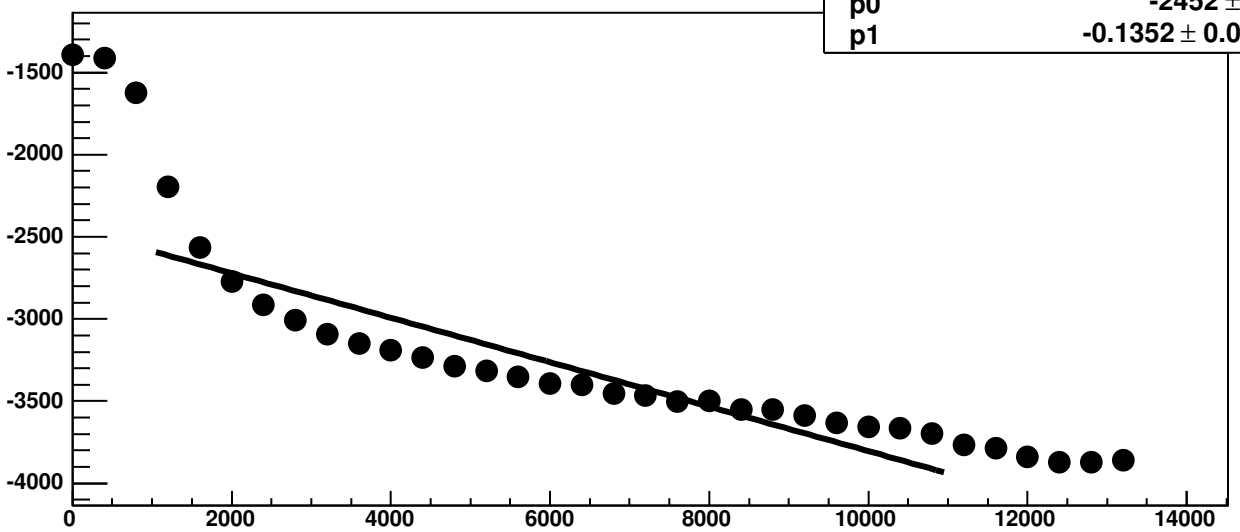

Chip 9, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 180.8 / 23
p0 -437.8 ± 2.89
p1 2.131 ± 0.00194
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

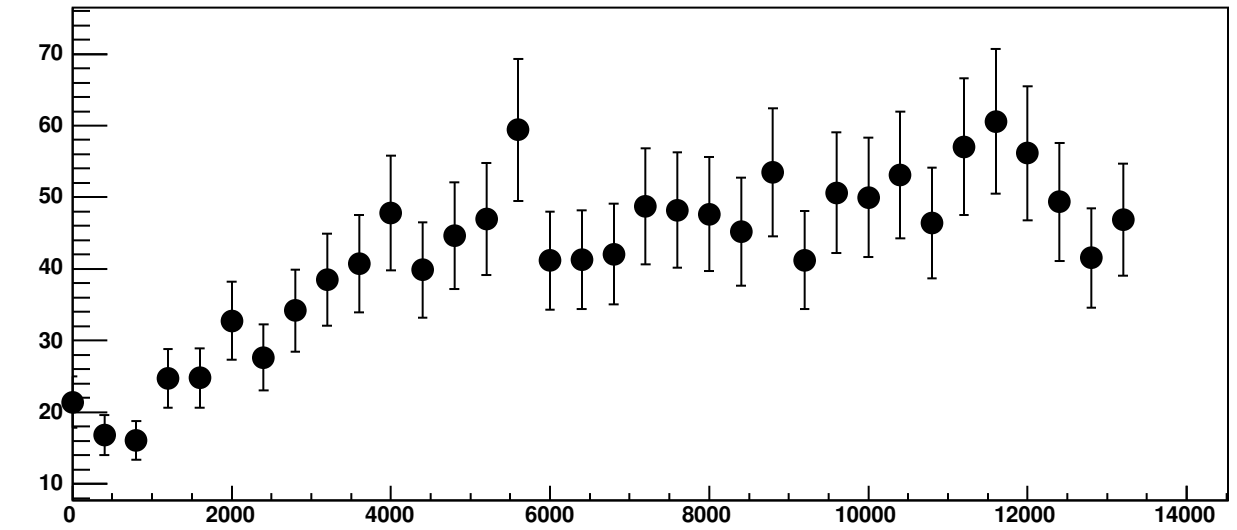

Chip 9, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

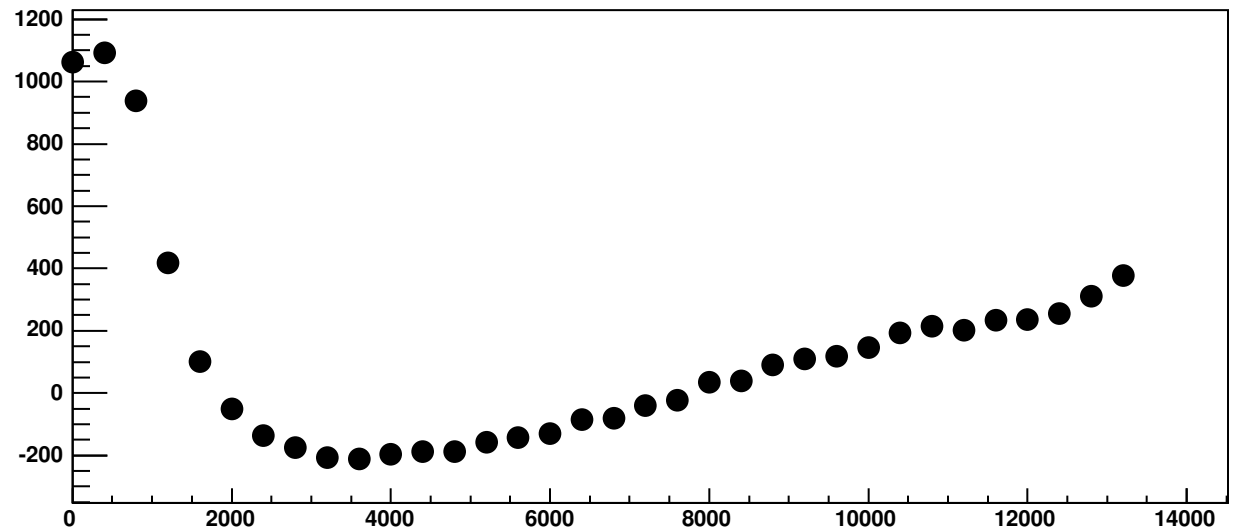
χ^2 / ndf	1.073e+04 / 23
p0	-2452 ± 3.422
p1	-0.1352 ± 0.000602



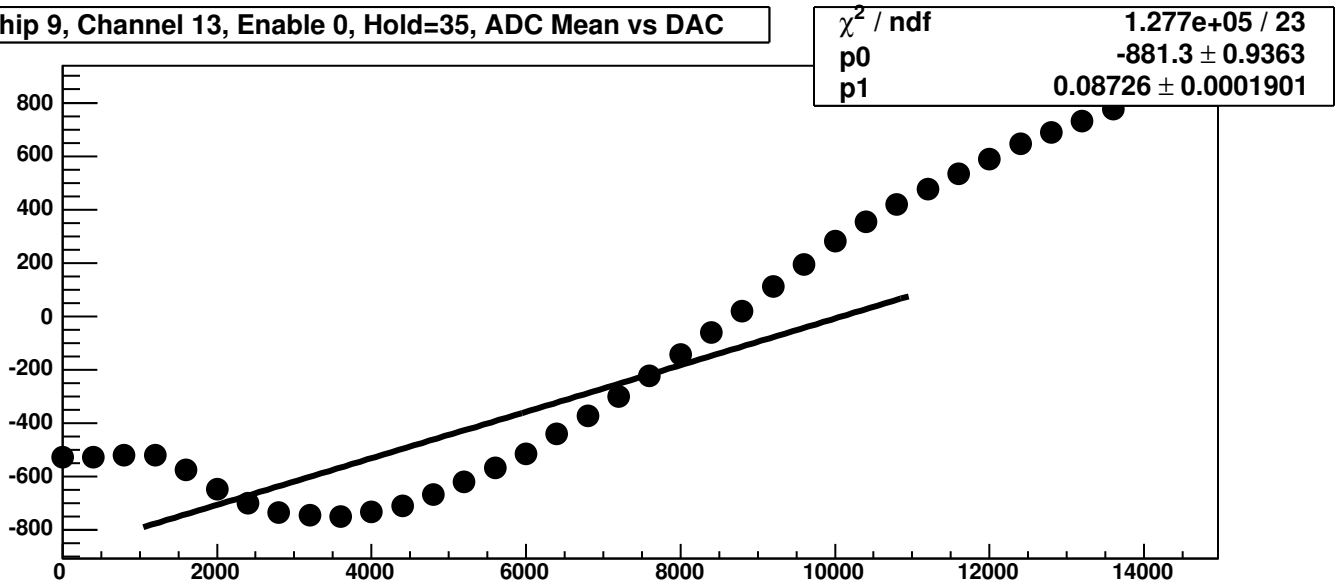
Chip 9, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



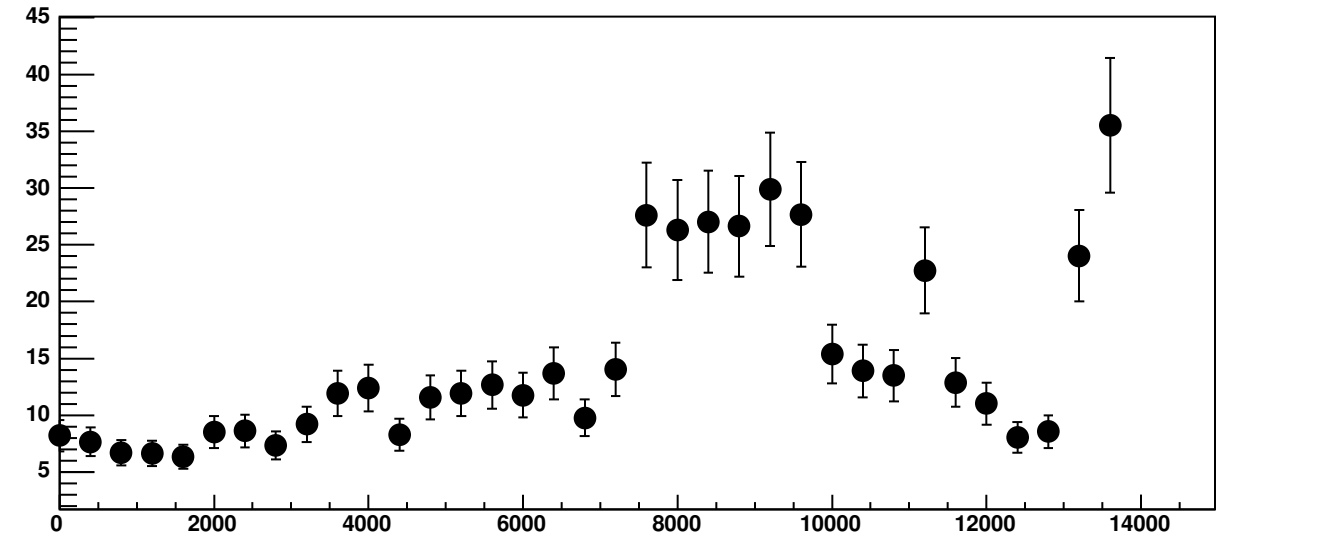
Chip 9, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



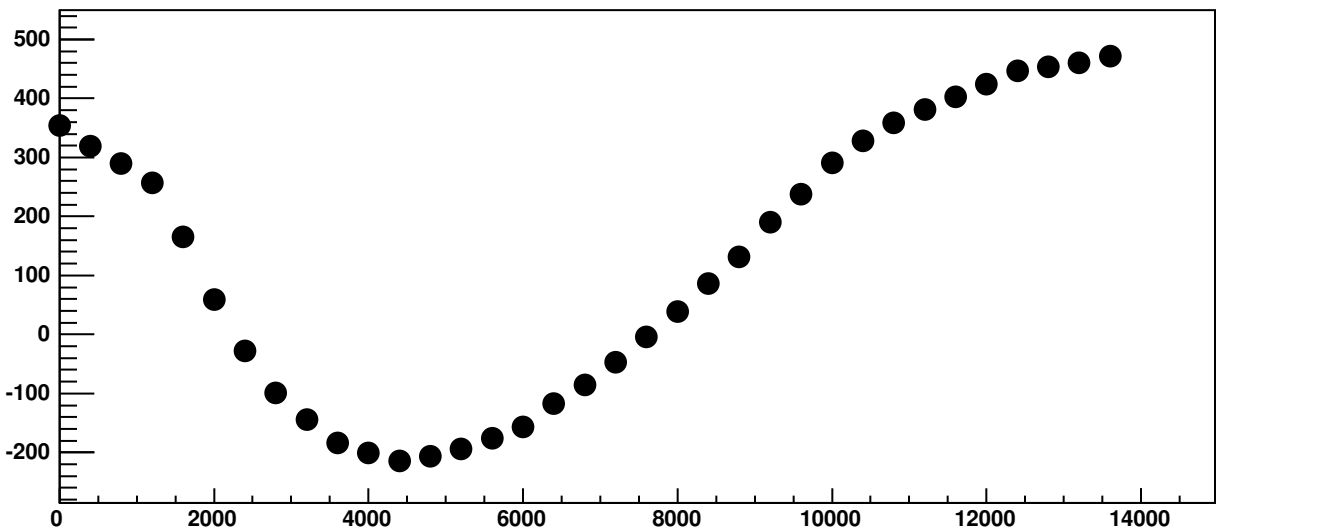
Chip 9, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

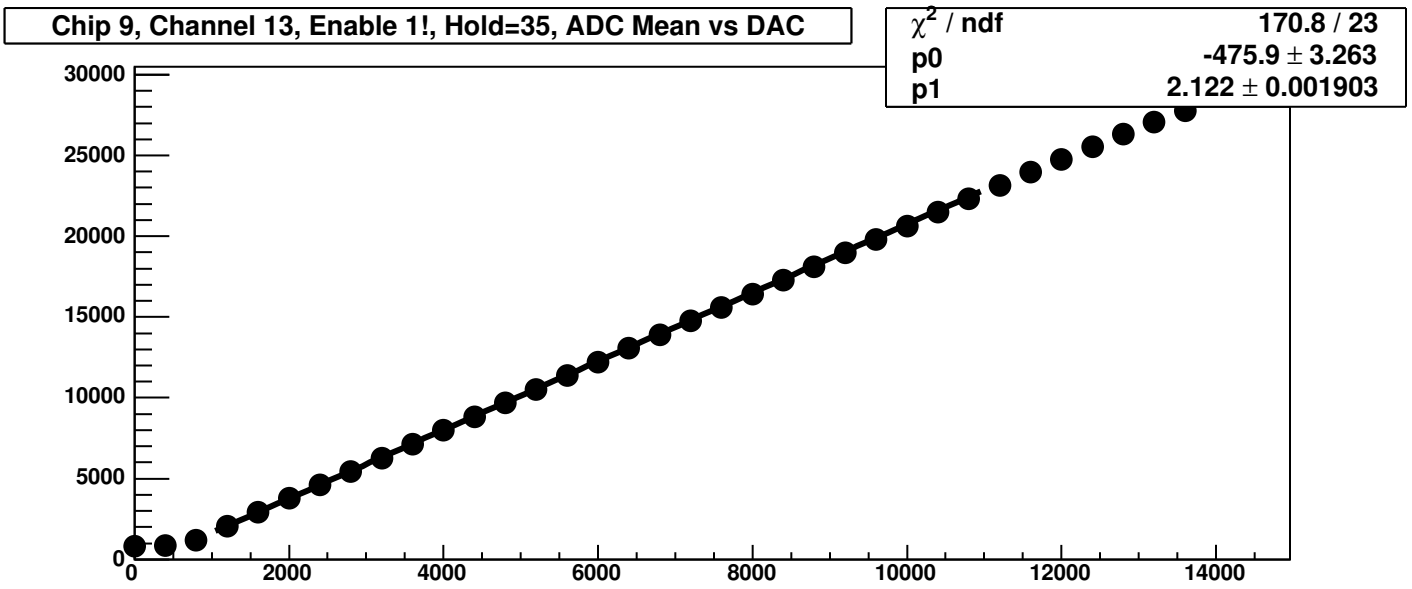


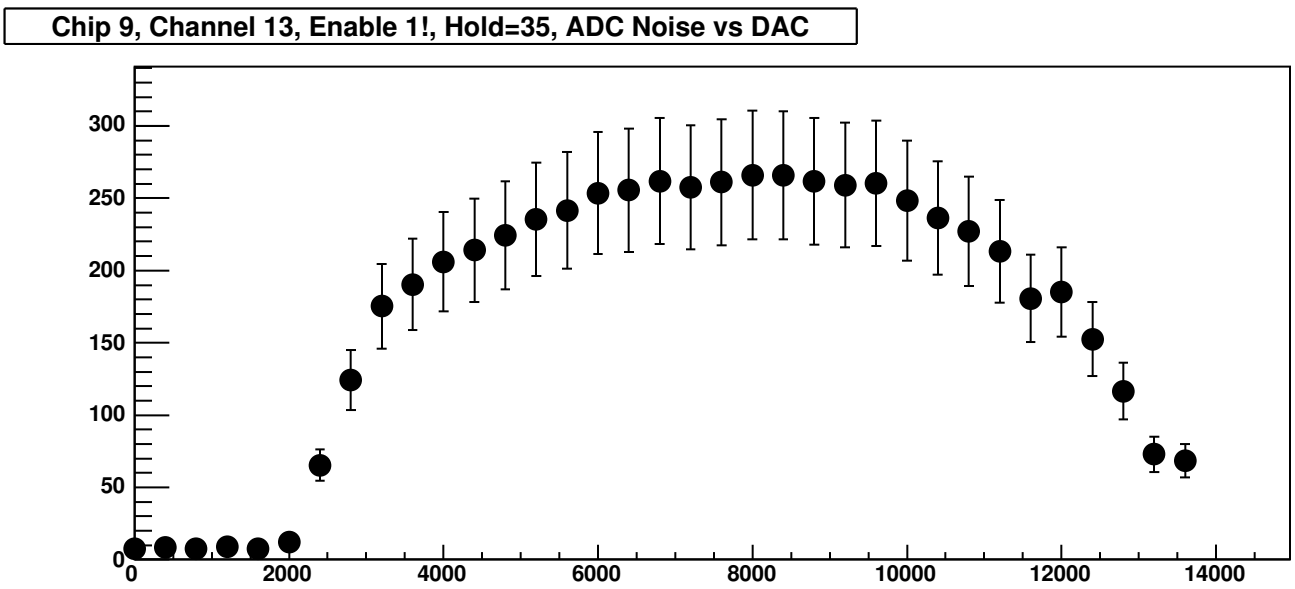
Chip 9, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

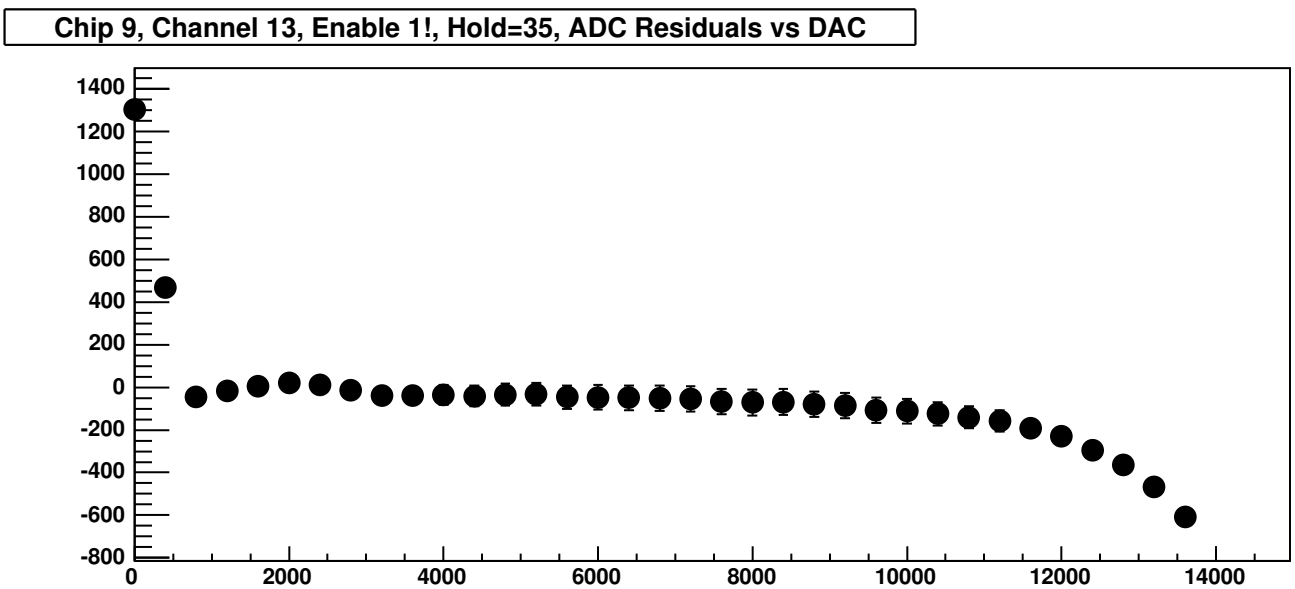


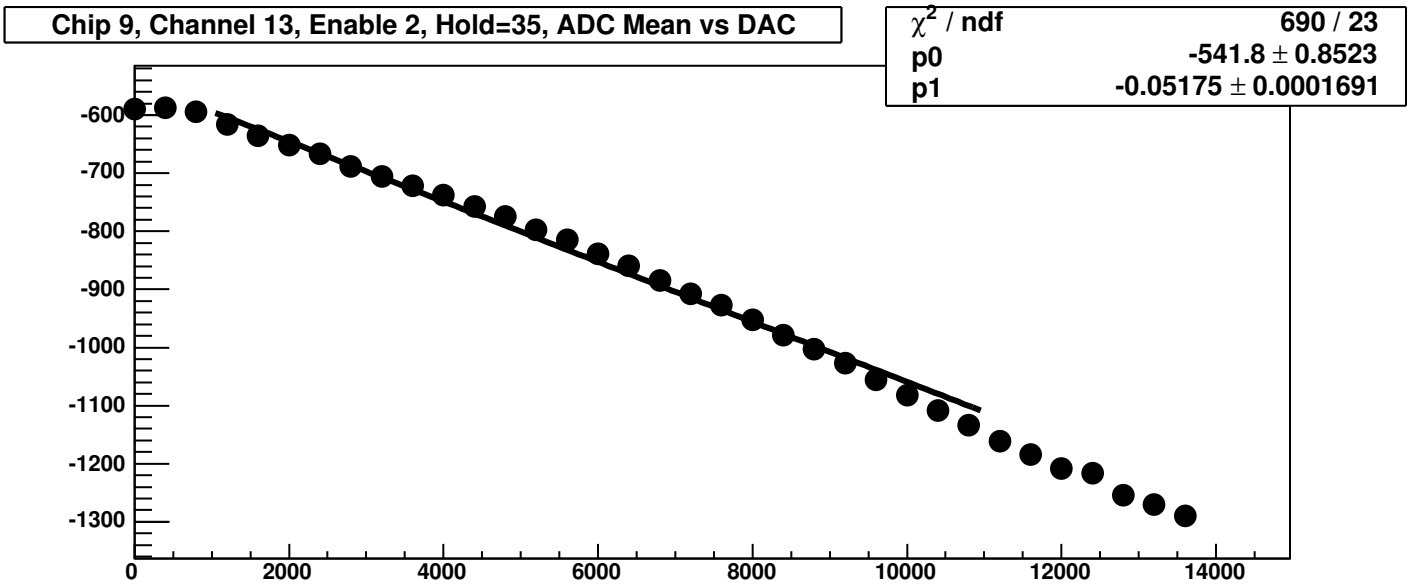
Chip 9, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

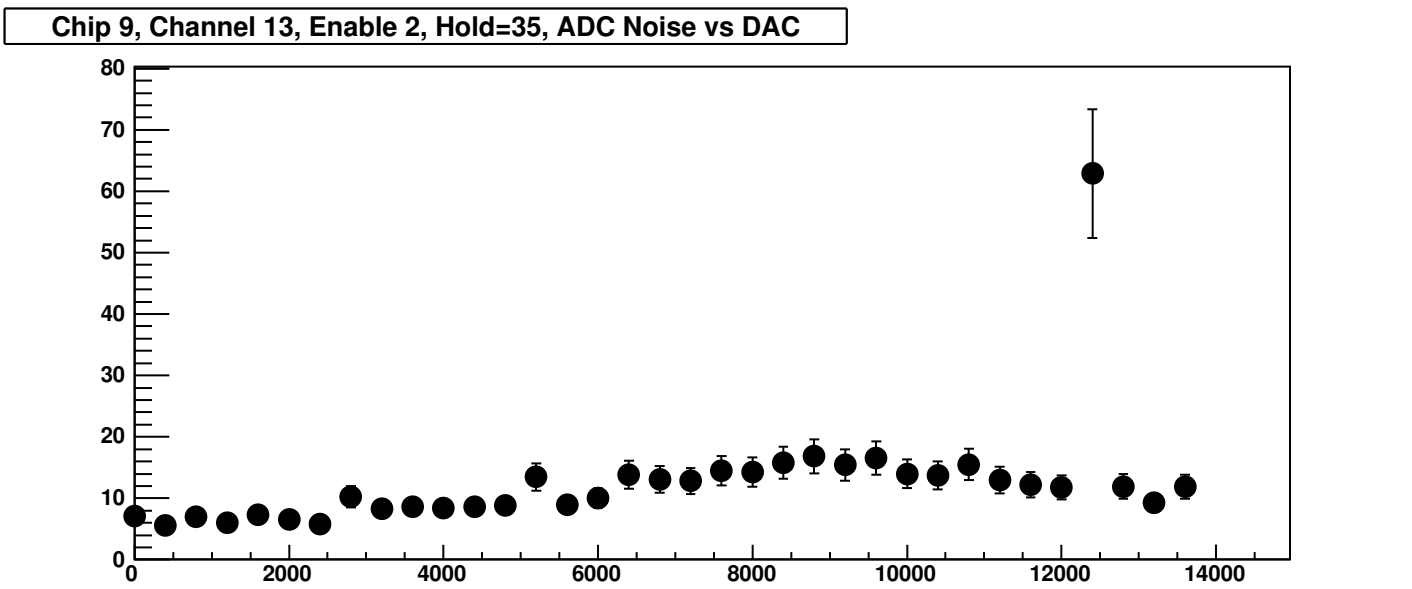


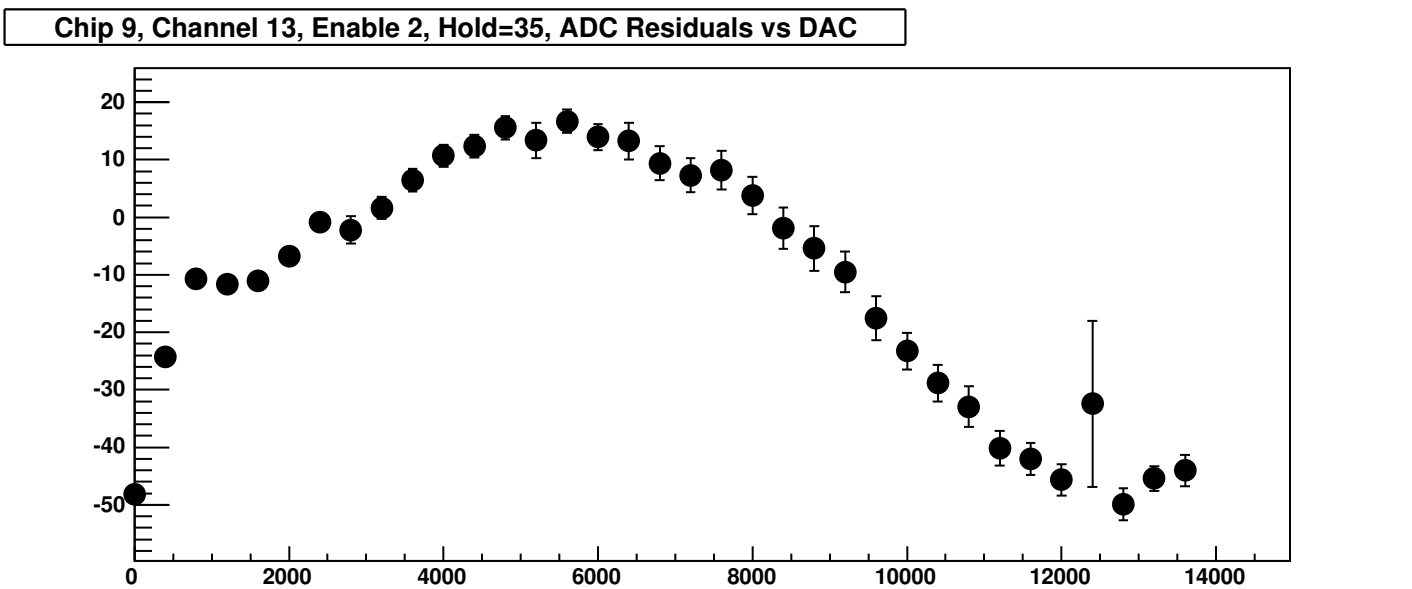

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 170.8 / 23
p0 -475.9 ± 3.263
p1 2.122 ± 0.001903
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

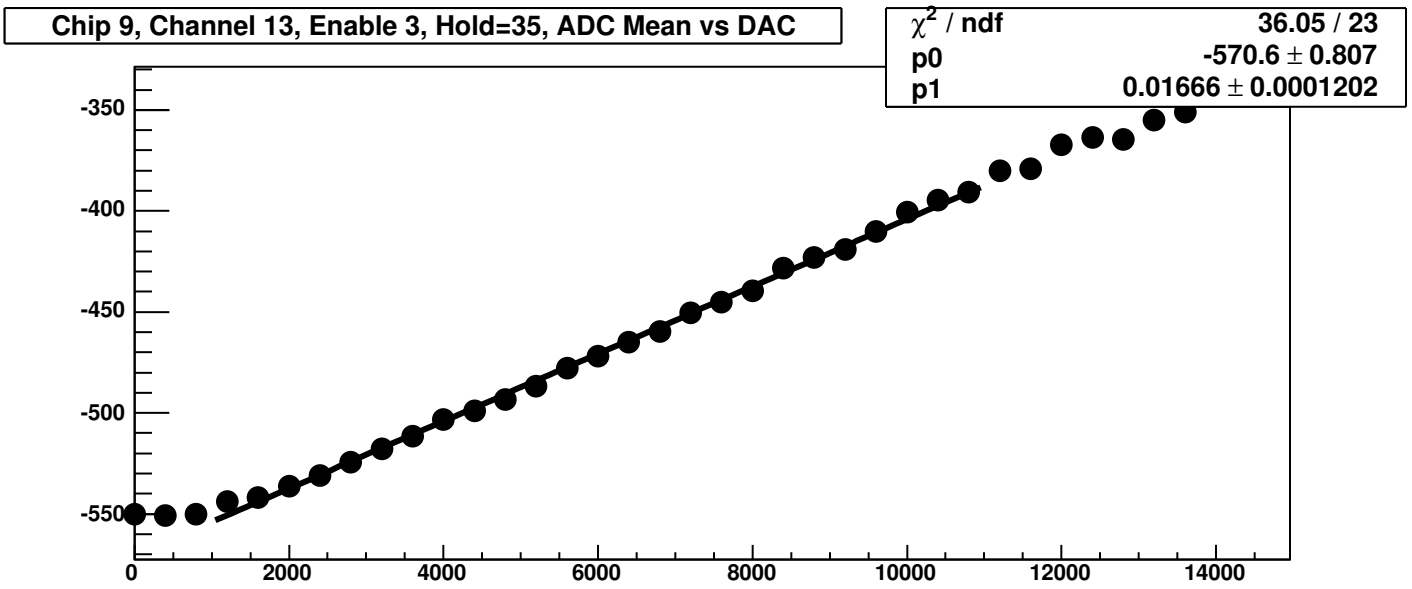

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

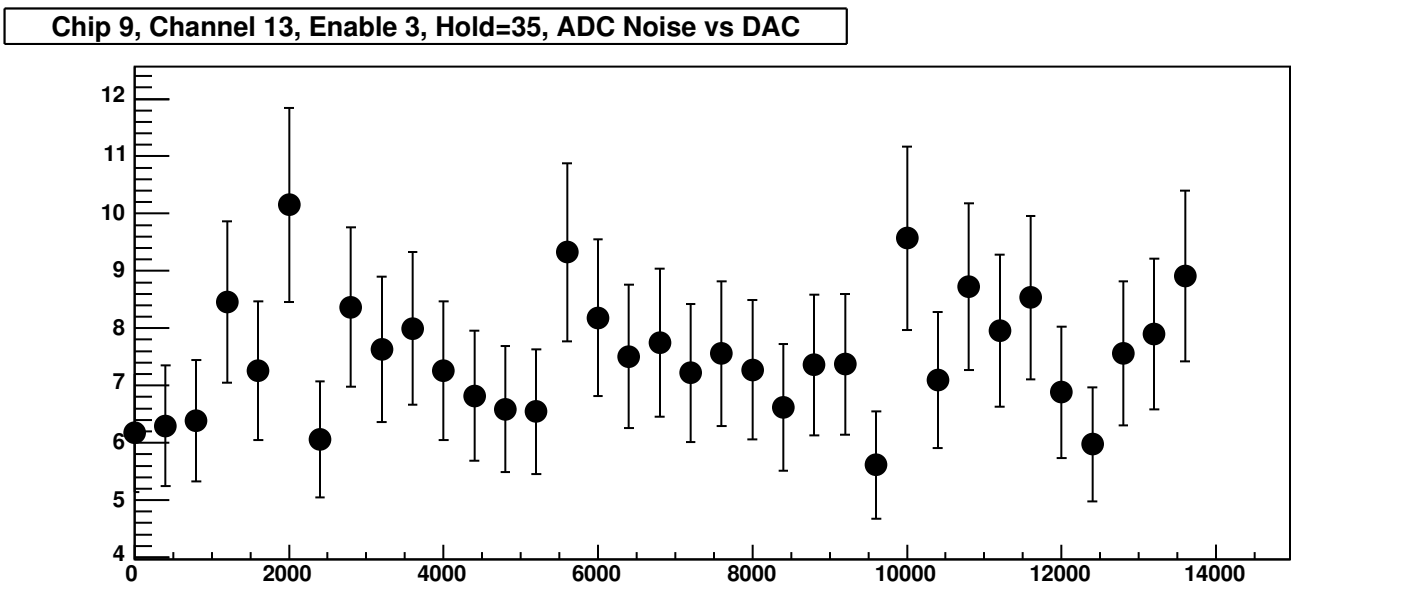

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

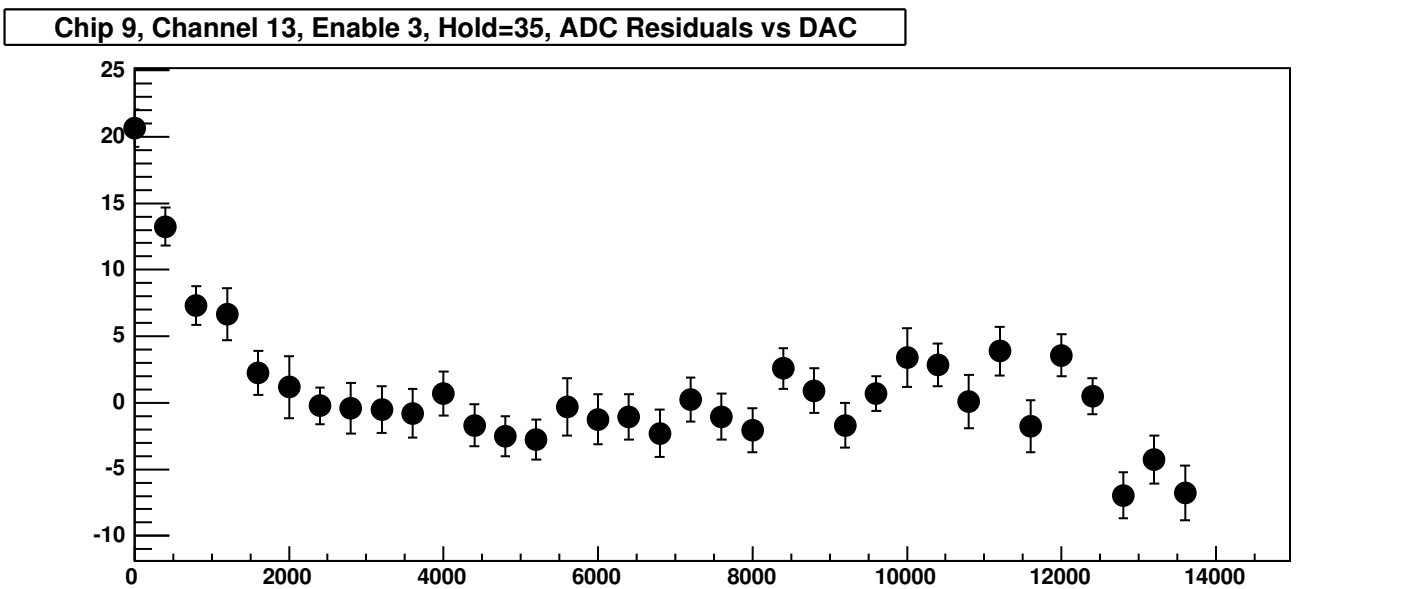

Chip 9, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 690 / 23
p0 -541.8 ± 0.8523
p1 -0.05175 ± 0.0001691
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

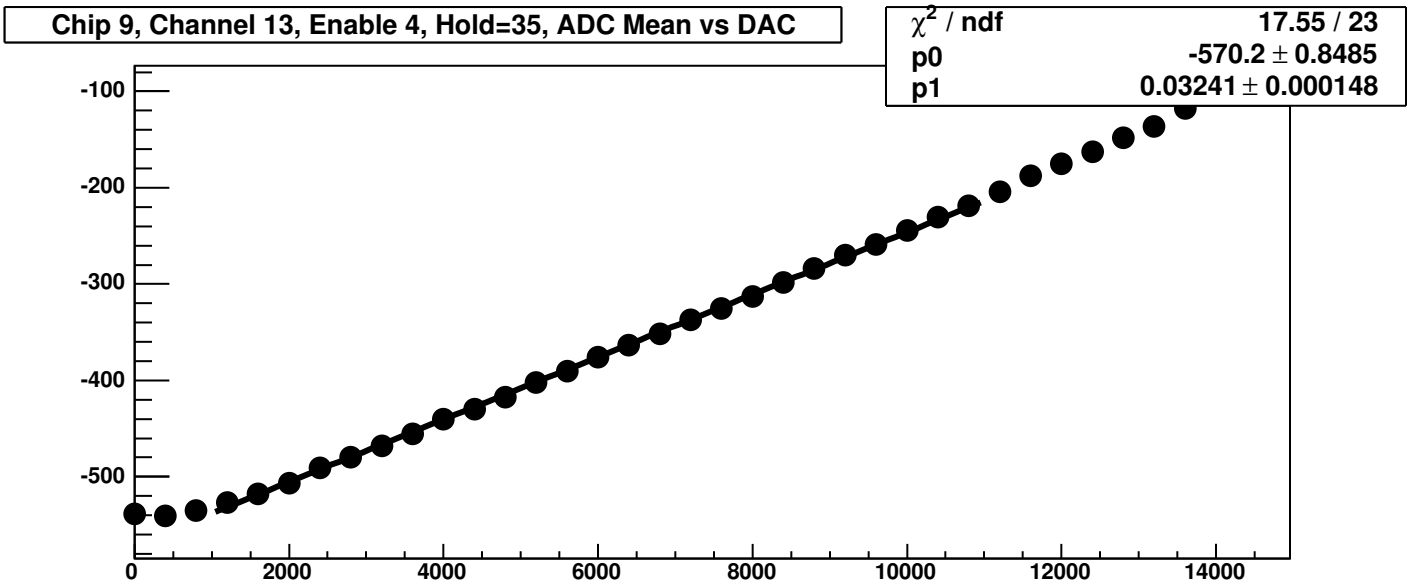

Chip 9, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

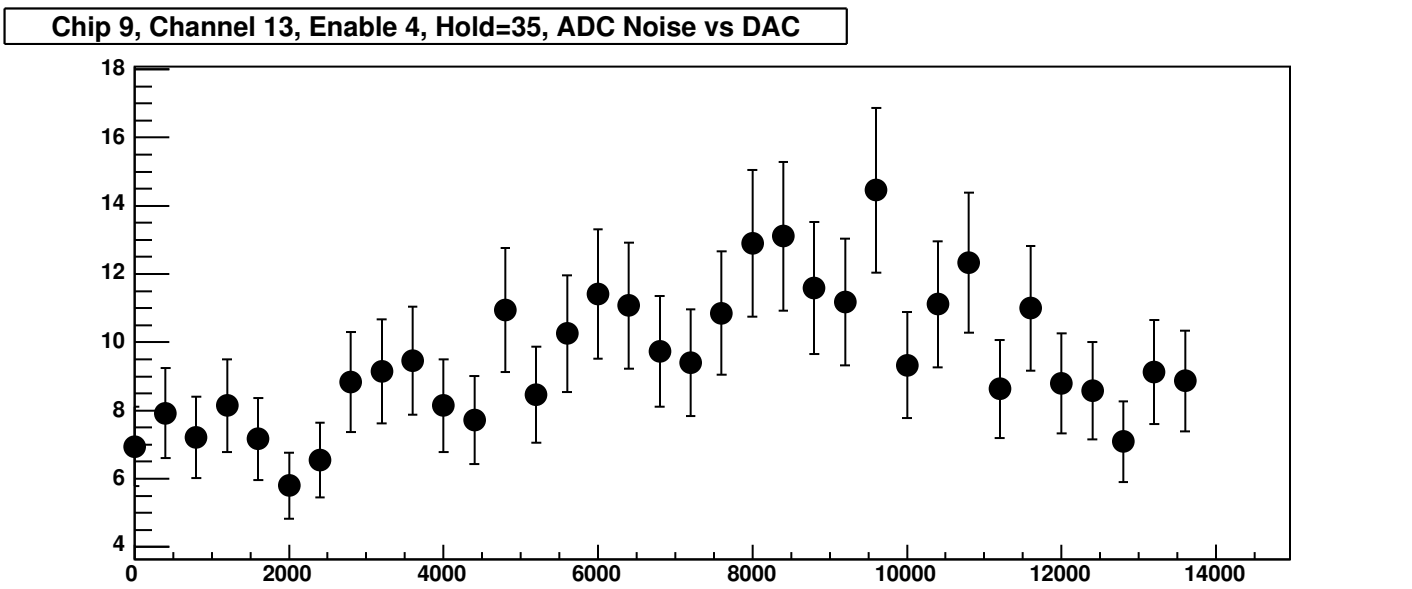

Chip 9, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

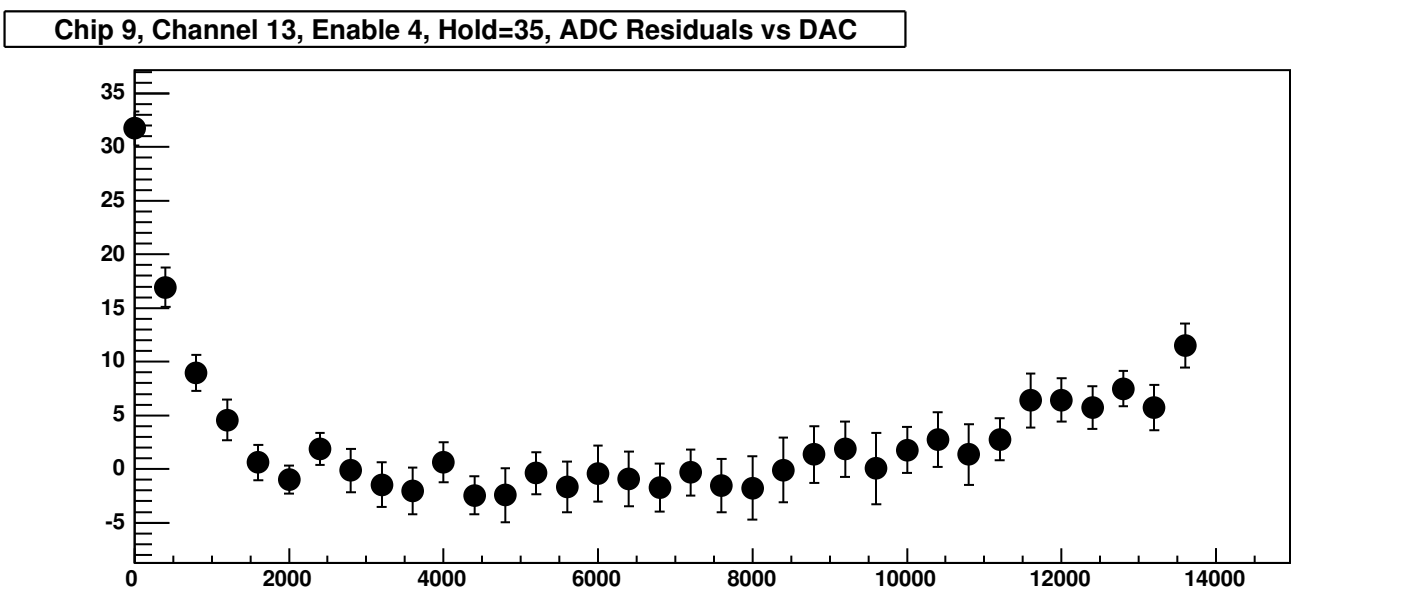

Chip 9, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.05 / 23
p0 -570.6 ± 0.807
p1 0.01666 ± 0.0001202
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

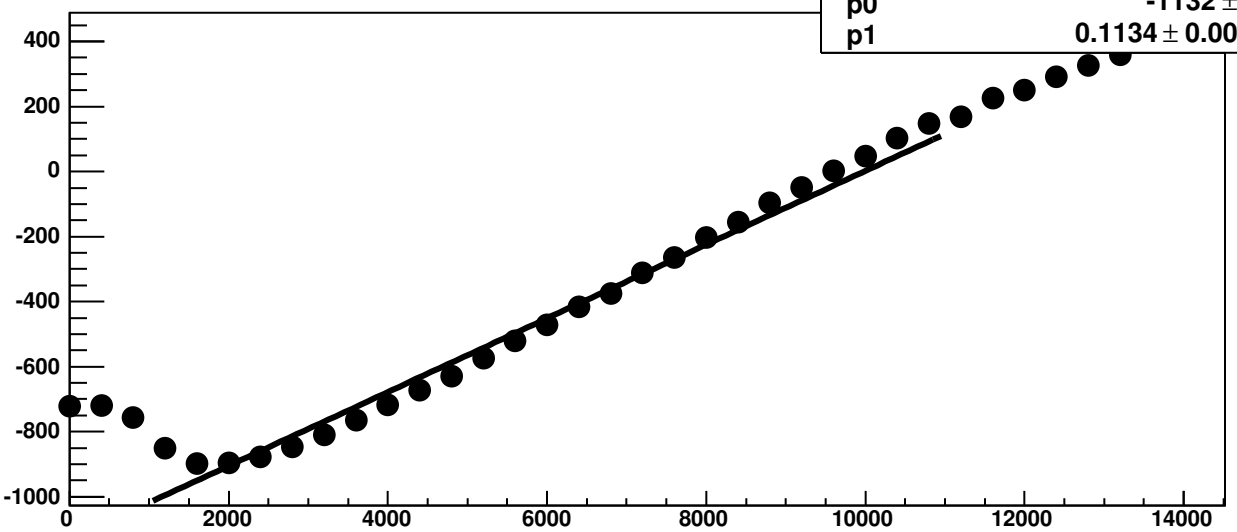

Chip 9, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.55 / 23
p0 -570.2 ± 0.8485
p1 0.03241 ± 0.000148
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

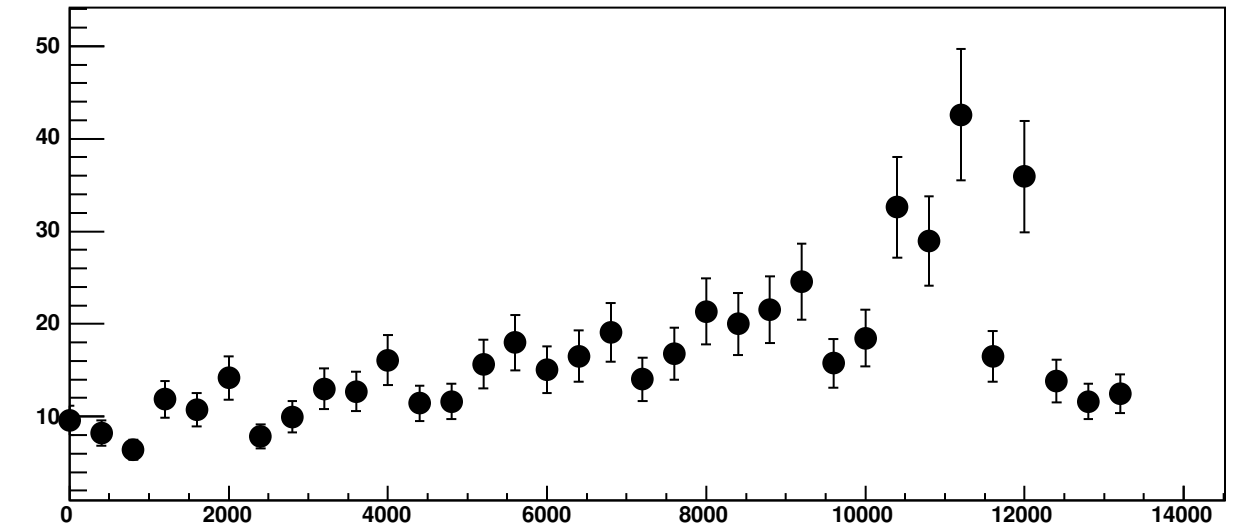

Chip 9, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

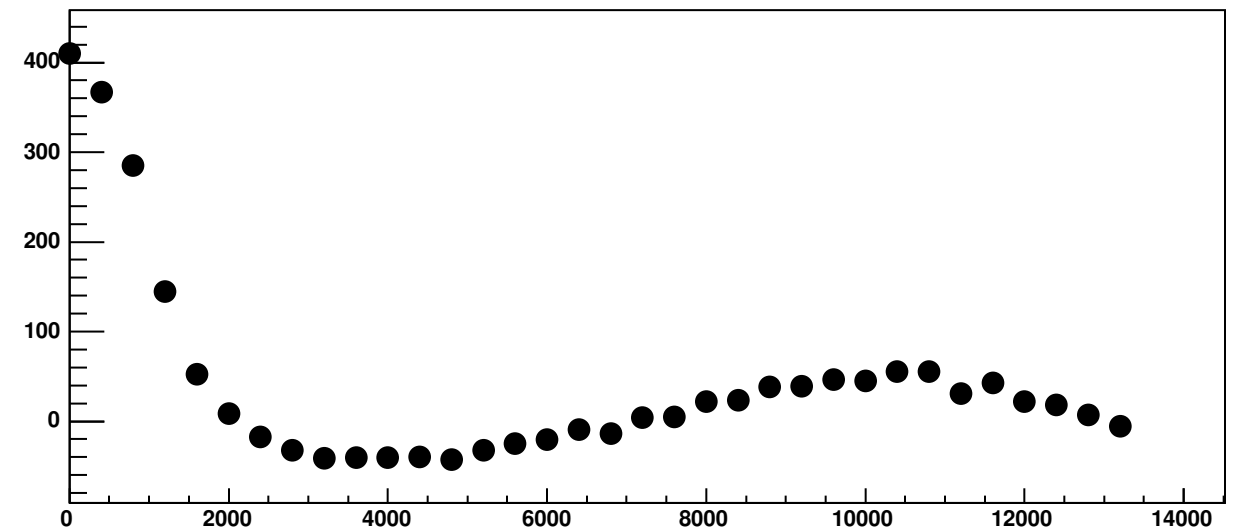
χ^2 / ndf	5293 / 23
p0	-1132 ± 1.328
p1	0.1134 ± 0.0002523



Chip 9, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

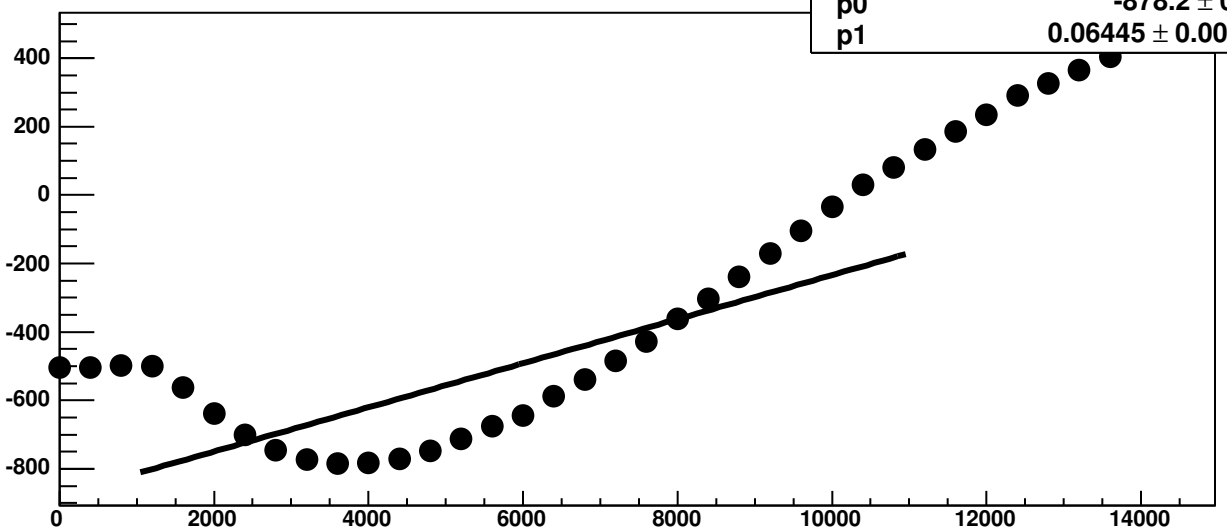


Chip 9, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

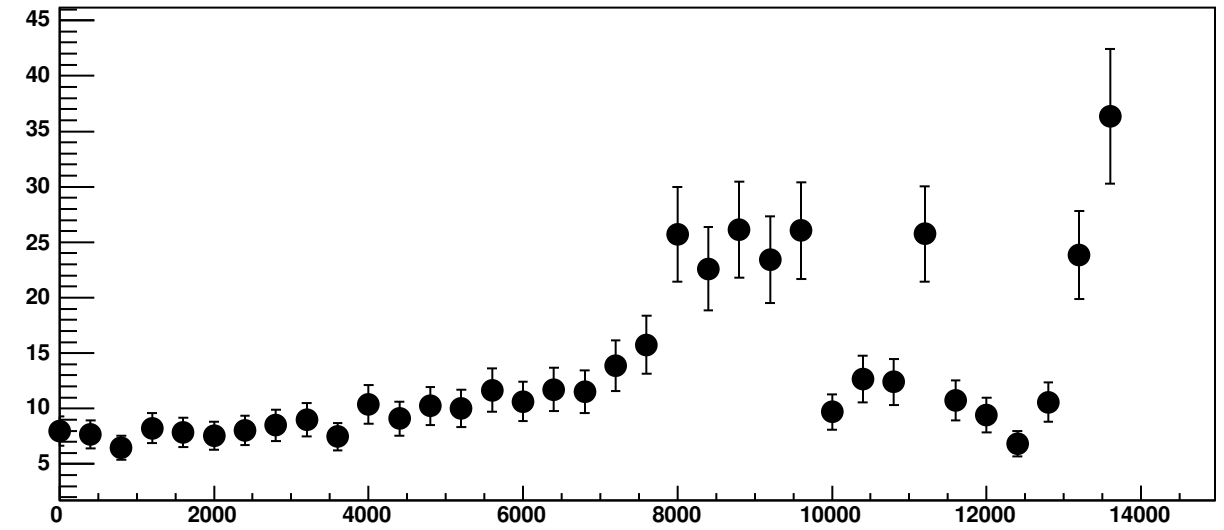


Chip 9, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

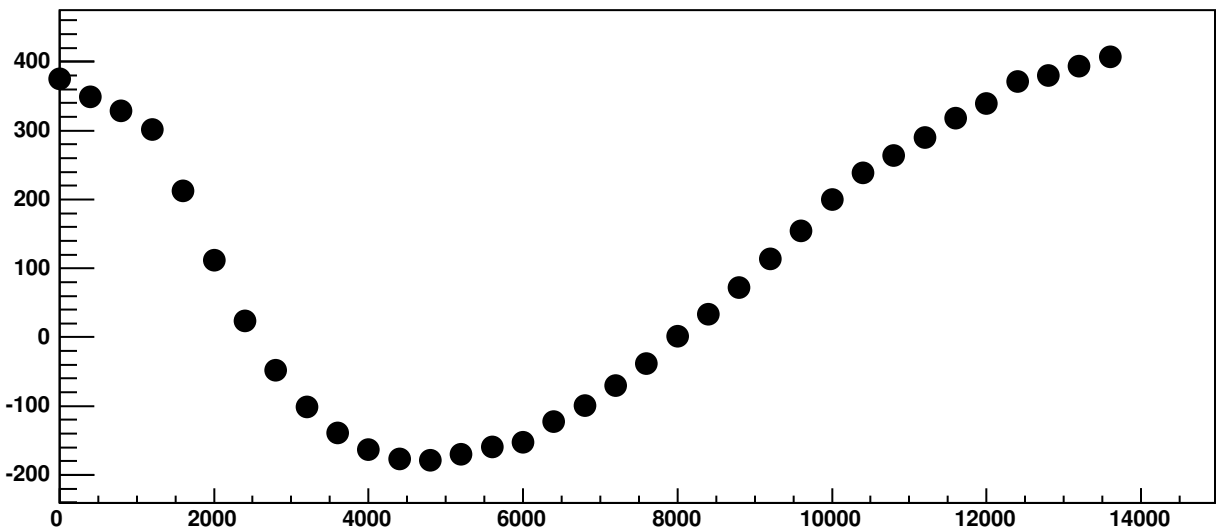
χ^2 / ndf	1.123e+05 / 23
p0	-878.2 ± 0.9537
p1	0.06445 ± 0.0001772

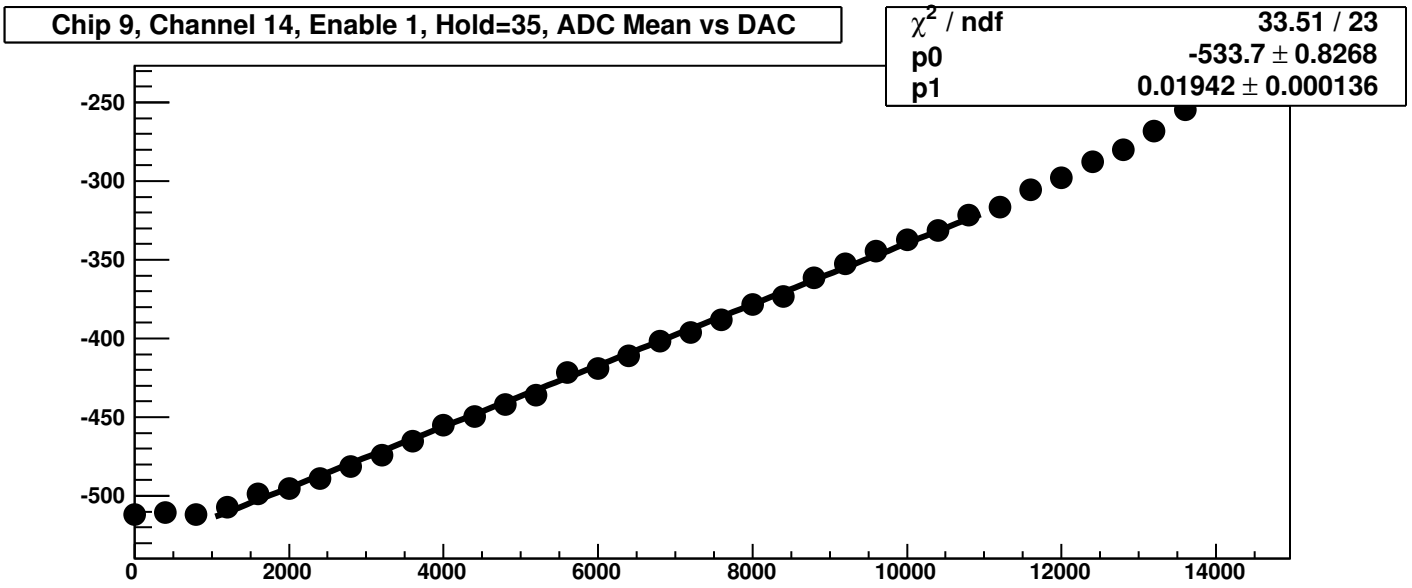


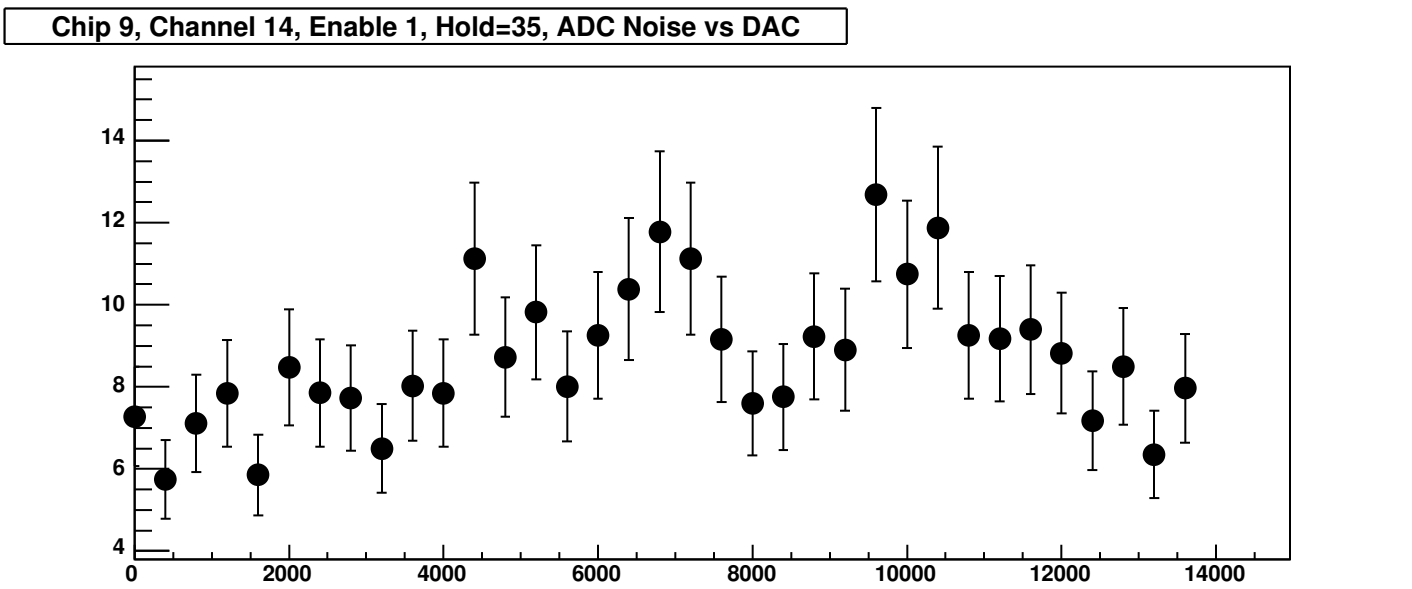
Chip 9, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

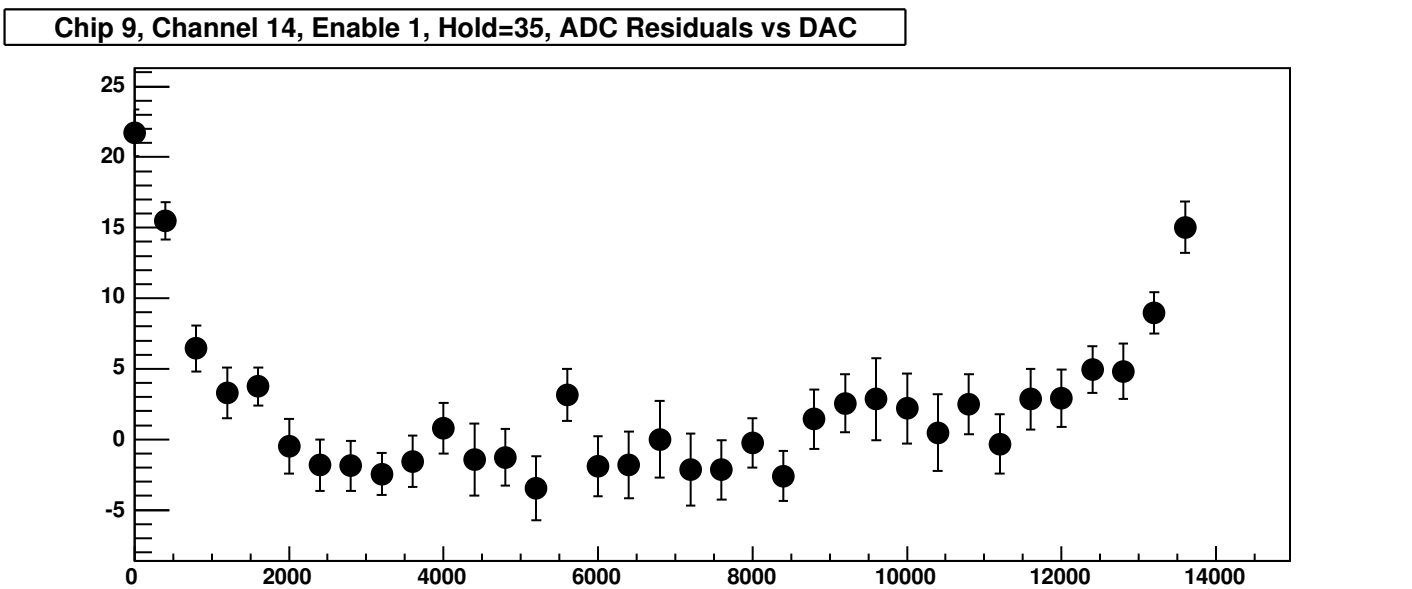


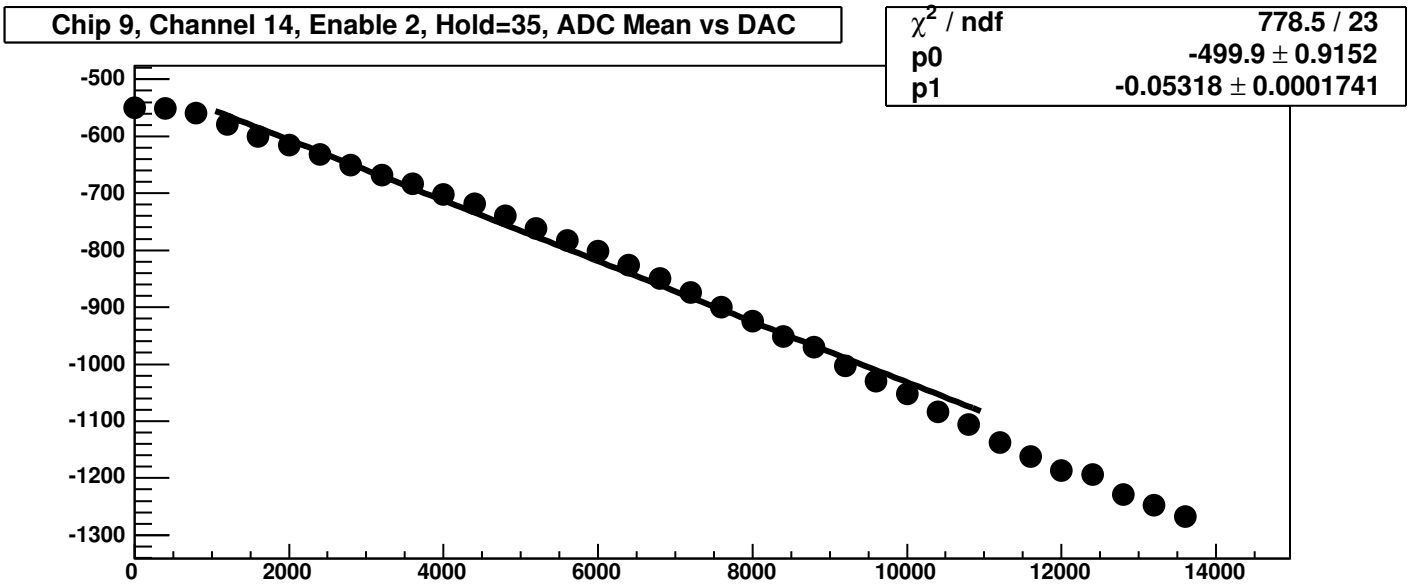
Chip 9, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

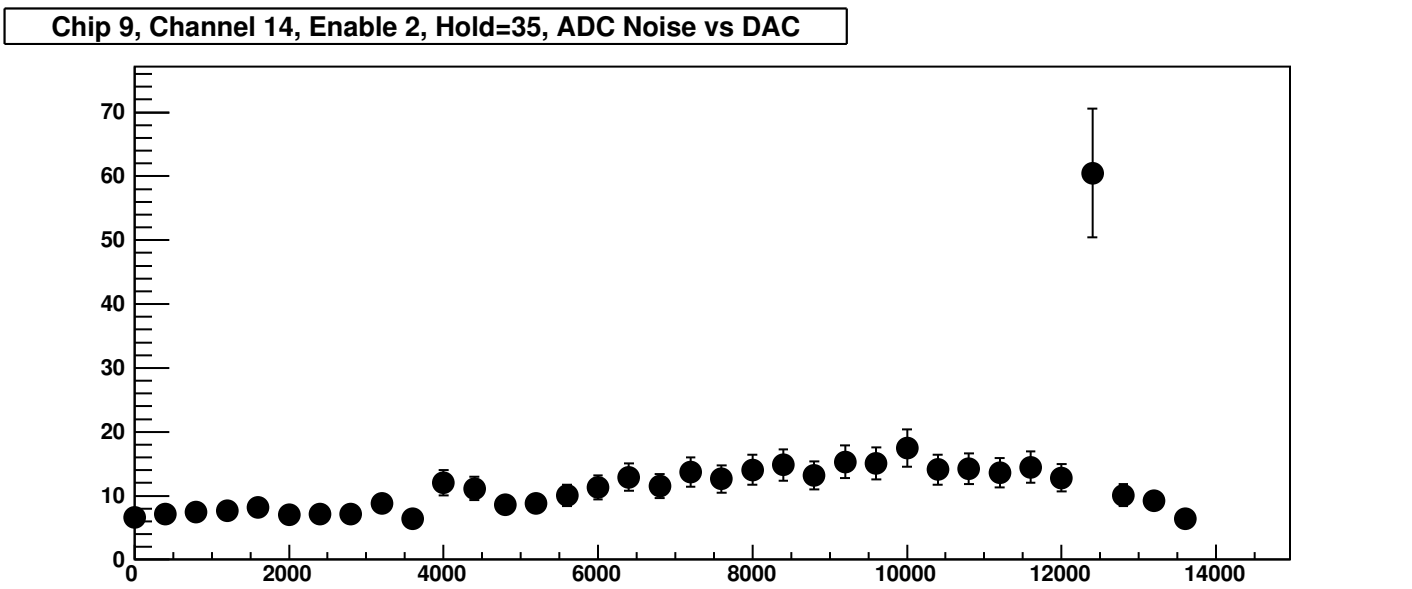


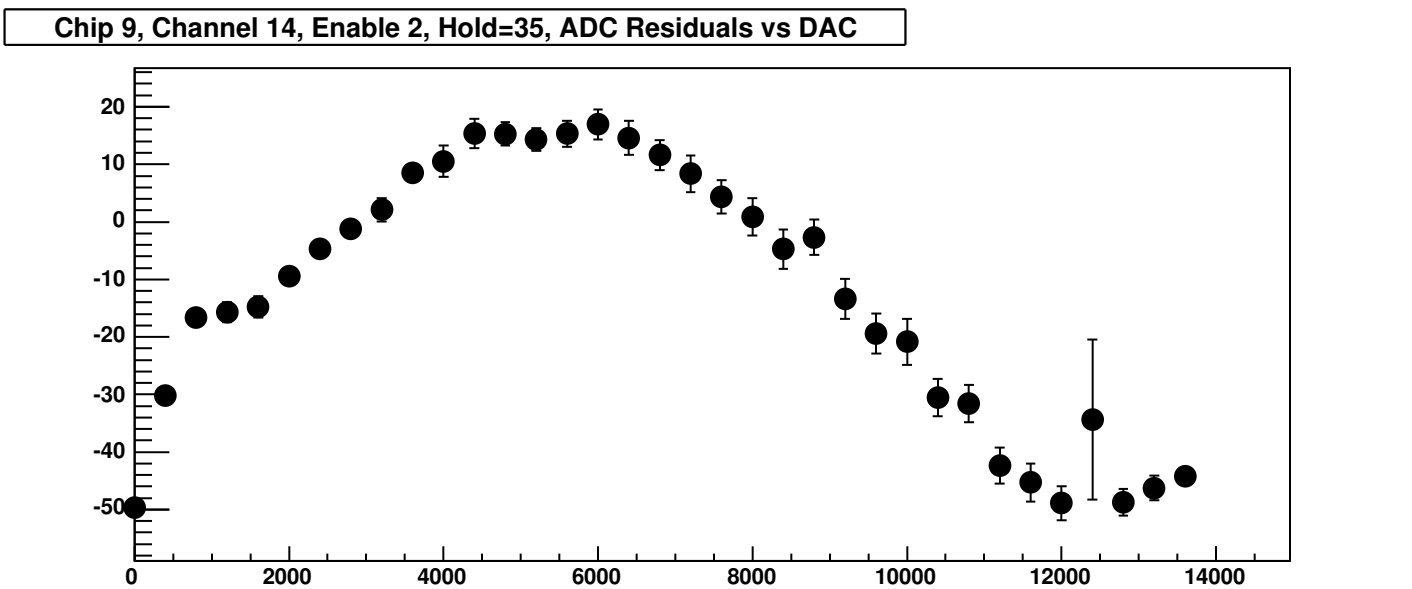

Chip 9, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.51 / 23
p0 -533.7 ± 0.8268
p1 0.01942 ± 0.000136
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

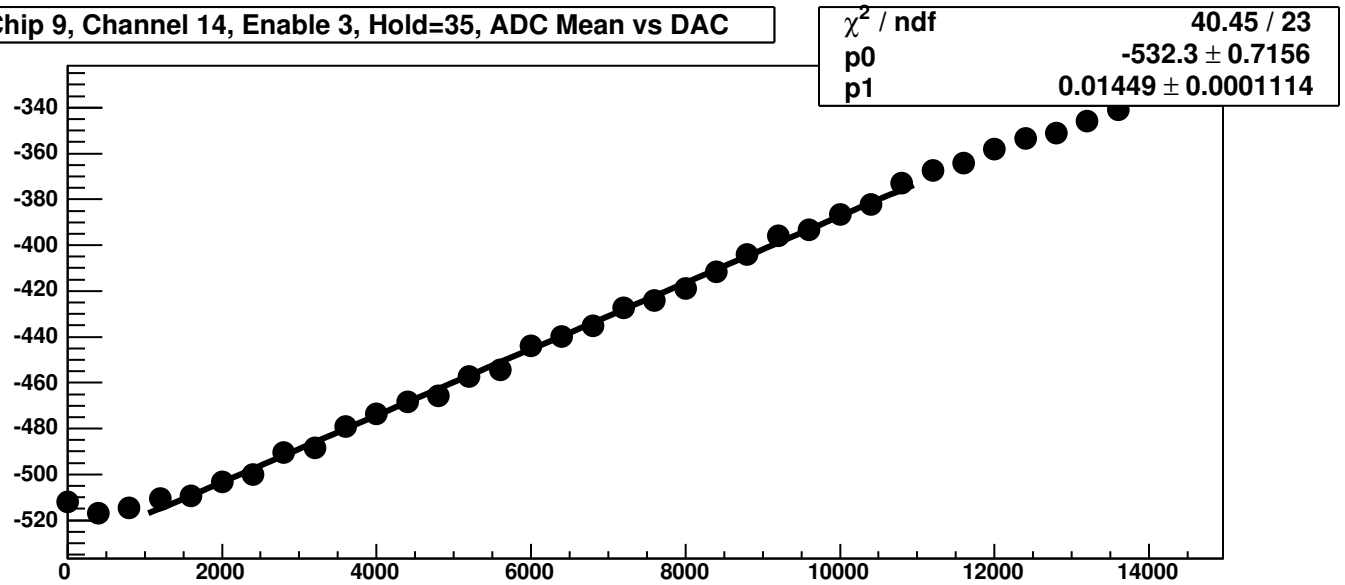

Chip 9, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 778.5 / 23
p0 -499.9 ± 0.9152
p1 -0.05318 ± 0.0001741
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

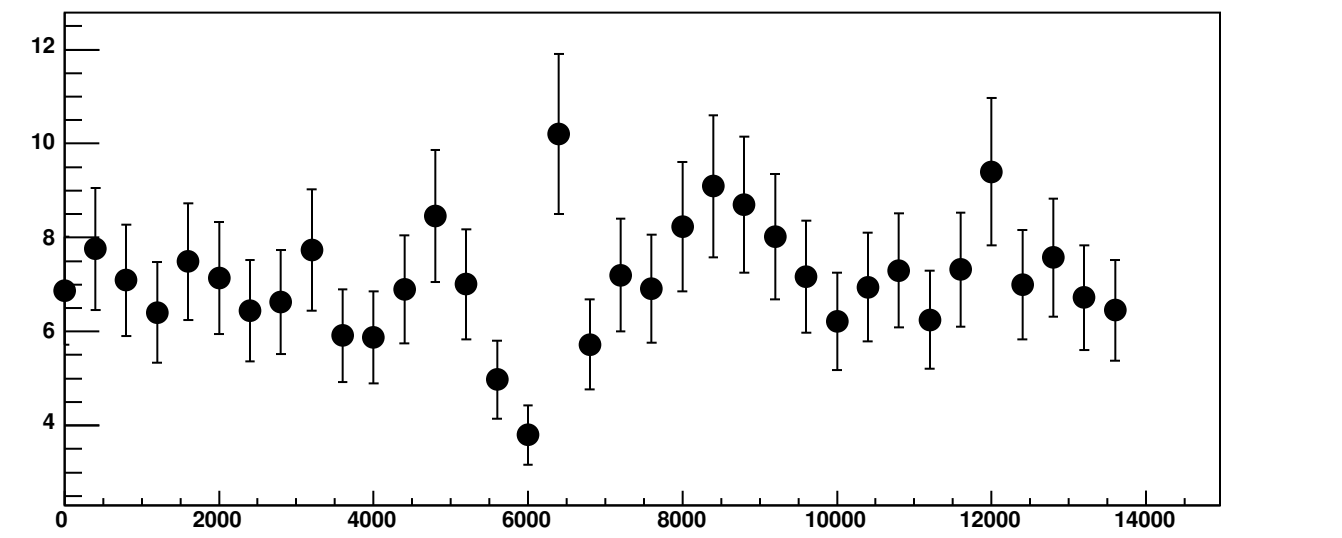

Chip 9, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

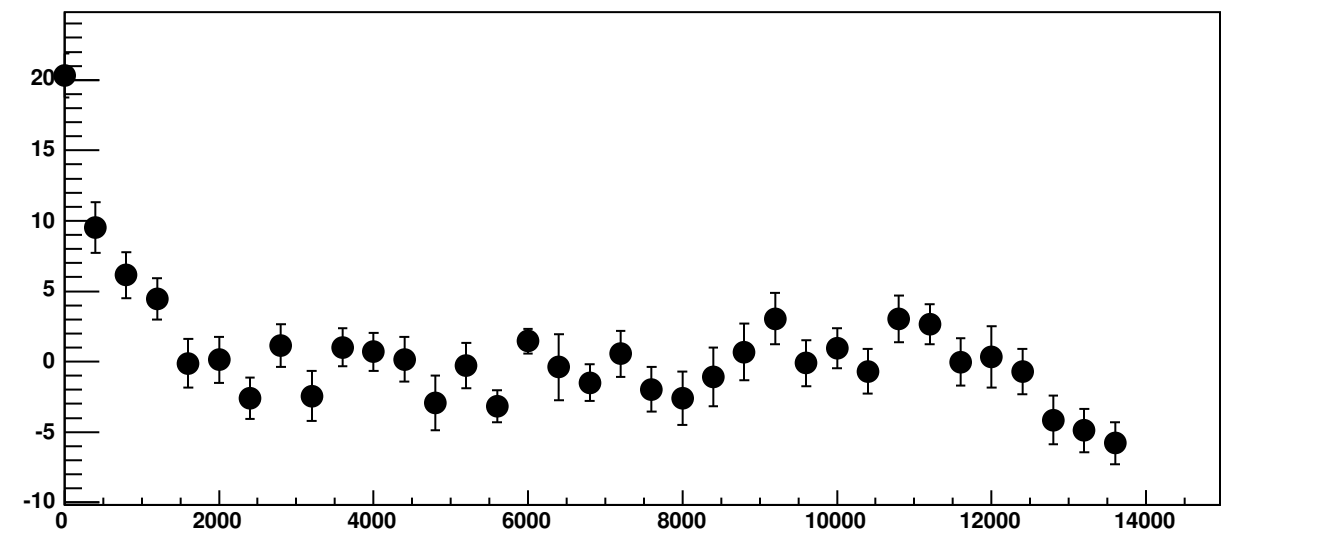
Chip 9, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC

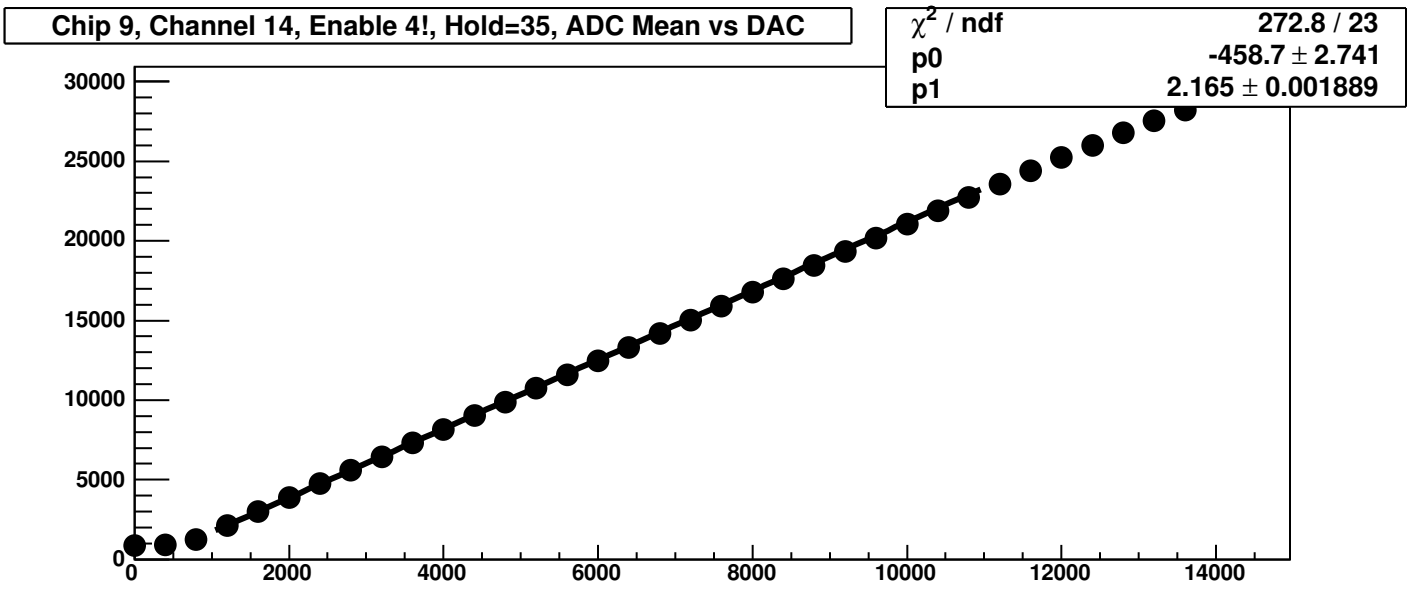


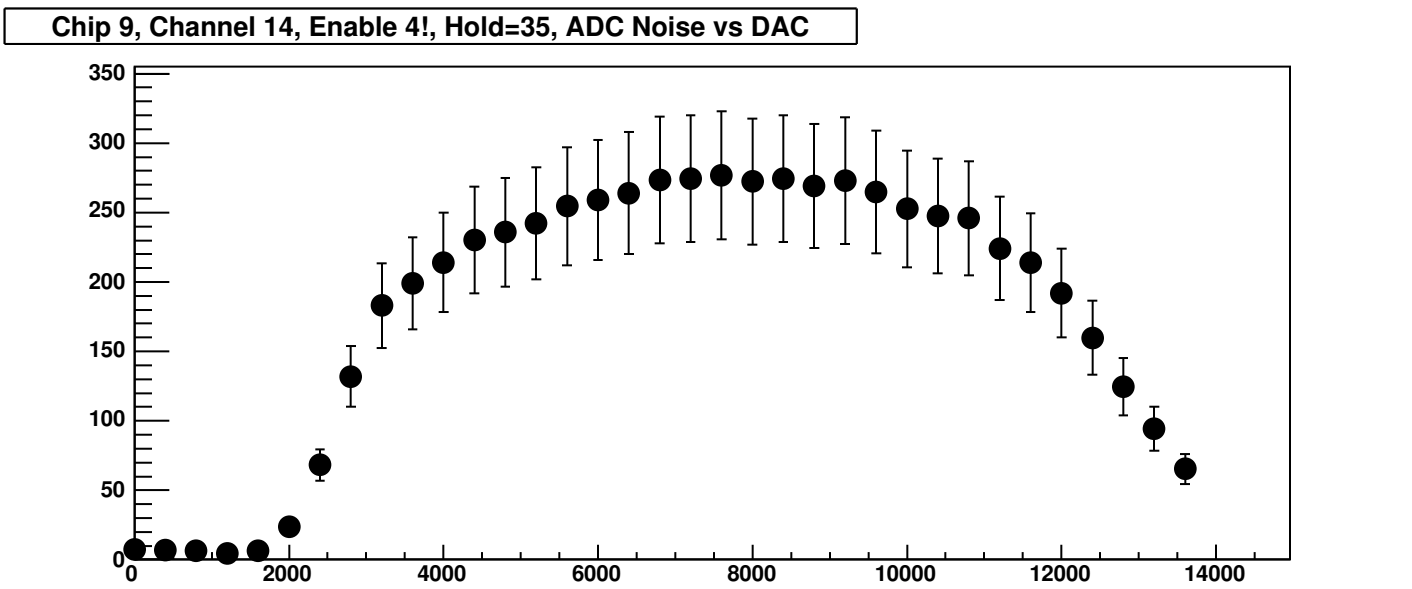
Chip 9, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC

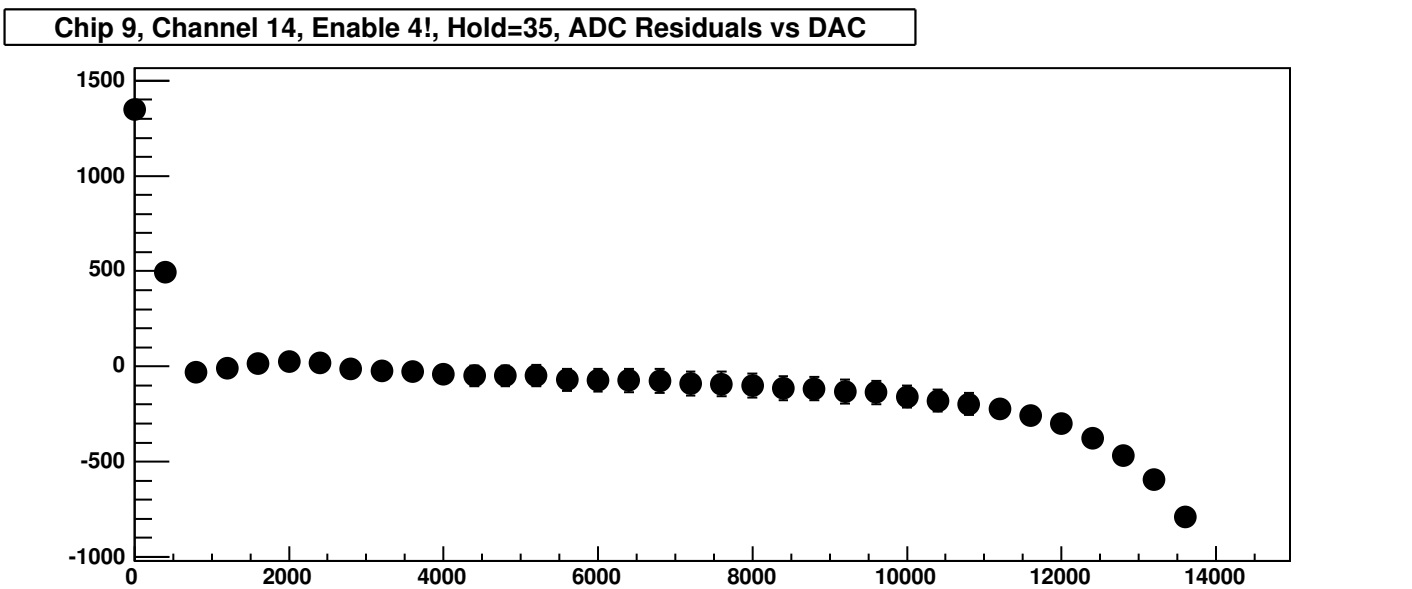


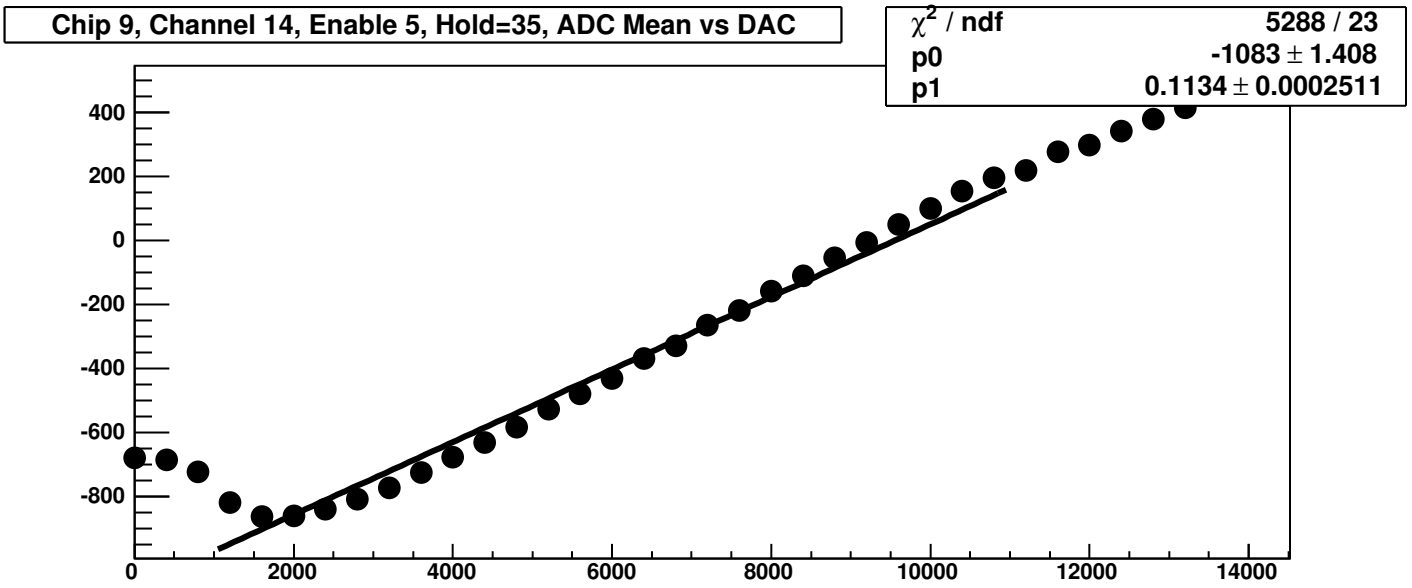
Chip 9, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC

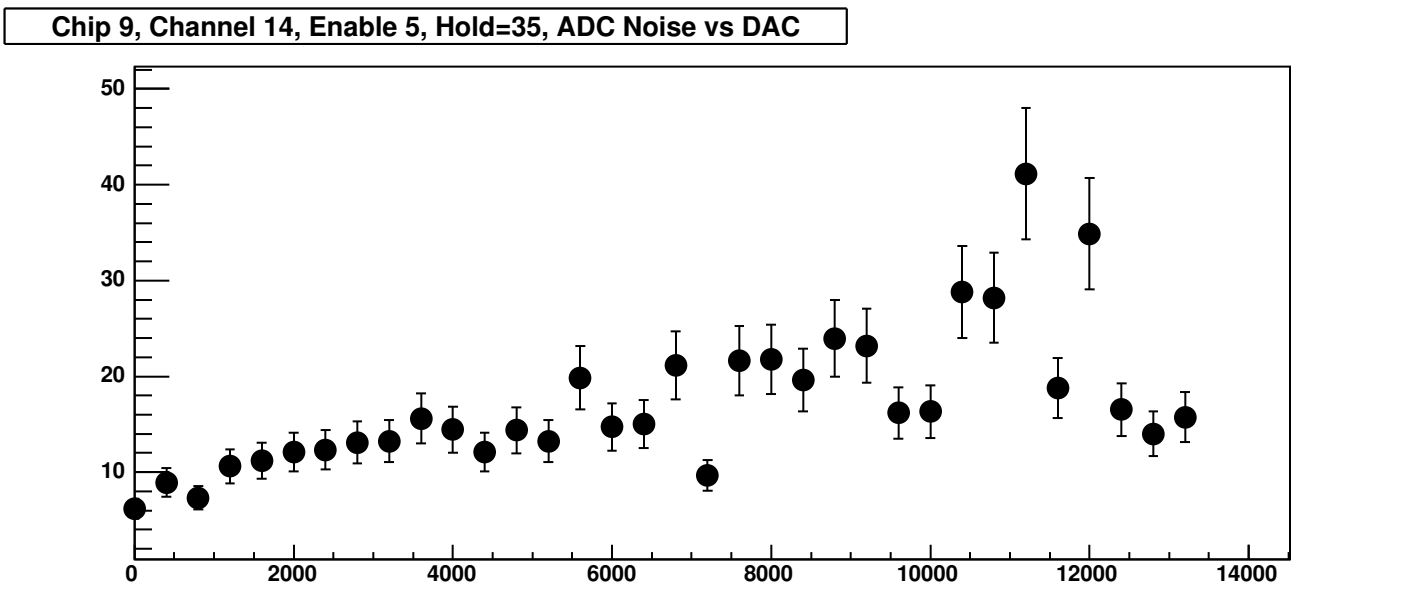


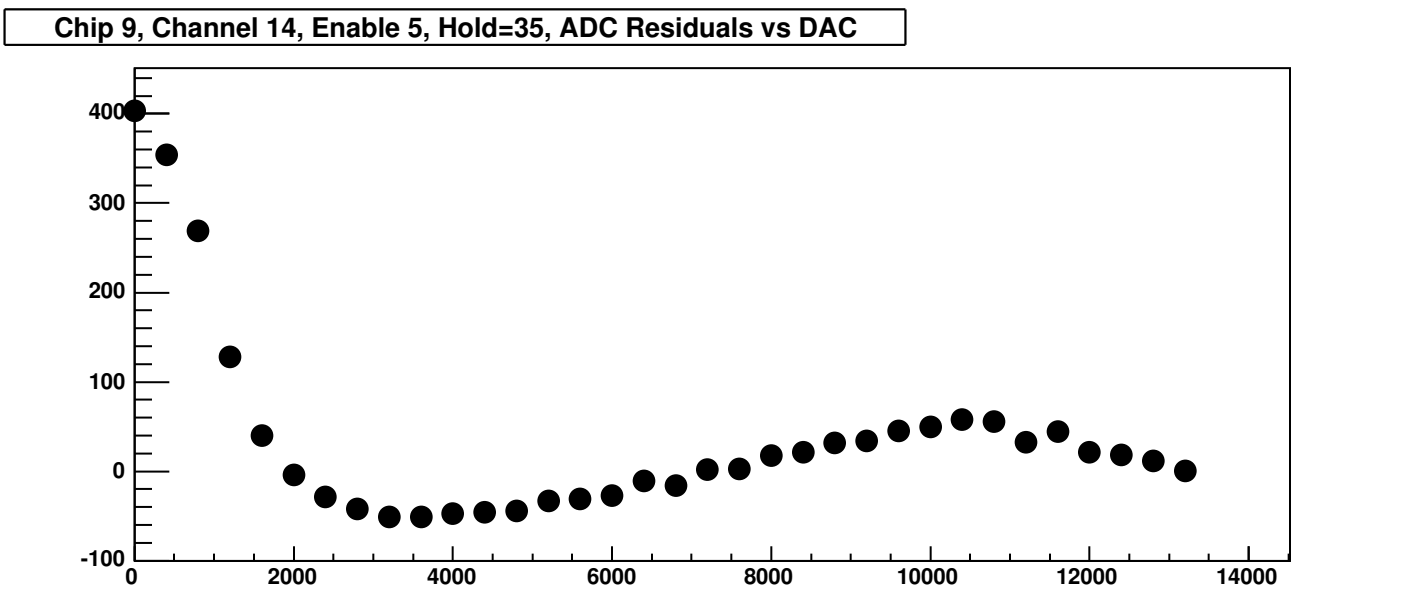

Chip 9, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 272.8 / 23
p0 -458.7 ± 2.741
p1 2.165 ± 0.001889
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

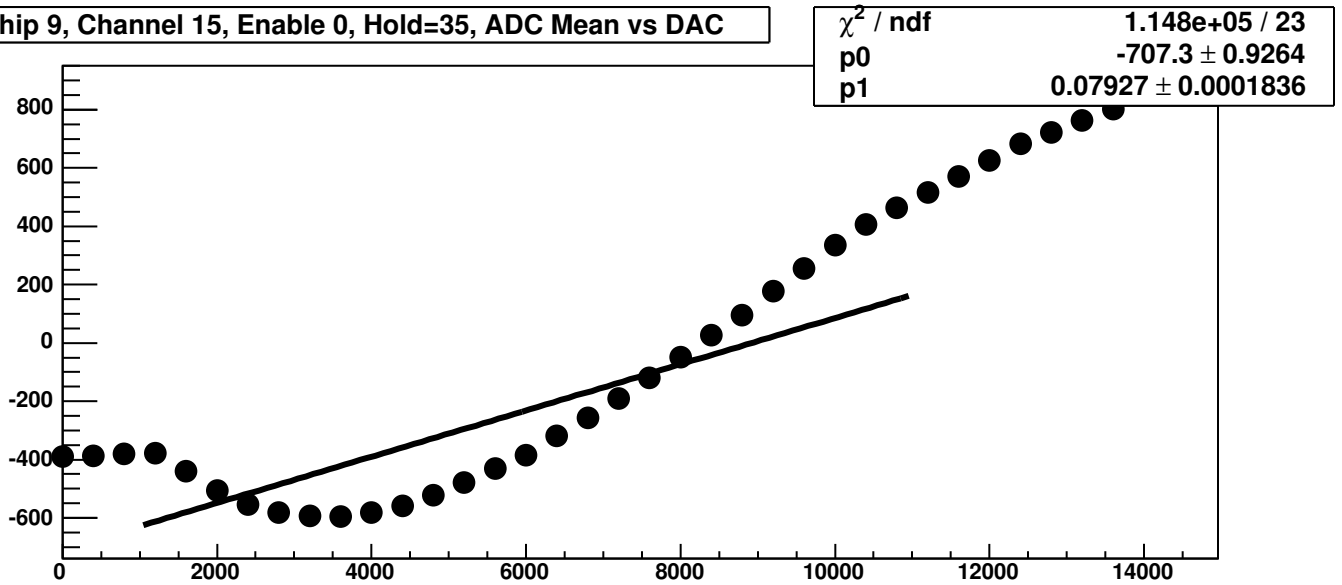

Chip 9, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5288 / 23
p0 -1083 ± 1.408
p1 0.1134 ± 0.0002511
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

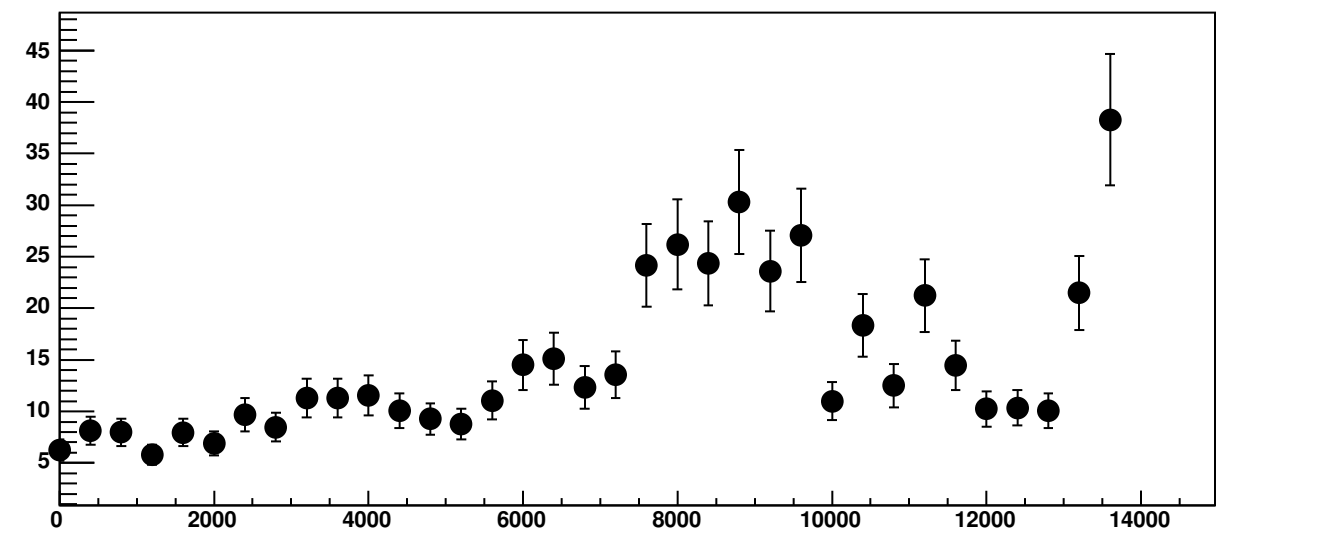

Chip 9, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

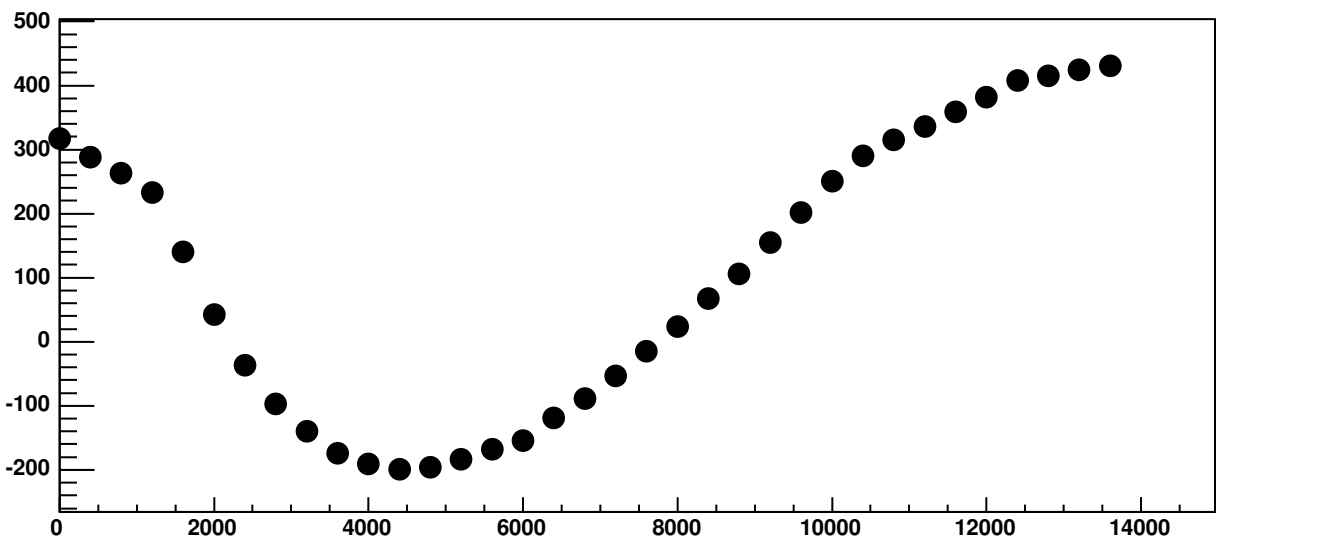
Chip 9, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

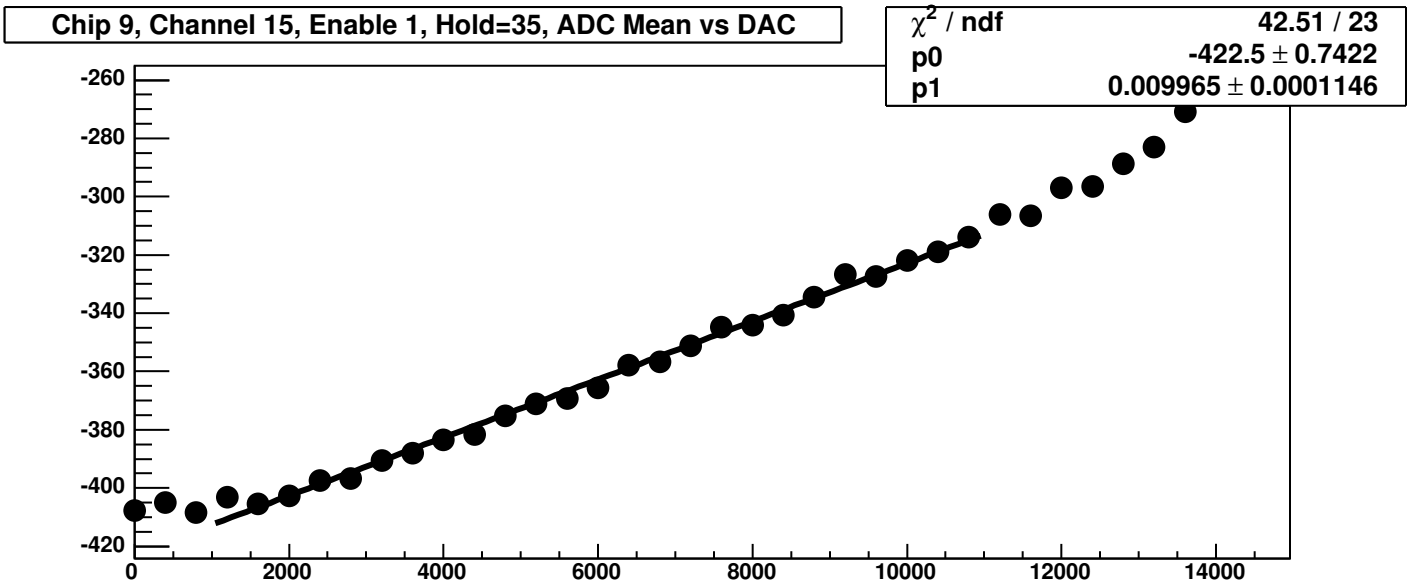


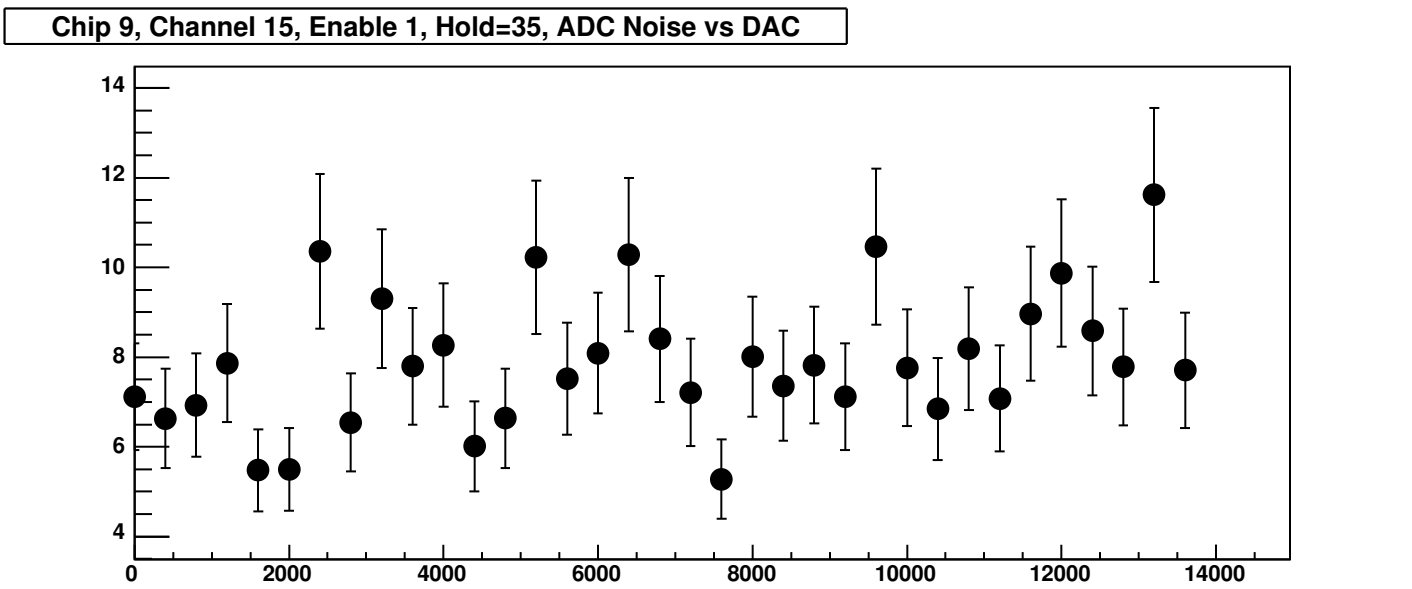
Chip 9, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

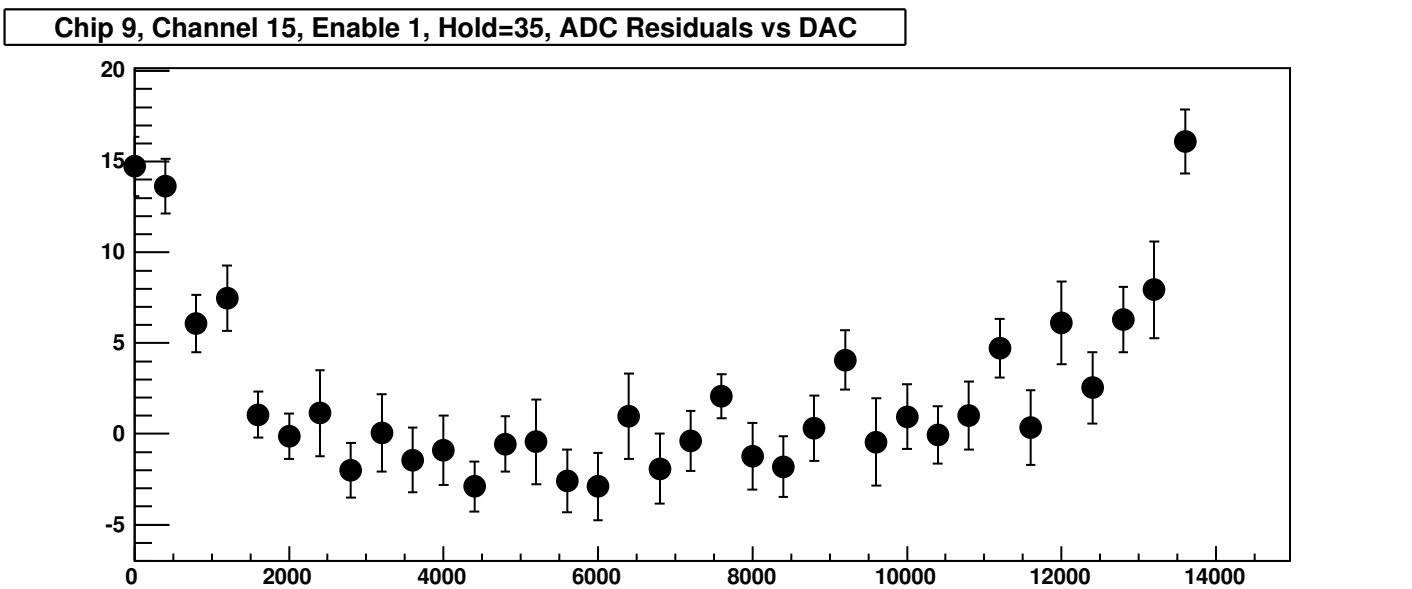


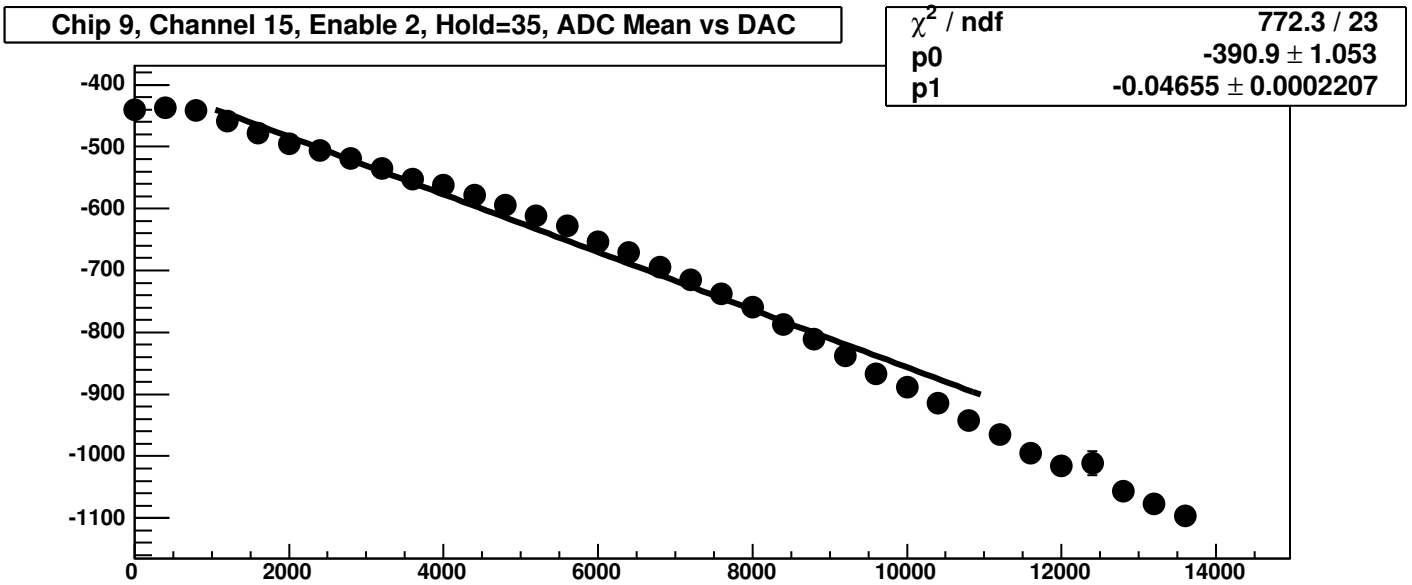
Chip 9, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

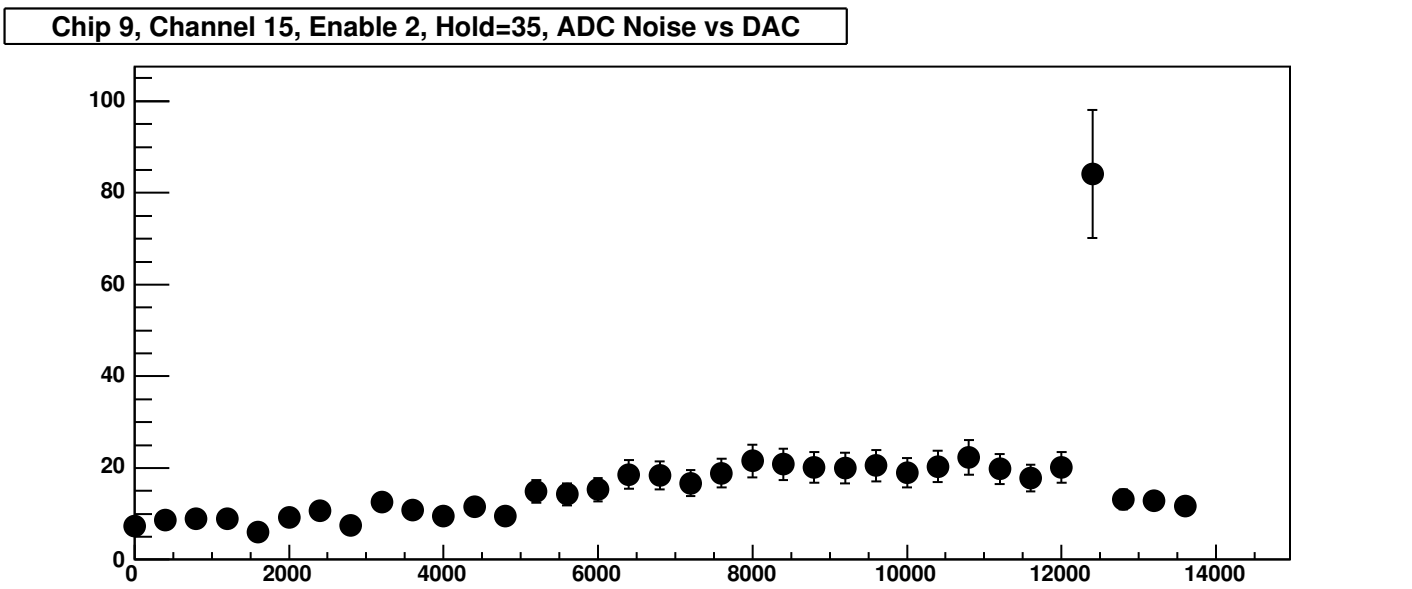


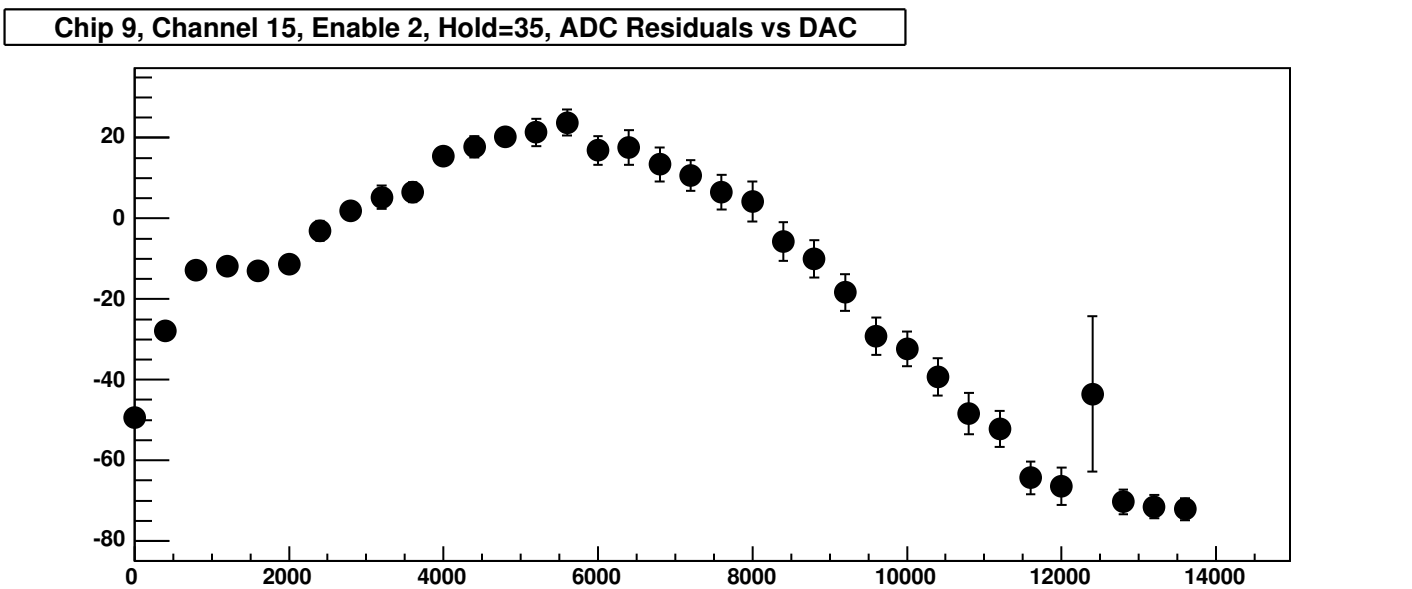

Chip 9, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.51 / 23
p0 -422.5 ± 0.7422
p1 0.009965 ± 0.0001146
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

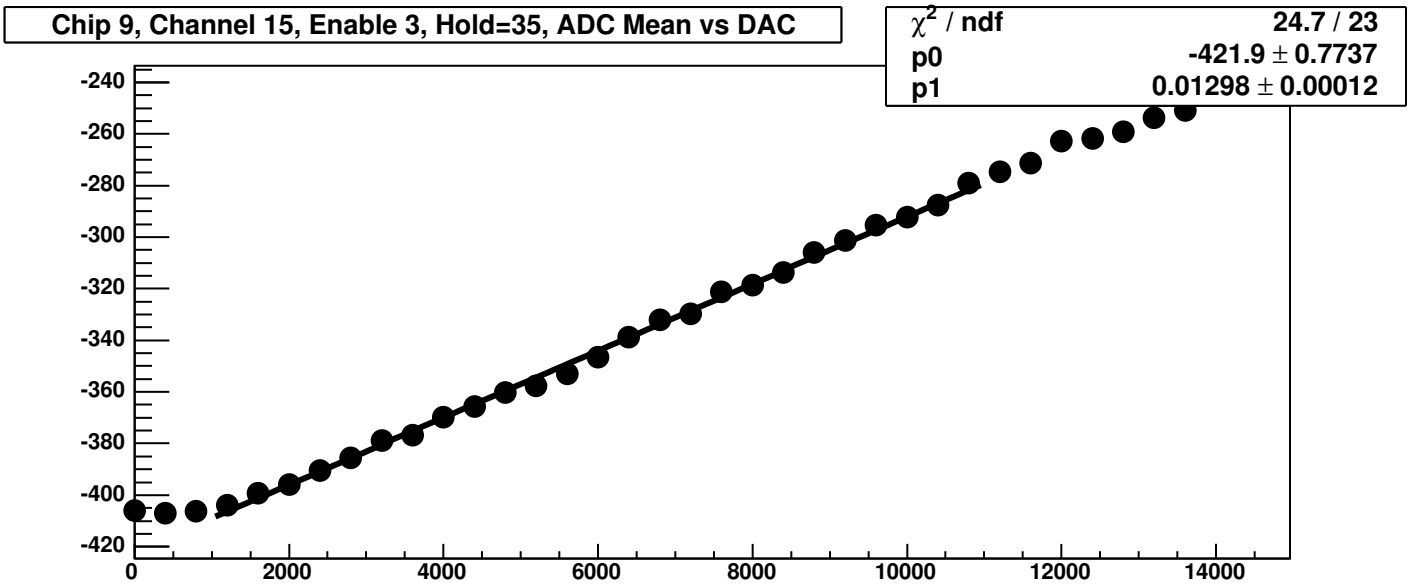

Chip 9, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

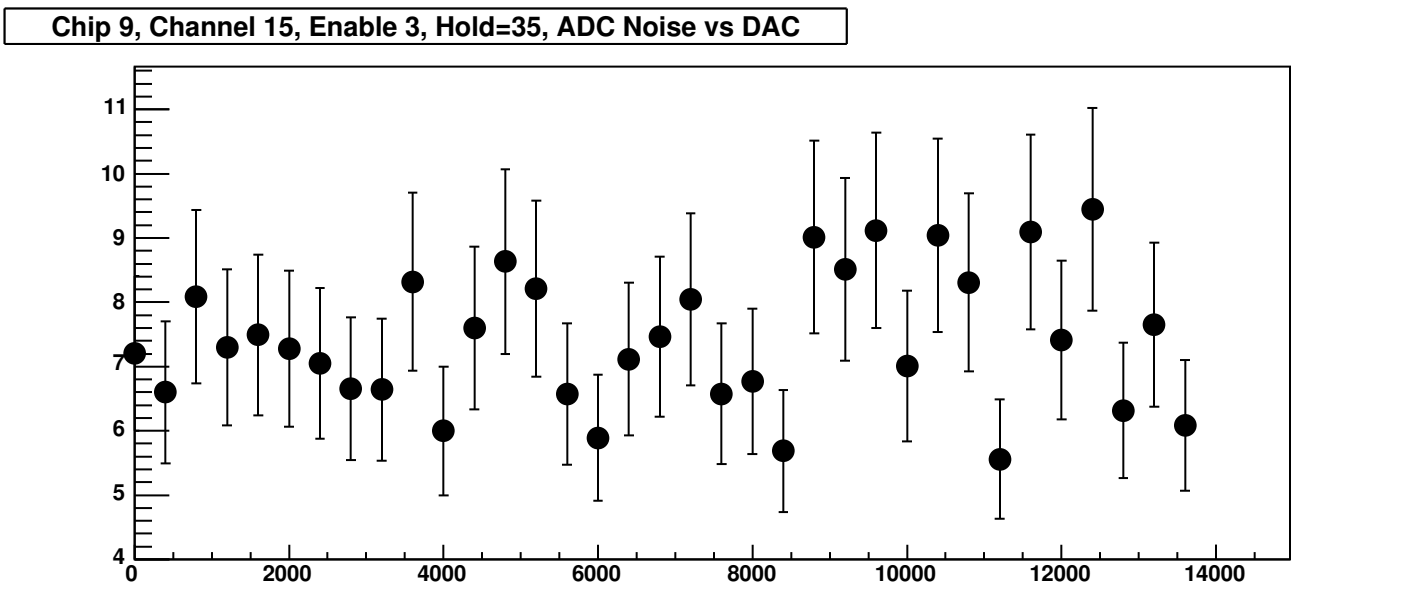

Chip 9, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

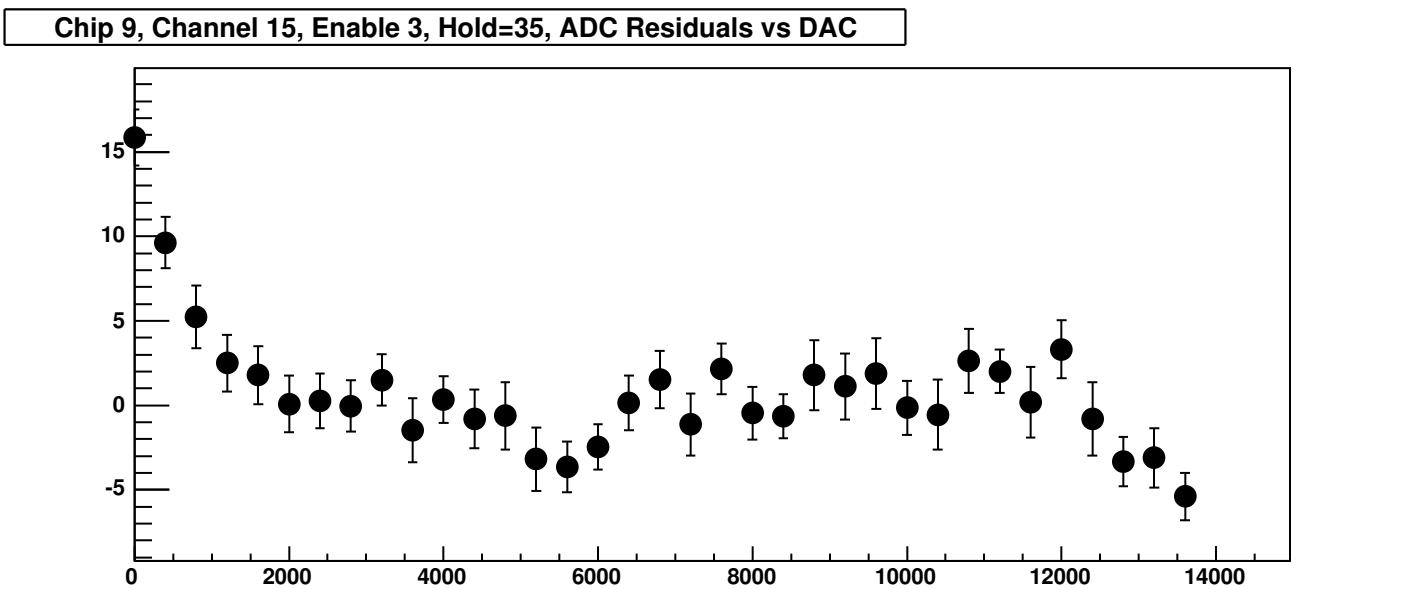

Chip 9, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 772.3 / 23
p0 -390.9 ± 1.053
p1 -0.04655 ± 0.0002207
-400
-500
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

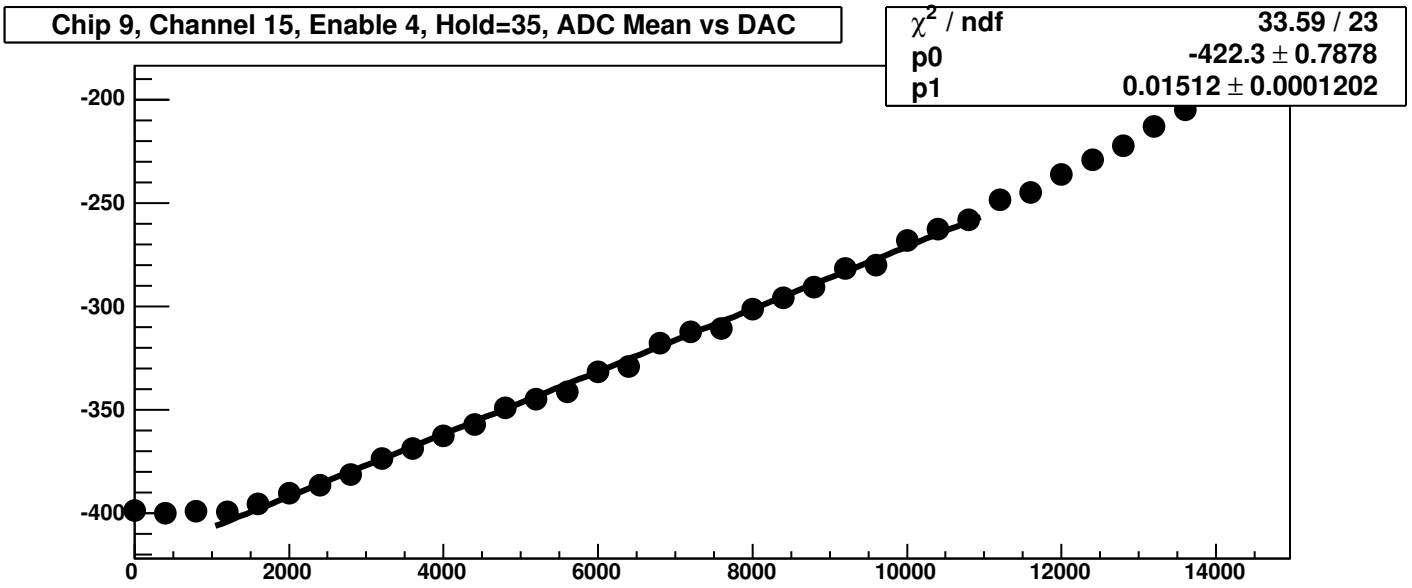

Chip 9, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

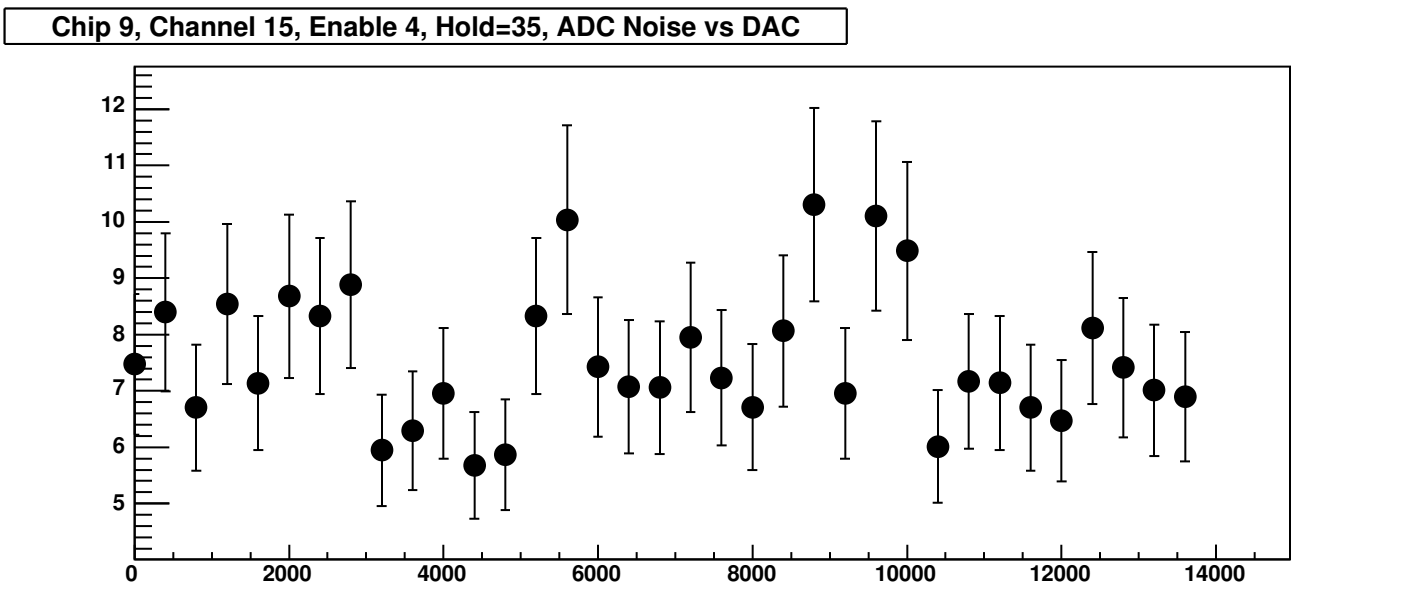

Chip 9, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

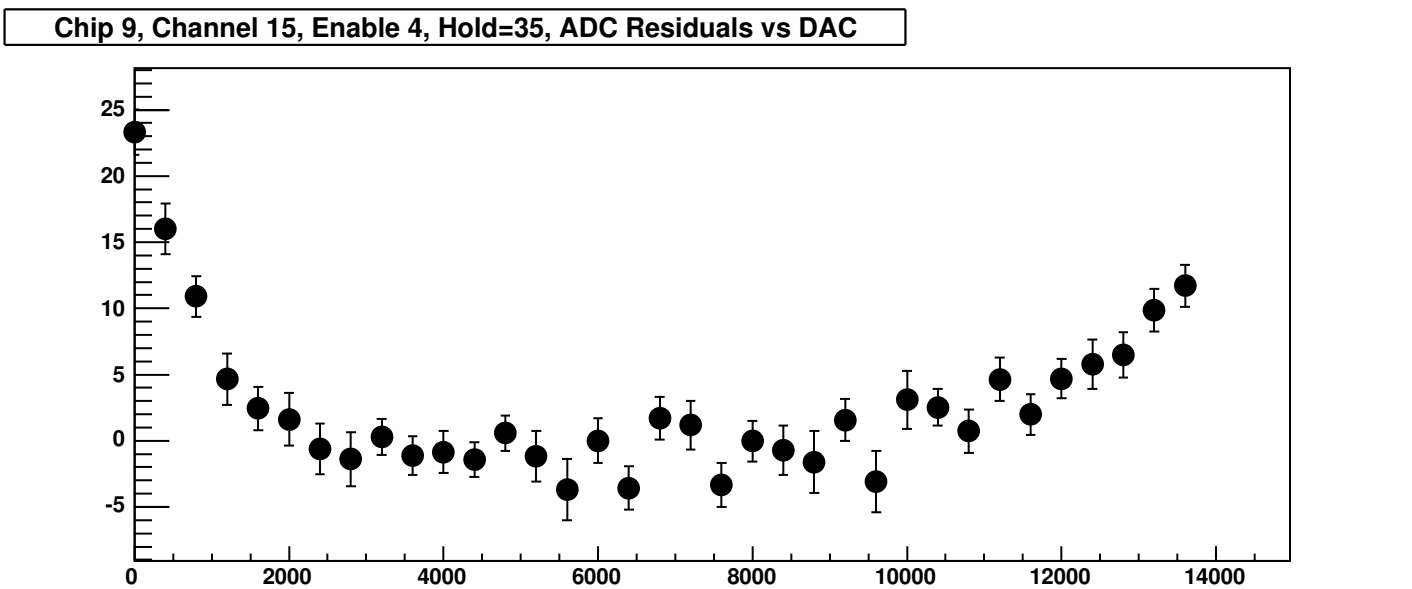

Chip 9, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.7 / 23
p0 -421.9 ± 0.7737
p1 0.01298 ± 0.00012
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

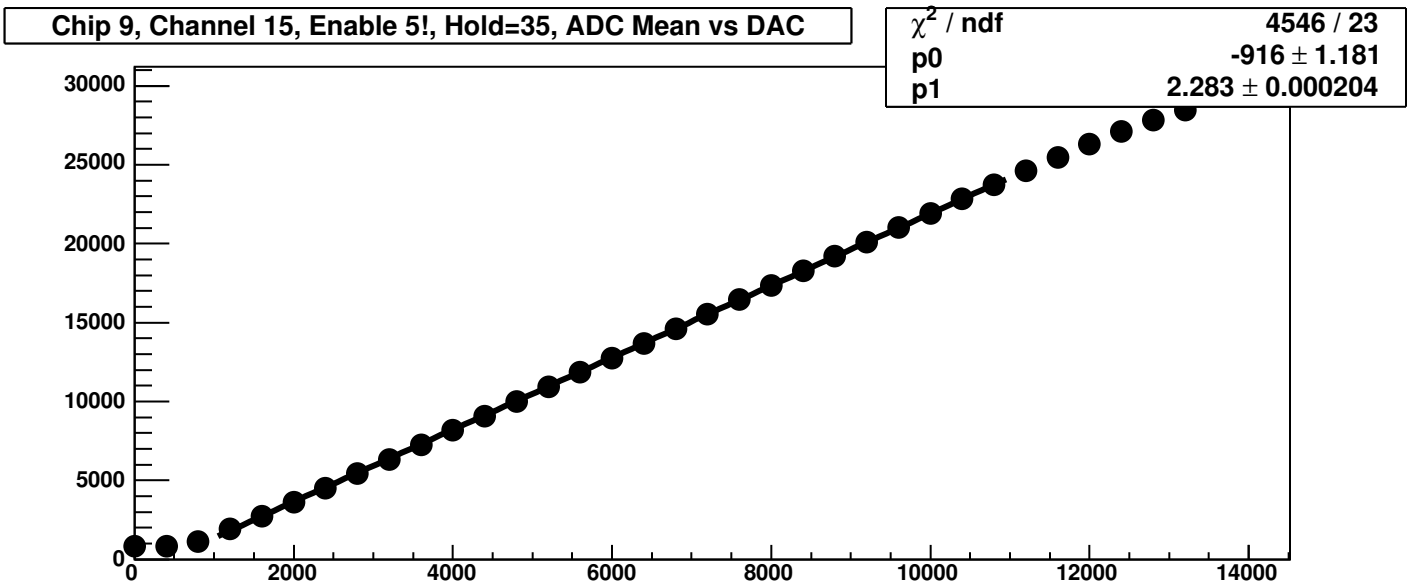

Chip 9, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

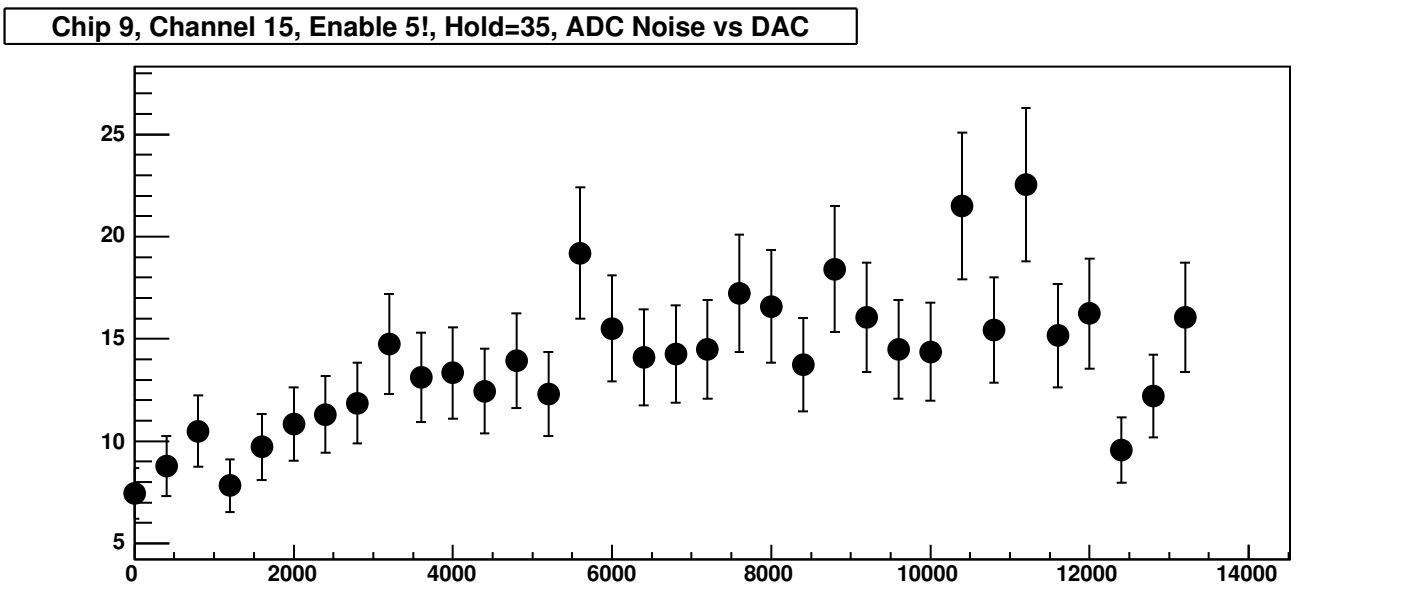

Chip 9, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

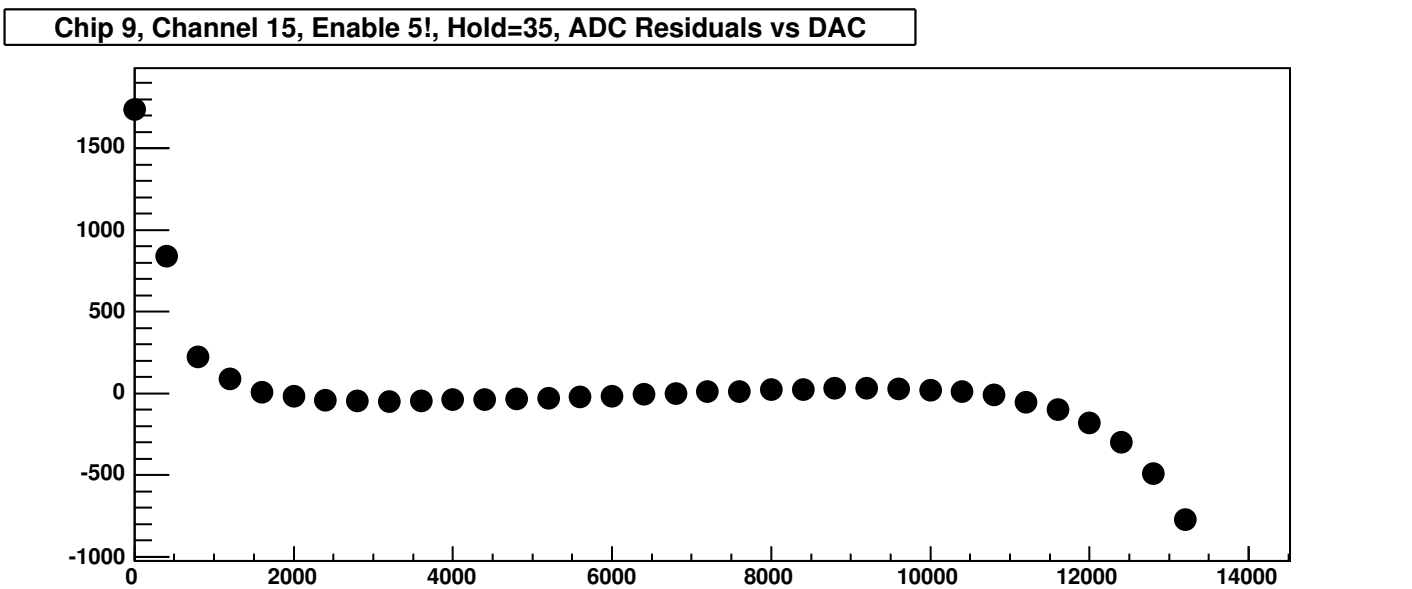

Chip 9, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.59 / 23
p0 -422.3 ± 0.7878
p1 0.01512 ± 0.0001202
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

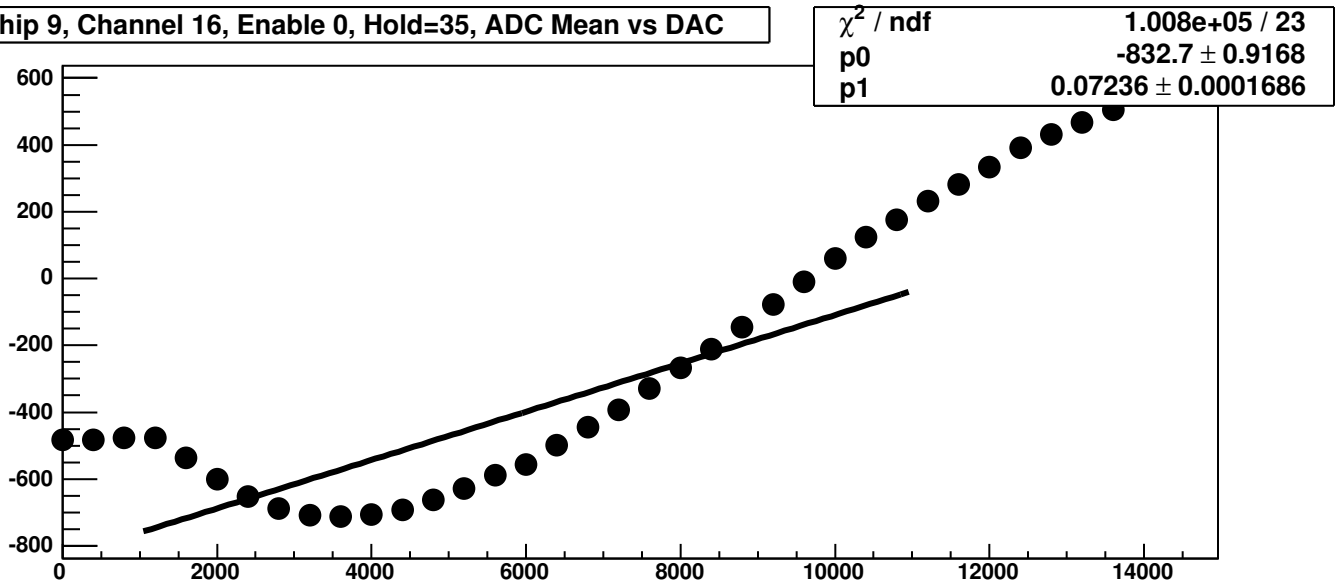

Chip 9, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4546 / 23
p0 -916 ± 1.181
p1 2.283 ± 0.000204
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

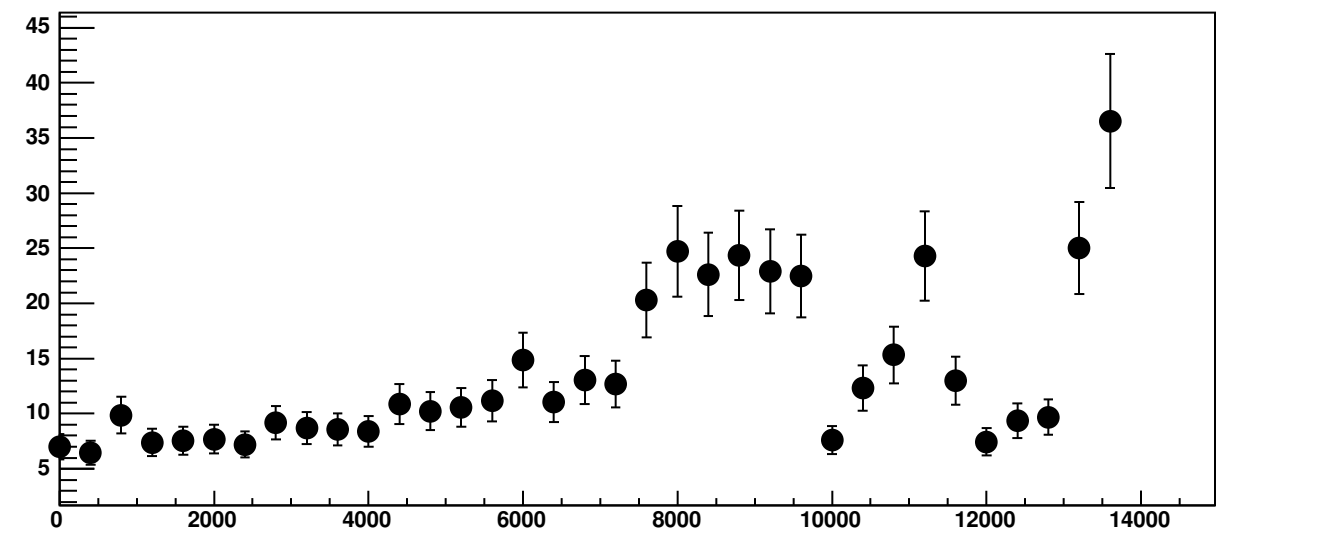

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

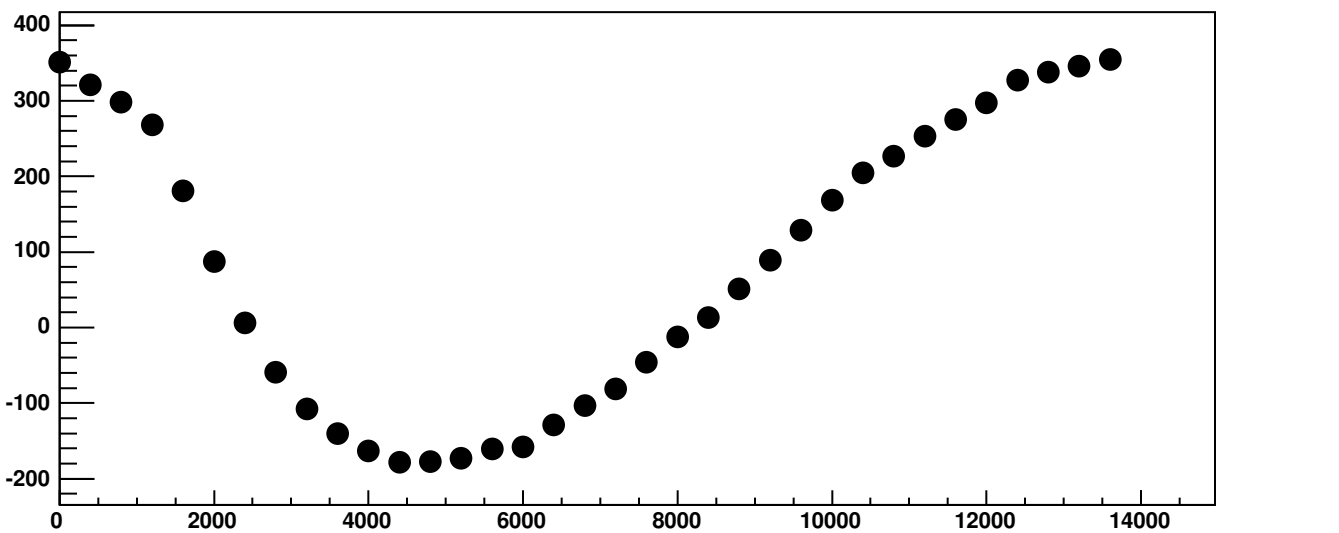
Chip 9, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

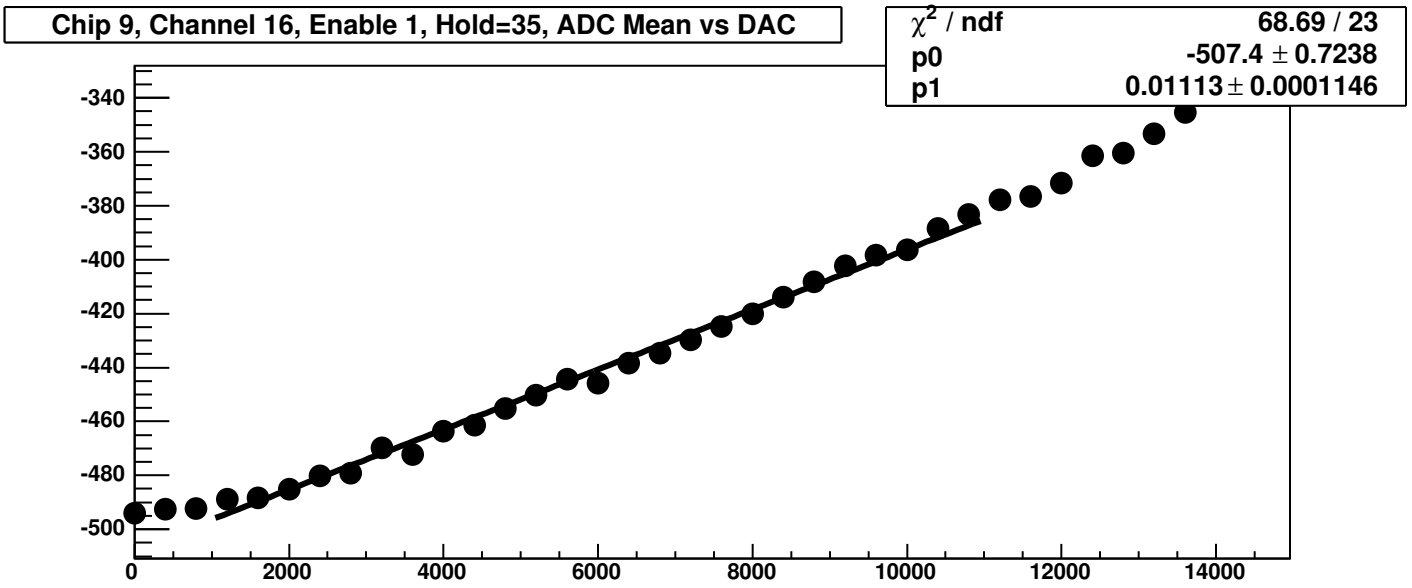


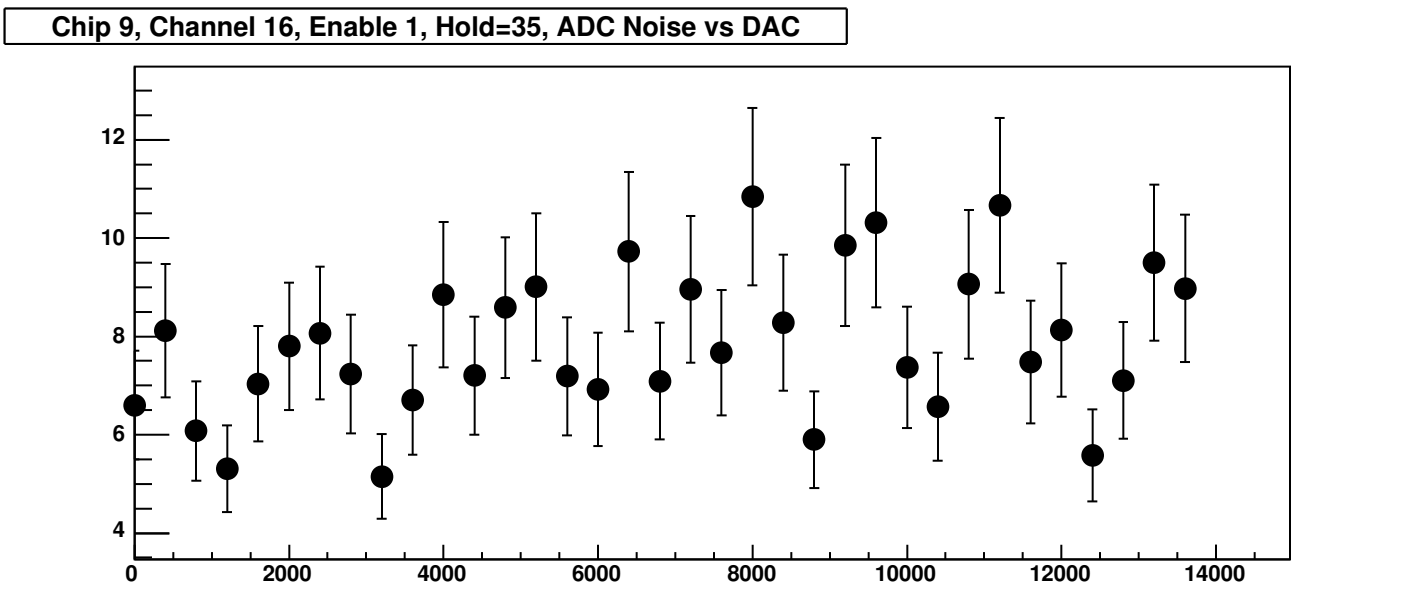
Chip 9, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

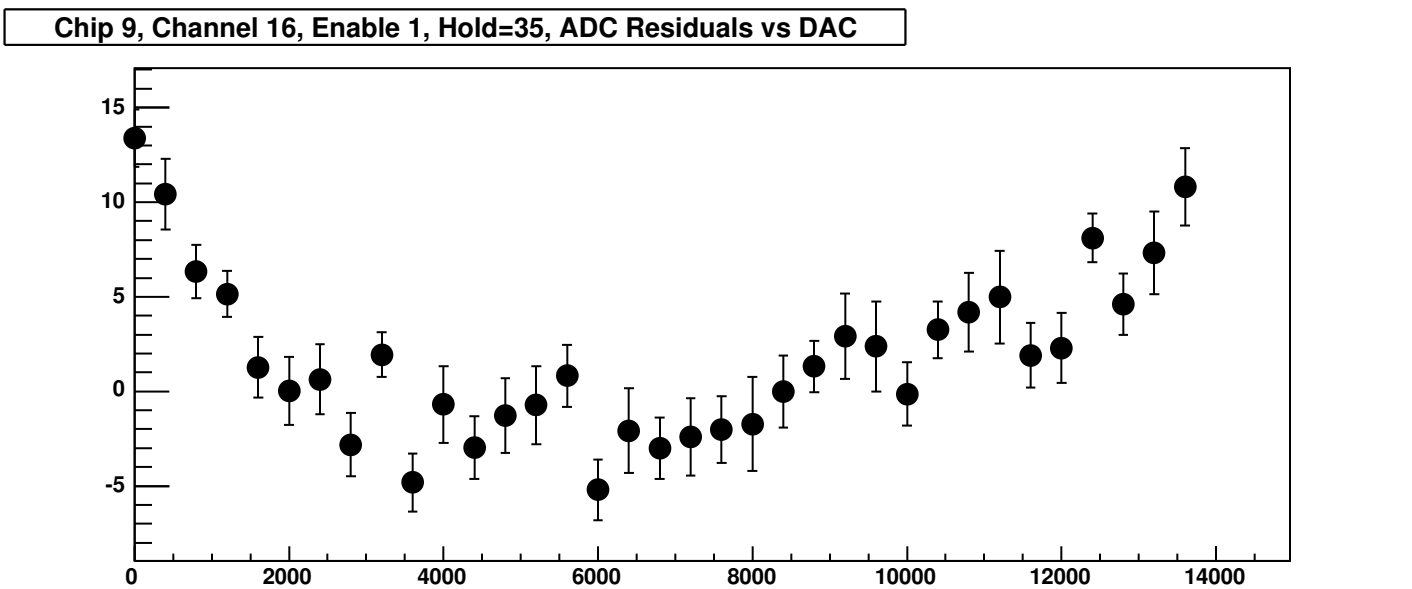


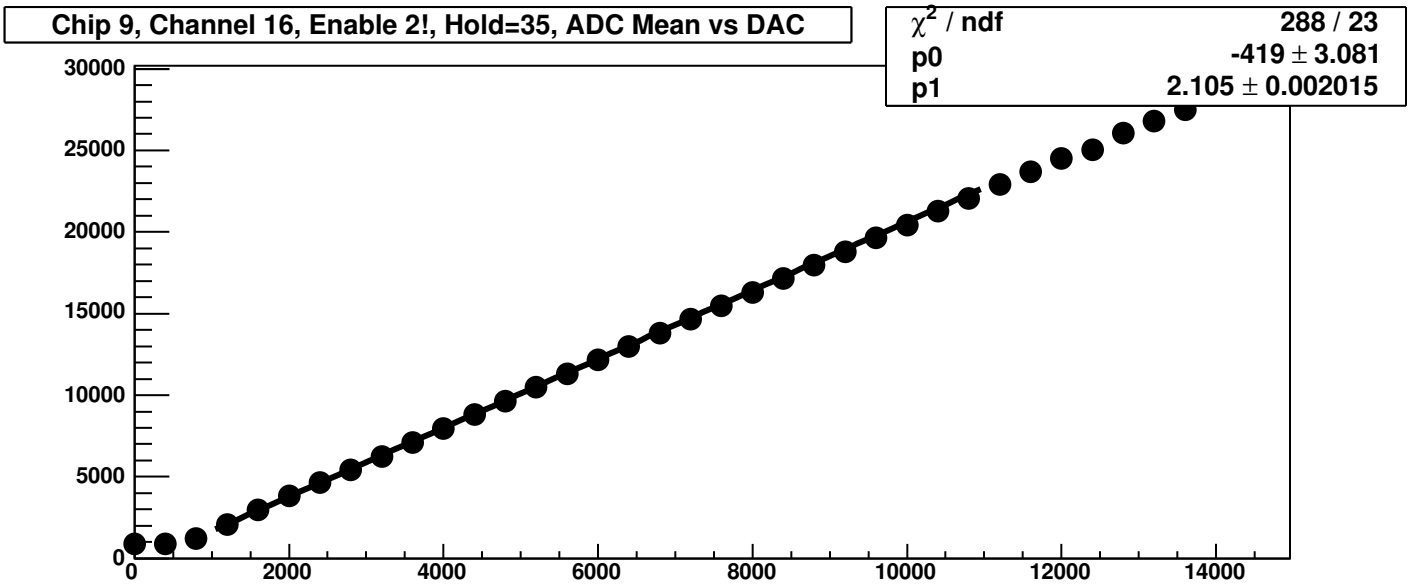
Chip 9, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

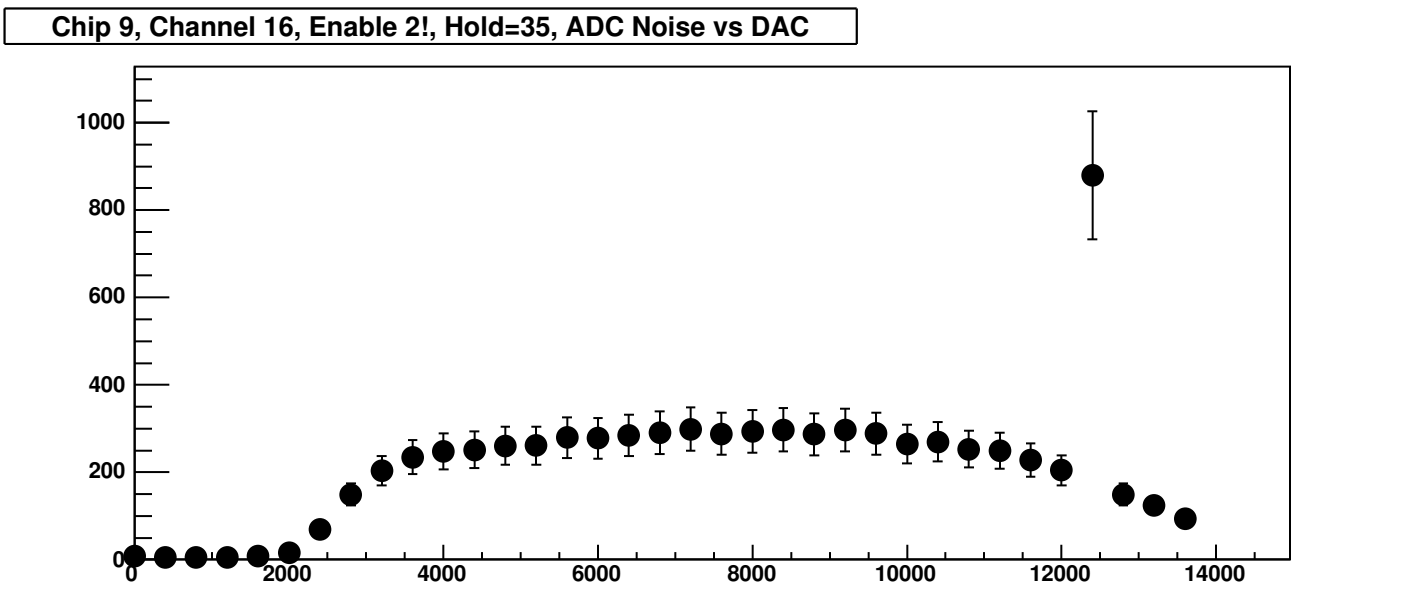


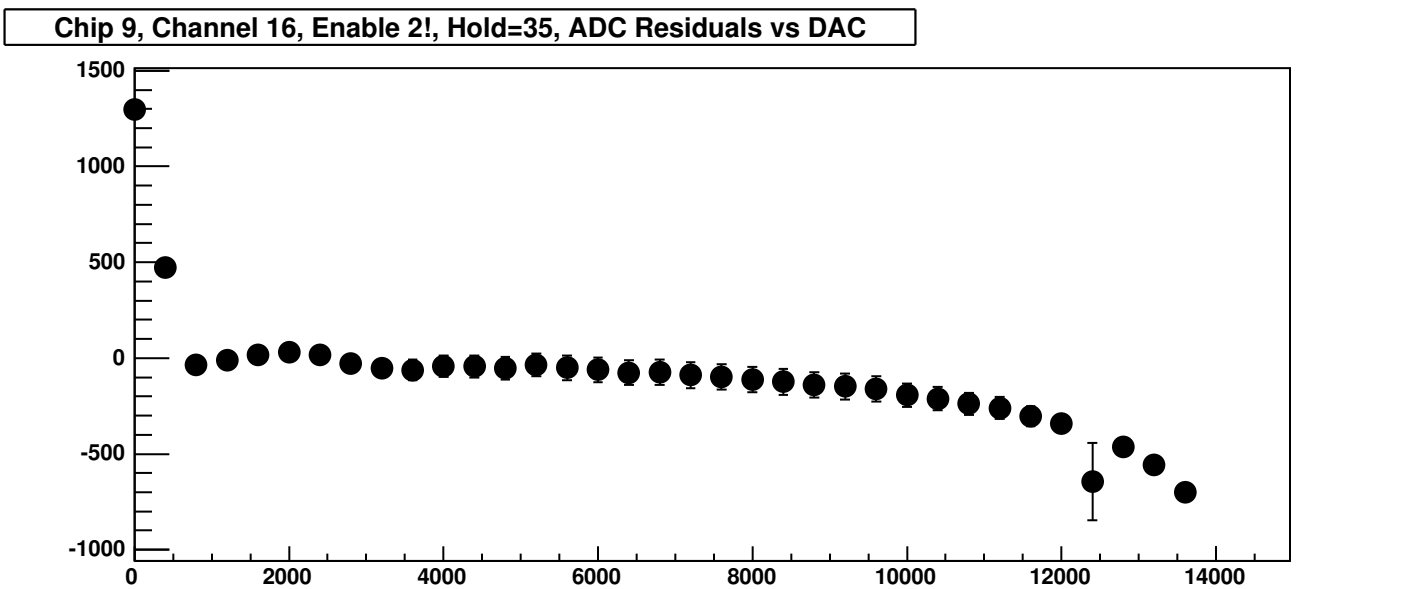

Chip 9, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 68.69 / 23
p0 -507.4 ± 0.7238
p1 0.01113 ± 0.0001146
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000

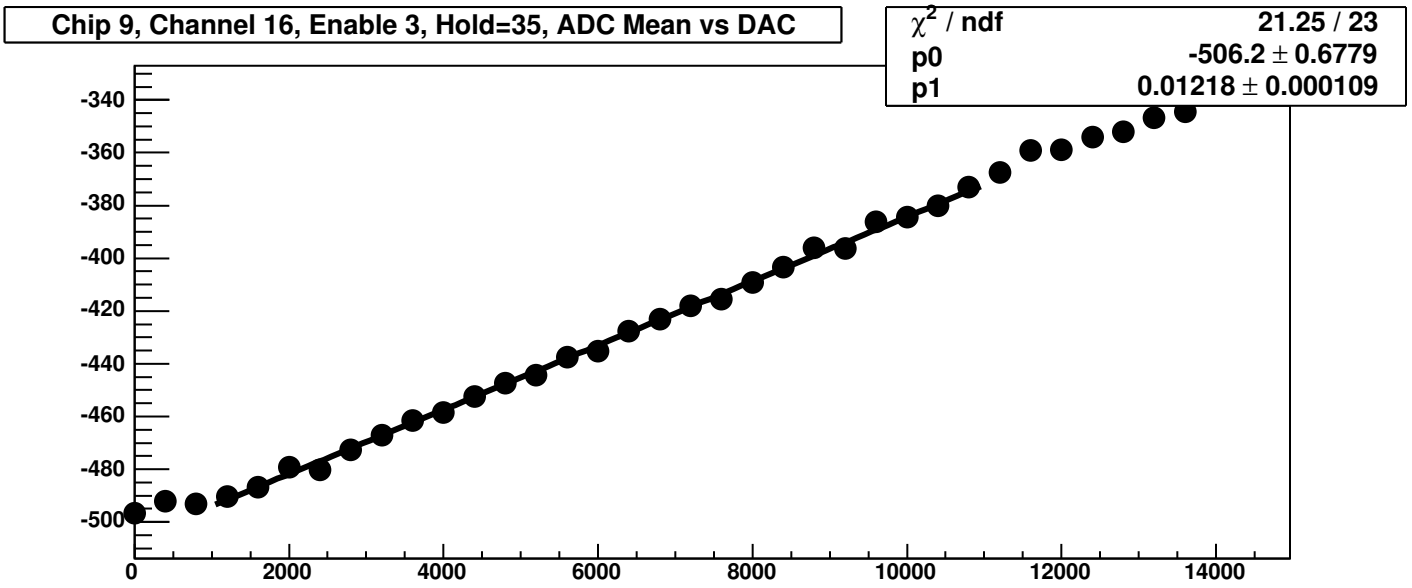

Chip 9, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

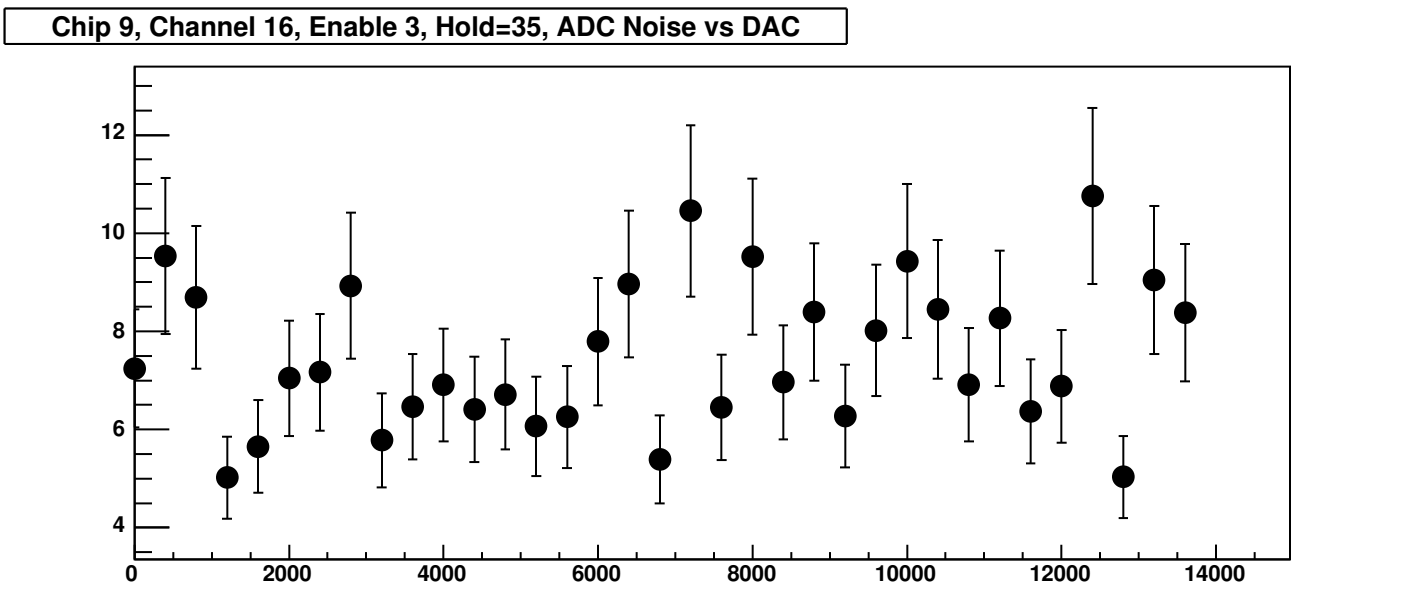

Chip 9, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

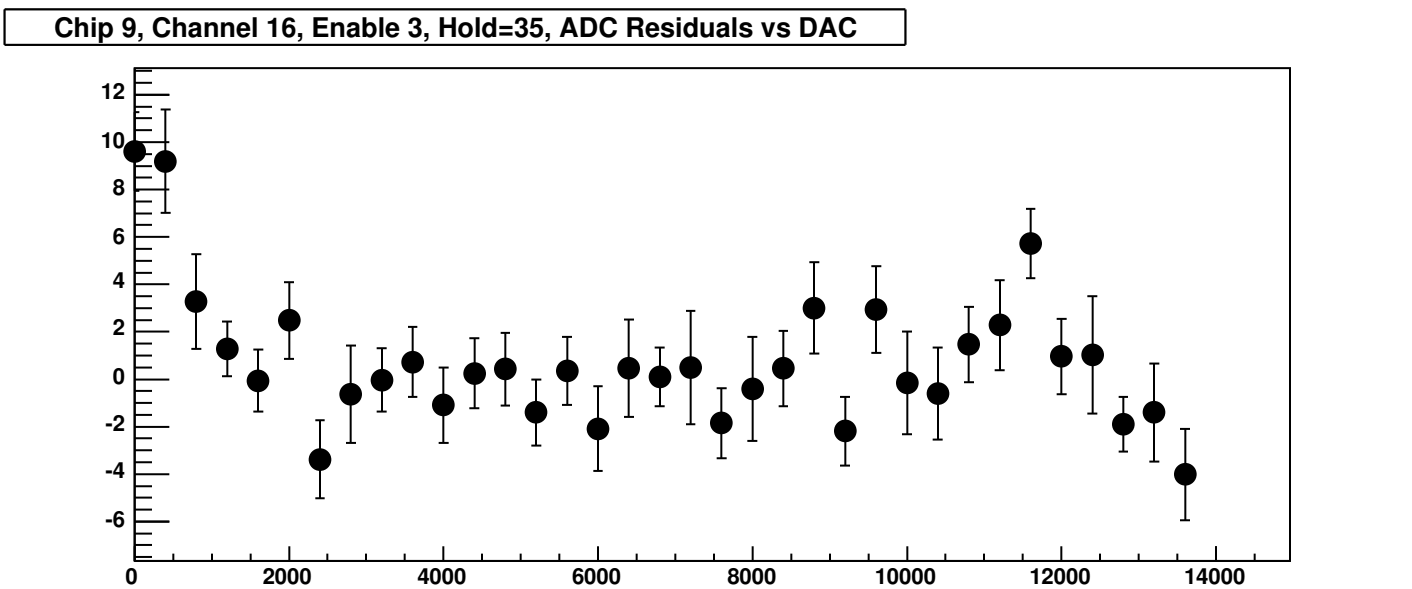

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 288 / 23
p0 -419 ± 3.081
p1 2.105 ± 0.002015
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

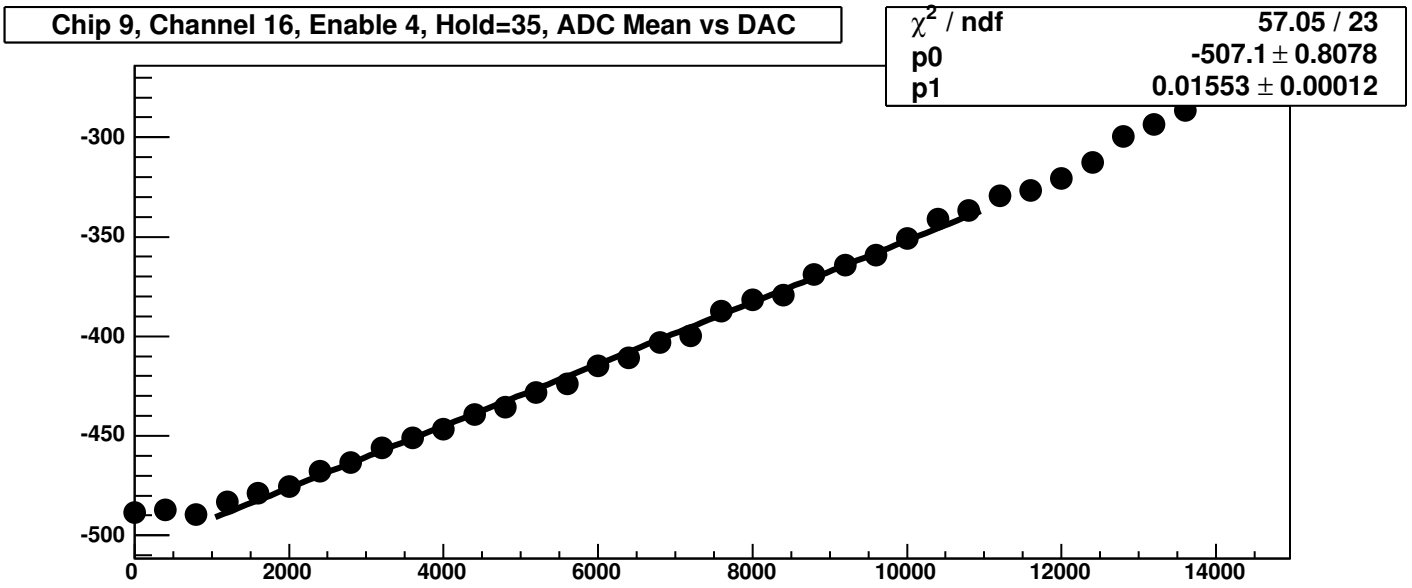

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

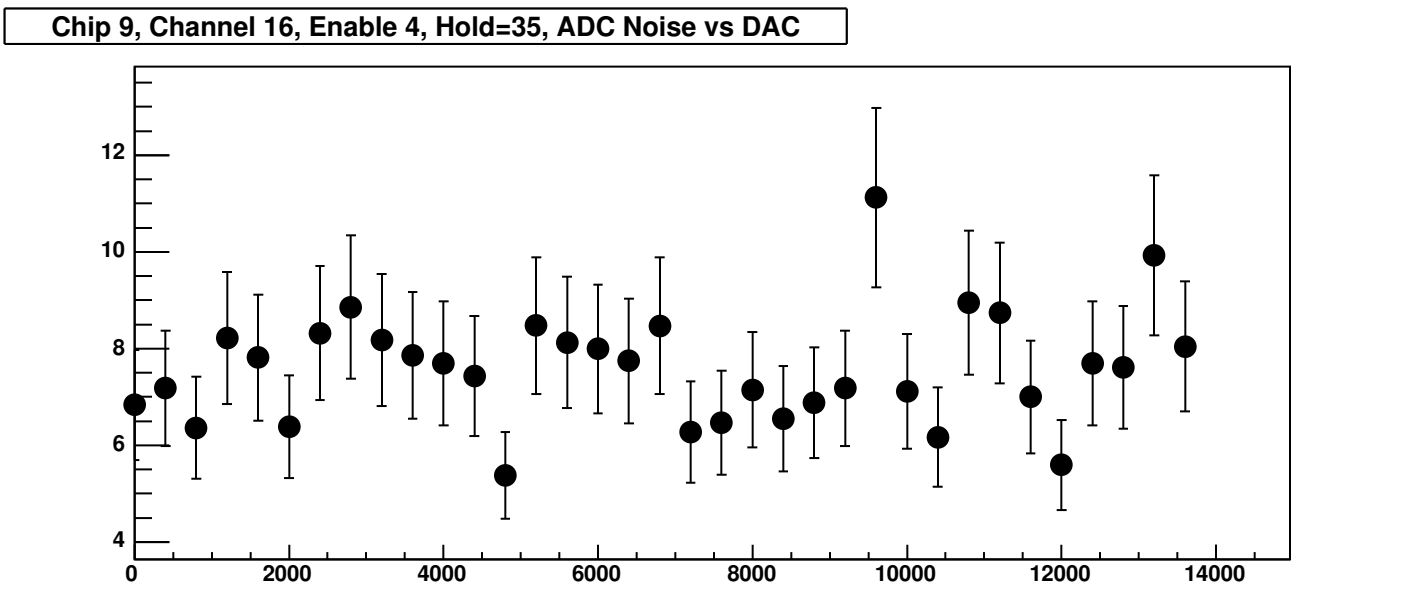

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

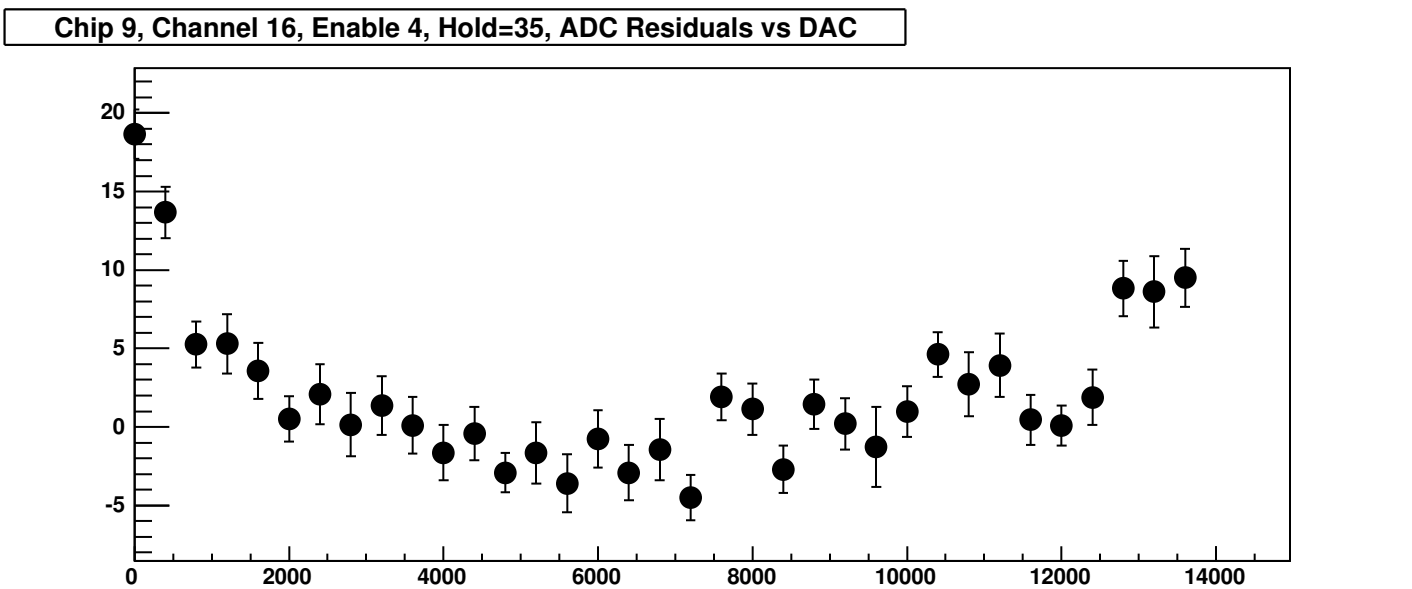

Chip 9, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.25 / 23
p0 -506.2 ± 0.6779
p1 0.01218 ± 0.000109
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000

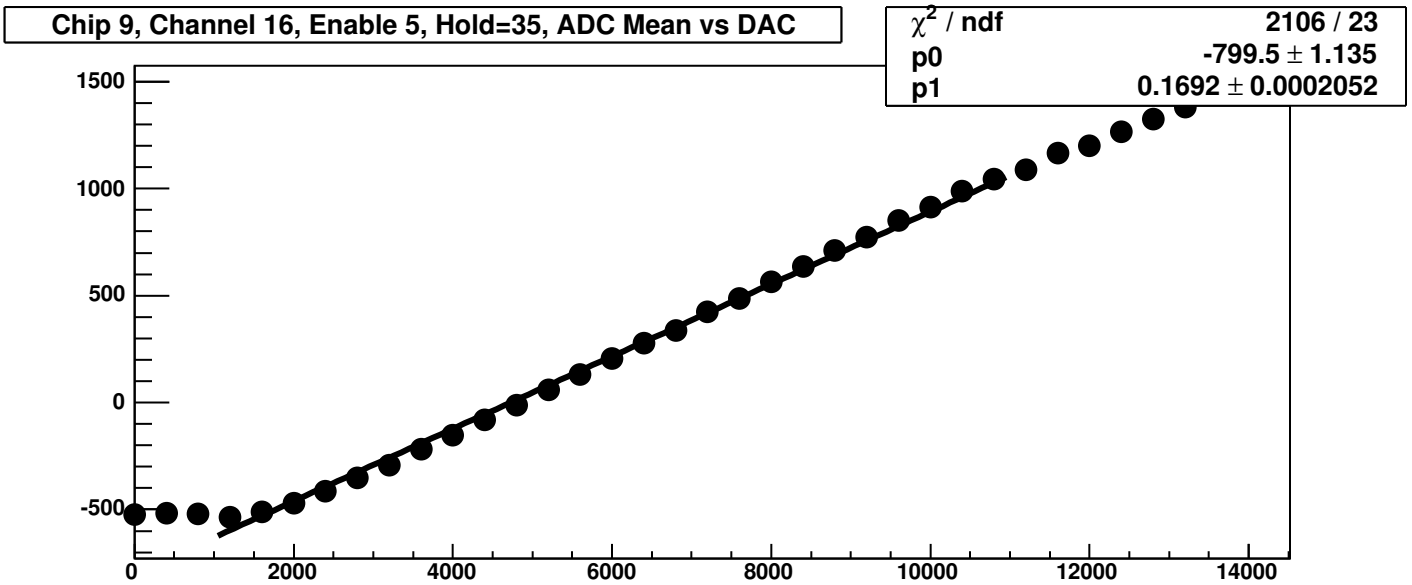

Chip 9, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

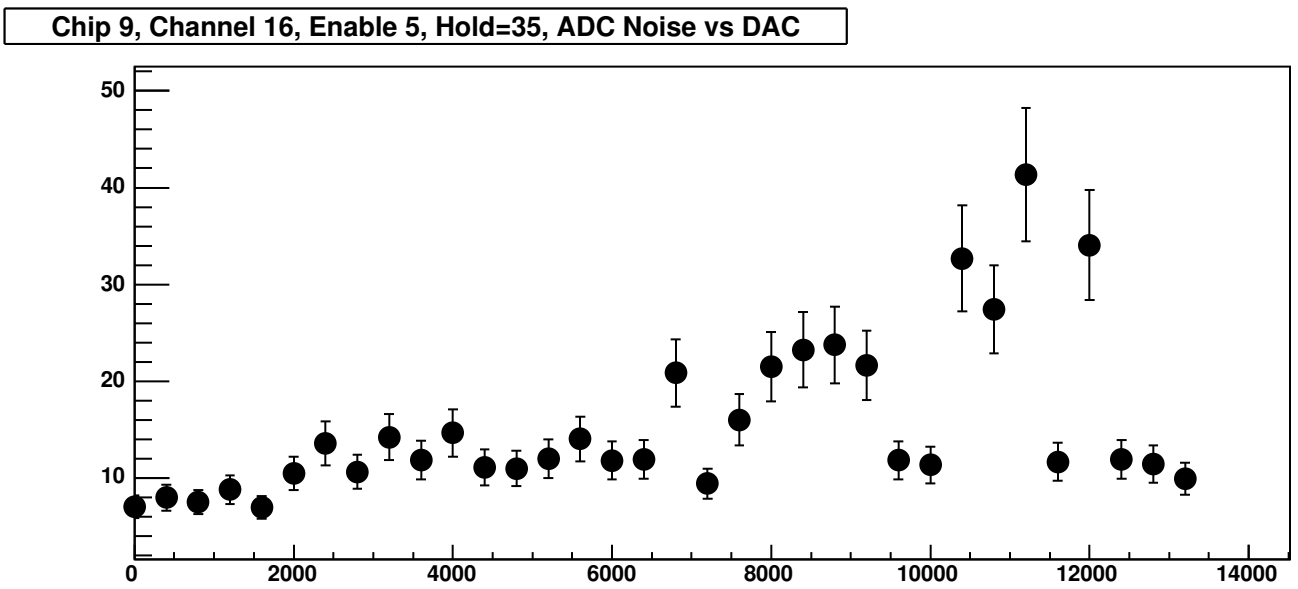

Chip 9, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

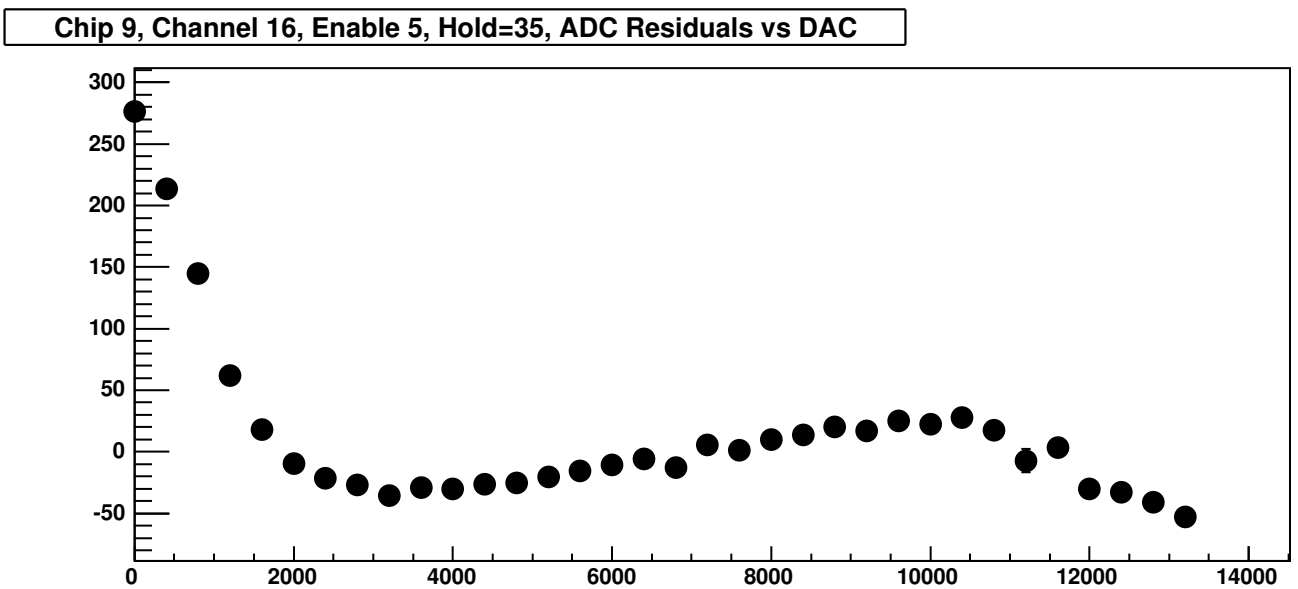

Chip 9, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 57.05 / 23
p0 -507.1 ± 0.8078
p1 0.01553 ± 0.00012
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

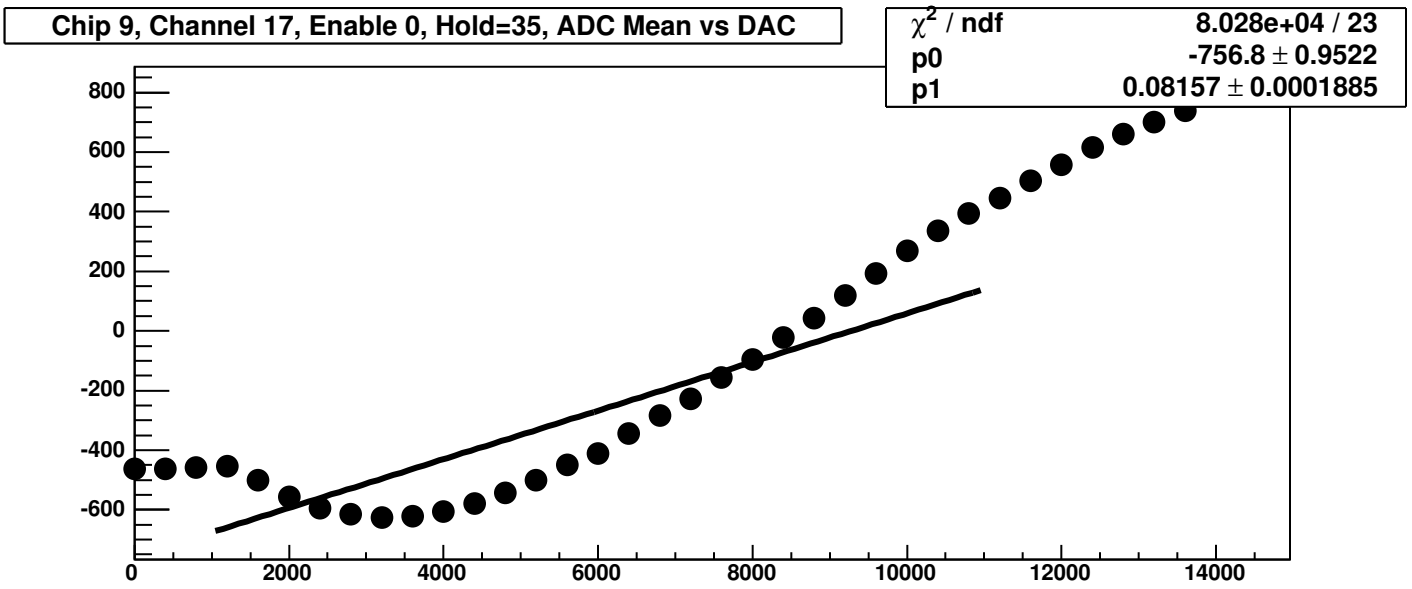

Chip 9, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

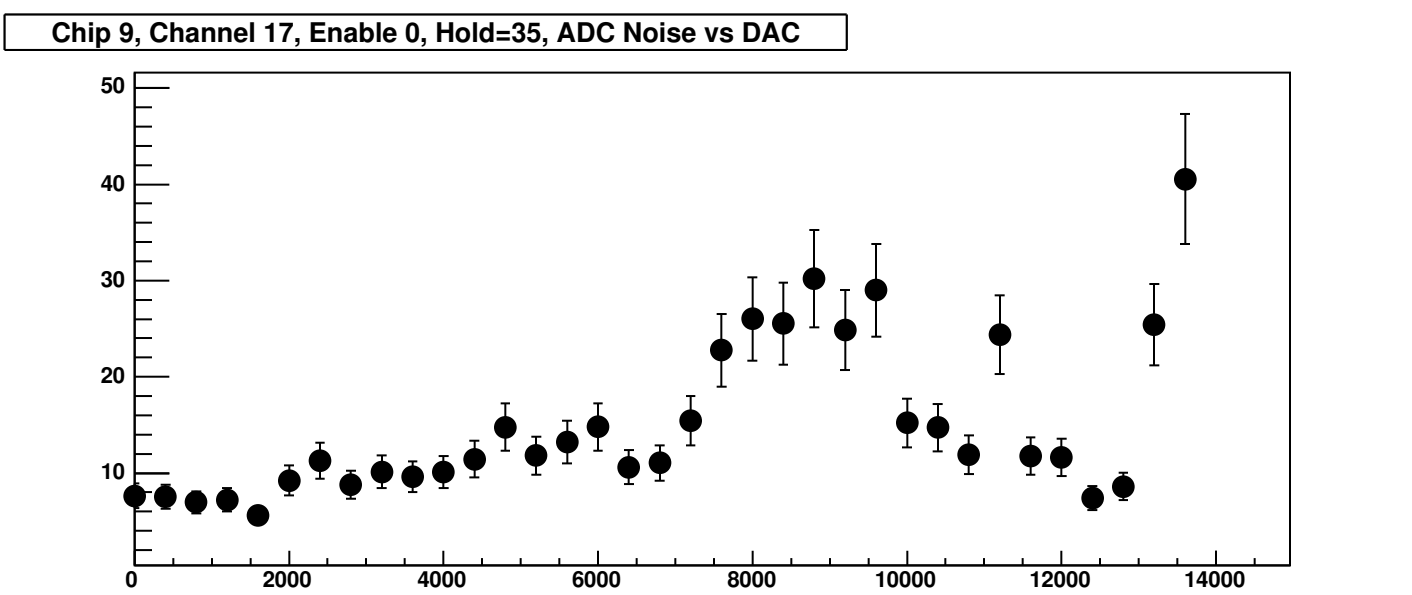

Chip 9, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

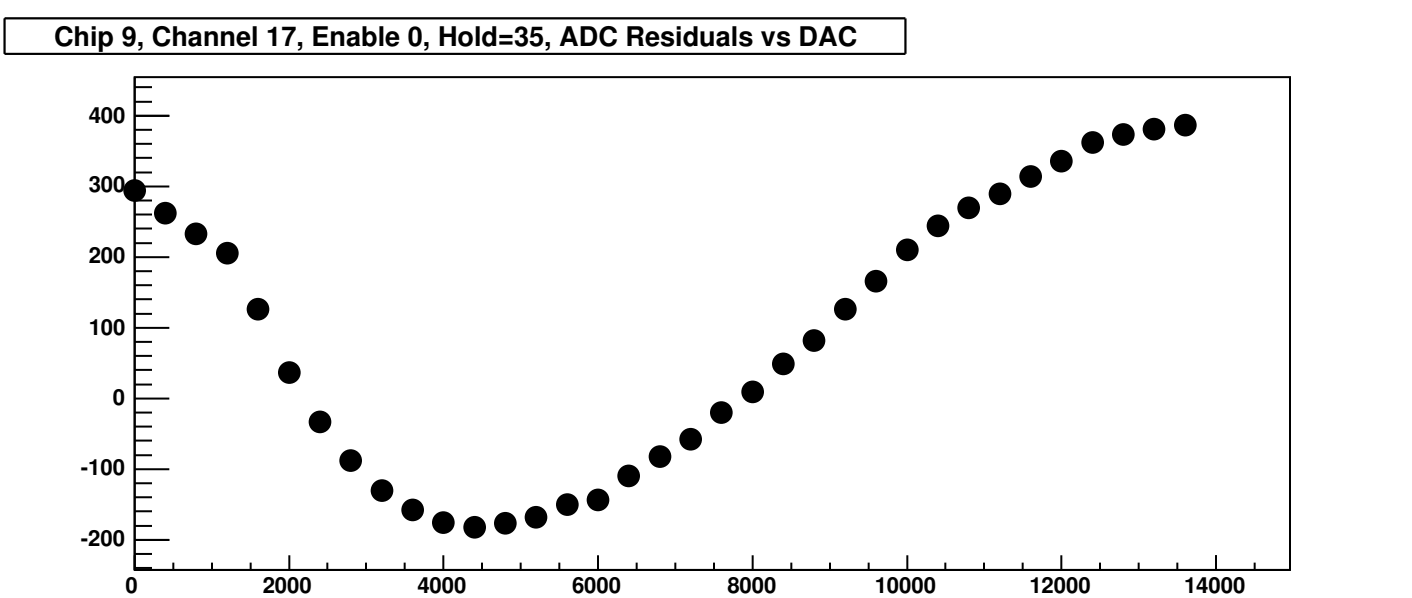

Chip 9, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2106 / 23
p0 -799.5 ± 1.135
p1 0.1692 ± 0.0002052
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

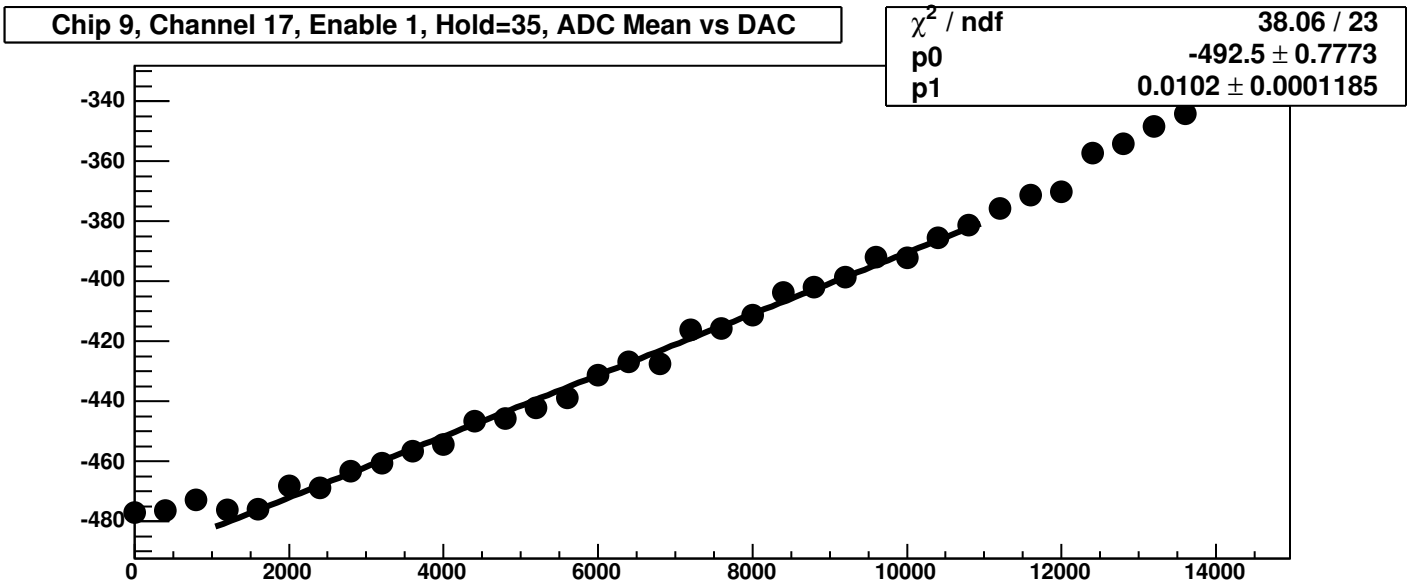

Chip 9, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

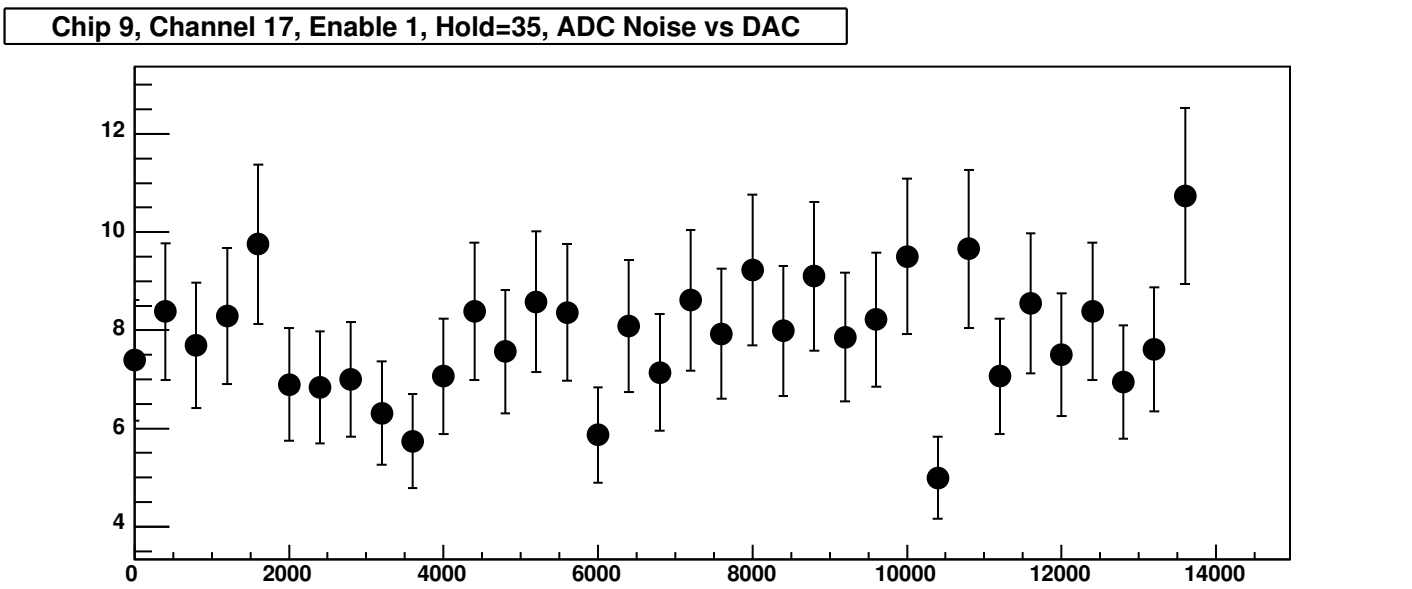

Chip 9, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

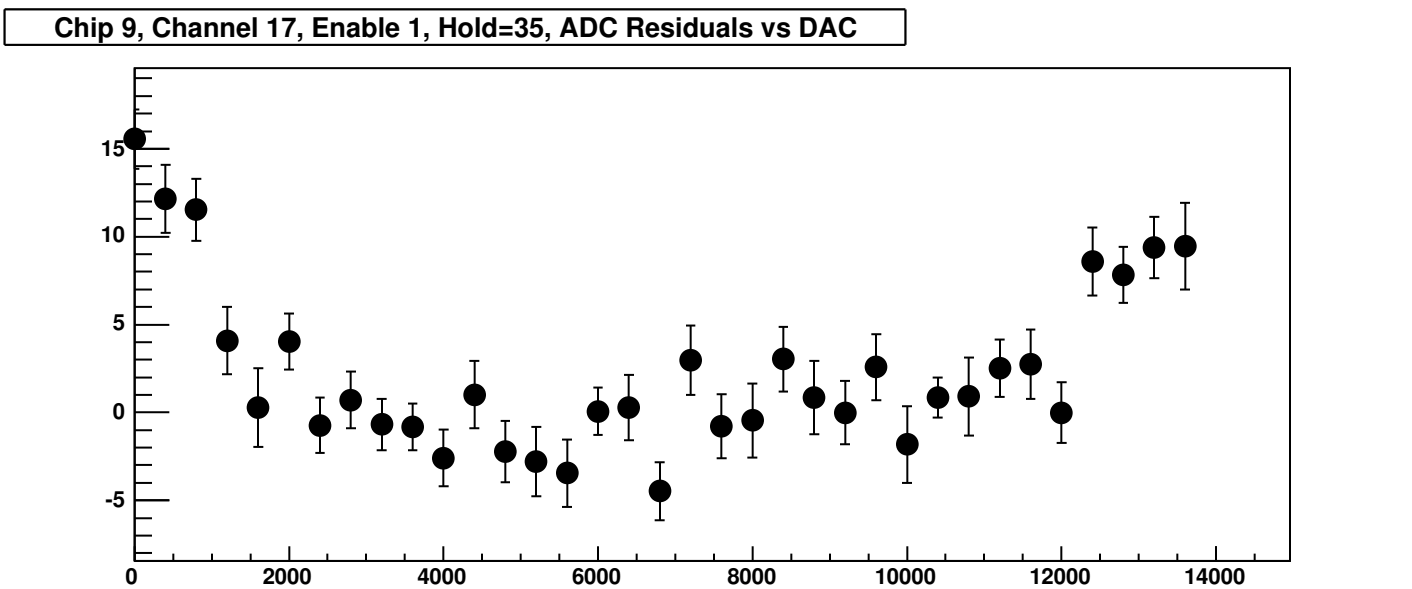

Chip 9, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 8.028e+04 / 23
p0 -756.8 ± 0.9522
p1 0.08157 ± 0.0001885
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

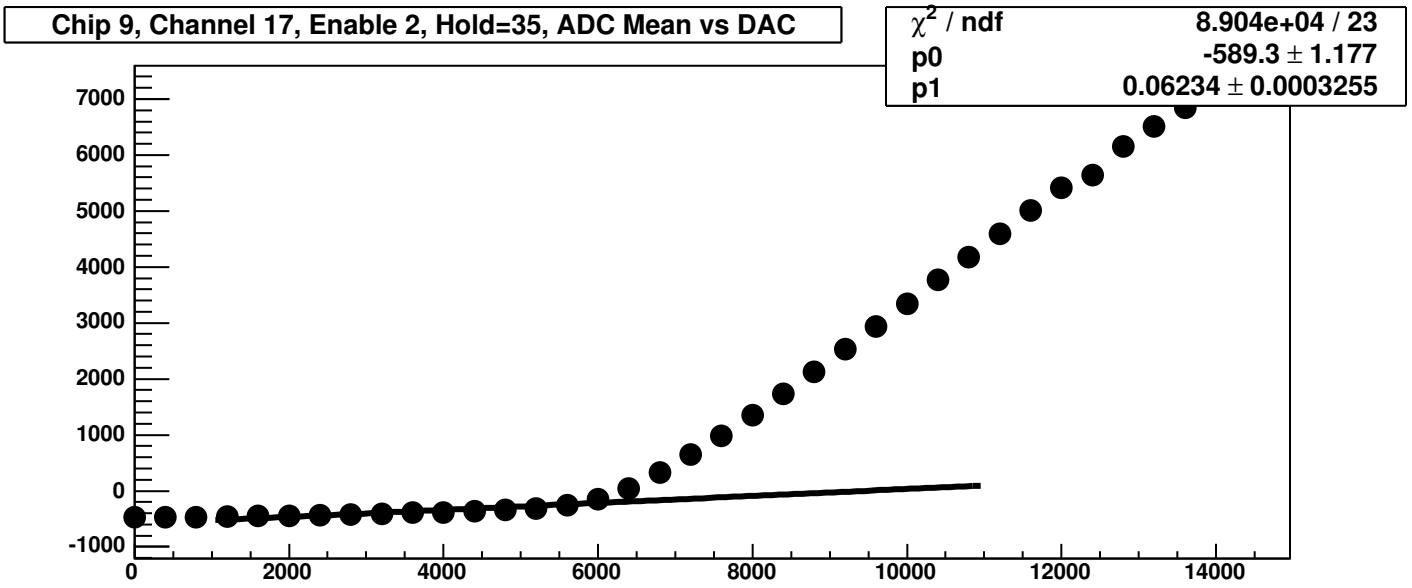

Chip 9, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

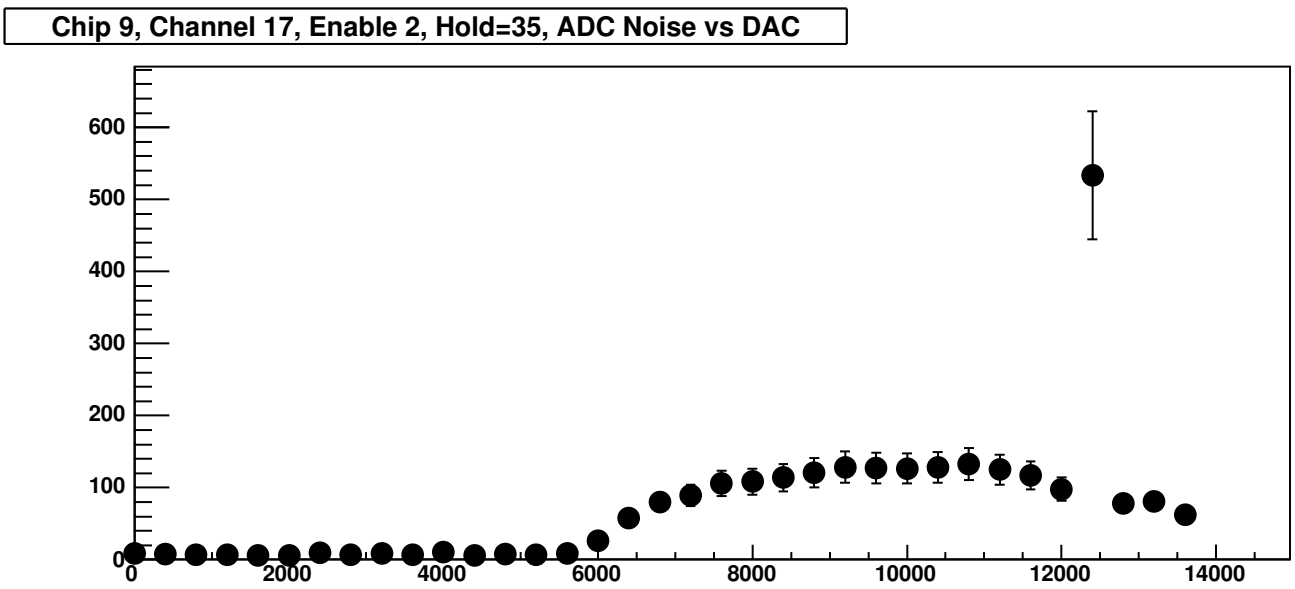

Chip 9, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

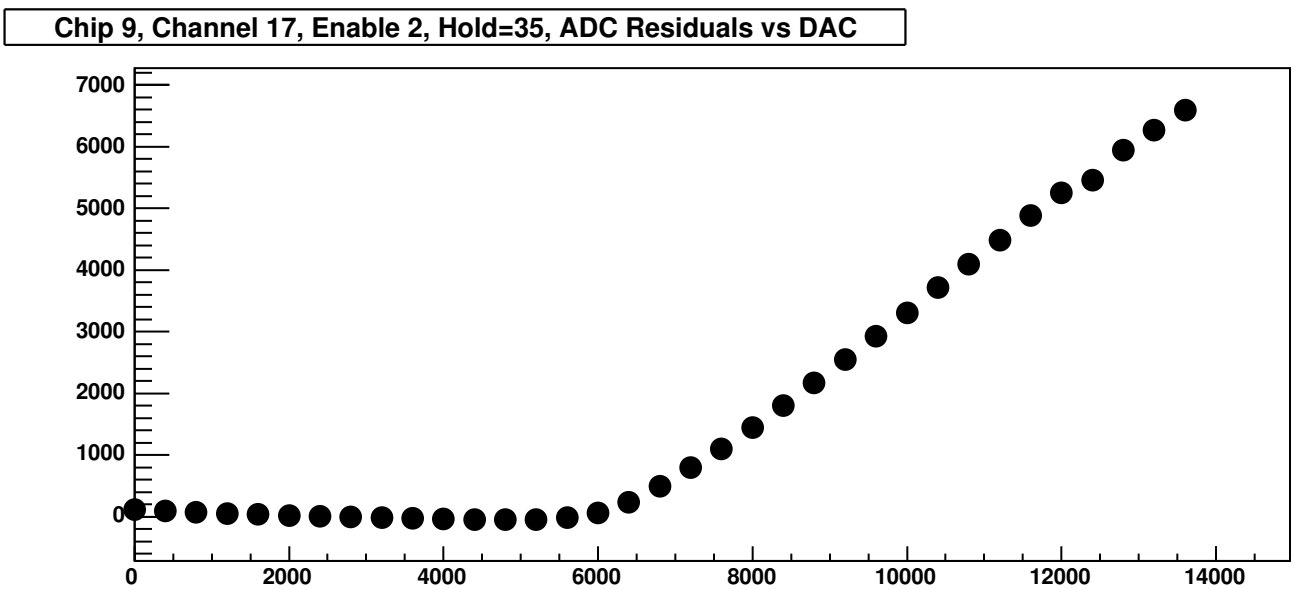

Chip 9, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.06 / 23
p0 -492.5 ± 0.7773
p1 0.0102 ± 0.0001185
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

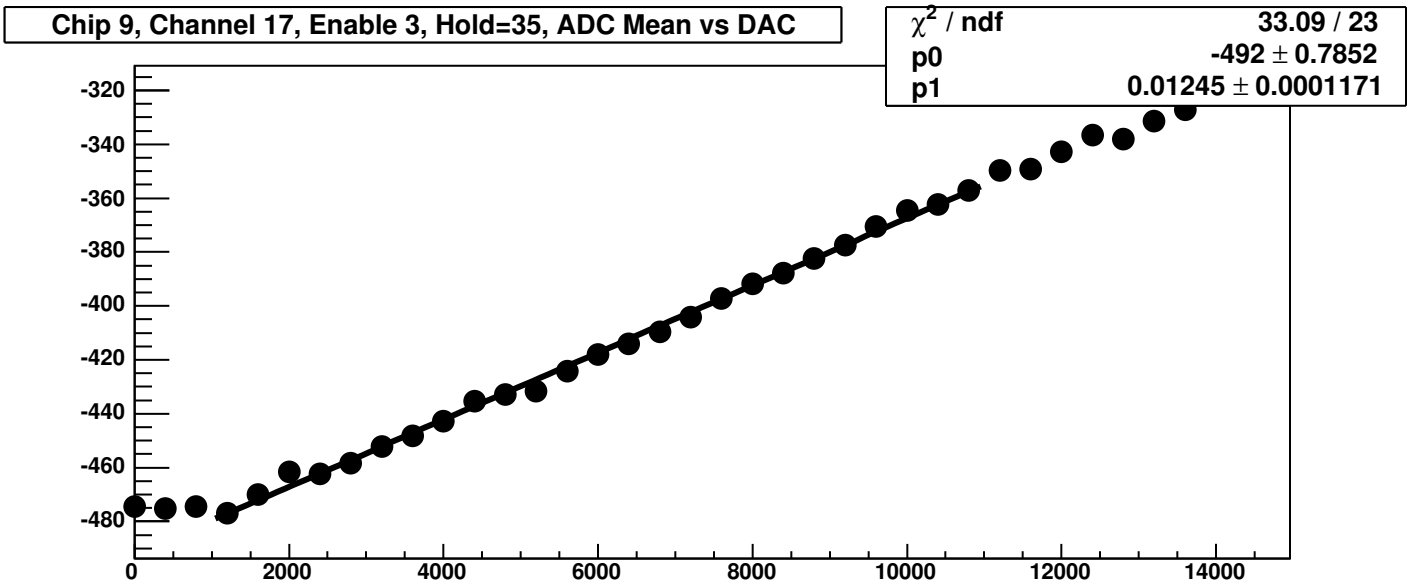

Chip 9, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

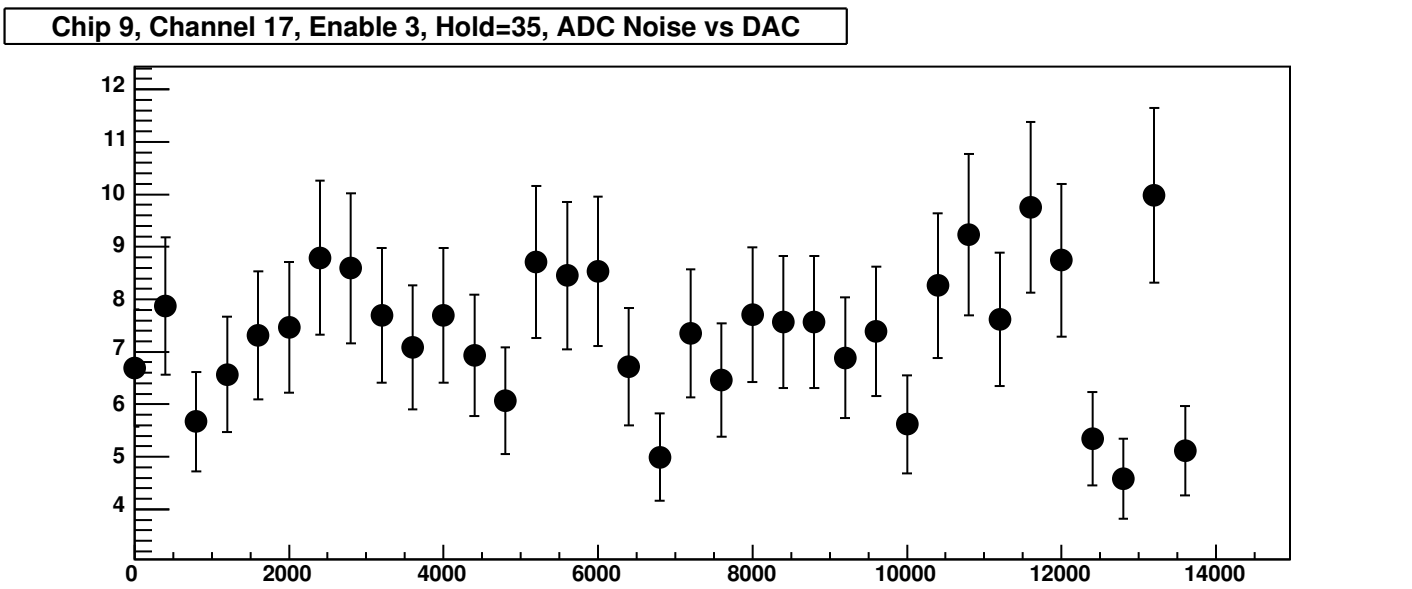

Chip 9, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

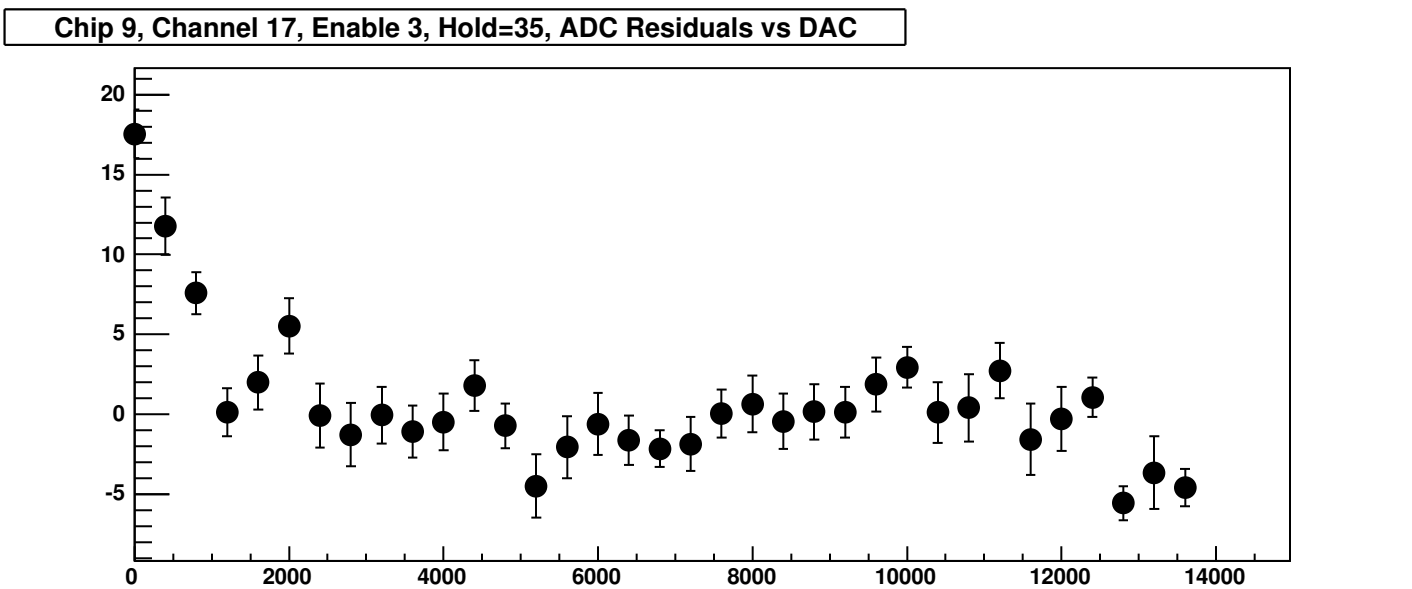

Chip 9, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 8.904e+04 / 23
p0 -589.3 ± 1.177
p1 0.06234 ± 0.0003255
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

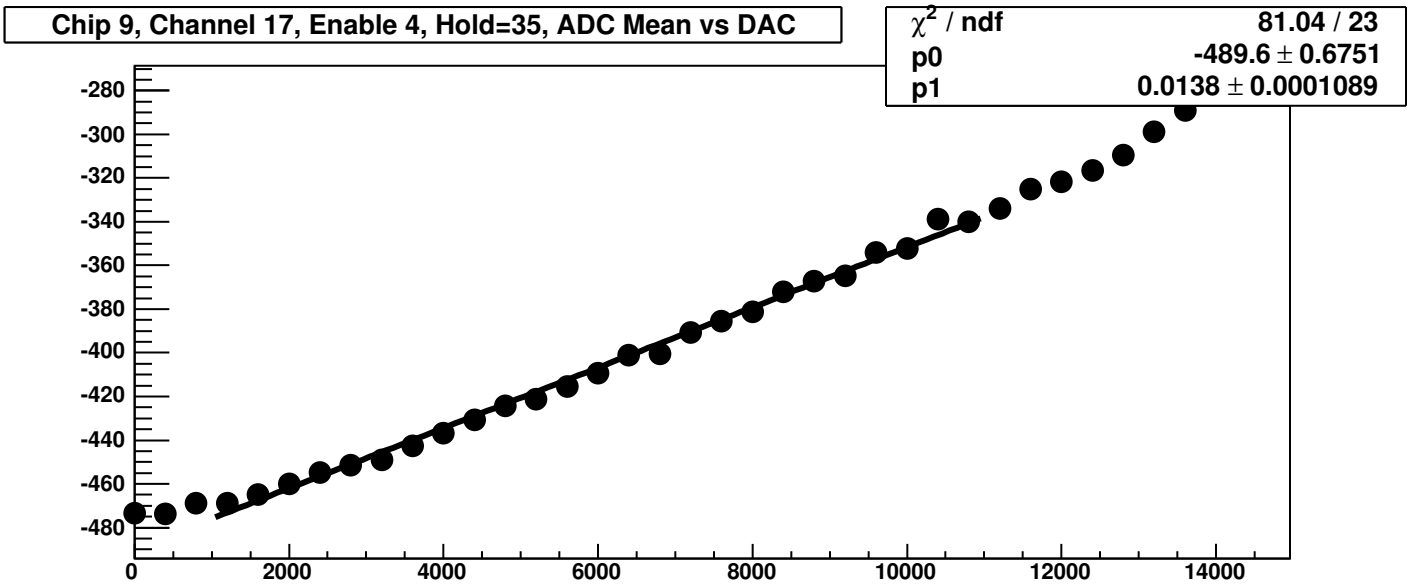

Chip 9, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

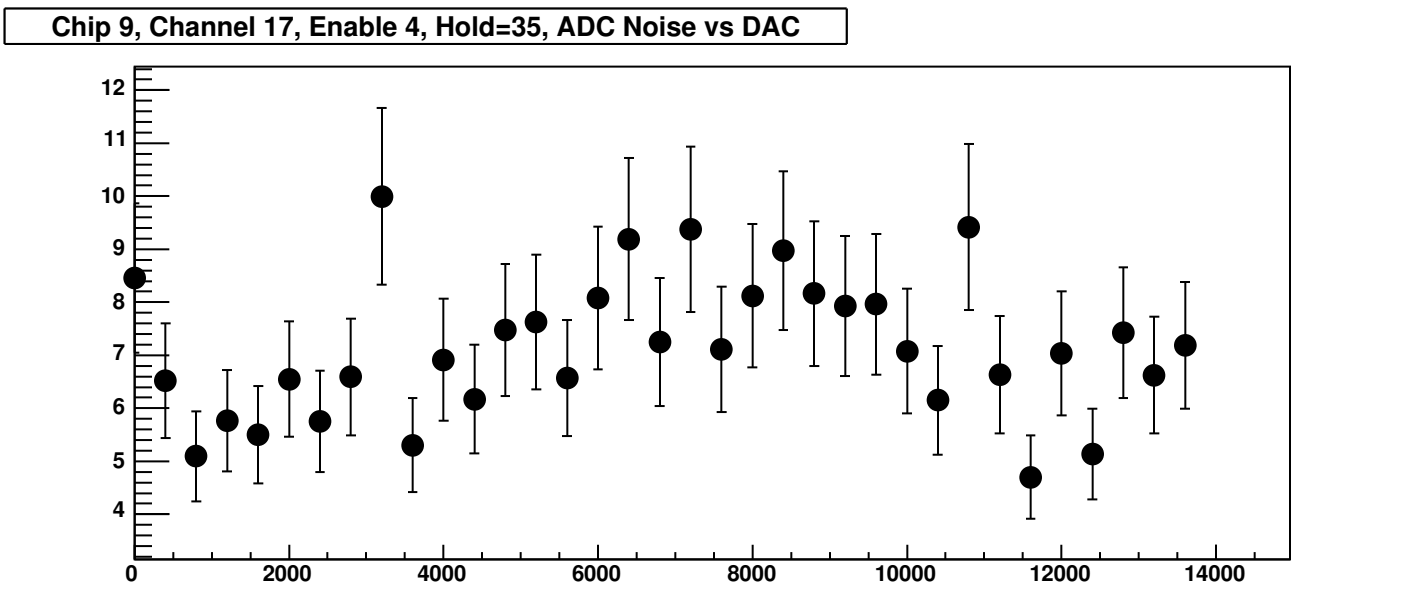

Chip 9, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

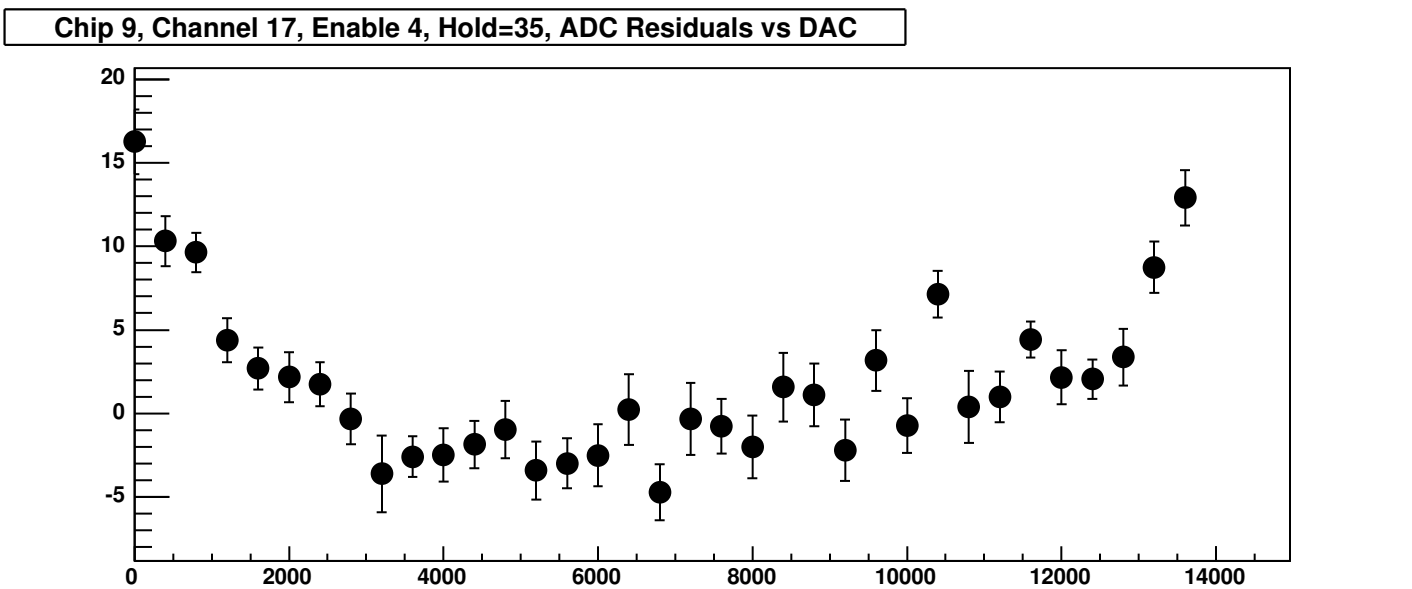

Chip 9, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.09 / 23
p0 -492 ± 0.7852
p1 0.01245 ± 0.0001171
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

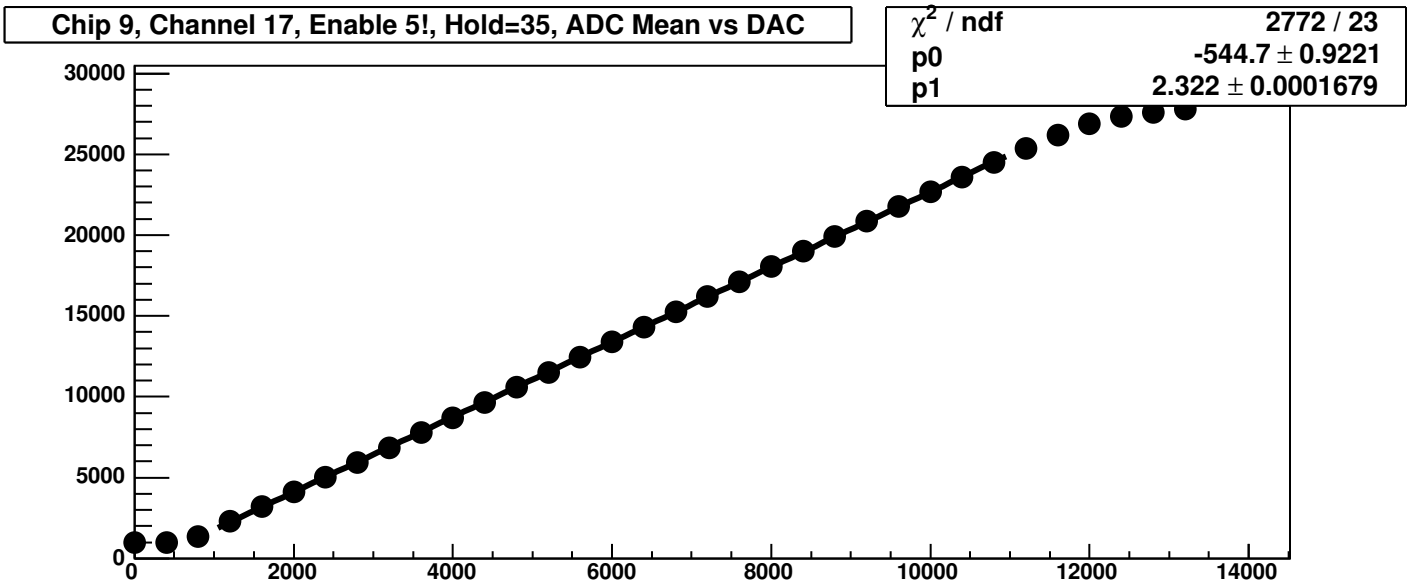

Chip 9, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

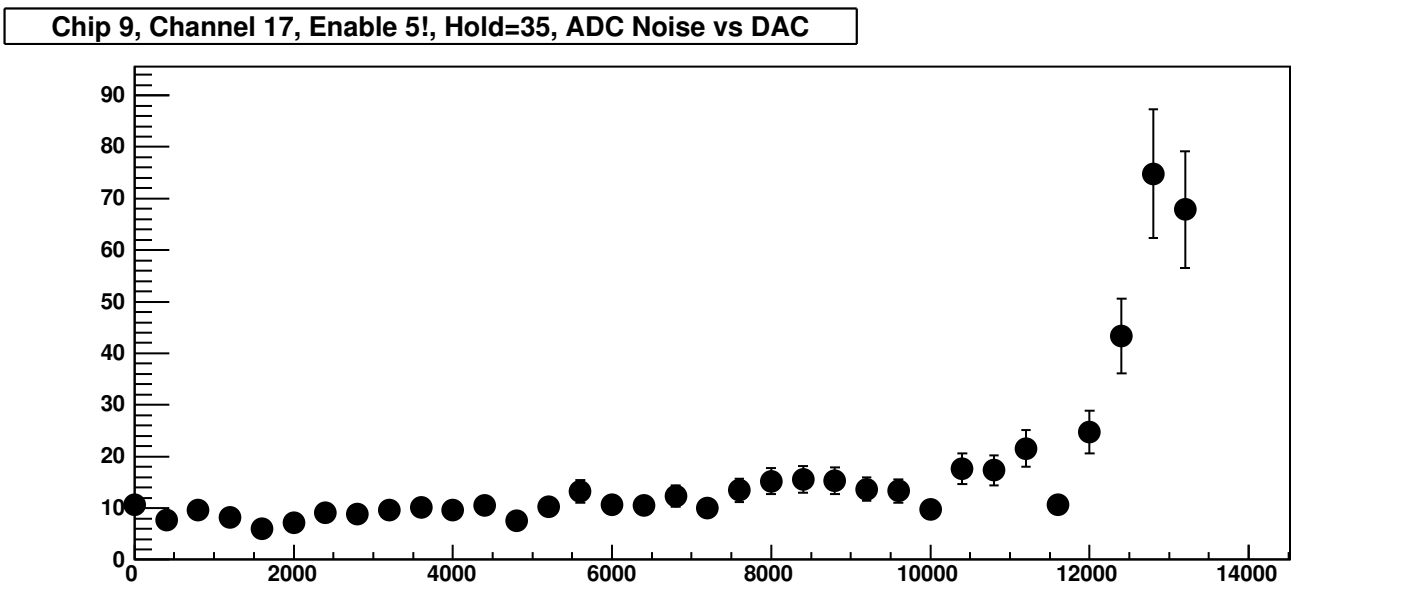

Chip 9, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

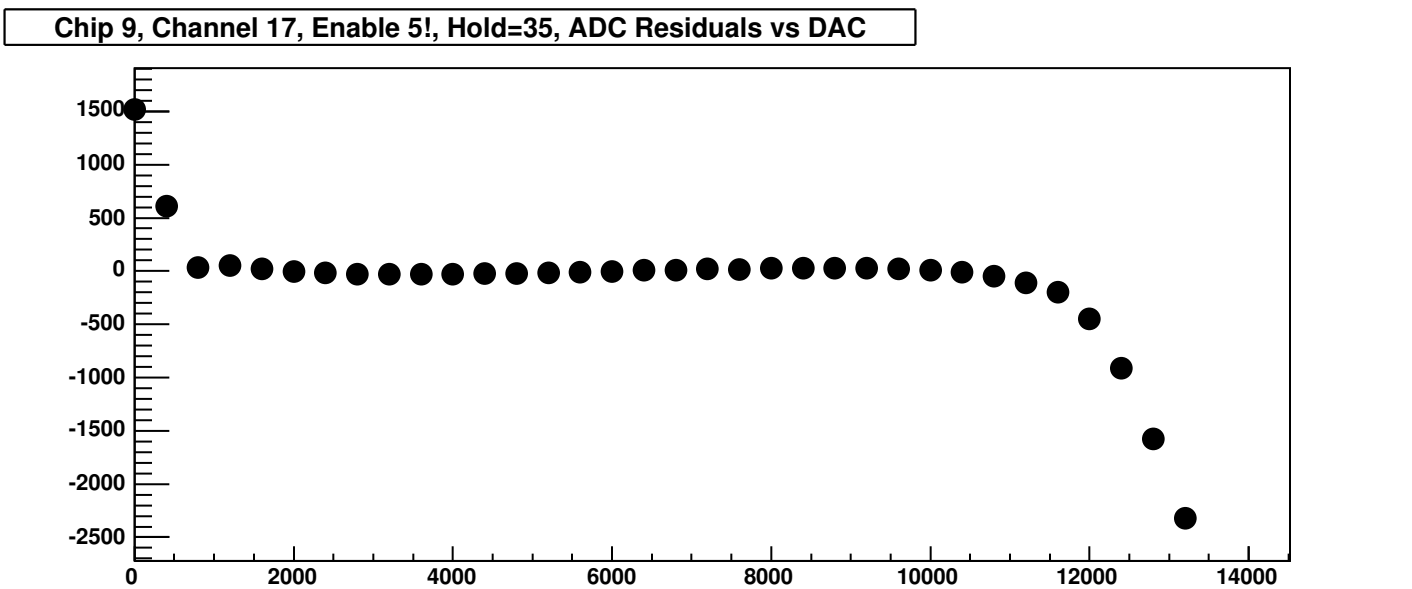

Chip 9, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 81.04 / 23
p0 -489.6 ± 0.6751
p1 0.0138 ± 0.0001089
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

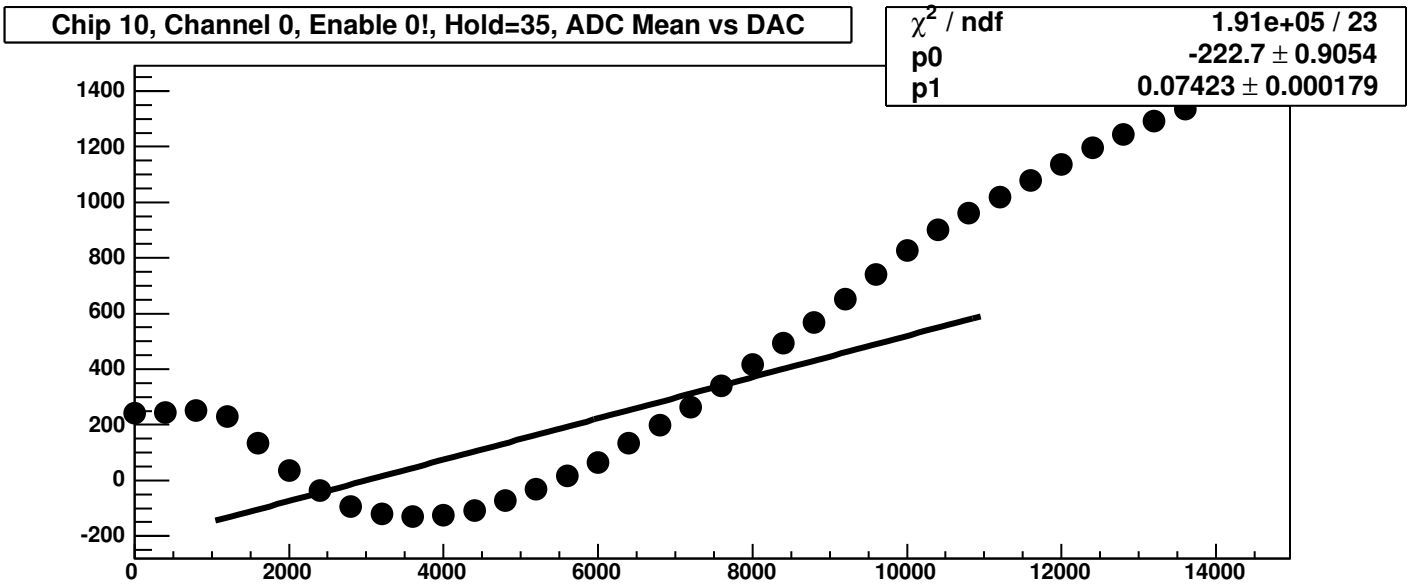

Chip 9, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

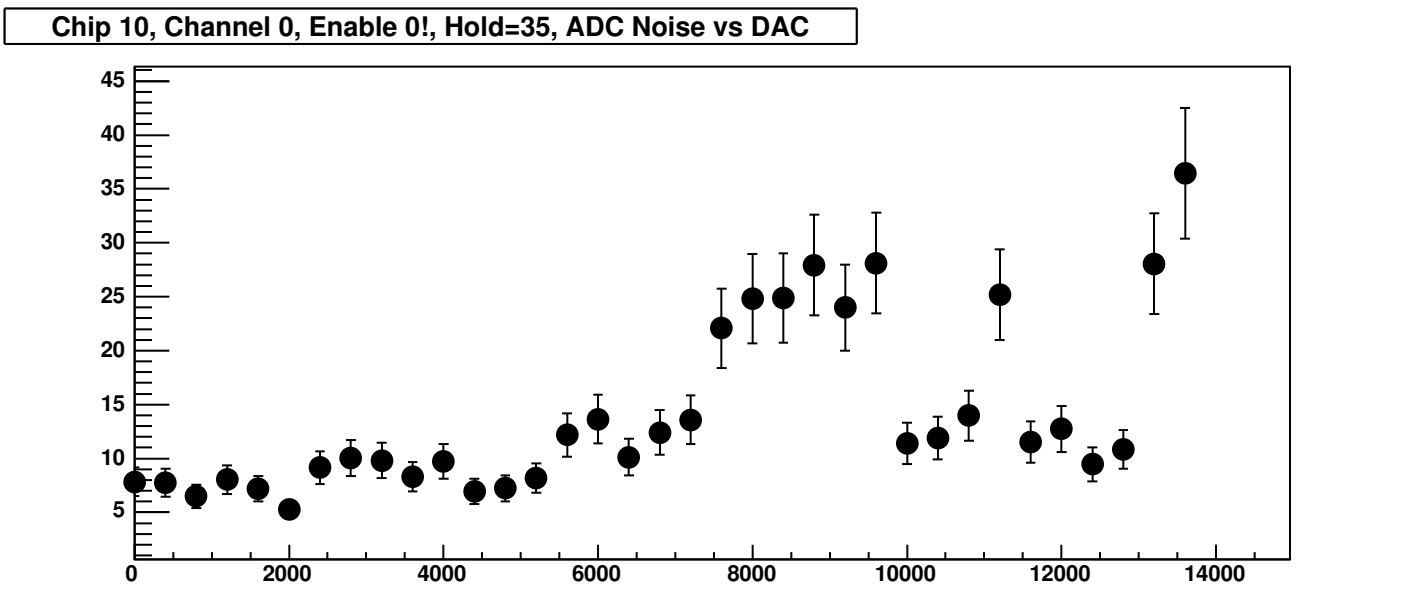

Chip 9, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

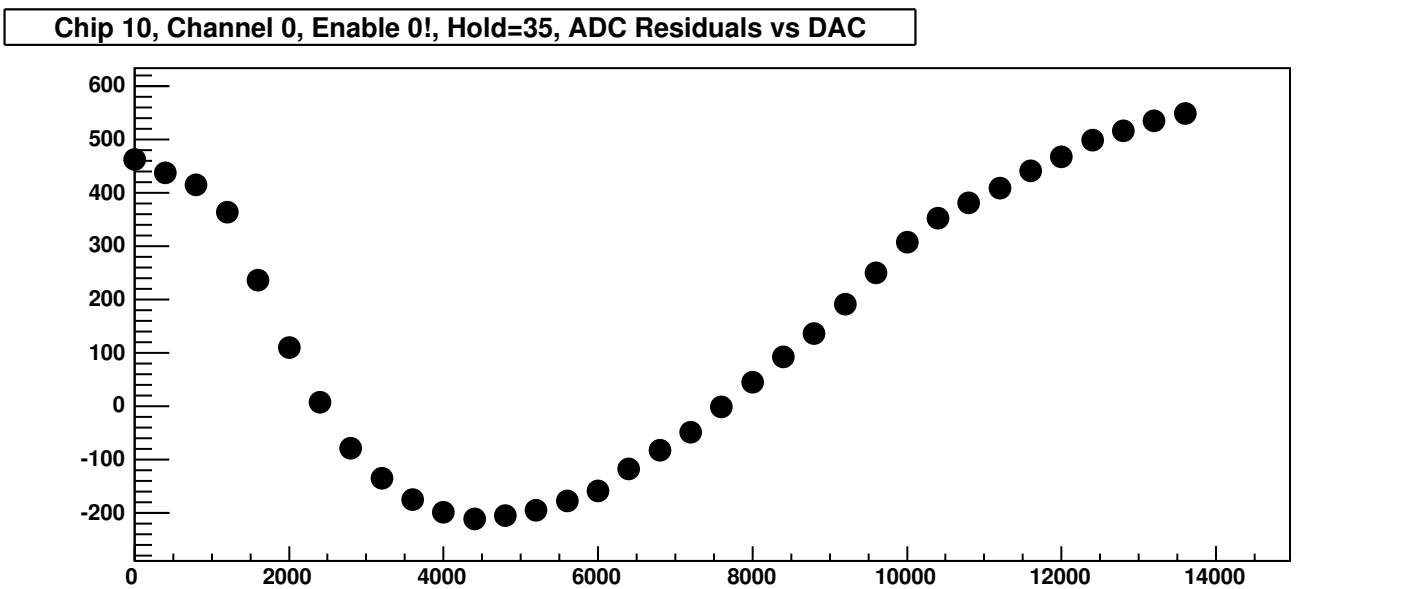

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2772 / 23
p0 -544.7 ± 0.9221
p1 2.322 ± 0.0001679
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

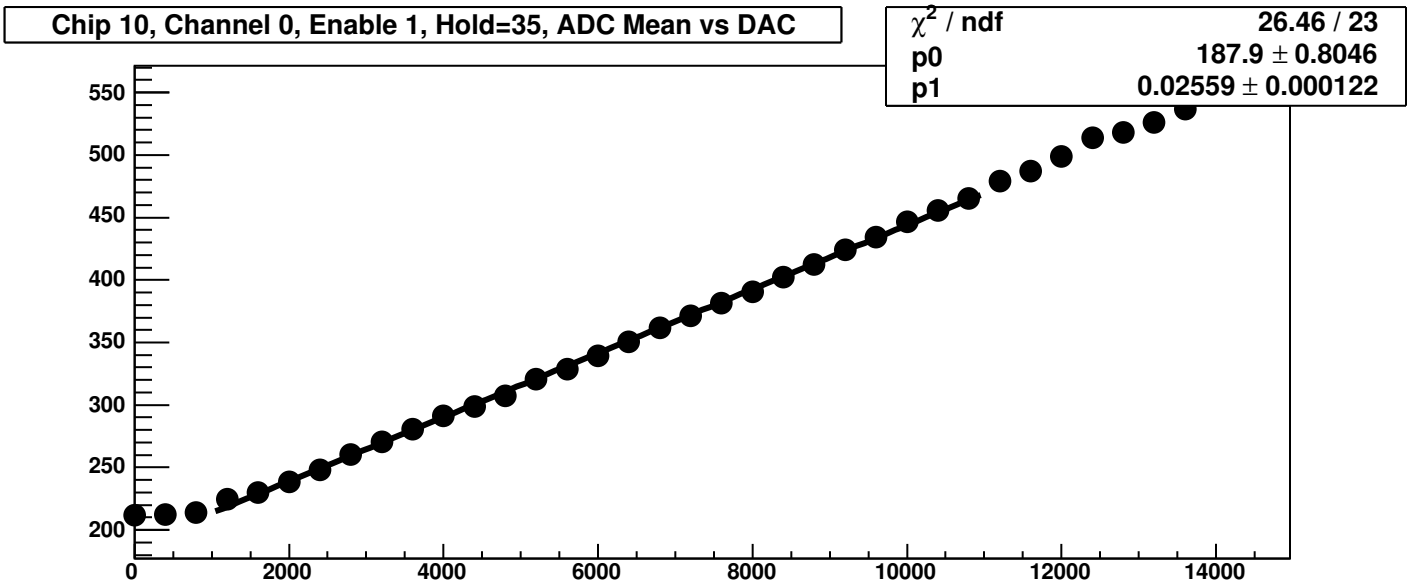

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

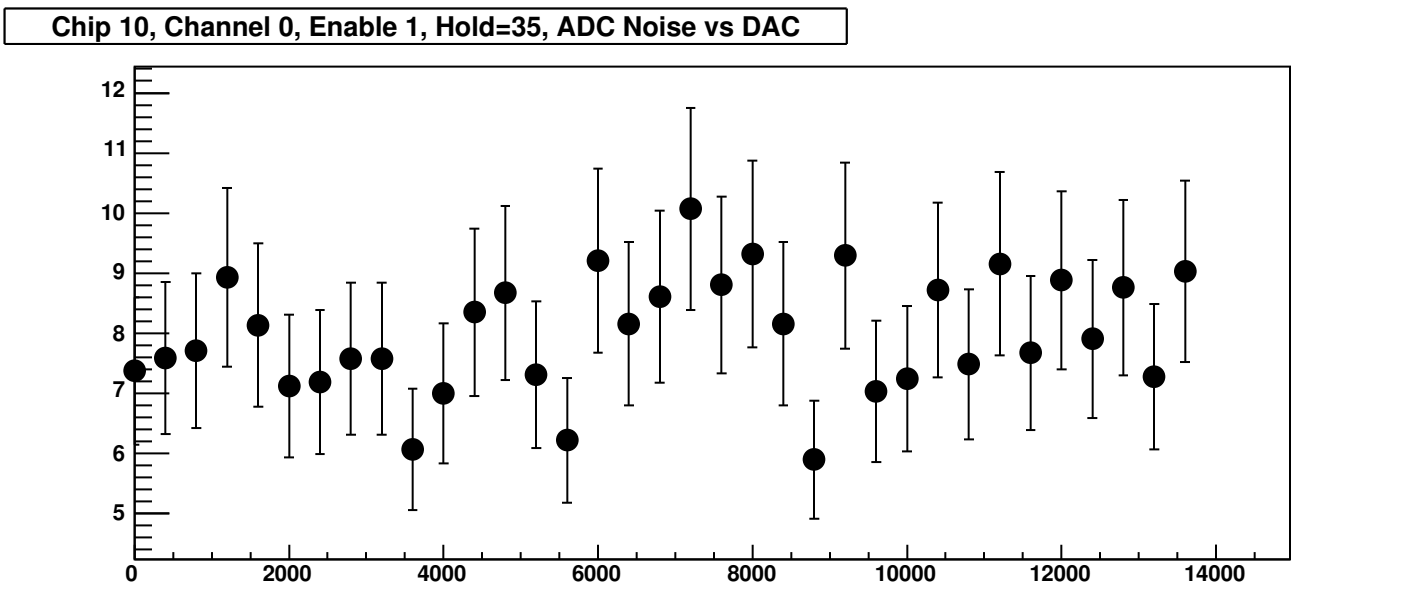

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

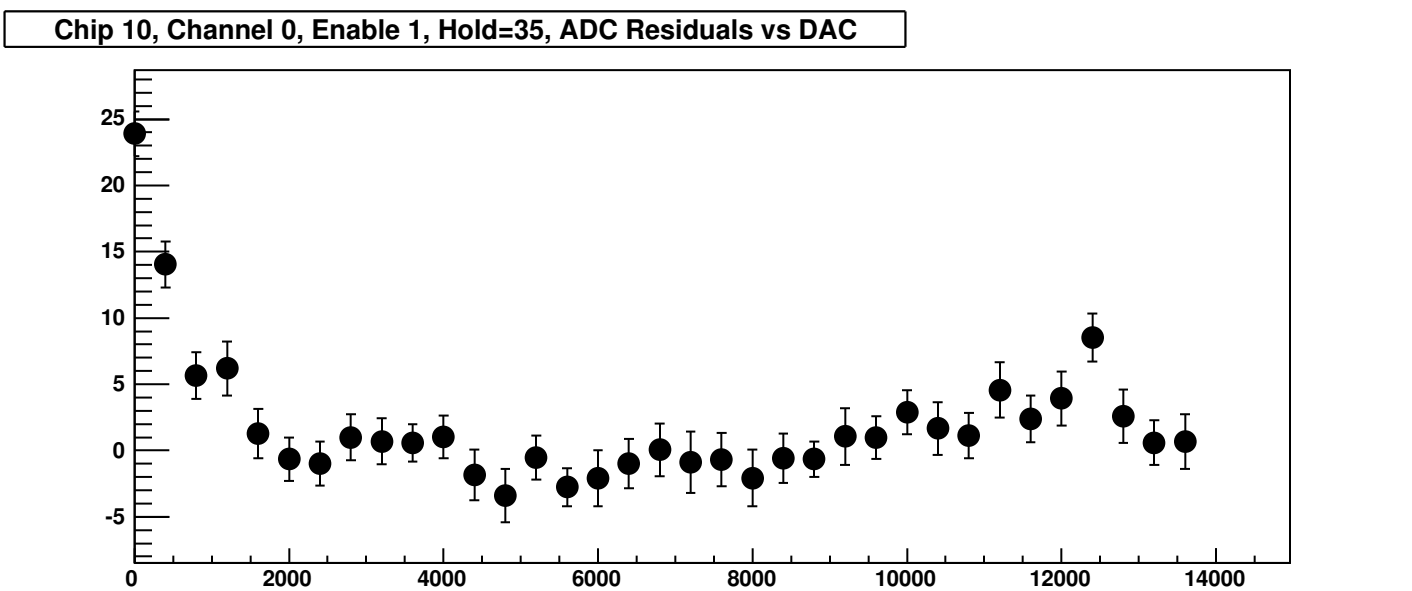

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.91e+05 / 23
p0 -222.7 ± 0.9054
p1 0.07423 ± 0.000179
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

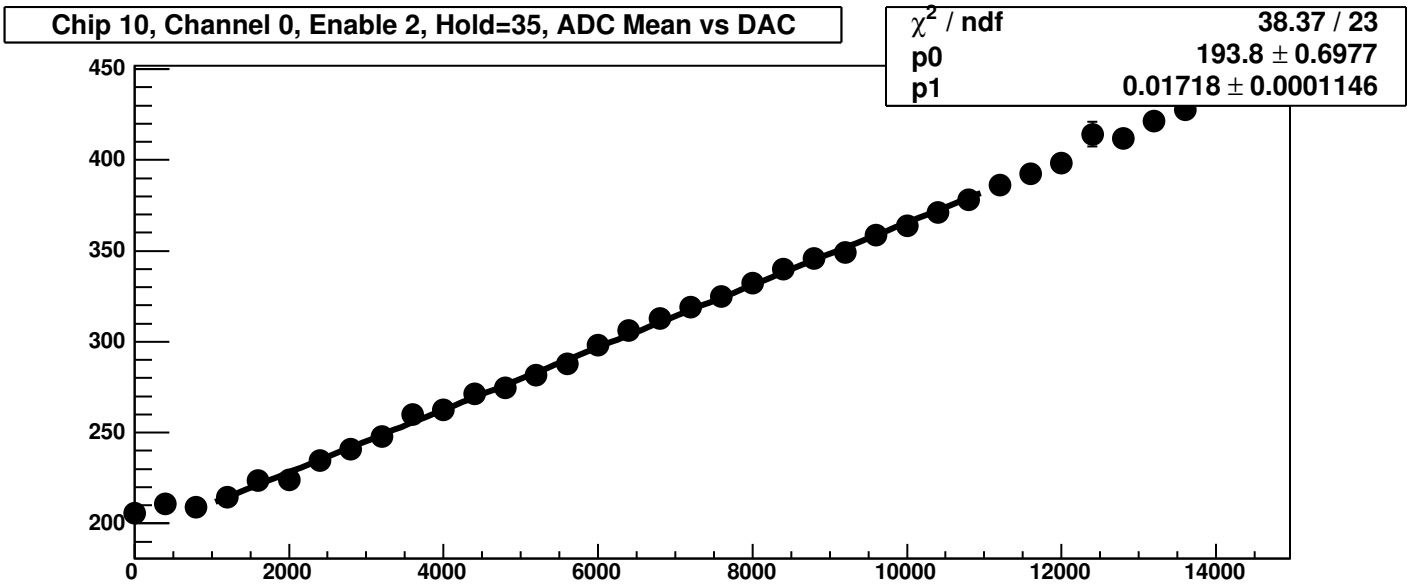

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

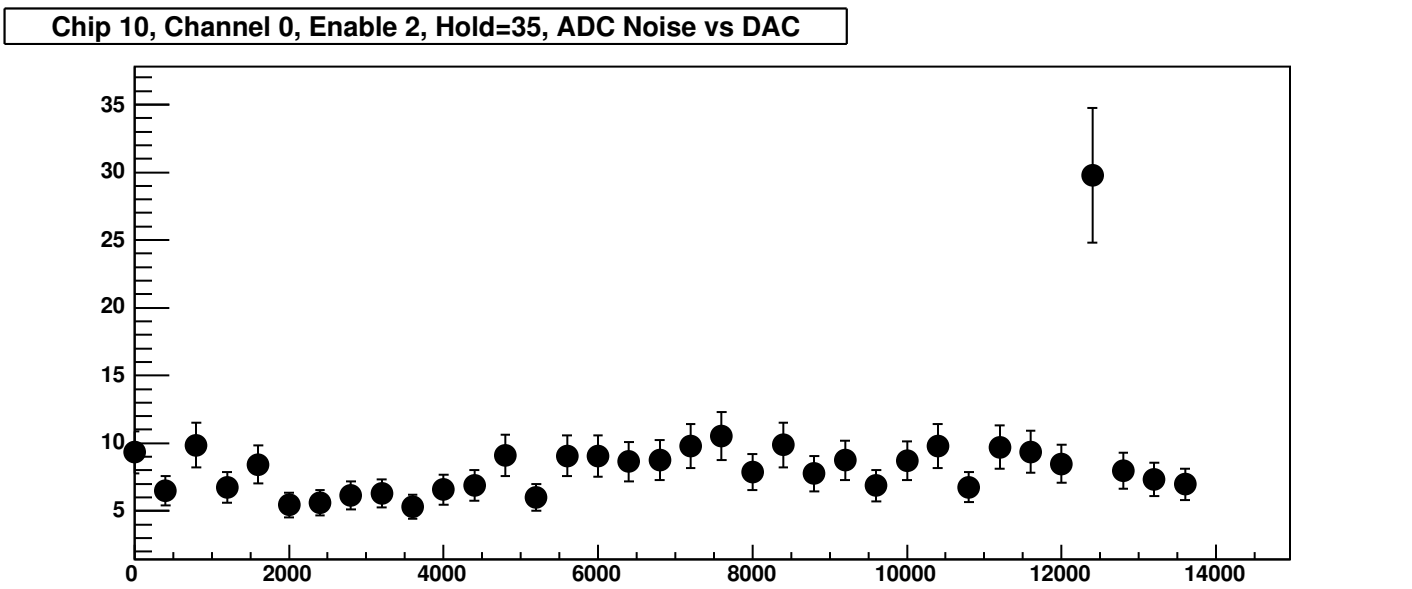

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

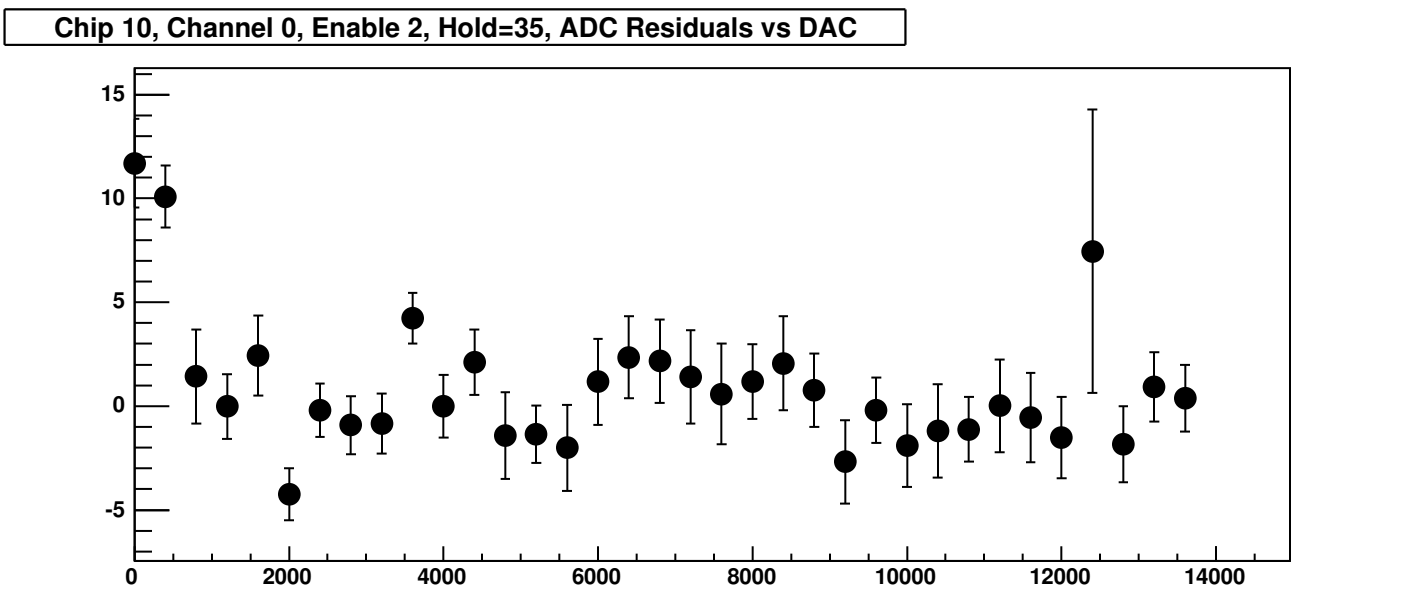

Chip 10, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.46 / 23
p0 187.9 ± 0.8046
p1 0.02559 ± 0.000122
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

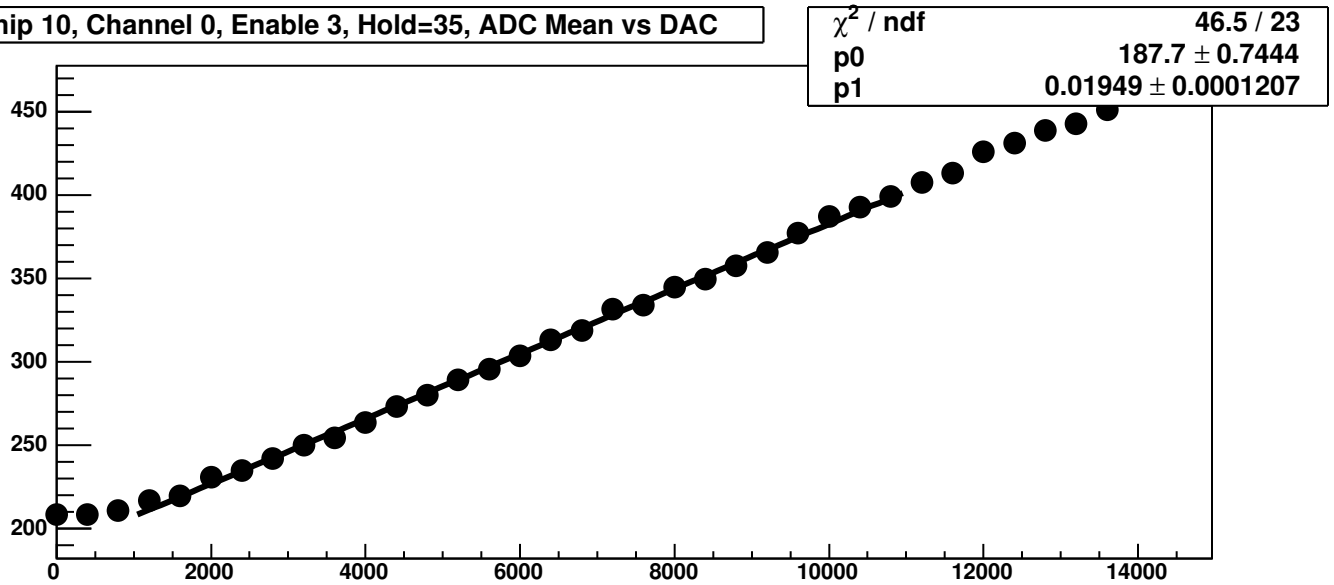

Chip 10, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.37 / 23
p0 193.8 ± 0.6977
p1 0.01718 ± 0.0001146
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

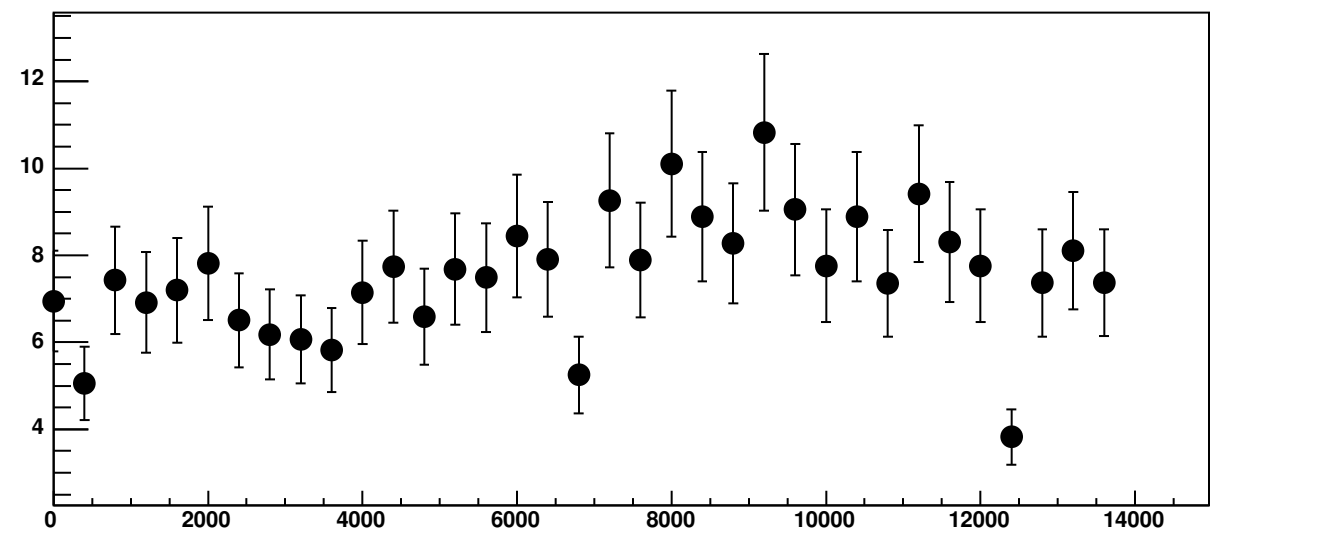

Chip 10, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

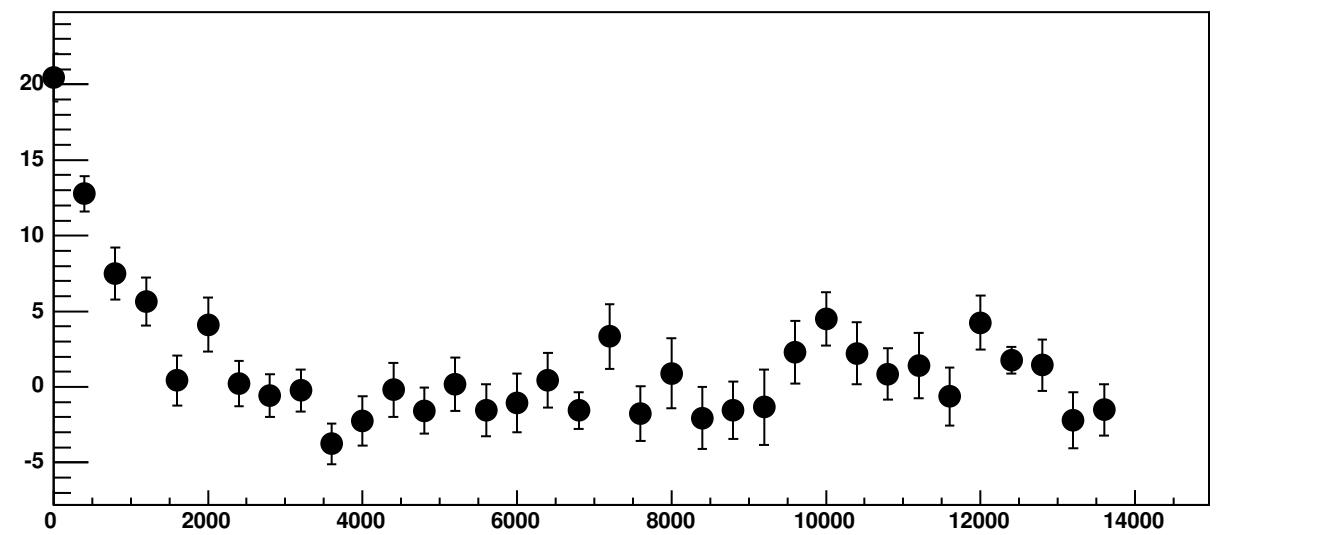
Chip 10, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

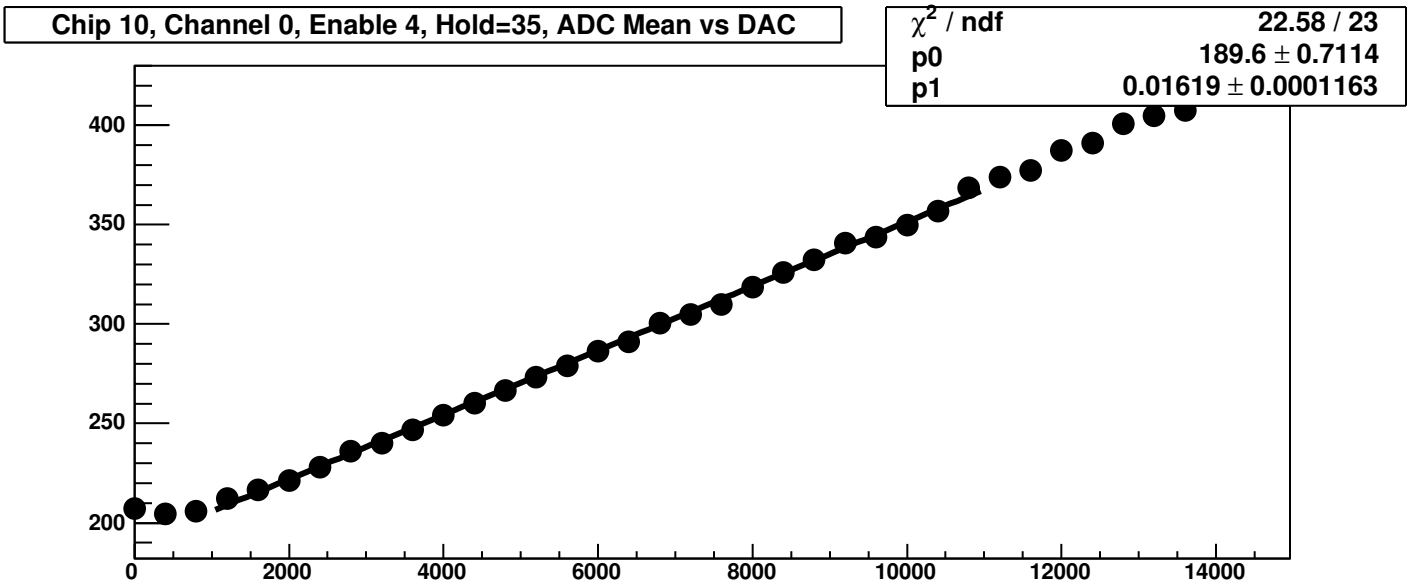


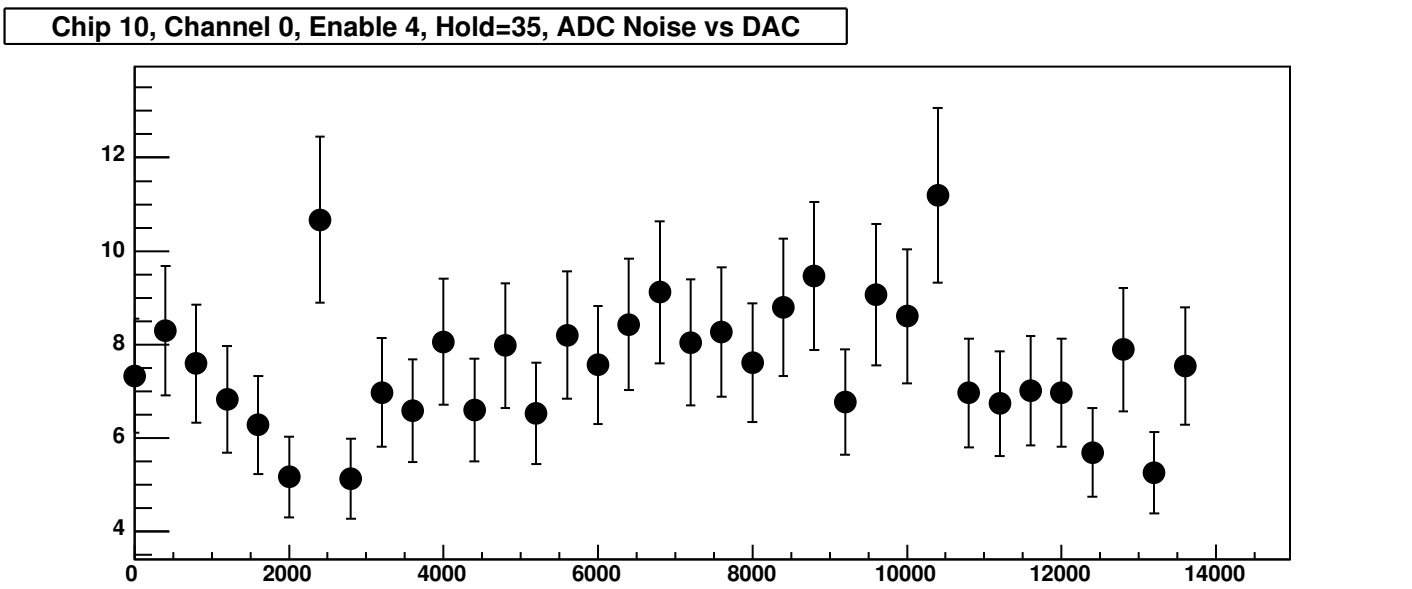
Chip 10, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC

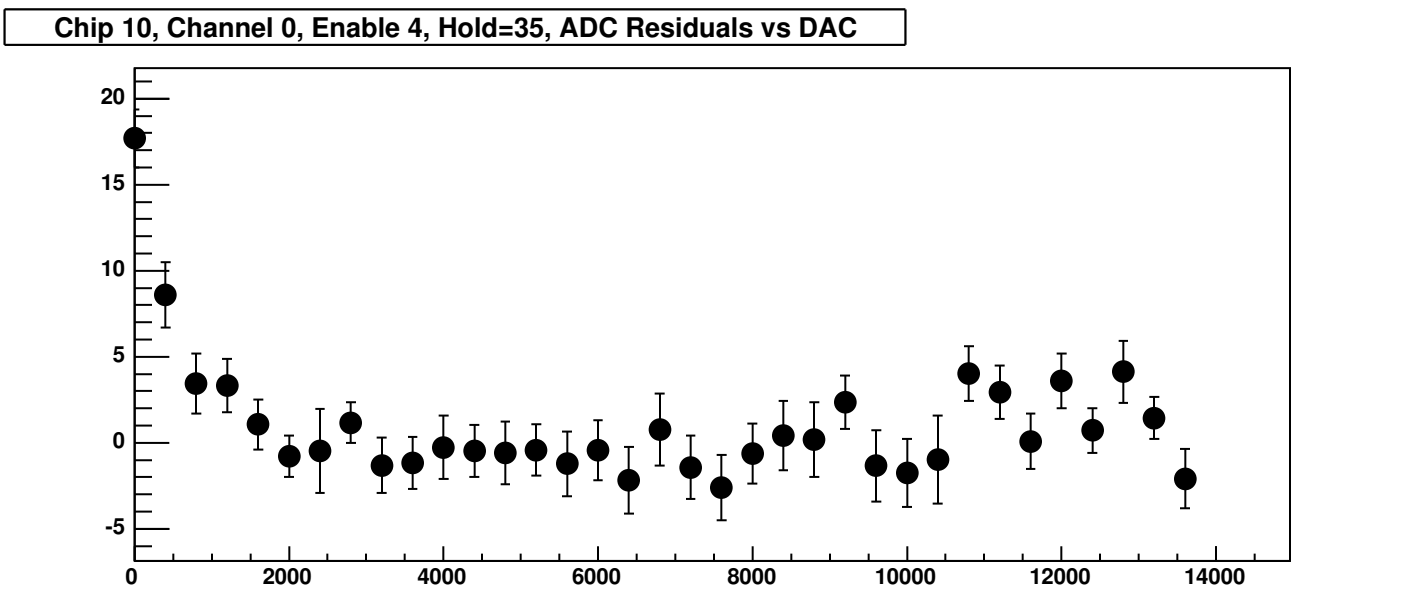


Chip 10, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC



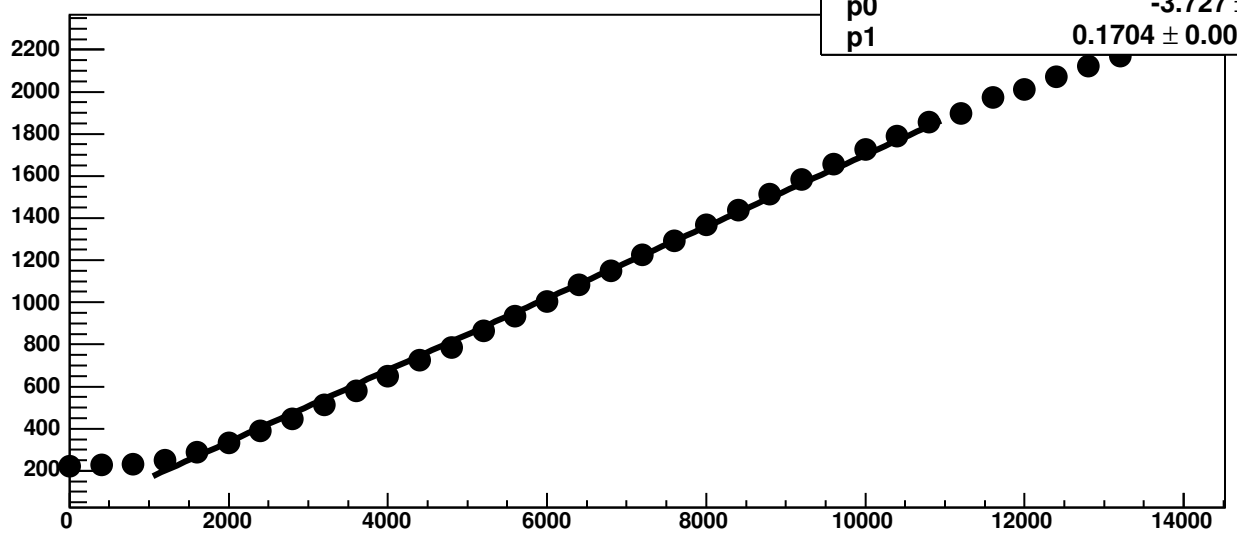

Chip 10, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.58 / 23
p0 189.6 ± 0.7114
p1 0.01619 ± 0.0001163
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

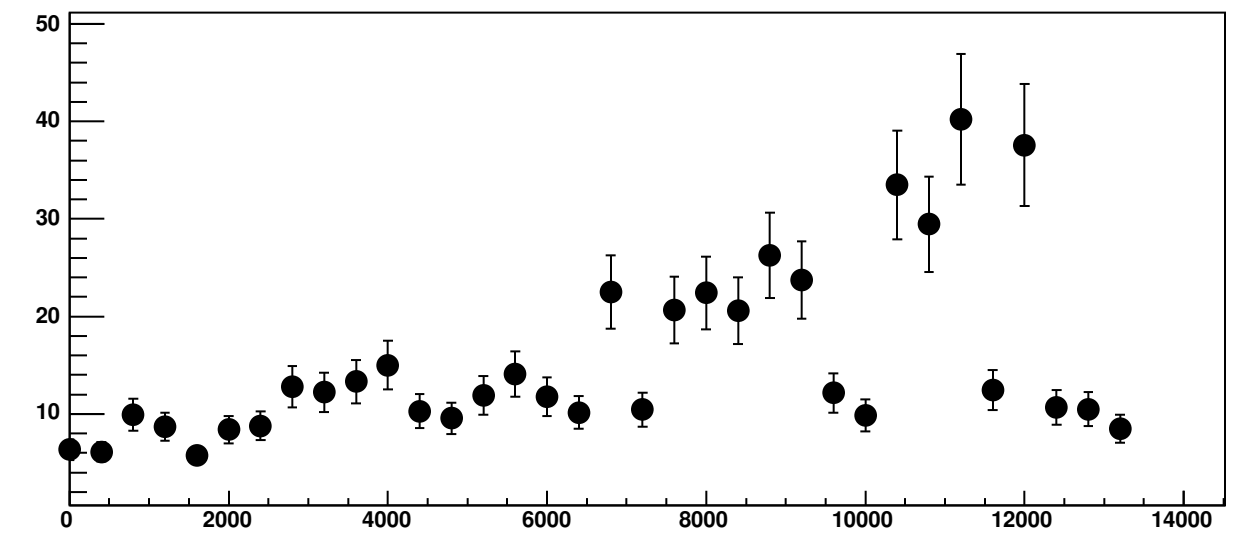

Chip 10, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

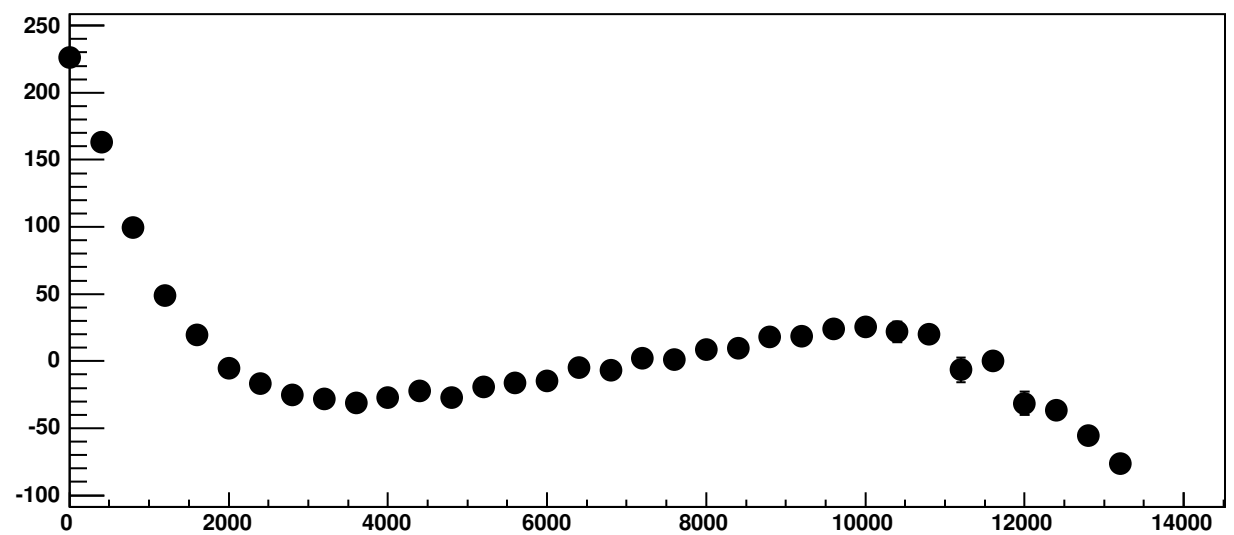
χ^2 / ndf	1837 / 23
p0	-3.727 ± 1.01
p1	0.1704 ± 0.0001918

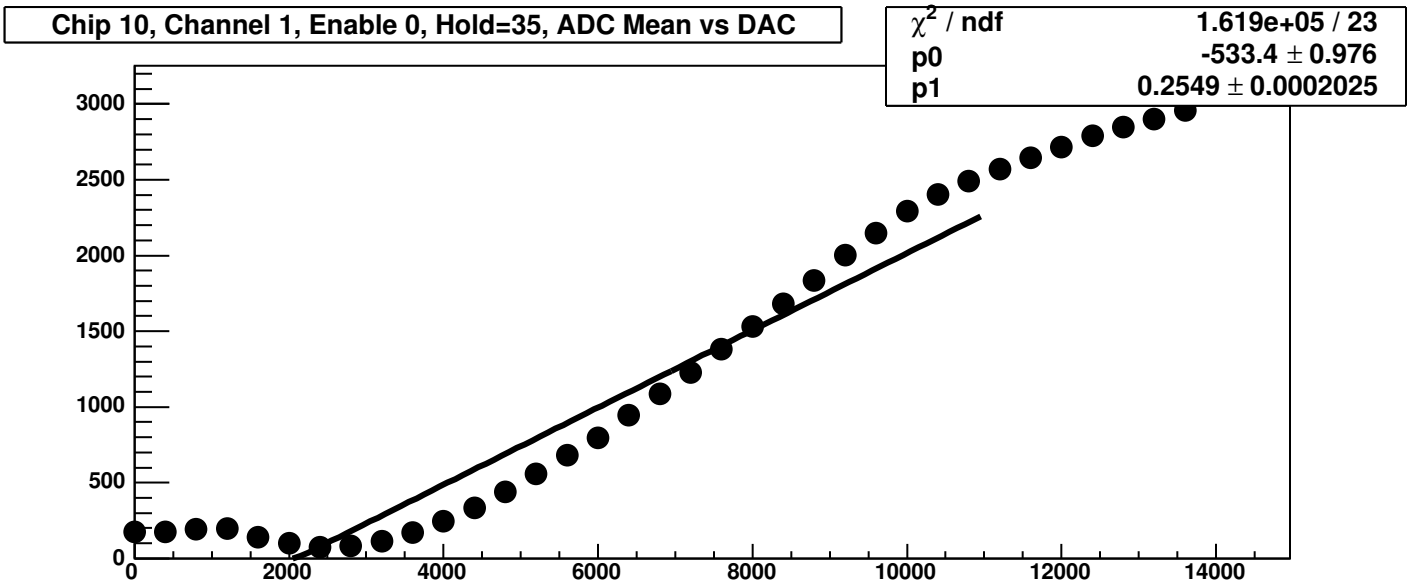


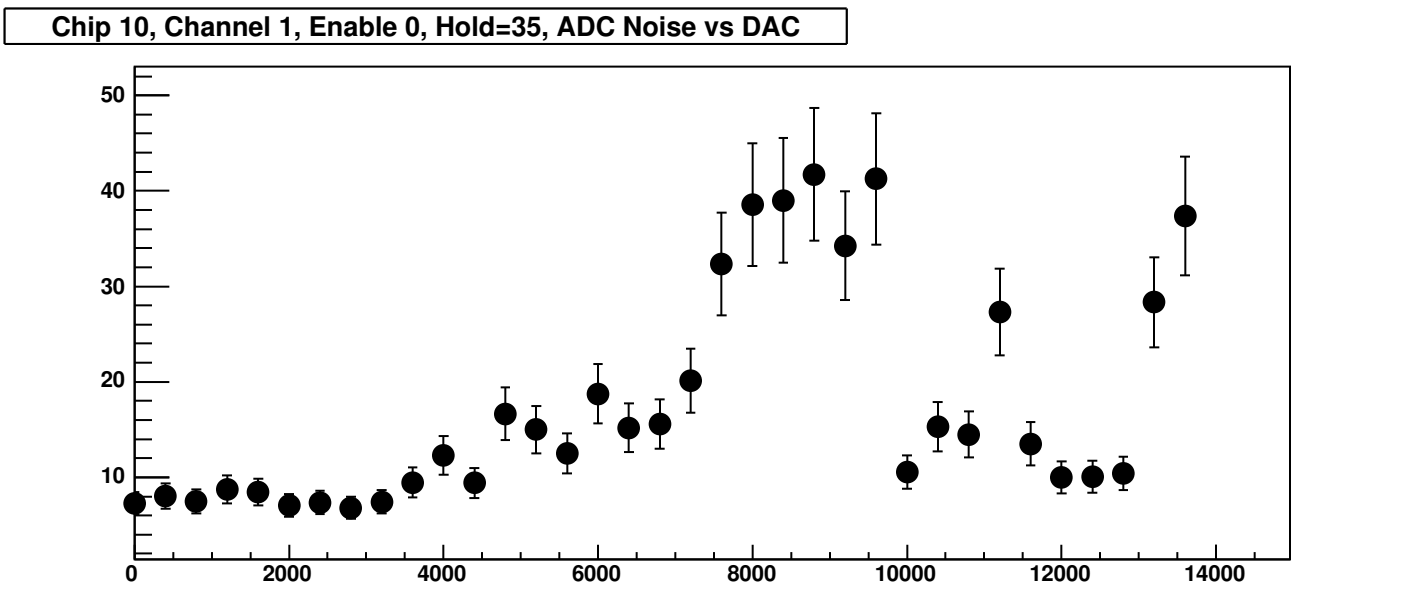
Chip 10, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

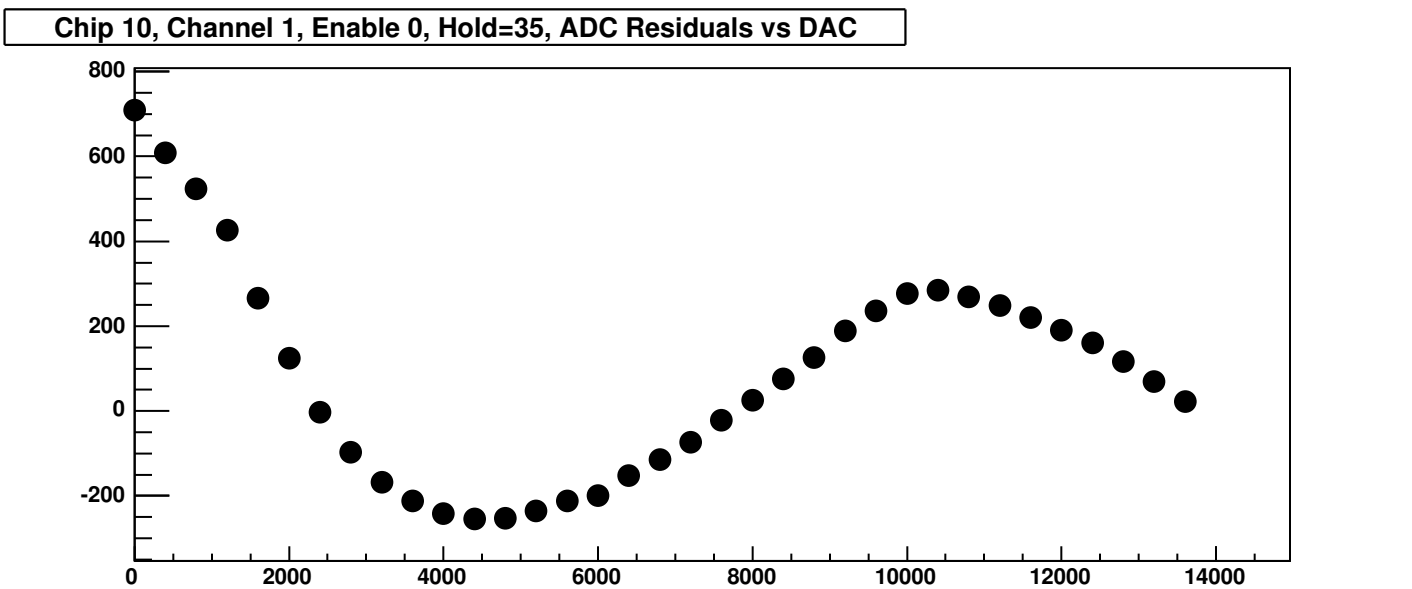


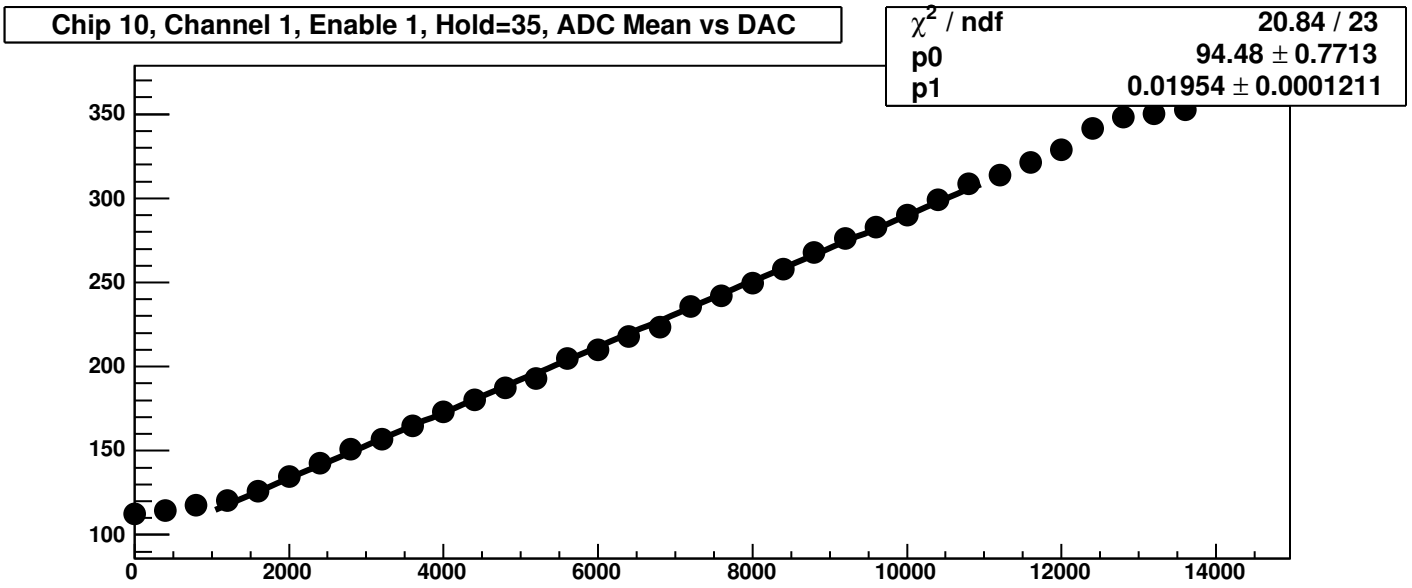
Chip 10, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

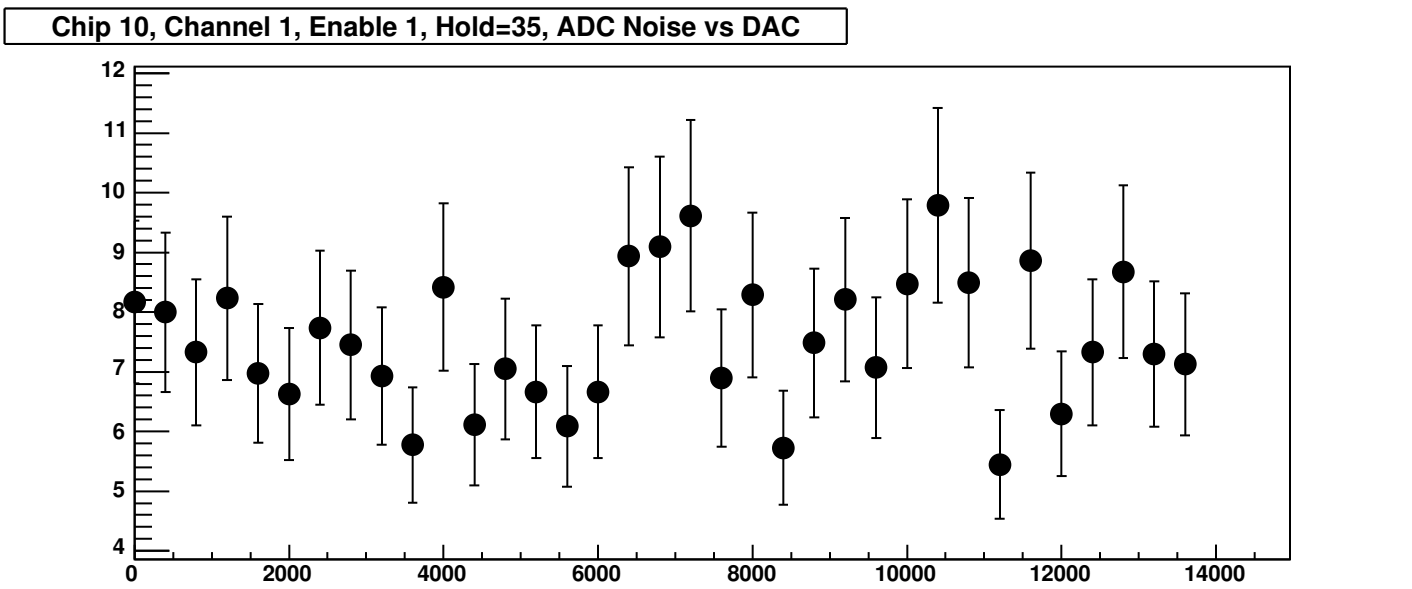


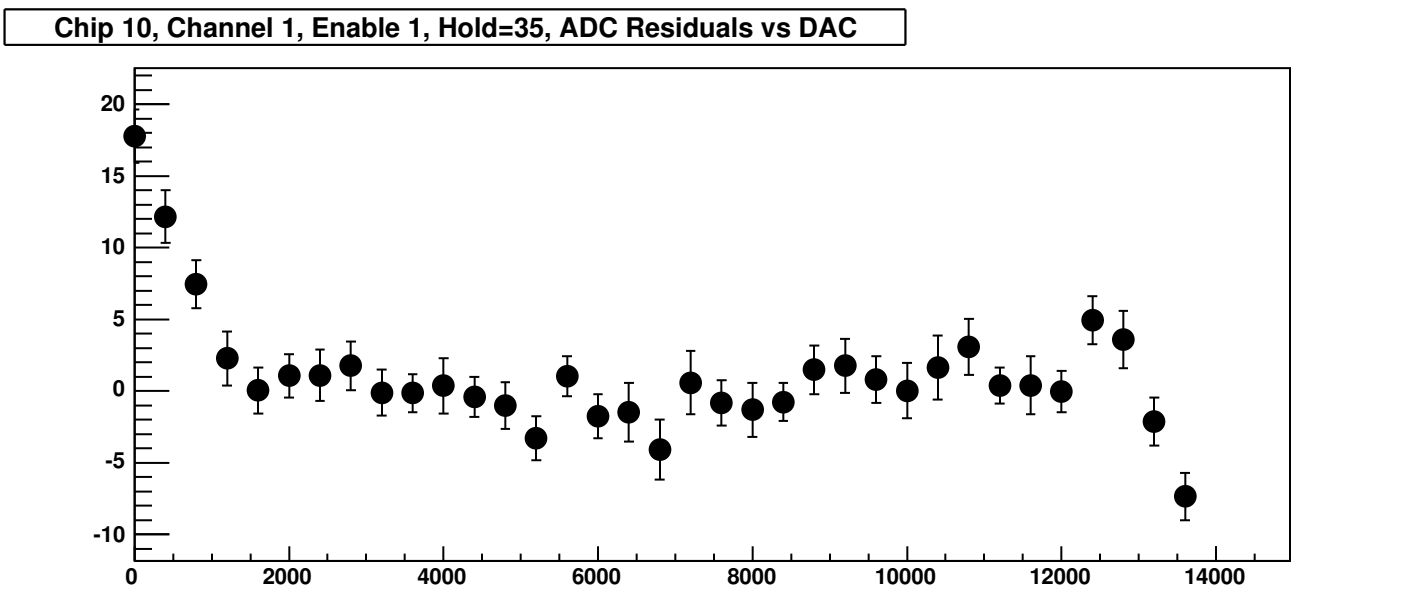

Chip 10, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.619e+05 / 23
p0 -533.4 ± 0.976
p1 0.2549 ± 0.0002025
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

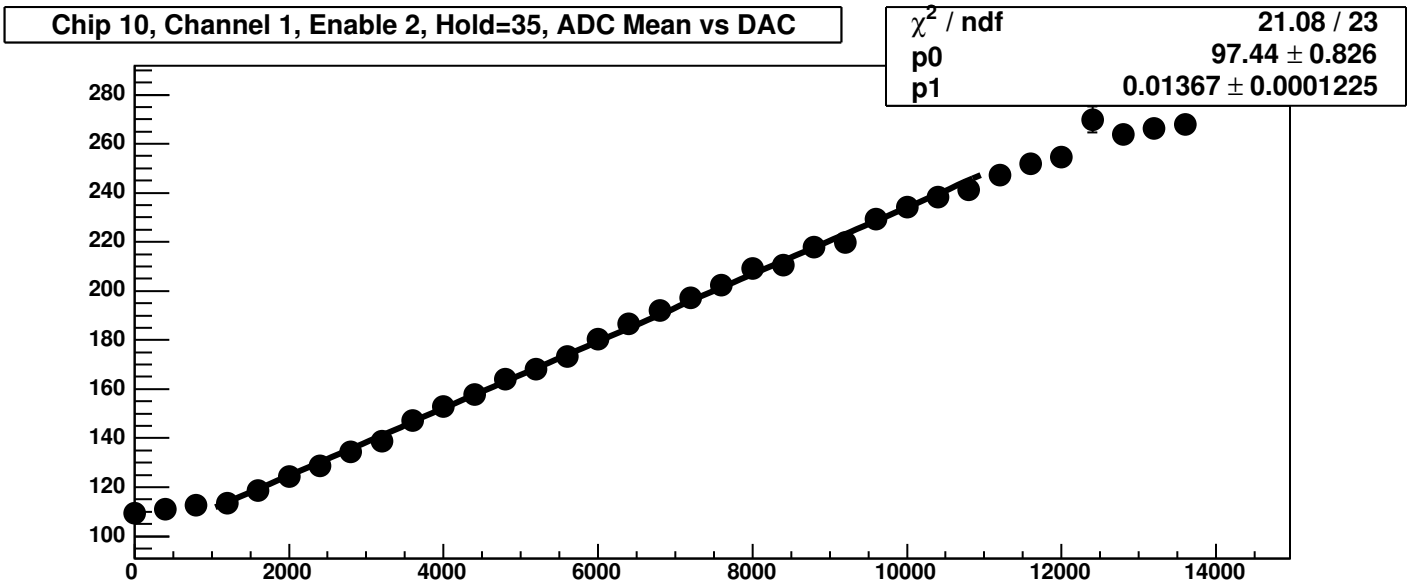

Chip 10, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

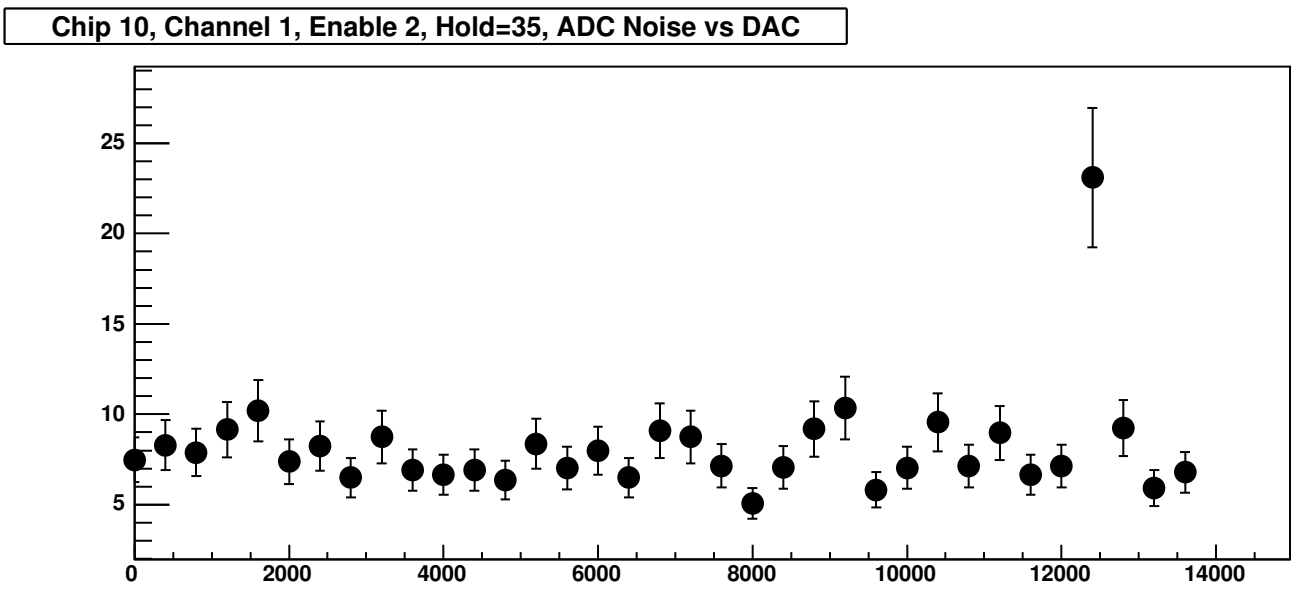

Chip 10, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

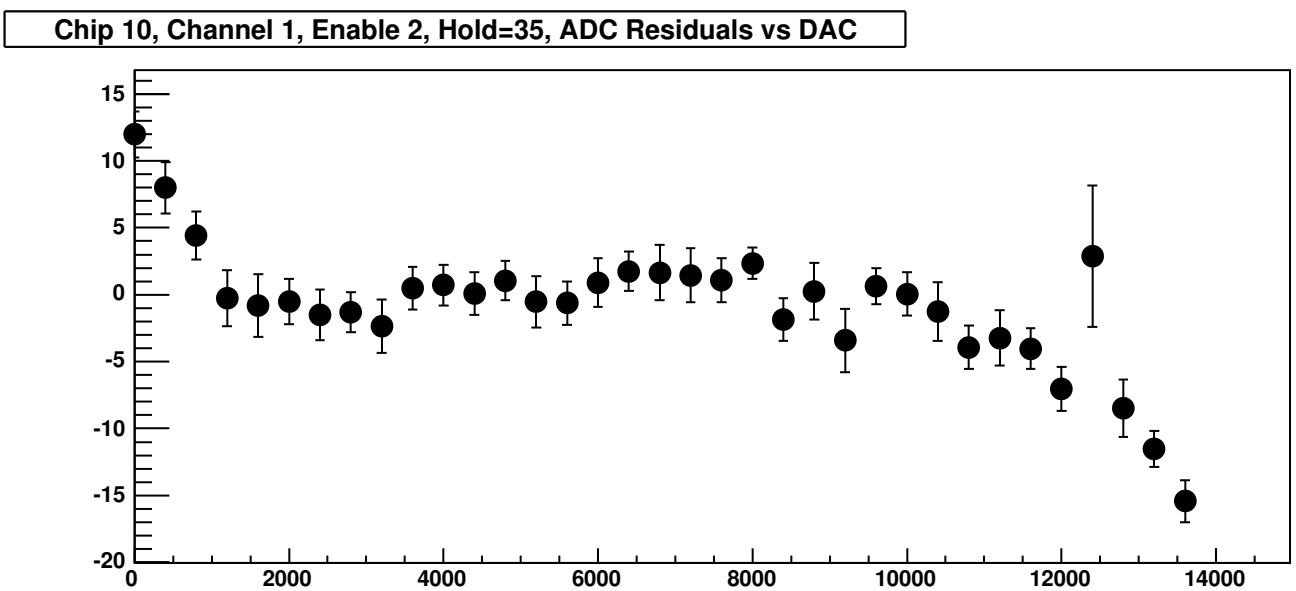

Chip 10, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.84 / 23
p0 94.48 ± 0.7713
p1 0.01954 ± 0.0001211
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

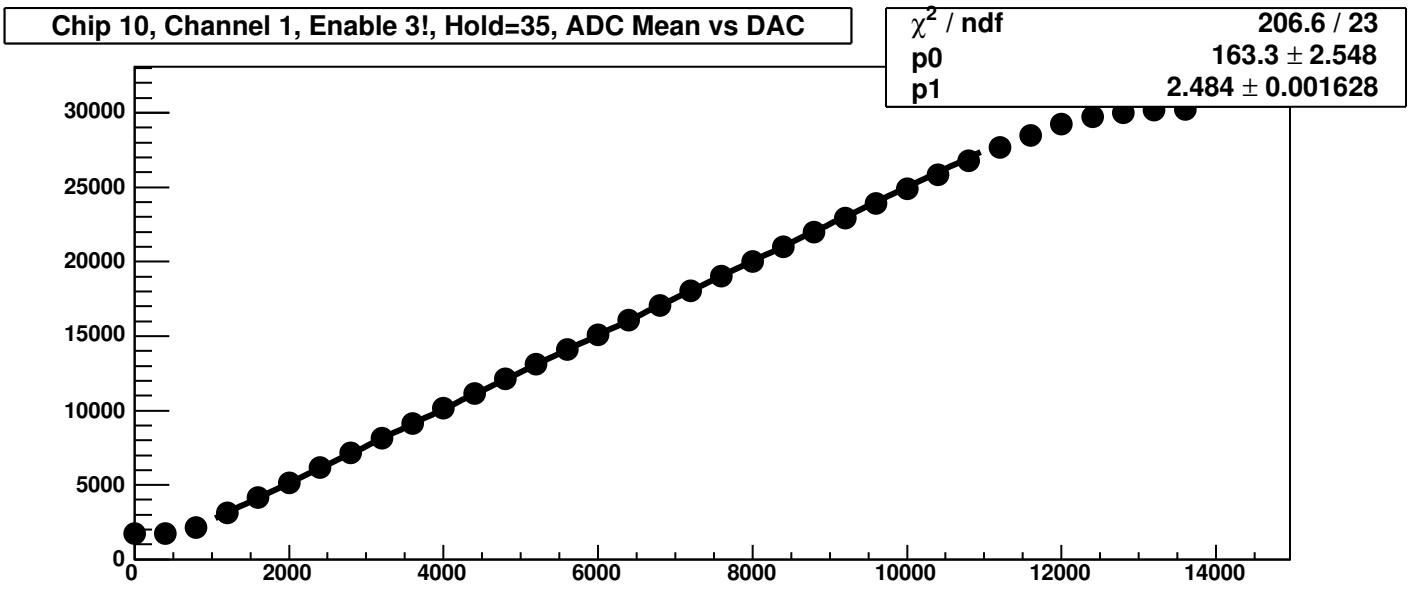

Chip 10, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

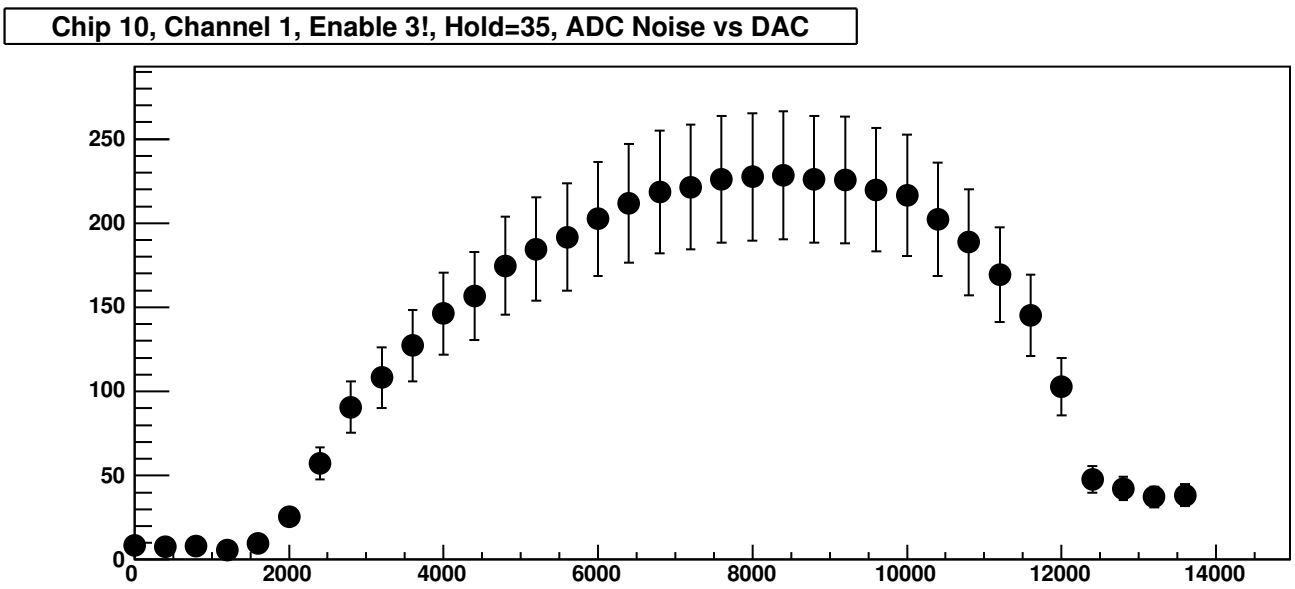

Chip 10, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

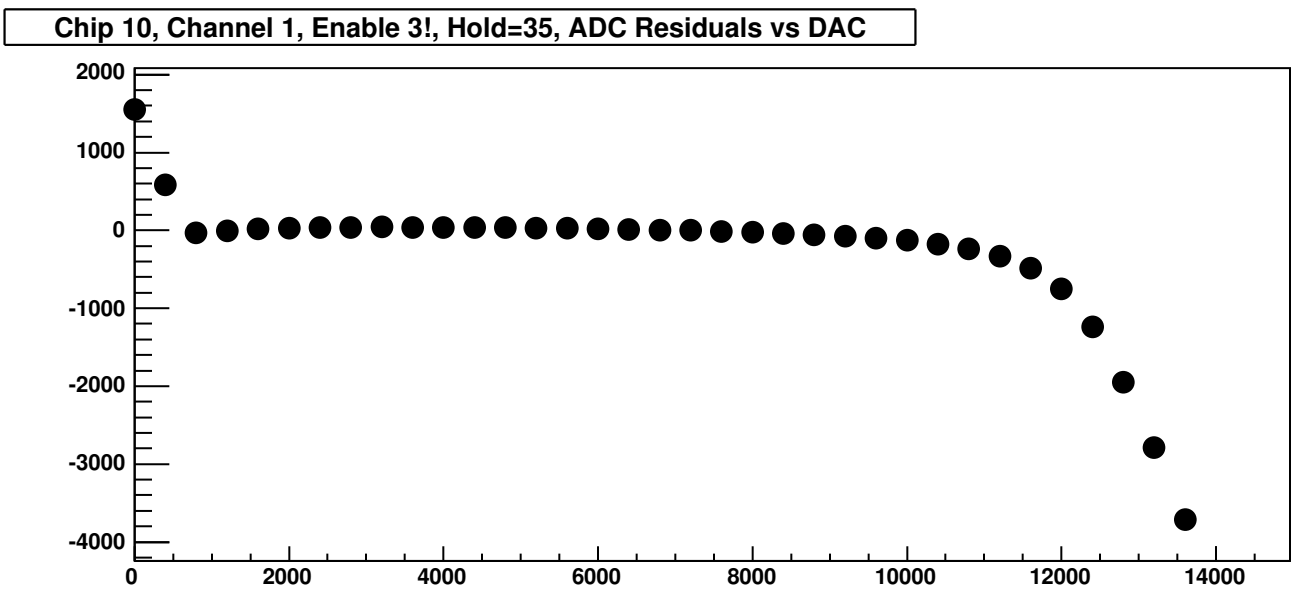

Chip 10, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.08 / 23
p0 97.44 ± 0.826
p1 0.01367 ± 0.0001225
280
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

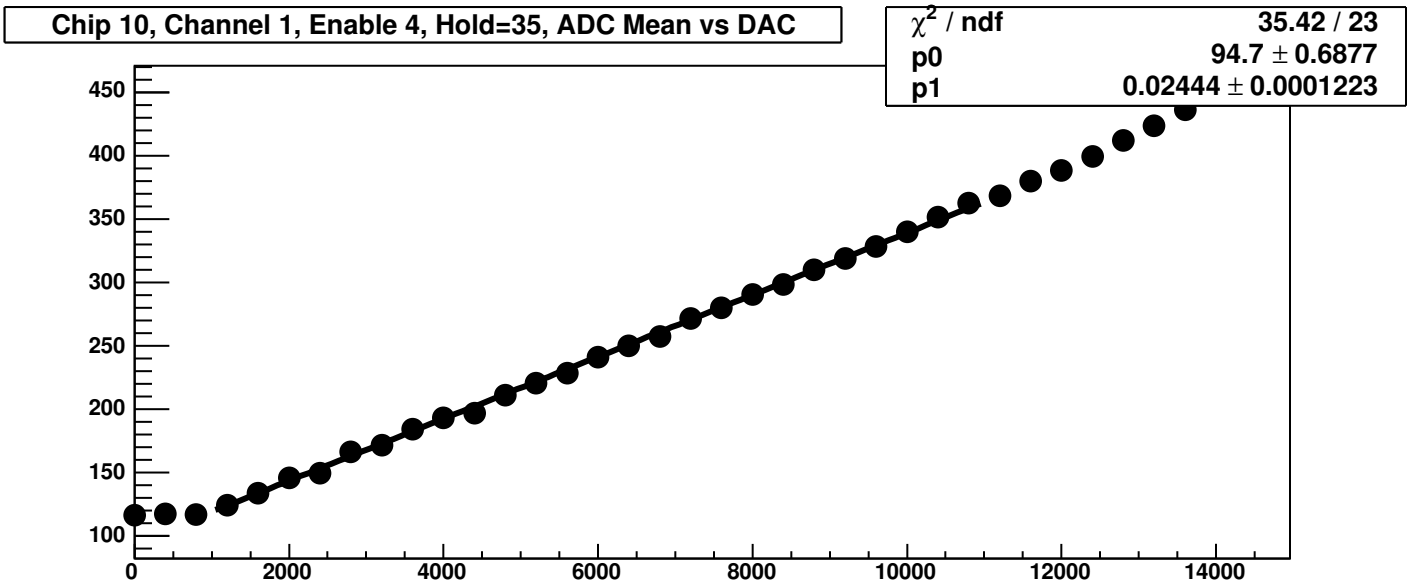

Chip 10, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

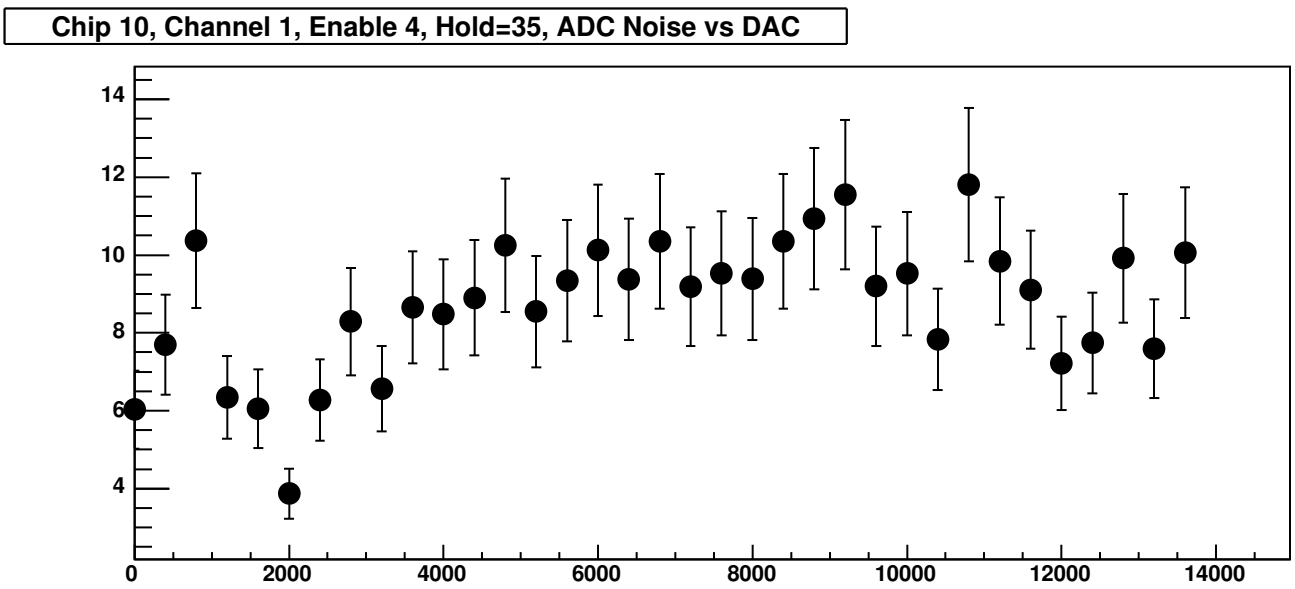

Chip 10, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

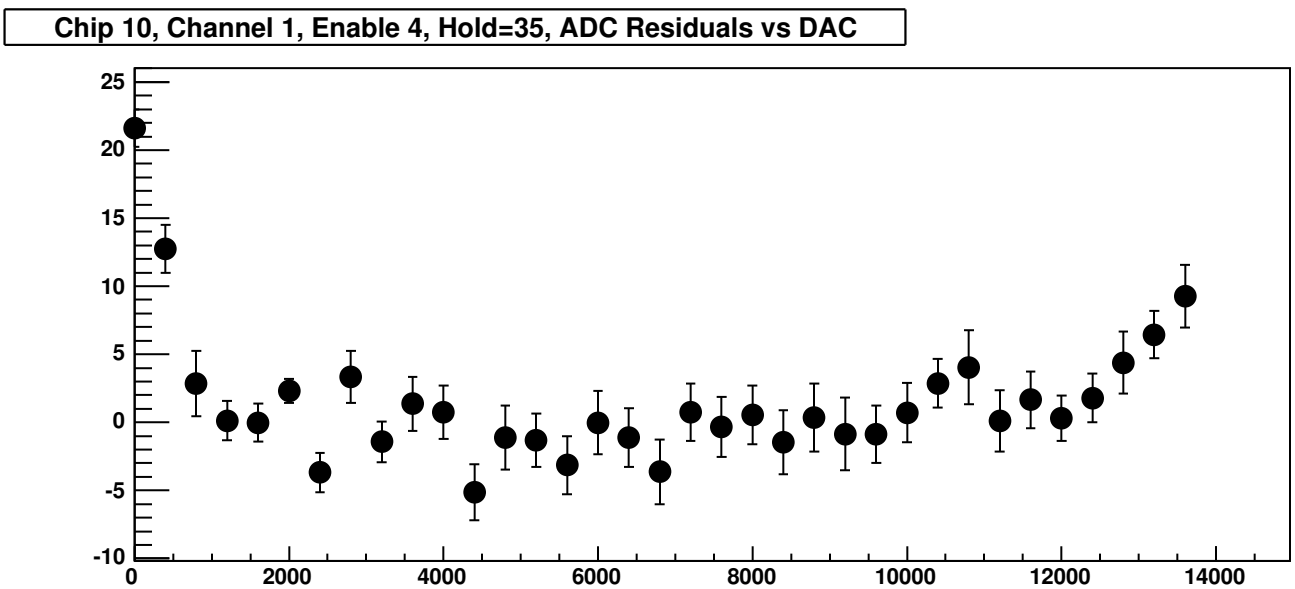

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 206.6 / 23
p0 163.3 ± 2.548
p1 2.484 ± 0.001628
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

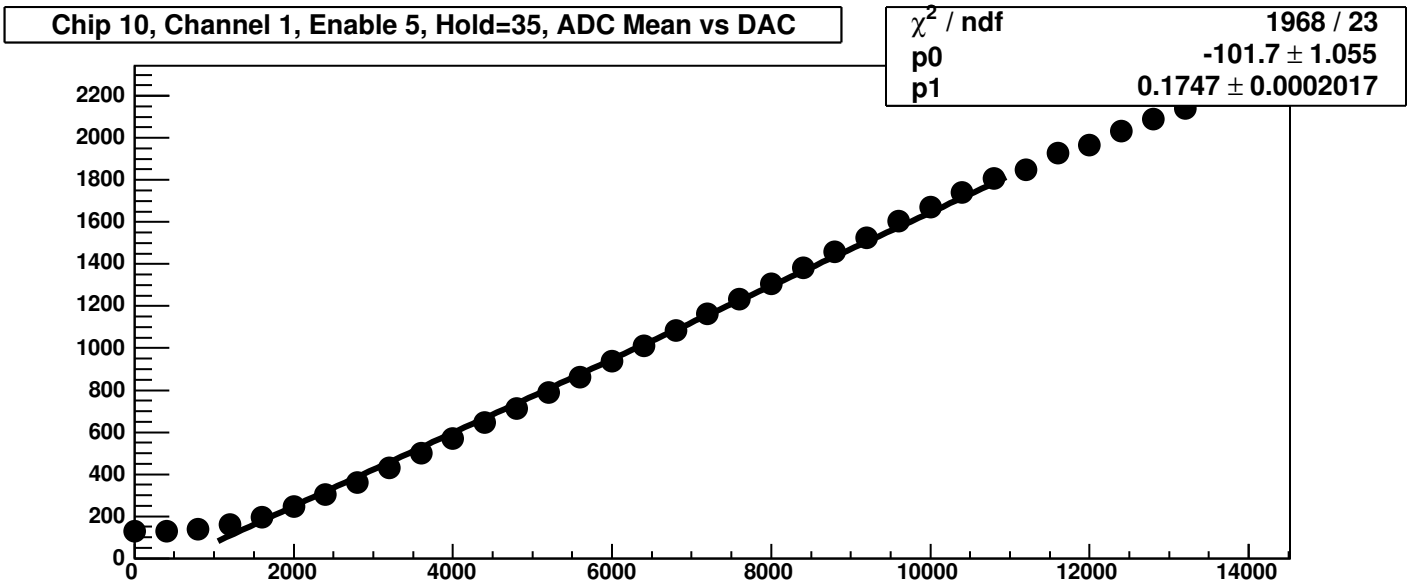

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

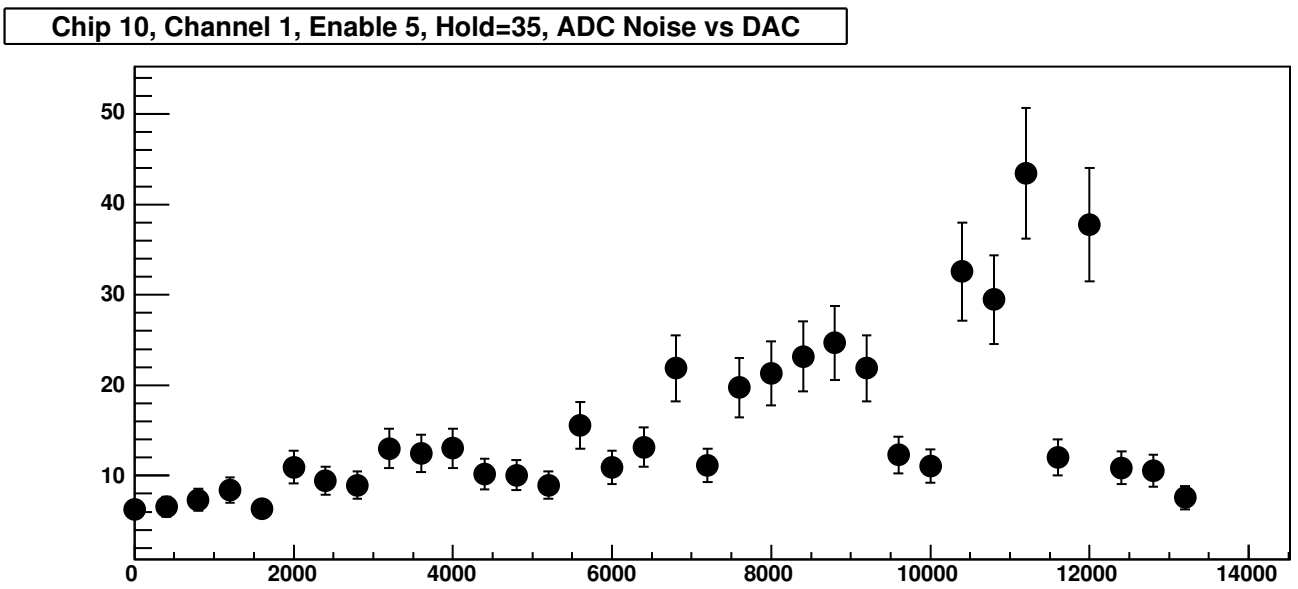

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

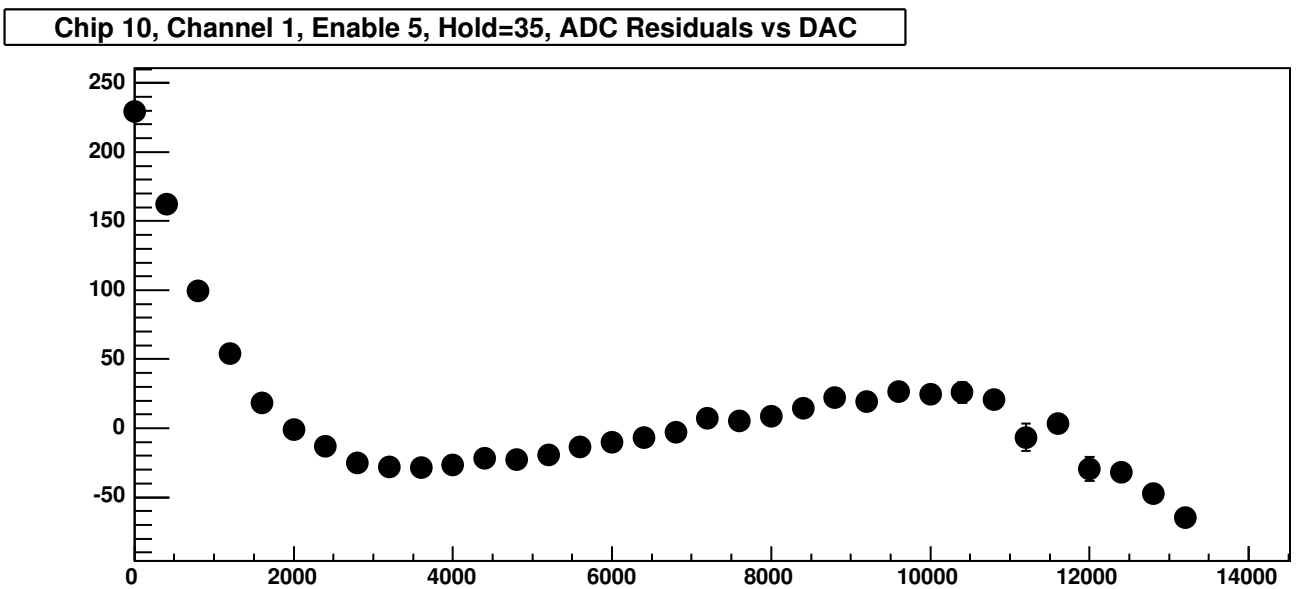

Chip 10, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.42 / 23
p0 94.7 ± 0.6877
p1 0.02444 ± 0.0001223
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

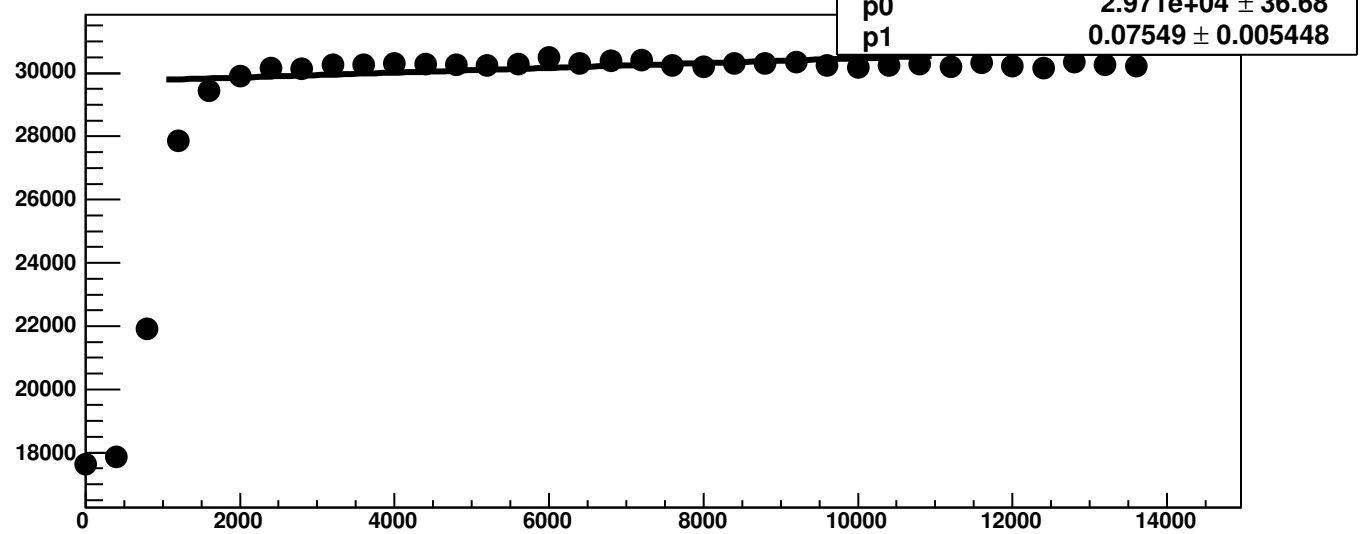

Chip 10, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1968 / 23
p0 -101.7 ± 1.055
p1 0.1747 ± 0.0002017
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

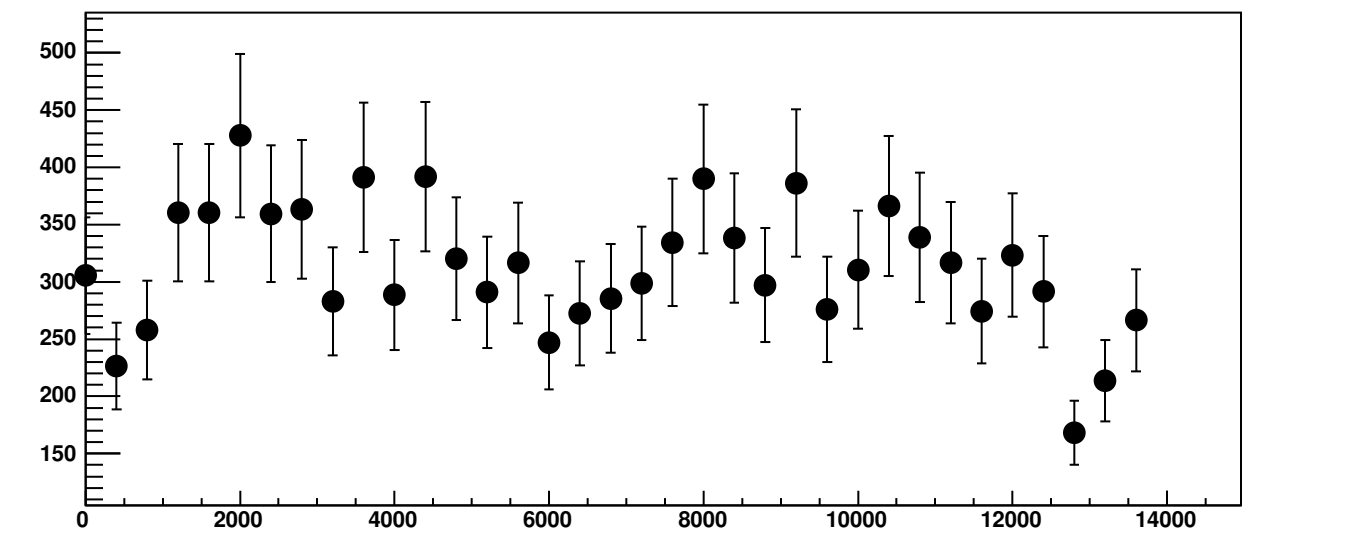

Chip 10, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

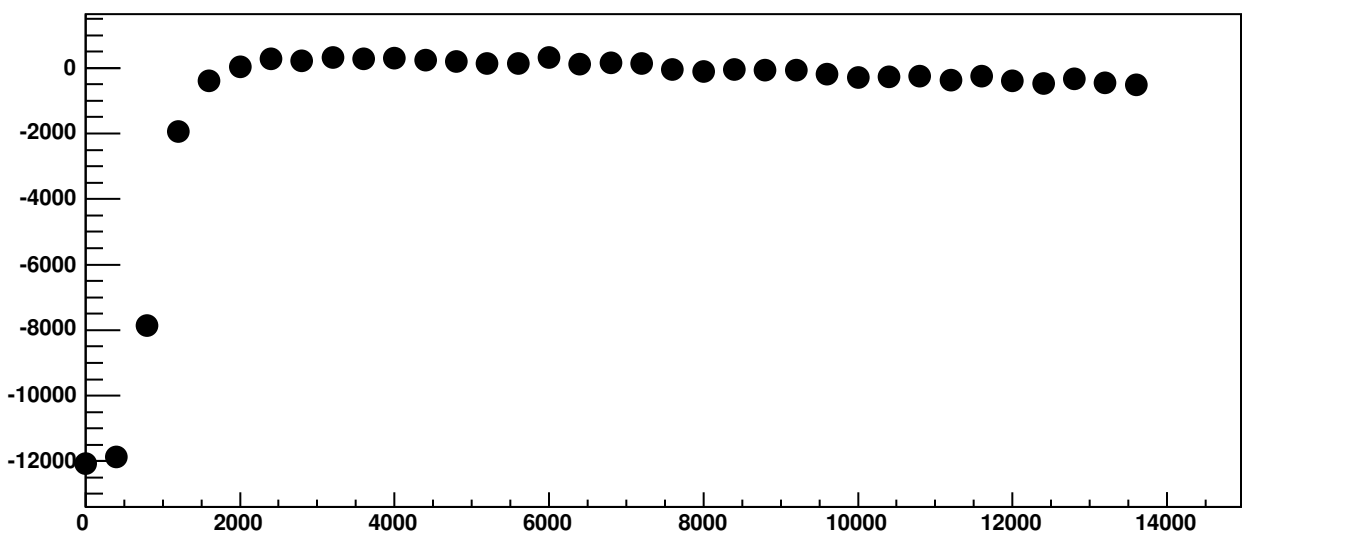
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC

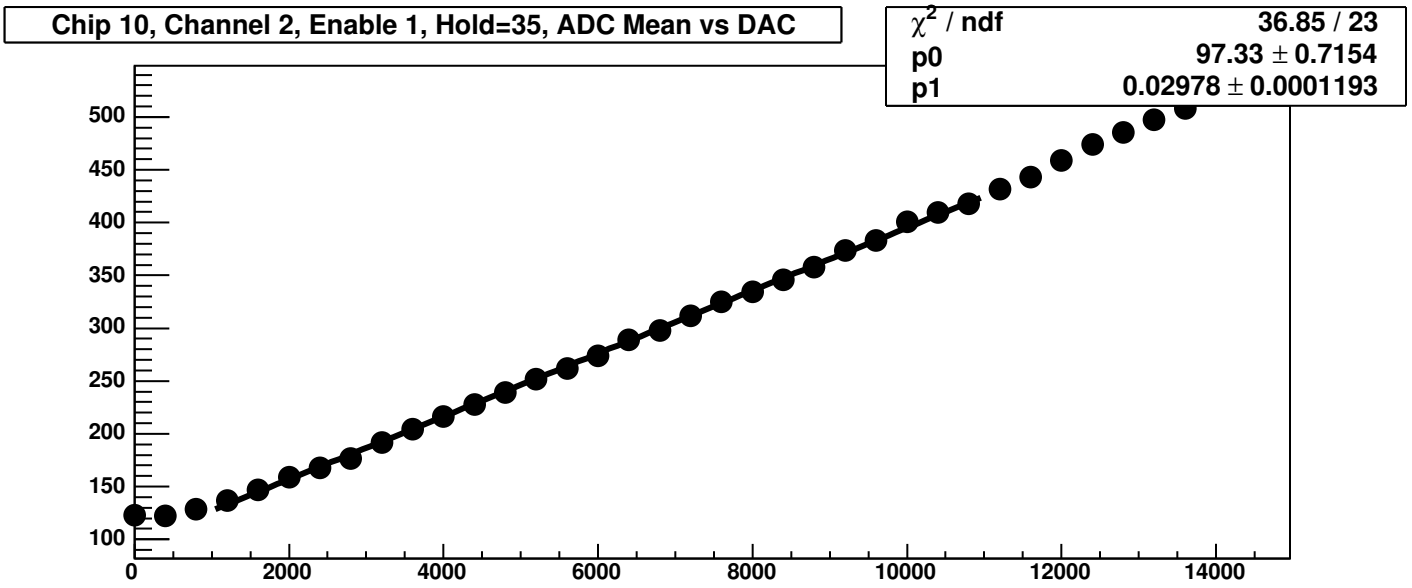


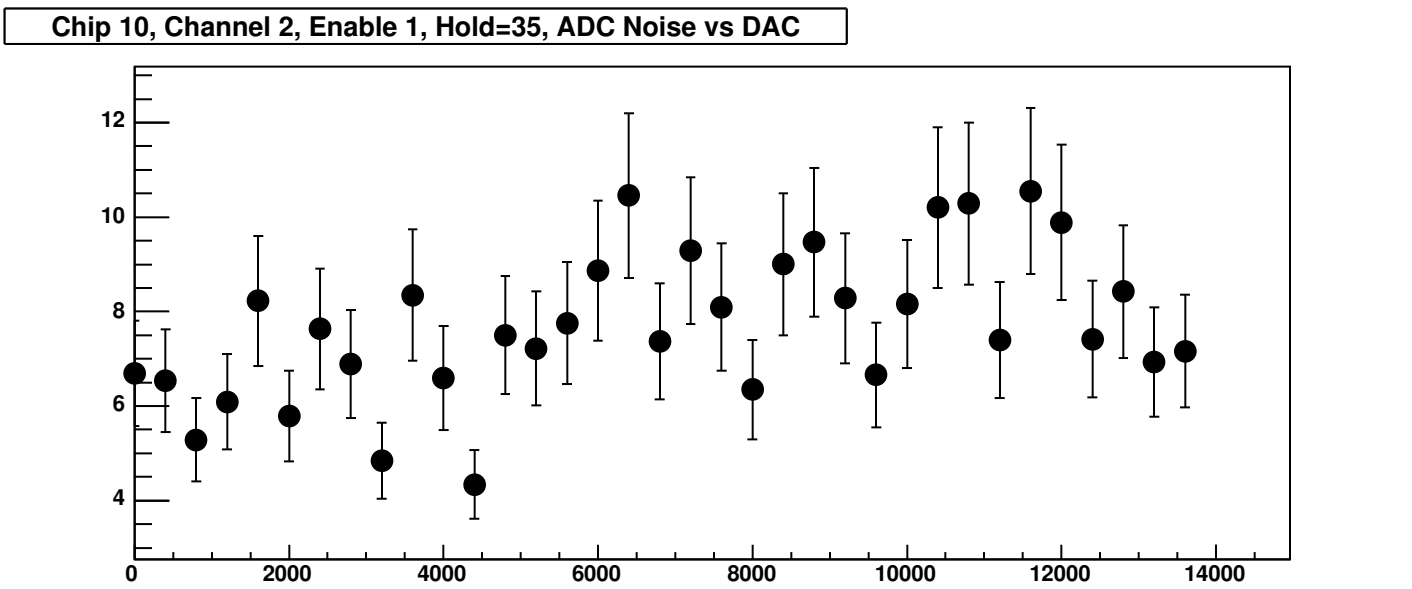
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC

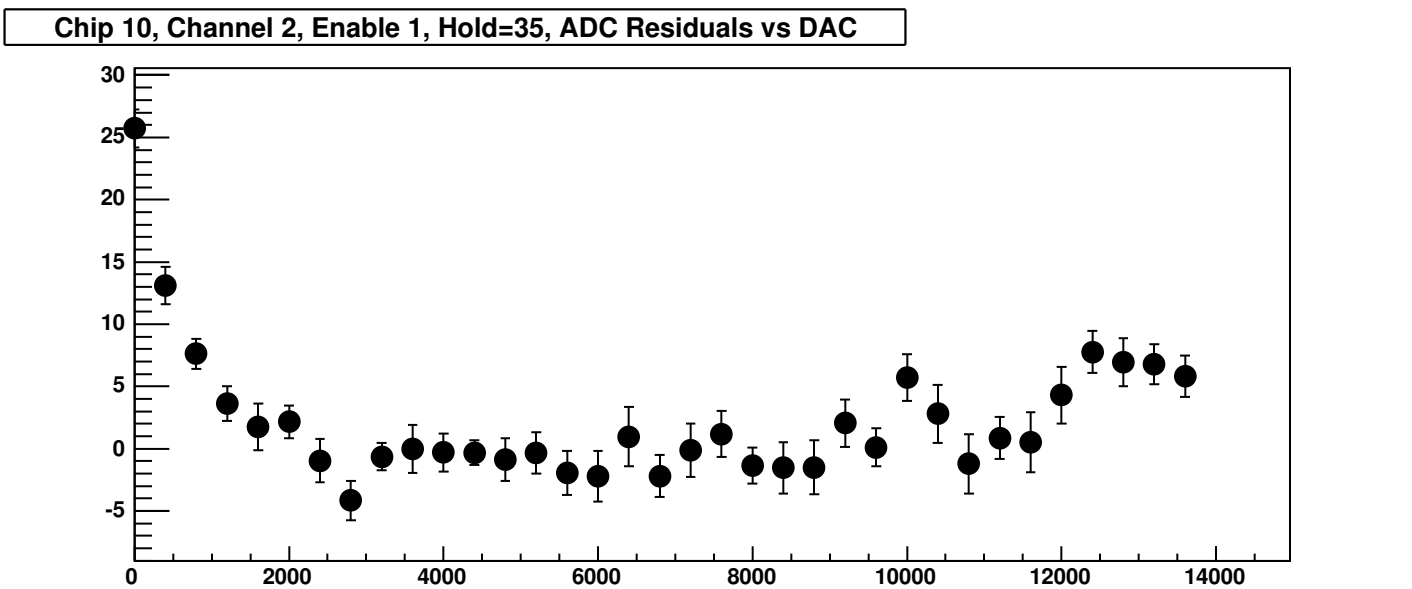


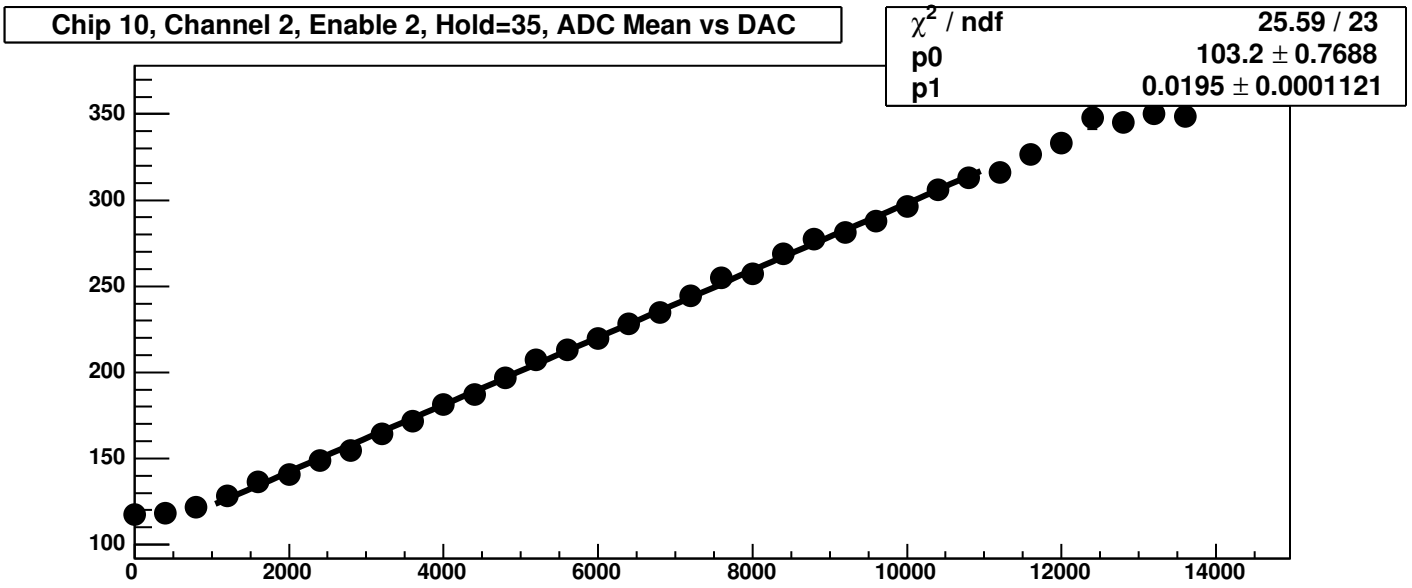
Chip 10, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC

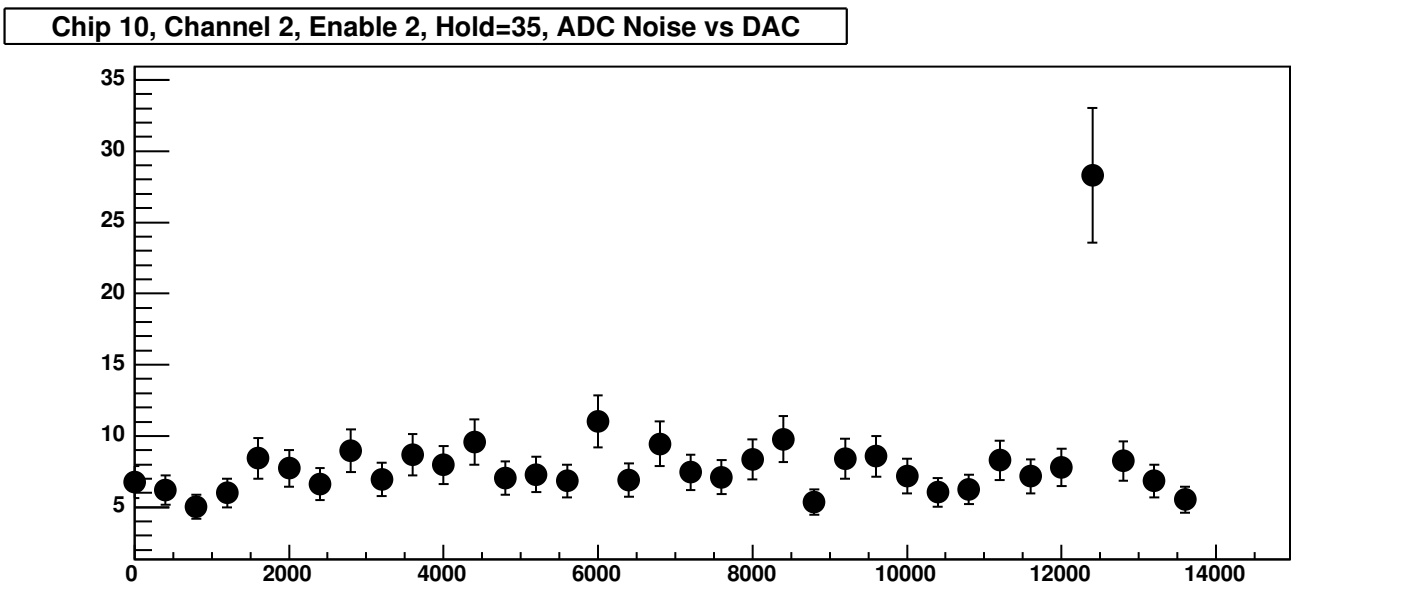


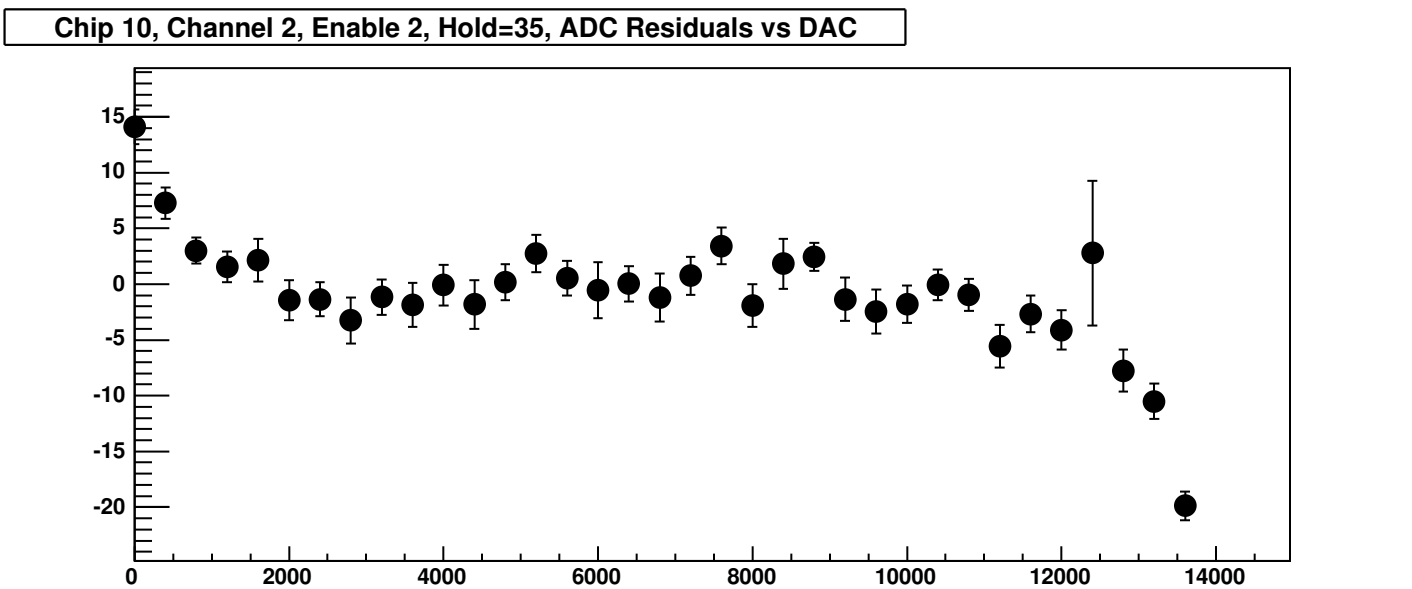

Chip 10, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.85 / 23
p0 97.33 ± 0.7154
p1 0.02978 ± 0.0001193
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

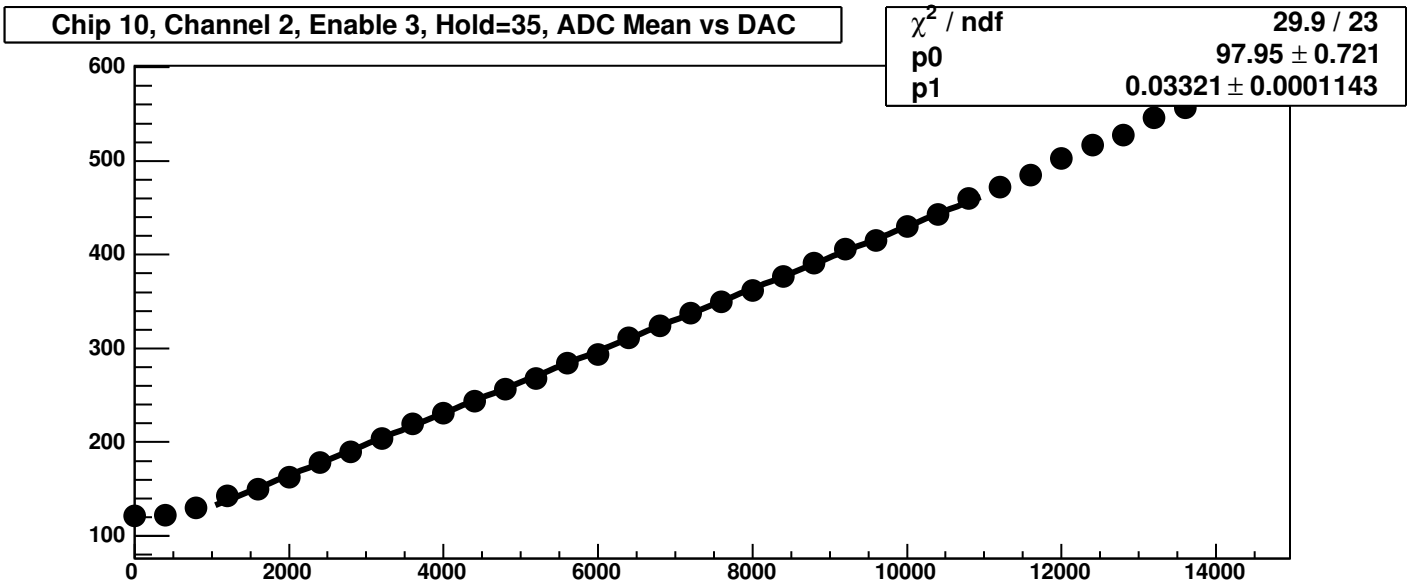

Chip 10, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

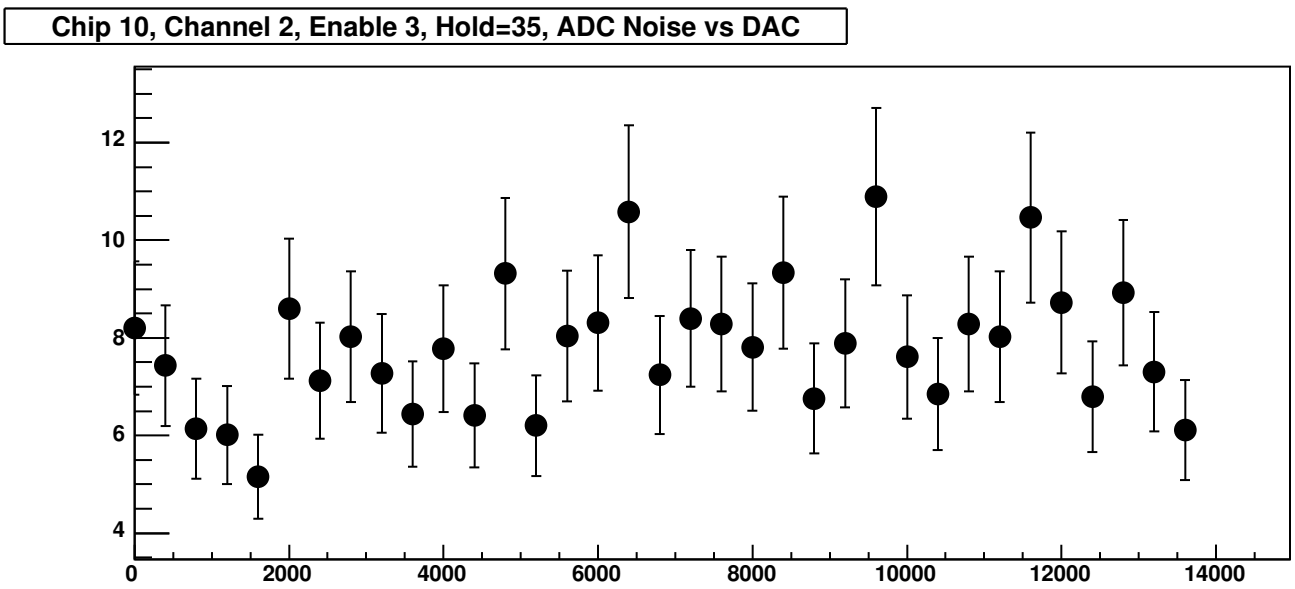

Chip 10, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

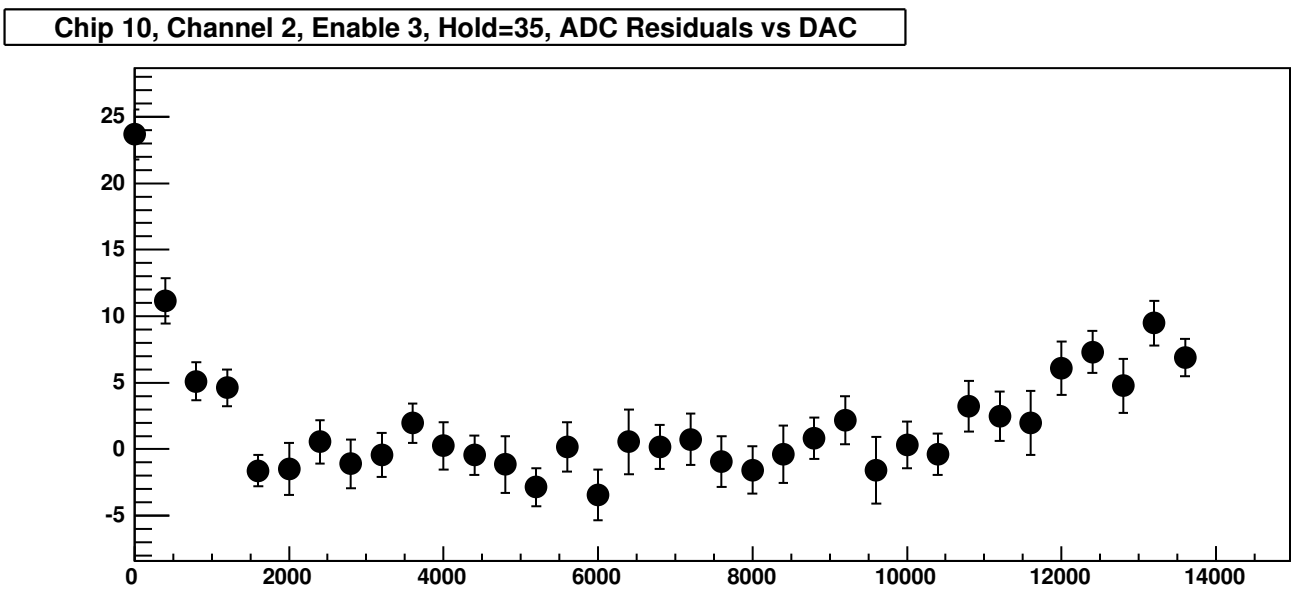

Chip 10, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.59 / 23
p0 103.2 ± 0.7688
p1 0.0195 ± 0.0001121
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

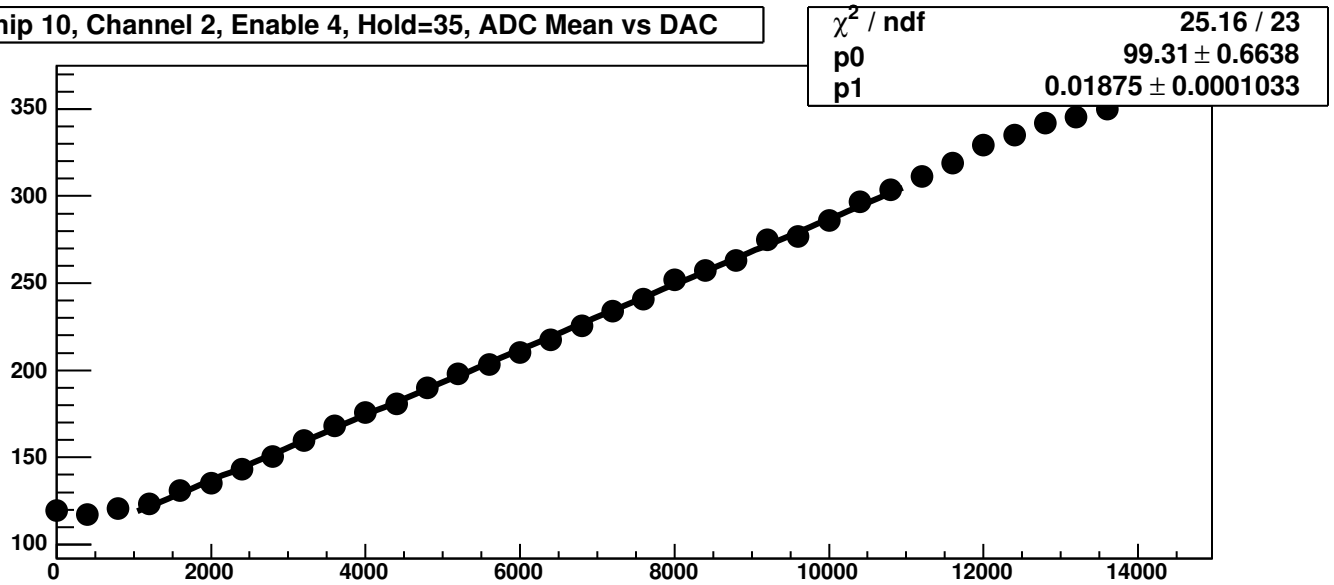

Chip 10, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.9 / 23
p0 97.95 ± 0.721
p1 0.03321 ± 0.0001143
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

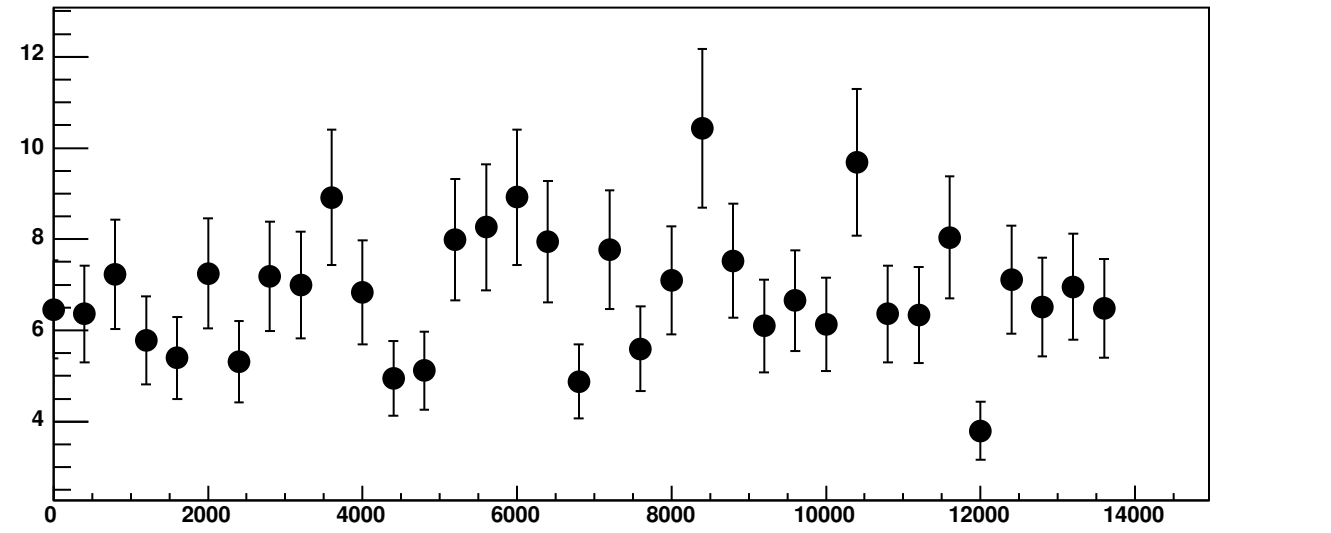

Chip 10, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

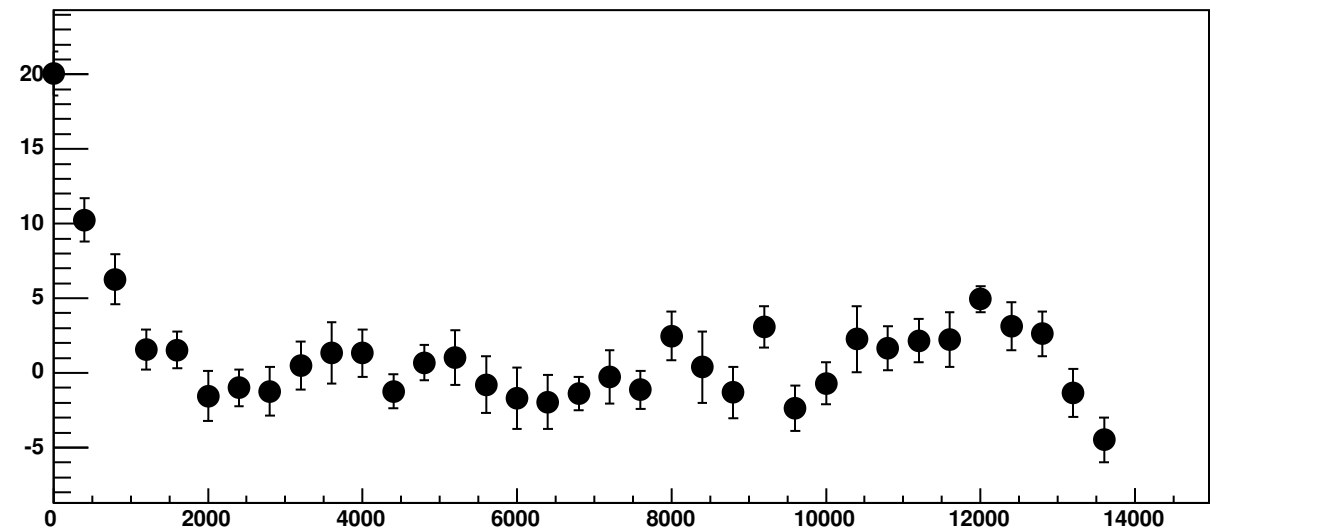
Chip 10, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

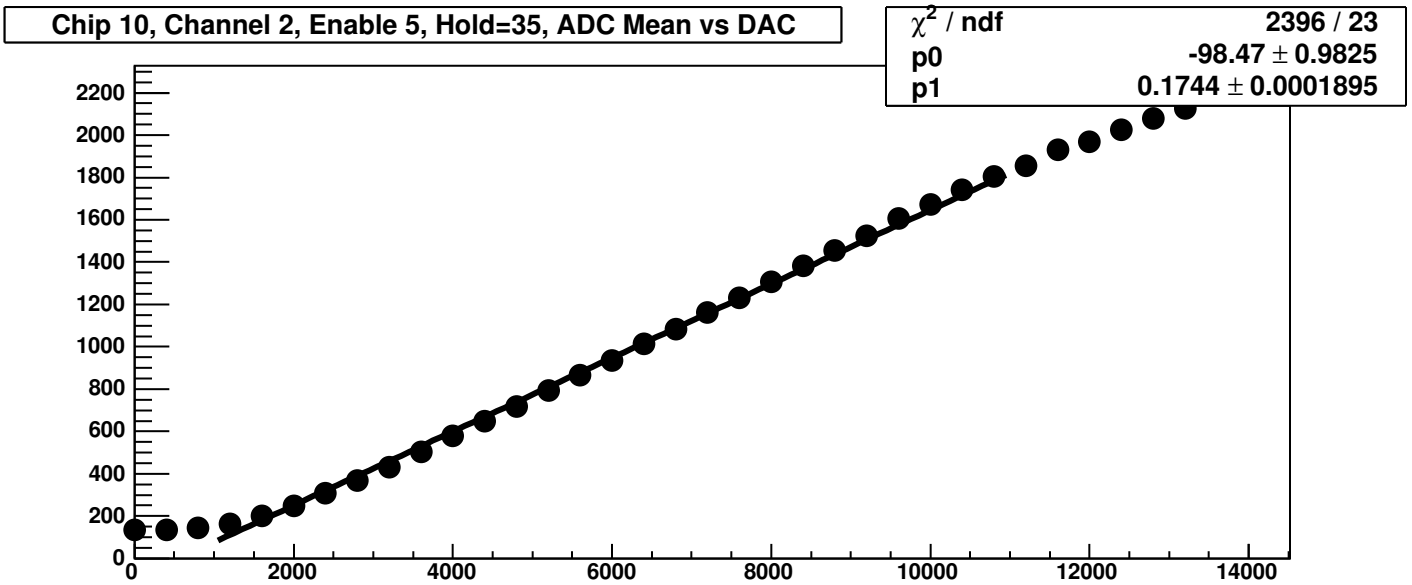


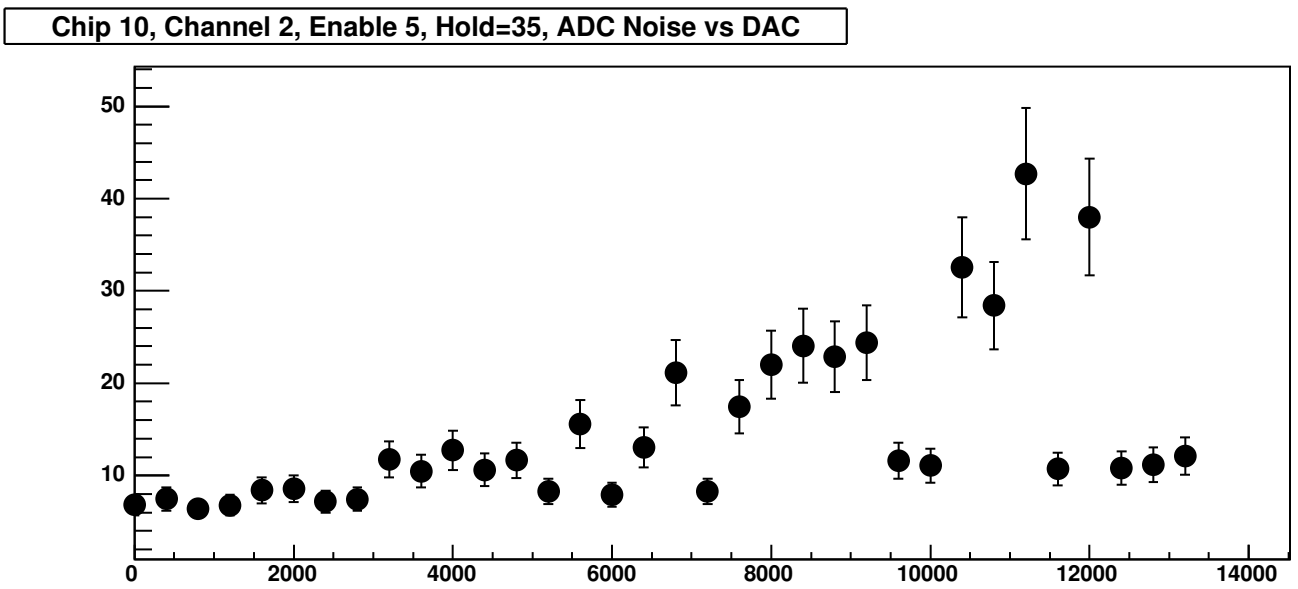
Chip 10, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

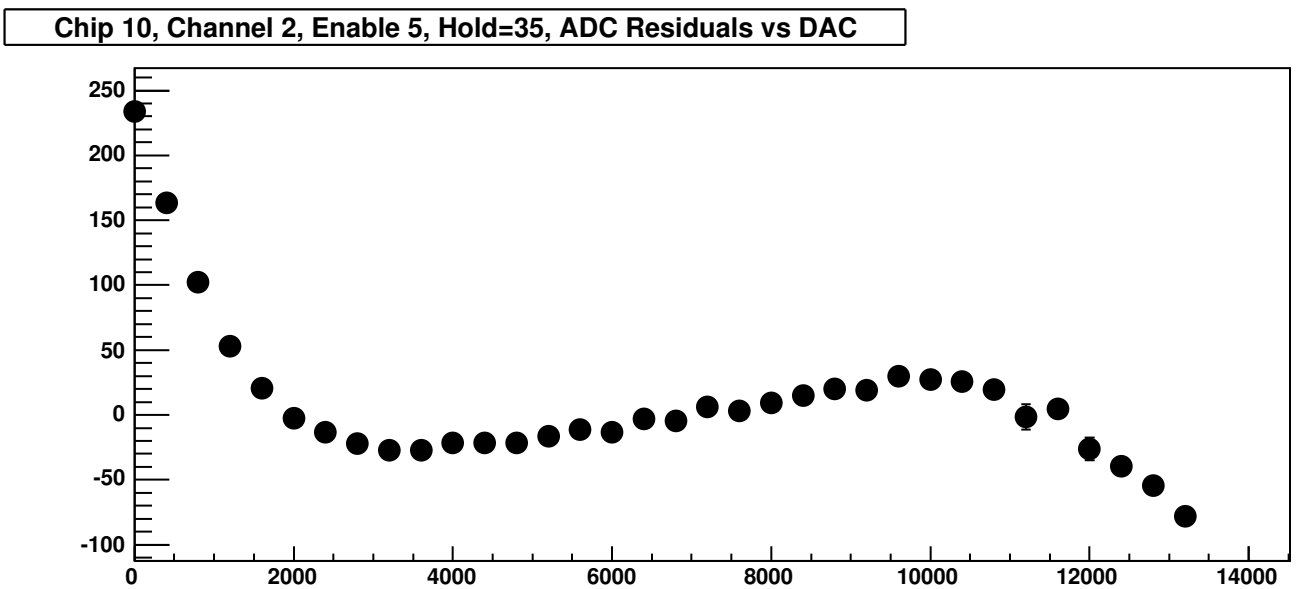


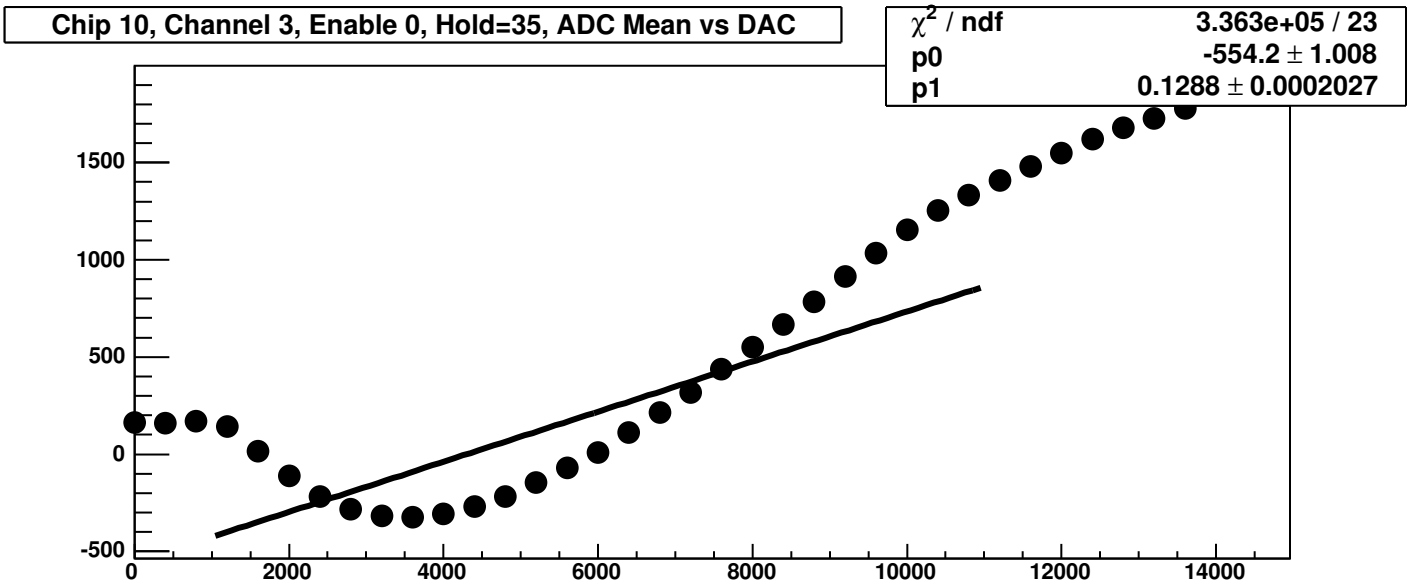
Chip 10, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

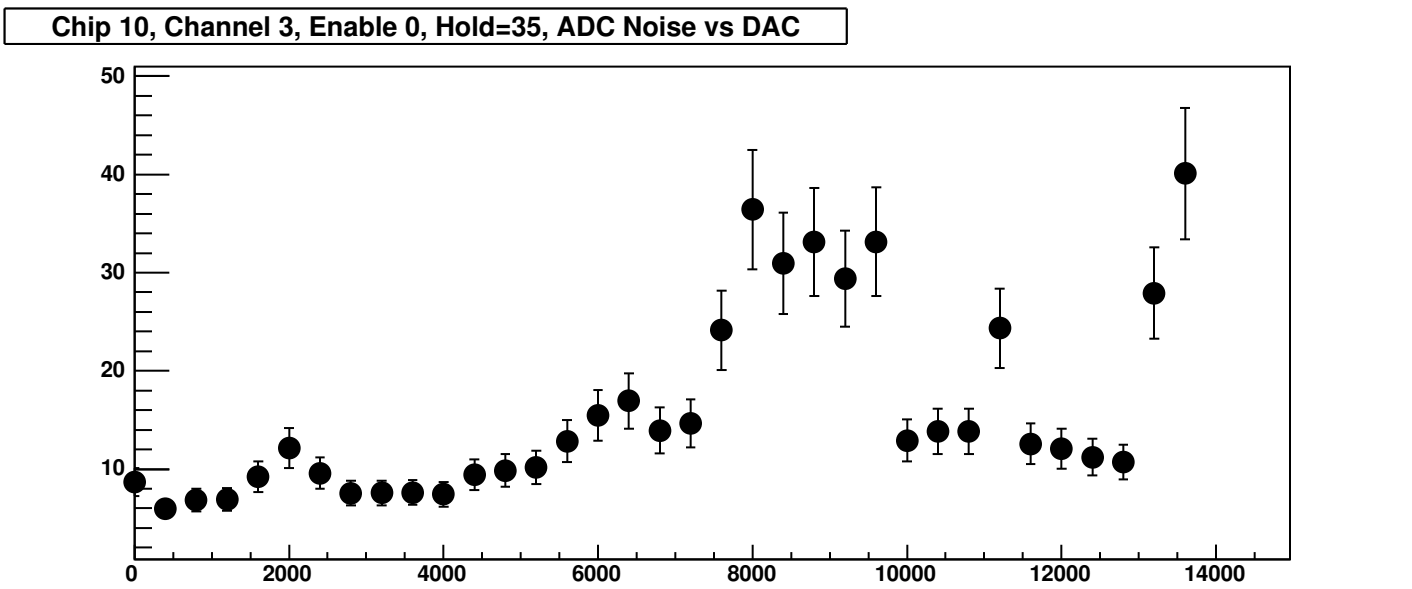


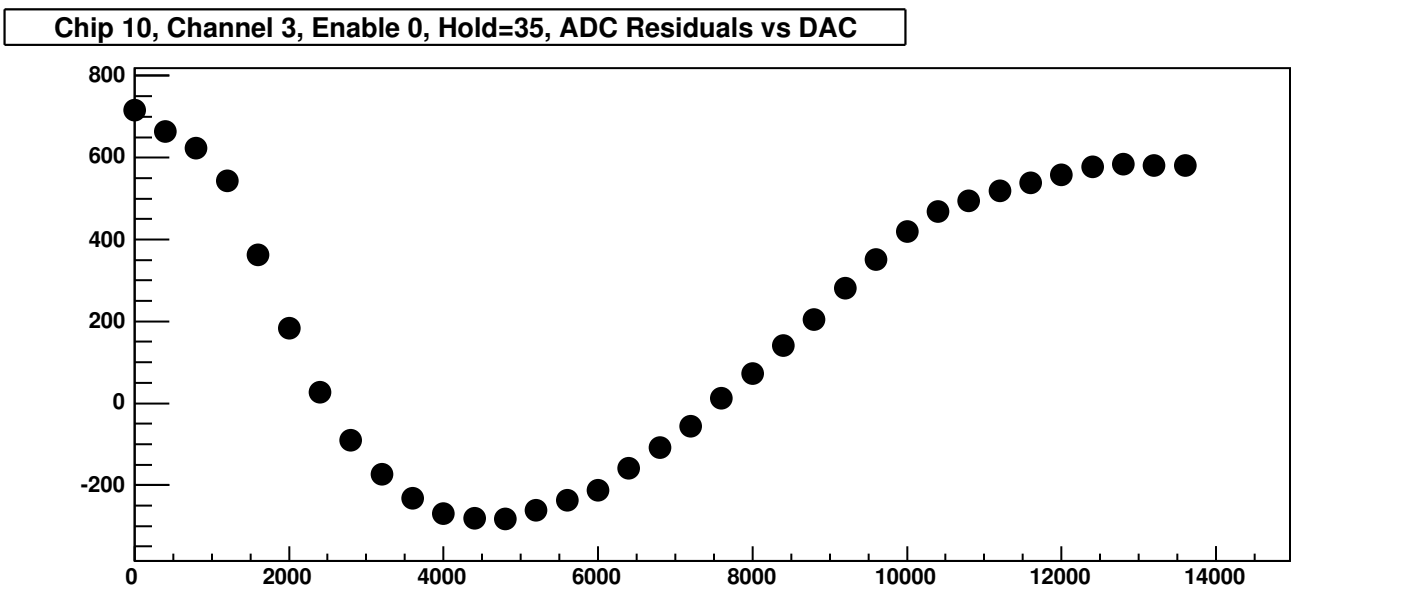

Chip 10, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2396 / 23
p0 -98.47 ± 0.9825
p1 0.1744 ± 0.0001895
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

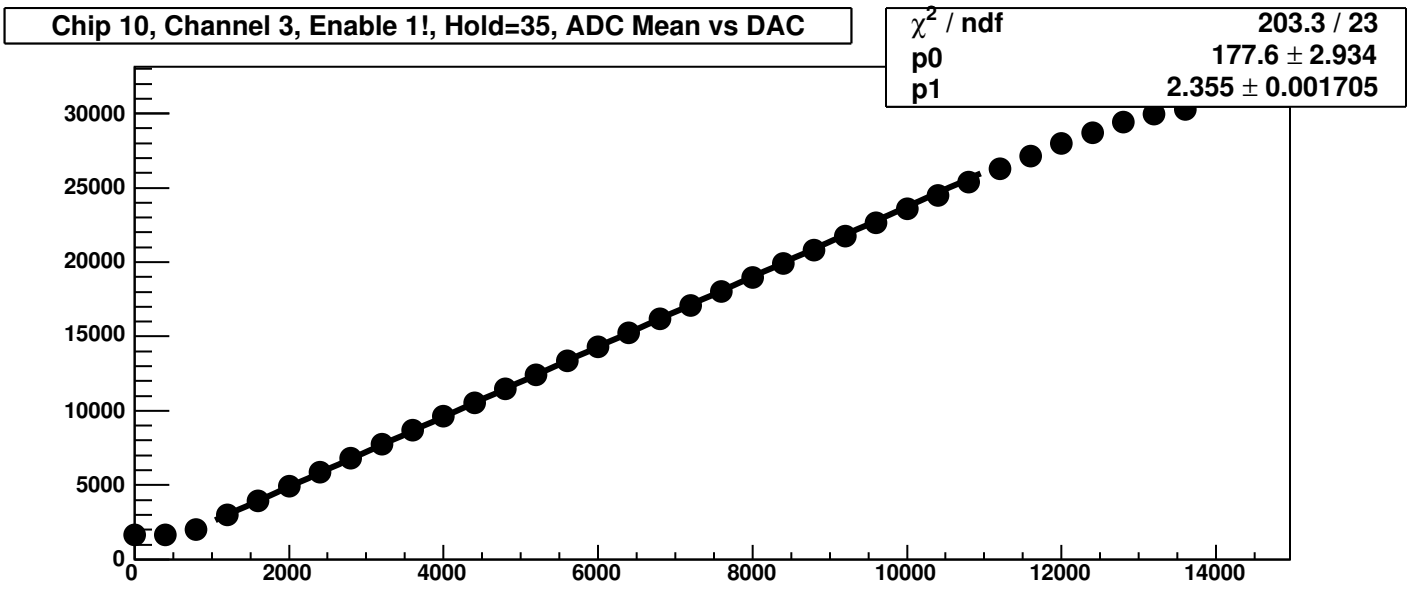

Chip 10, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

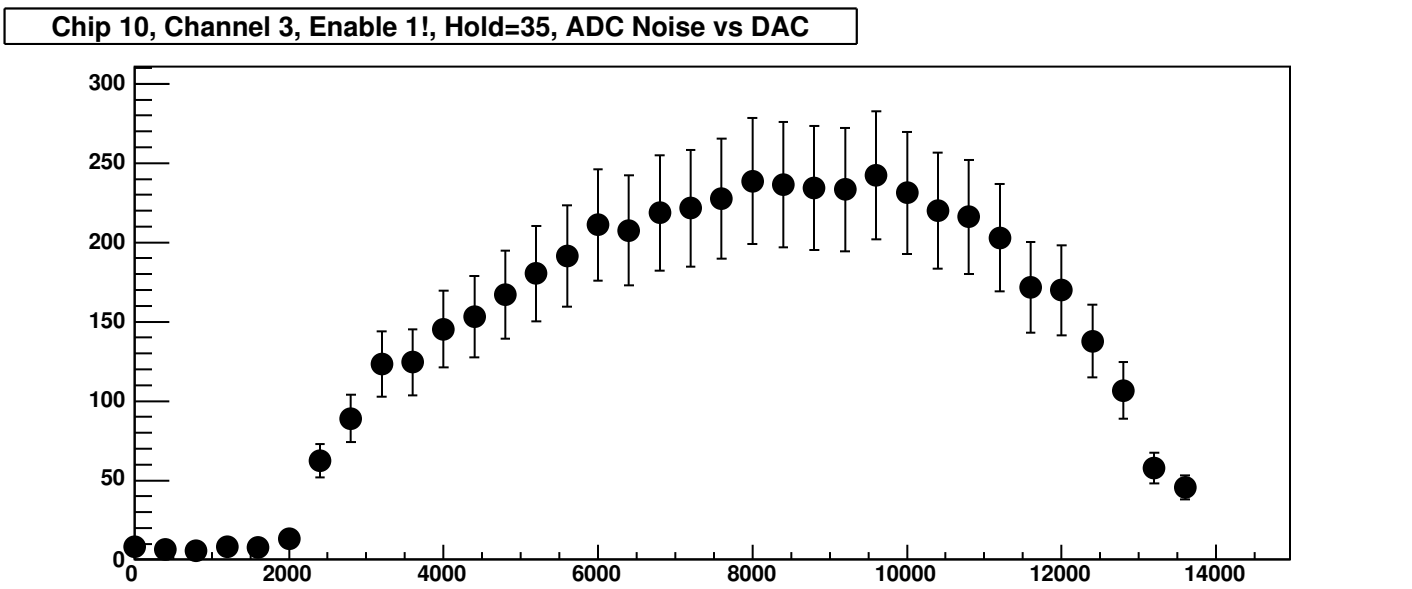

Chip 10, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

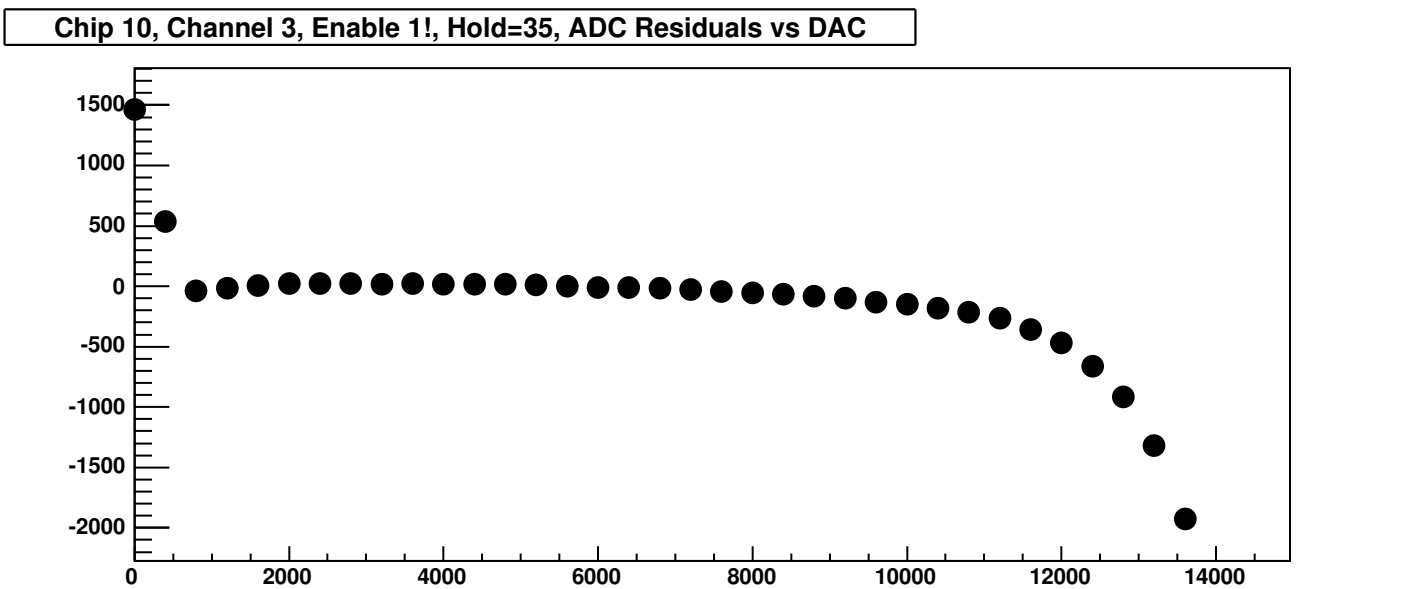

Chip 10, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.363e+05 / 23
p0 -554.2 ± 1.008
p1 0.1288 ± 0.0002027
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

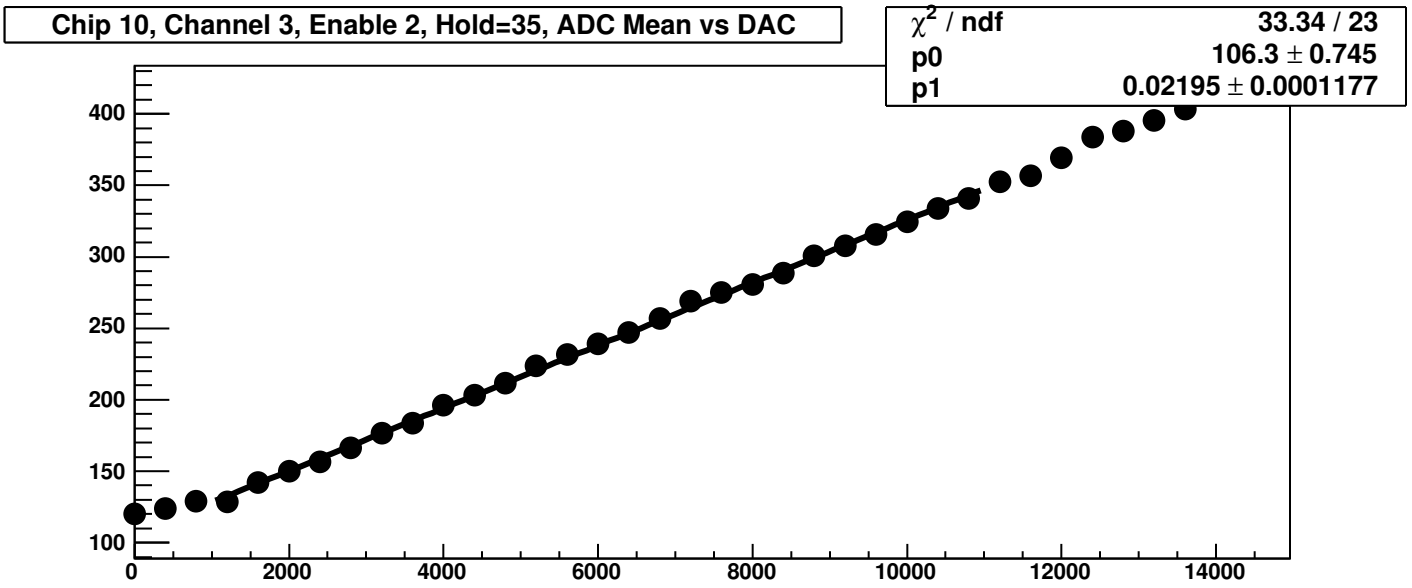

Chip 10, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

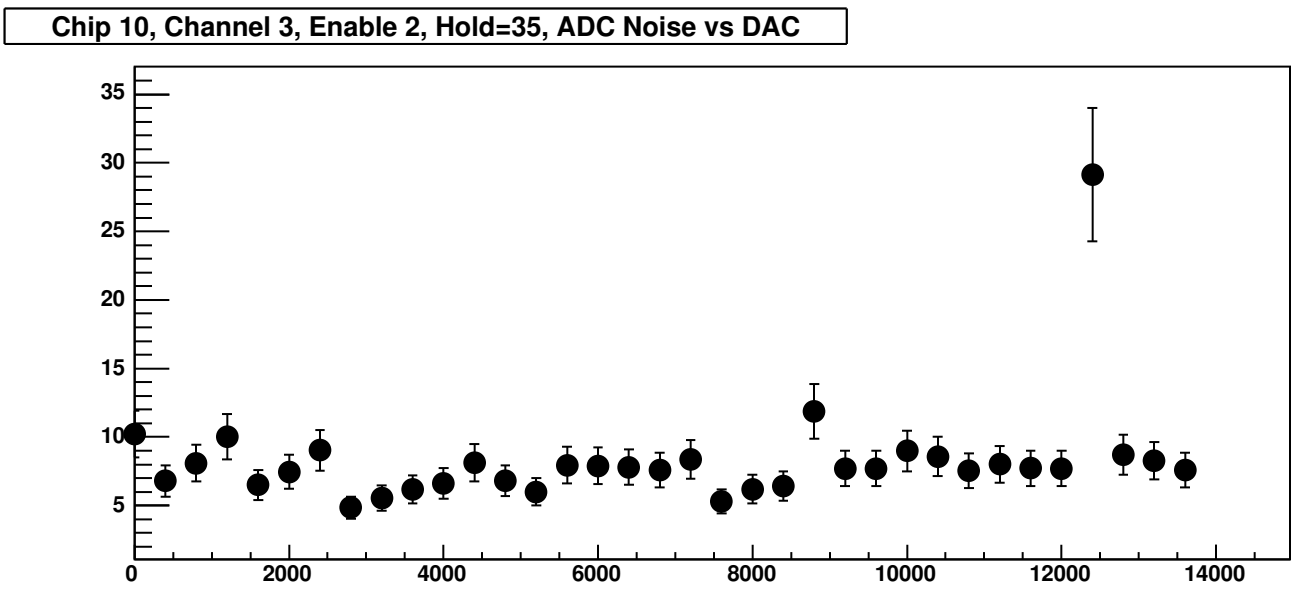

Chip 10, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

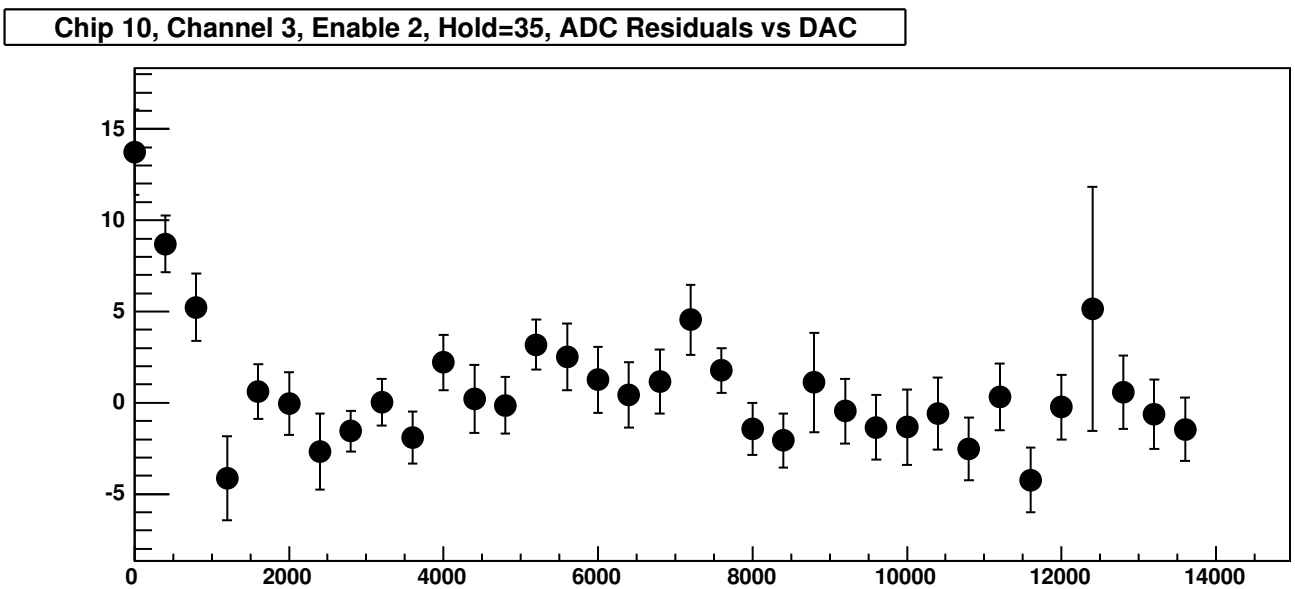

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 203.3 / 23
p0 177.6 ± 2.934
p1 2.355 ± 0.001705
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

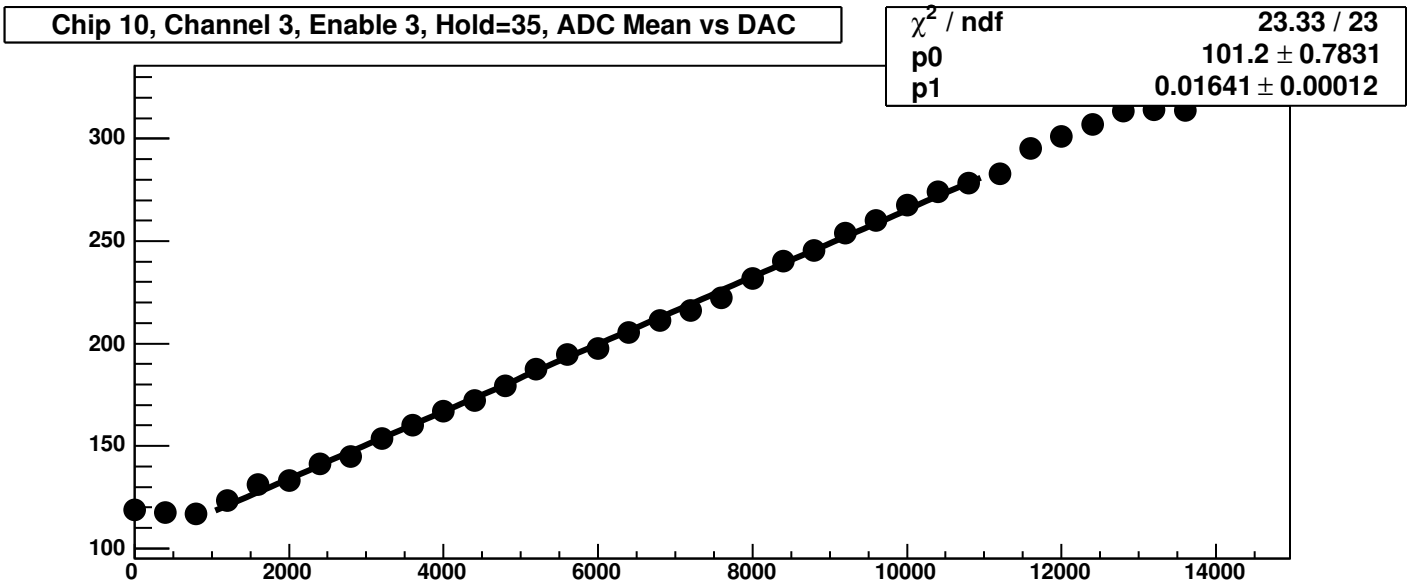

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

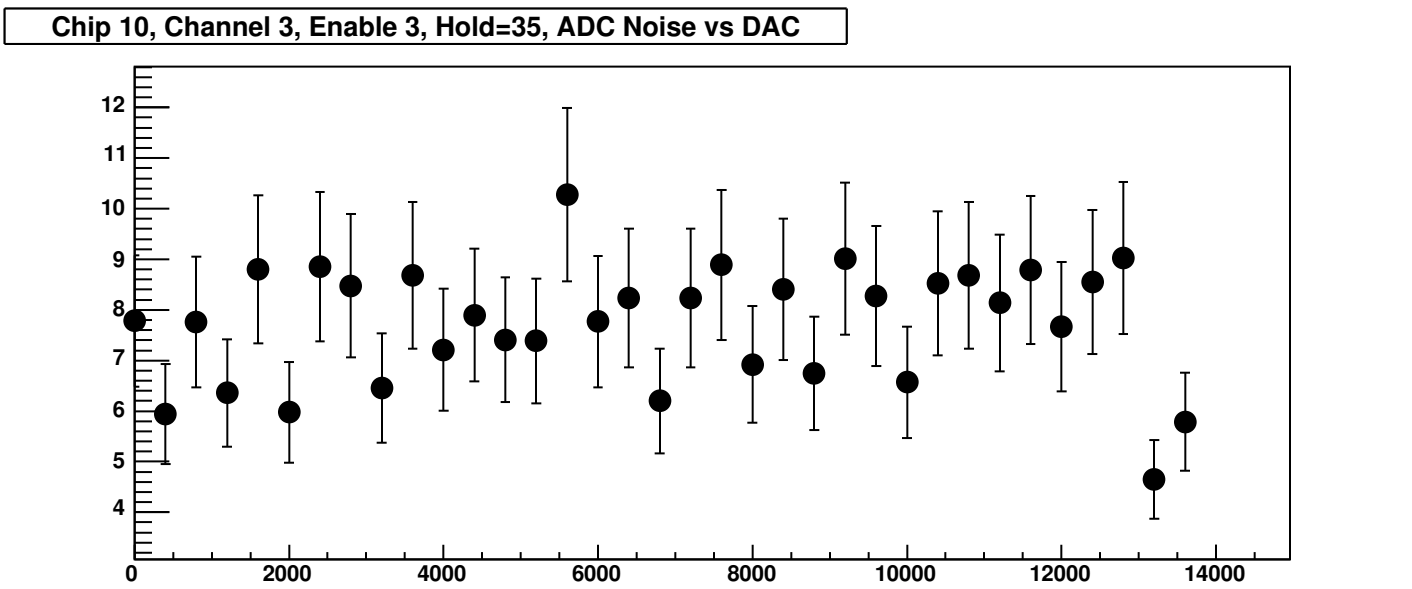

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

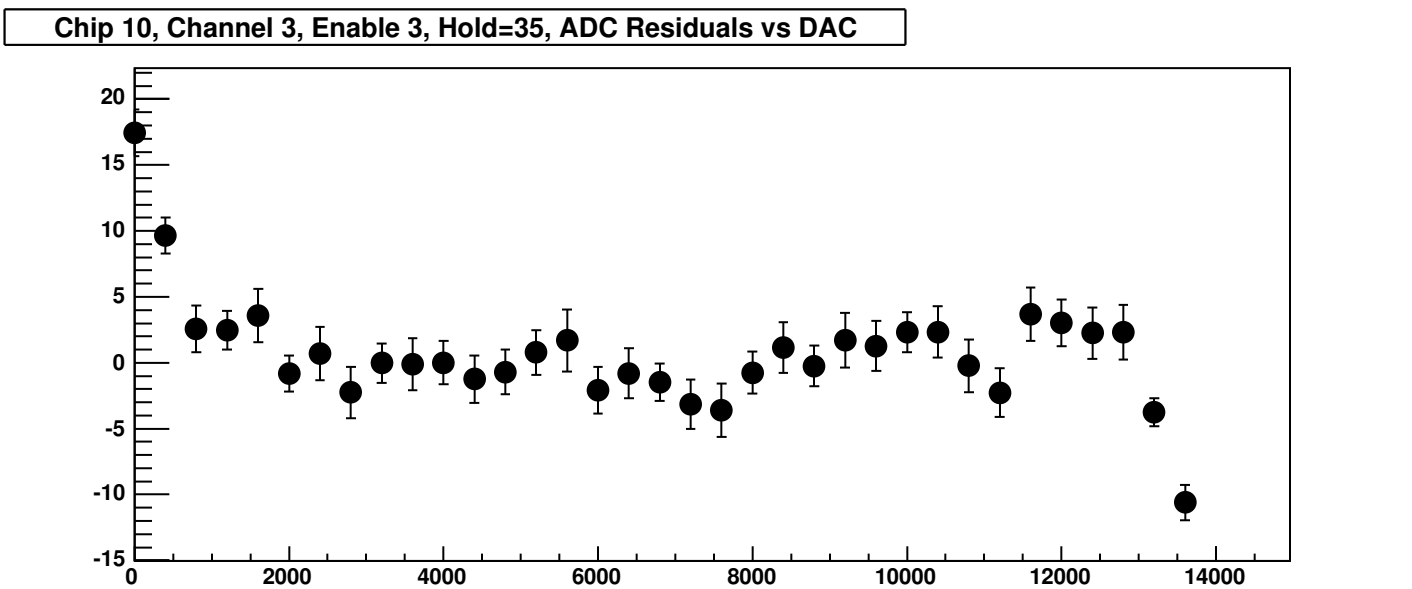

Chip 10, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.34 / 23
p0 106.3 ± 0.745
p1 0.02195 ± 0.0001177
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

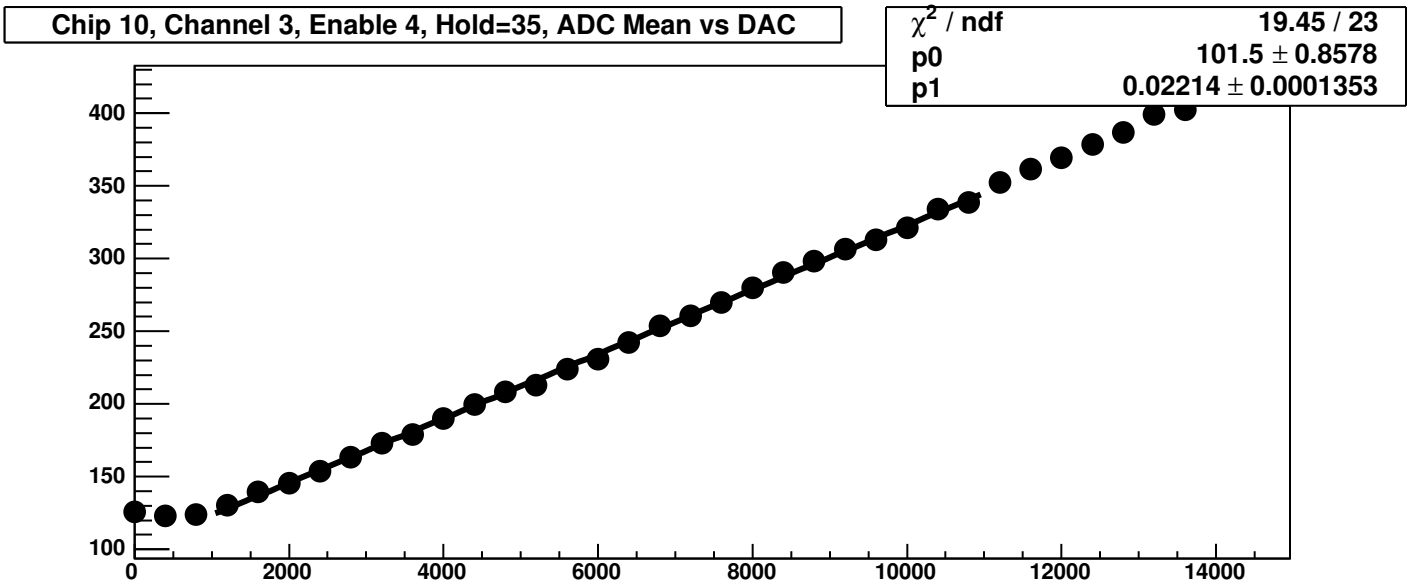

Chip 10, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

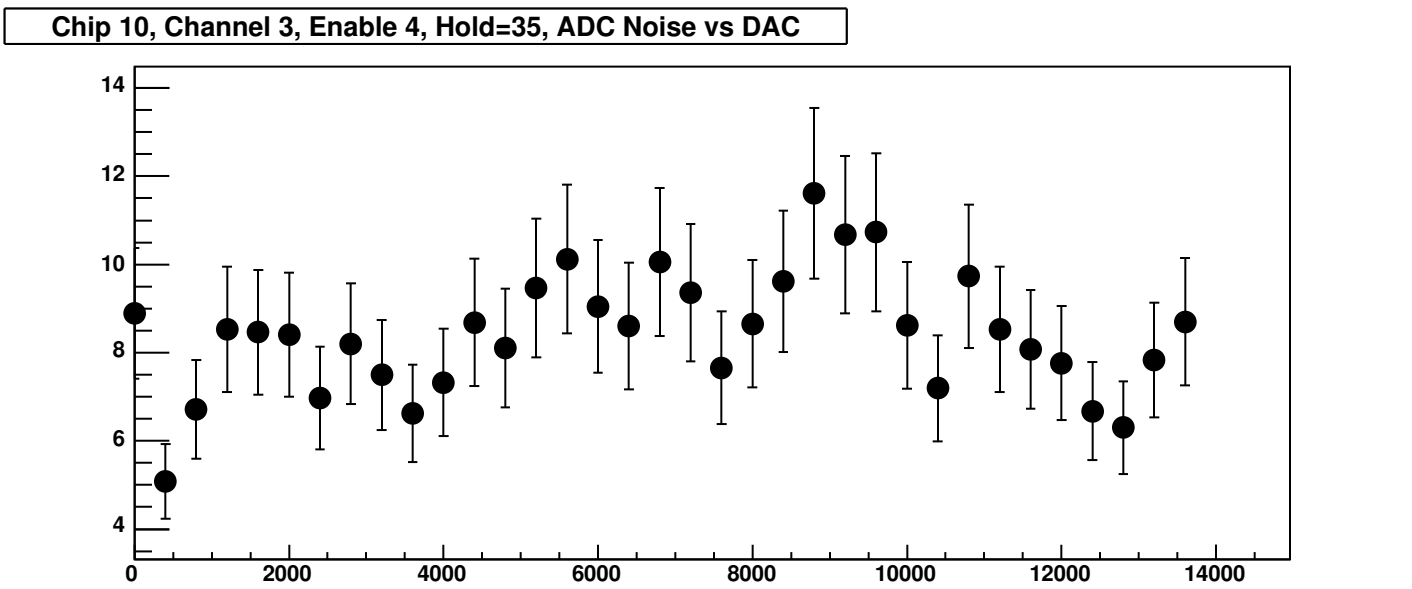

Chip 10, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

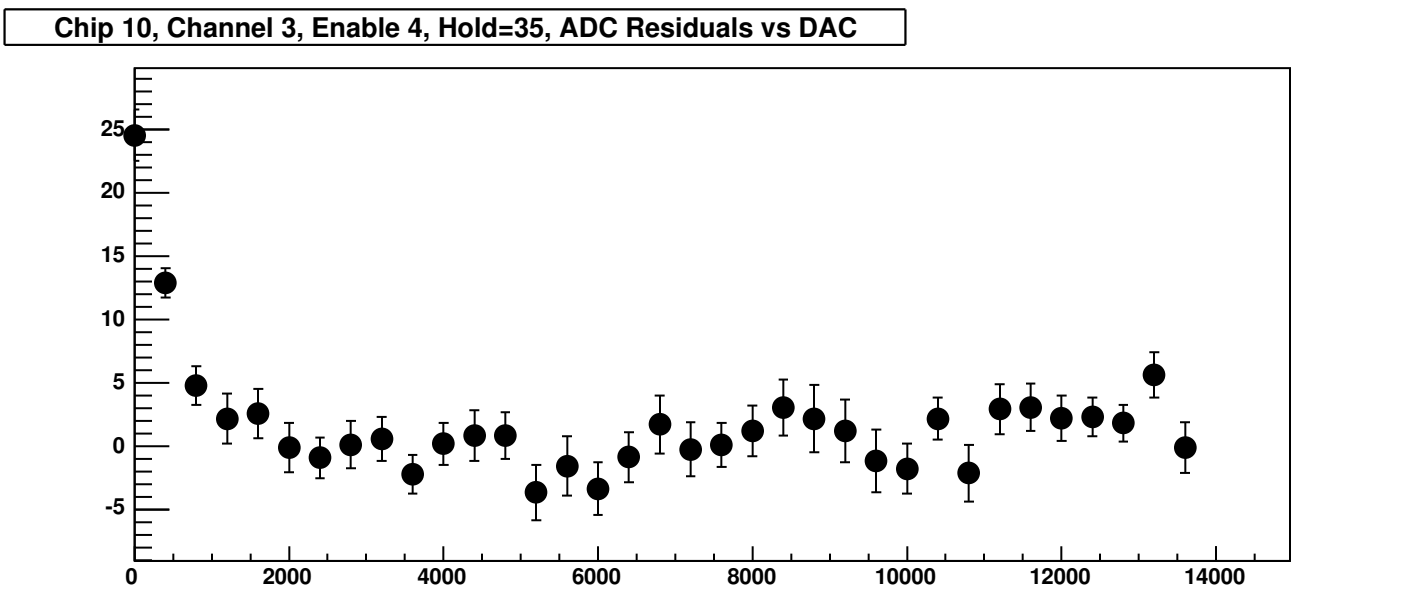

Chip 10, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.33 / 23
p0 101.2 ± 0.7831
p1 0.01641 ± 0.00012
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

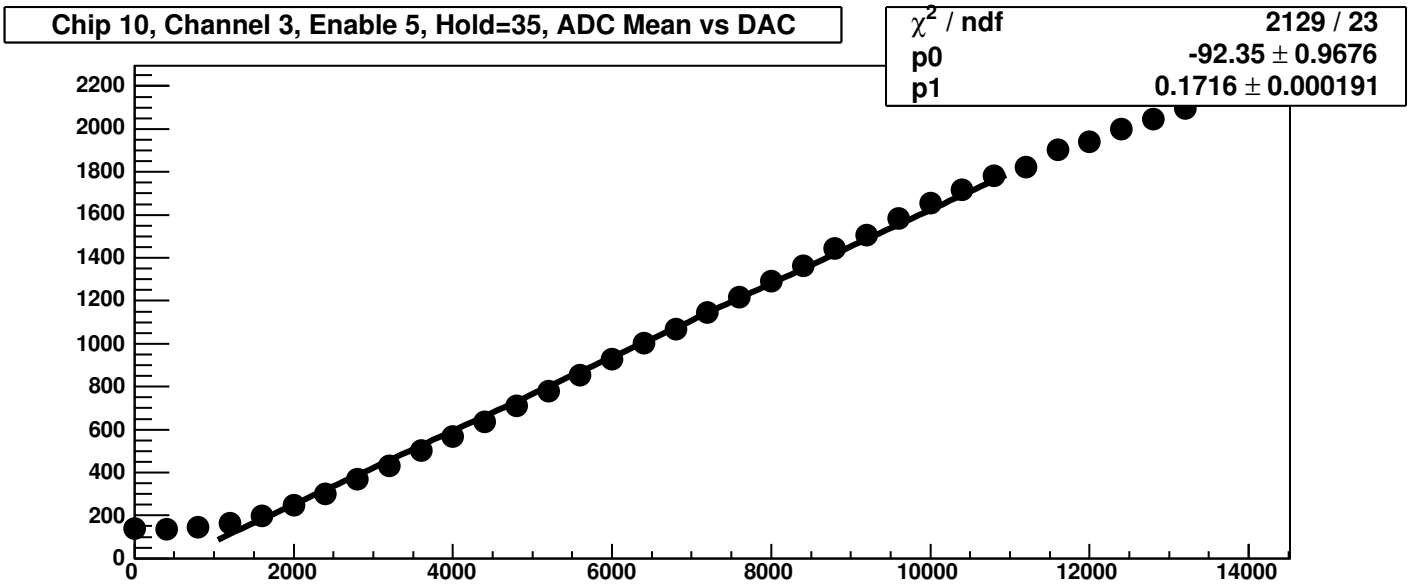

Chip 10, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

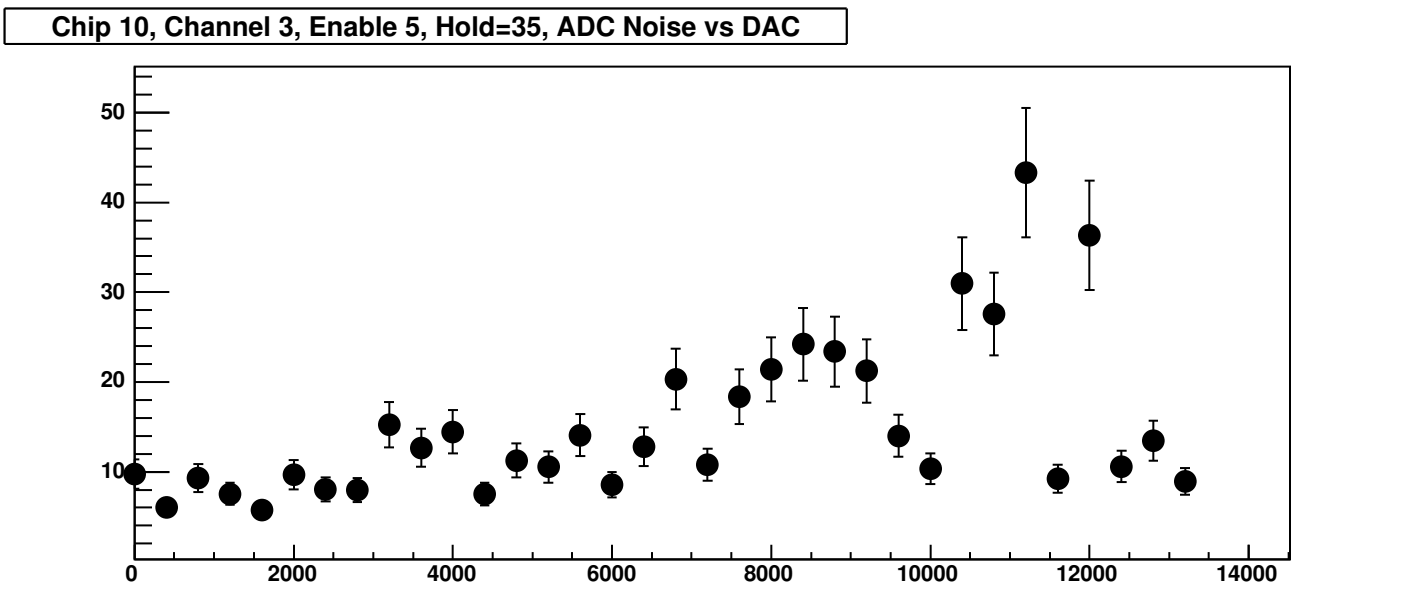

Chip 10, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

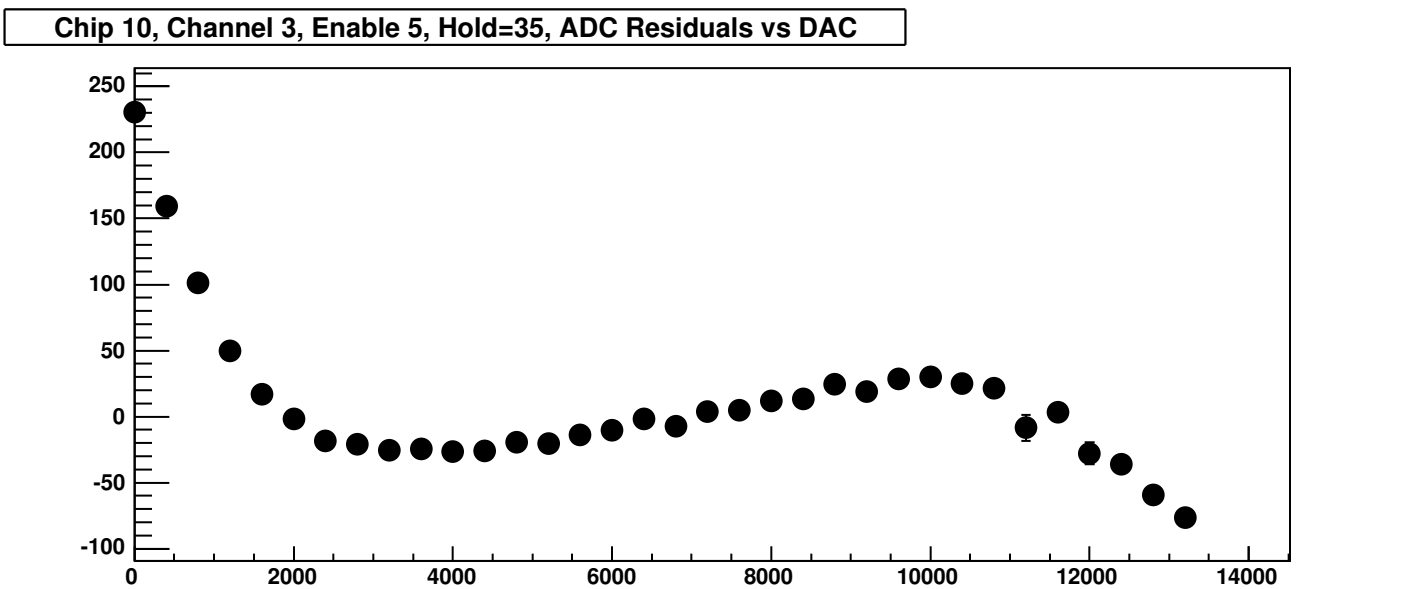

Chip 10, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.45 / 23
p0 101.5 ± 0.8578
p1 0.02214 ± 0.0001353
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

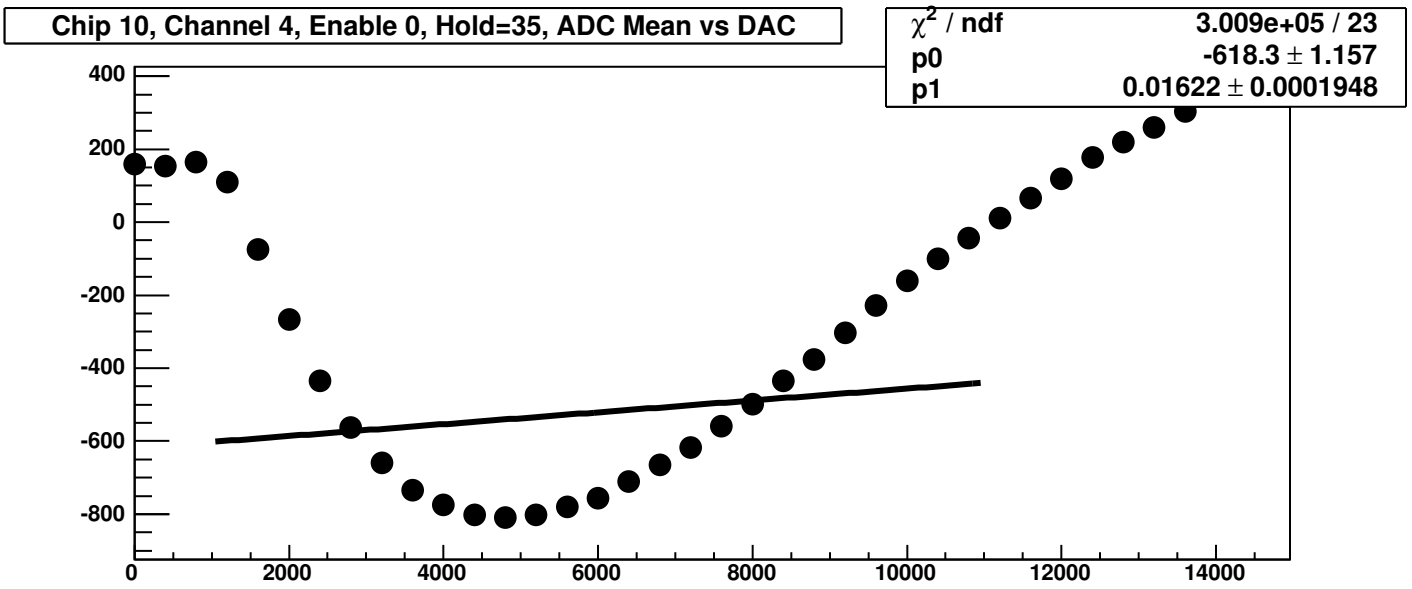

Chip 10, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

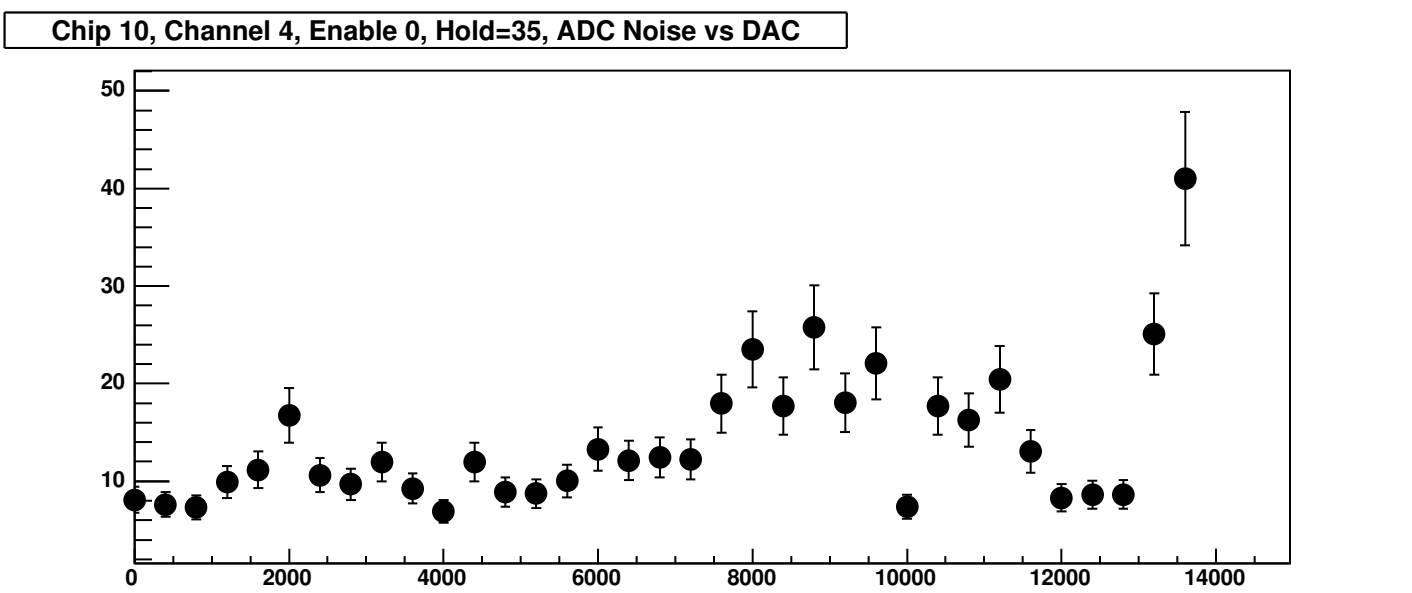

Chip 10, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

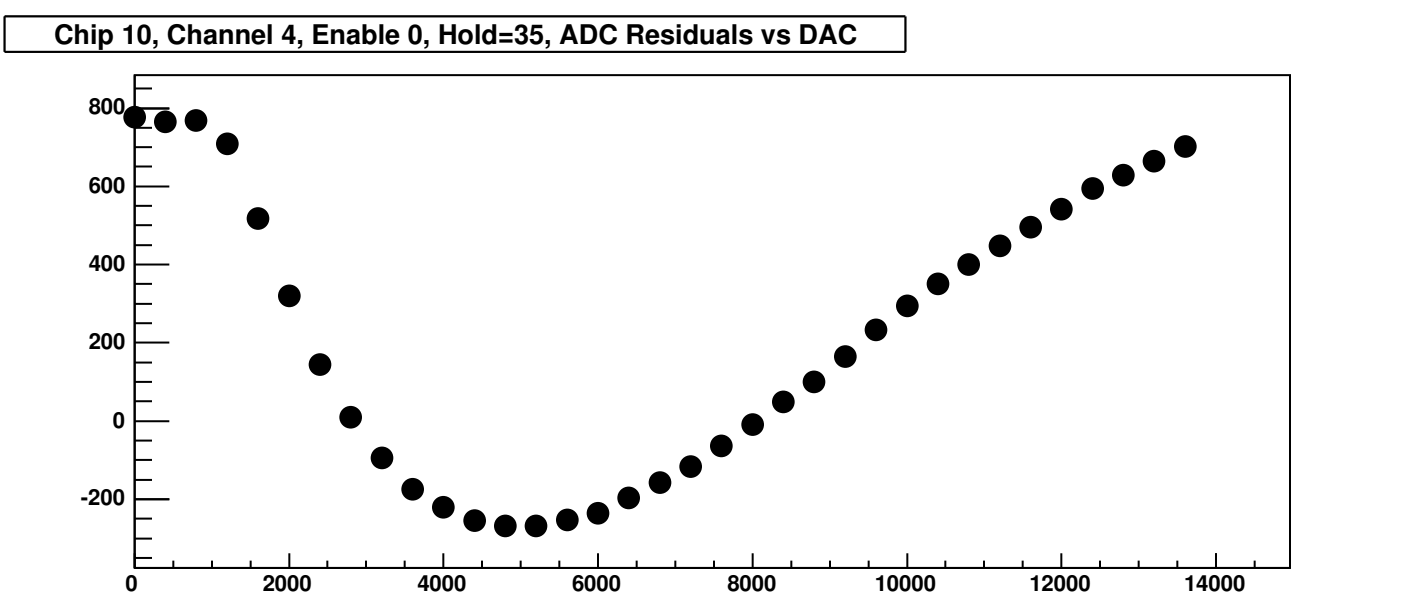

Chip 10, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2129 / 23
p0 -92.35 ± 0.9676
p1 0.1716 ± 0.000191
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

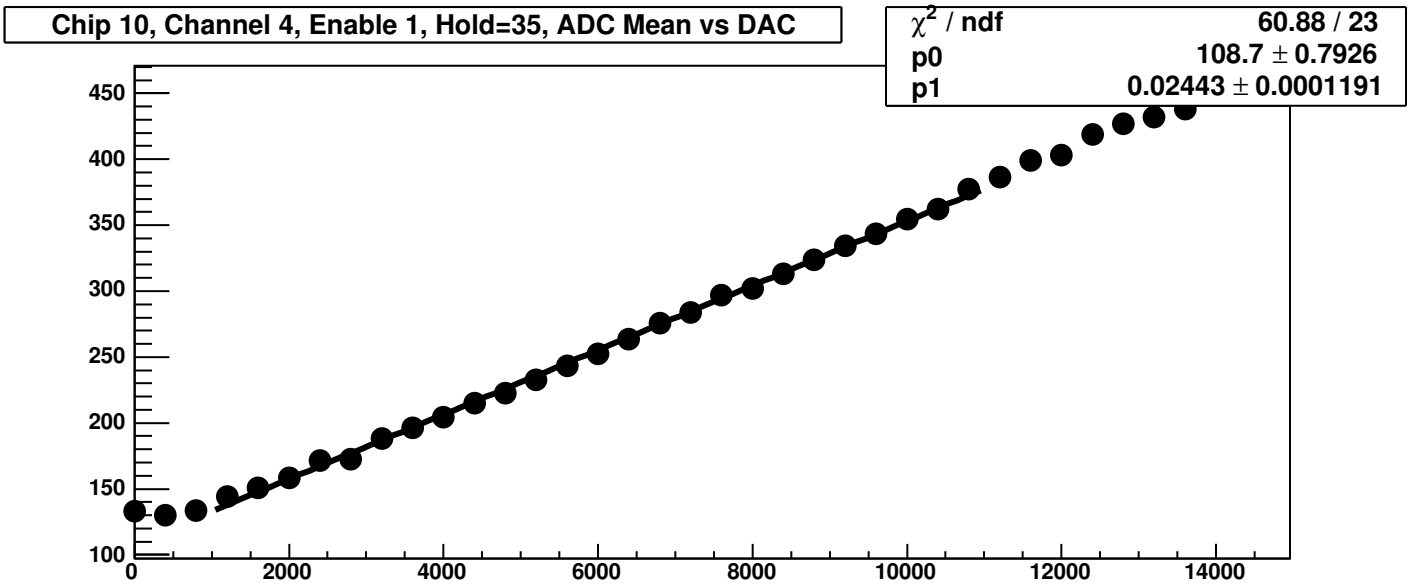

Chip 10, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

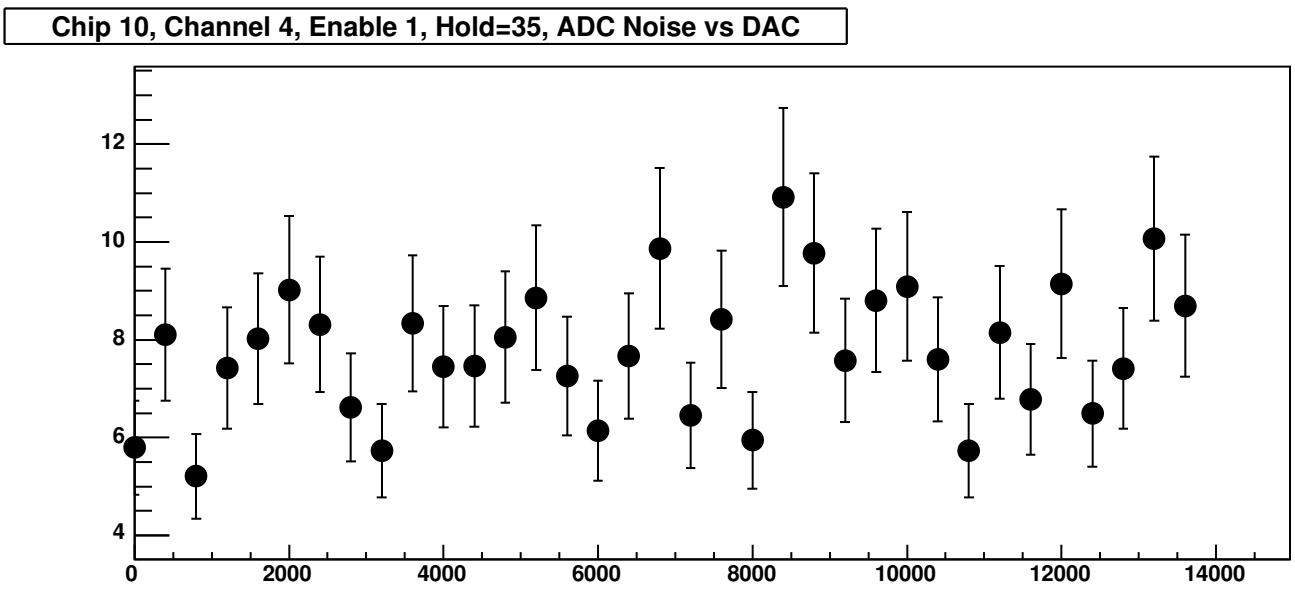

Chip 10, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

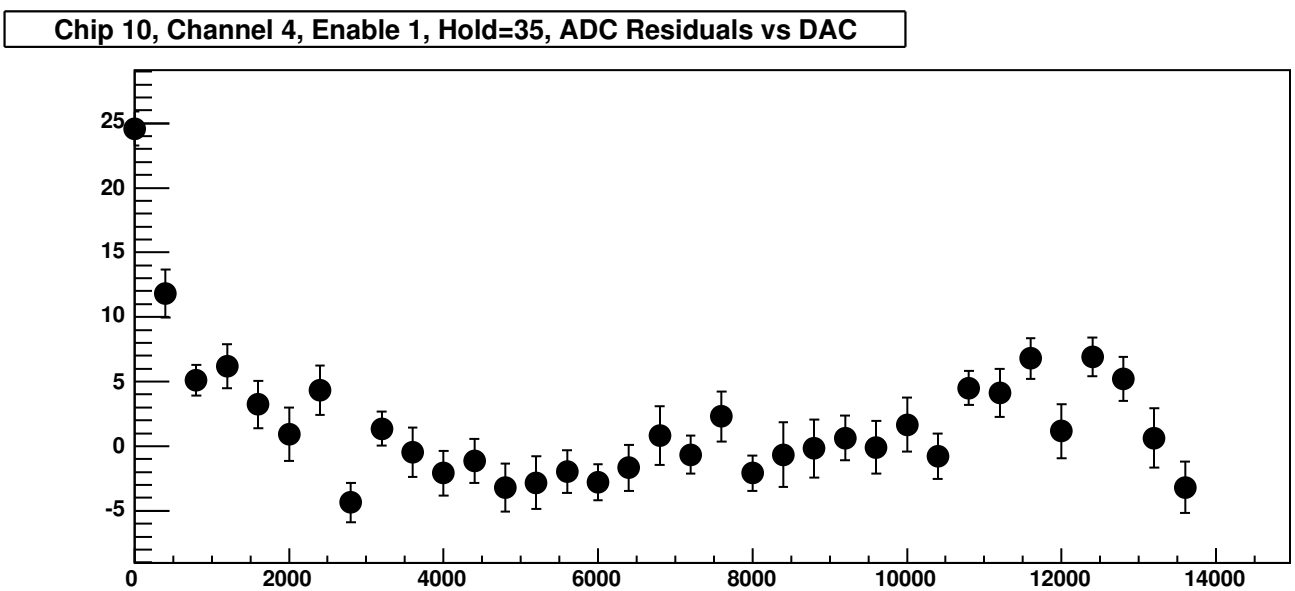

Chip 10, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.009e+05 / 23
p0 -618.3 ± 1.157
p1 0.01622 ± 0.0001948
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

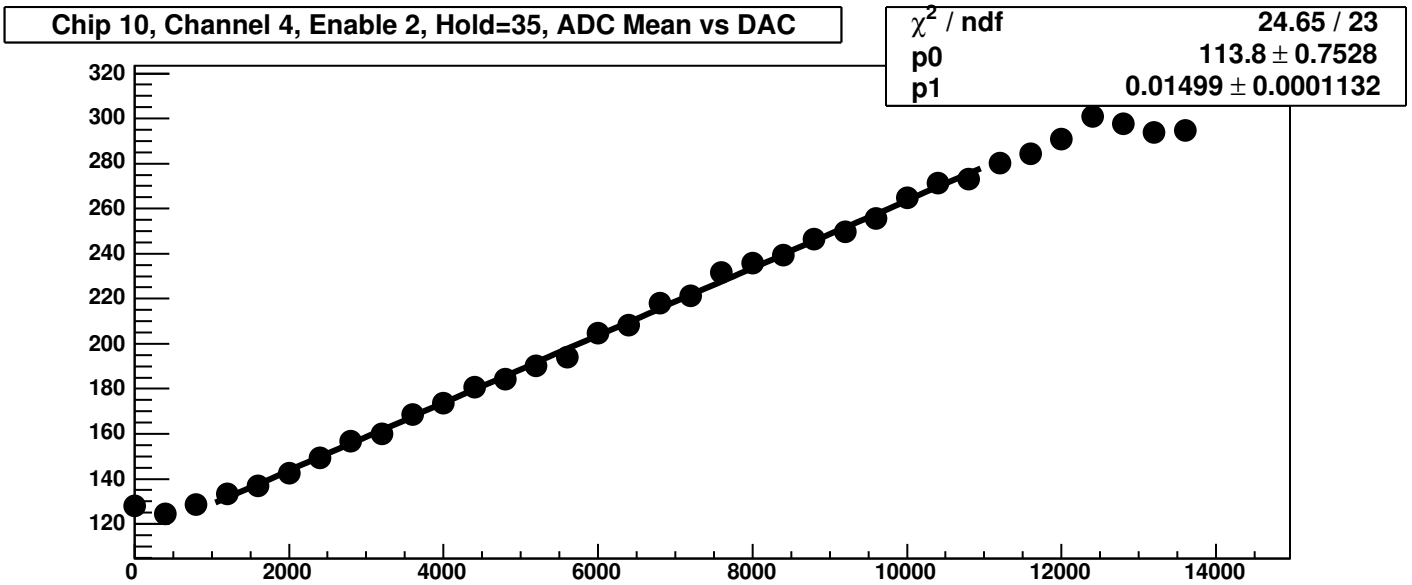

Chip 10, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

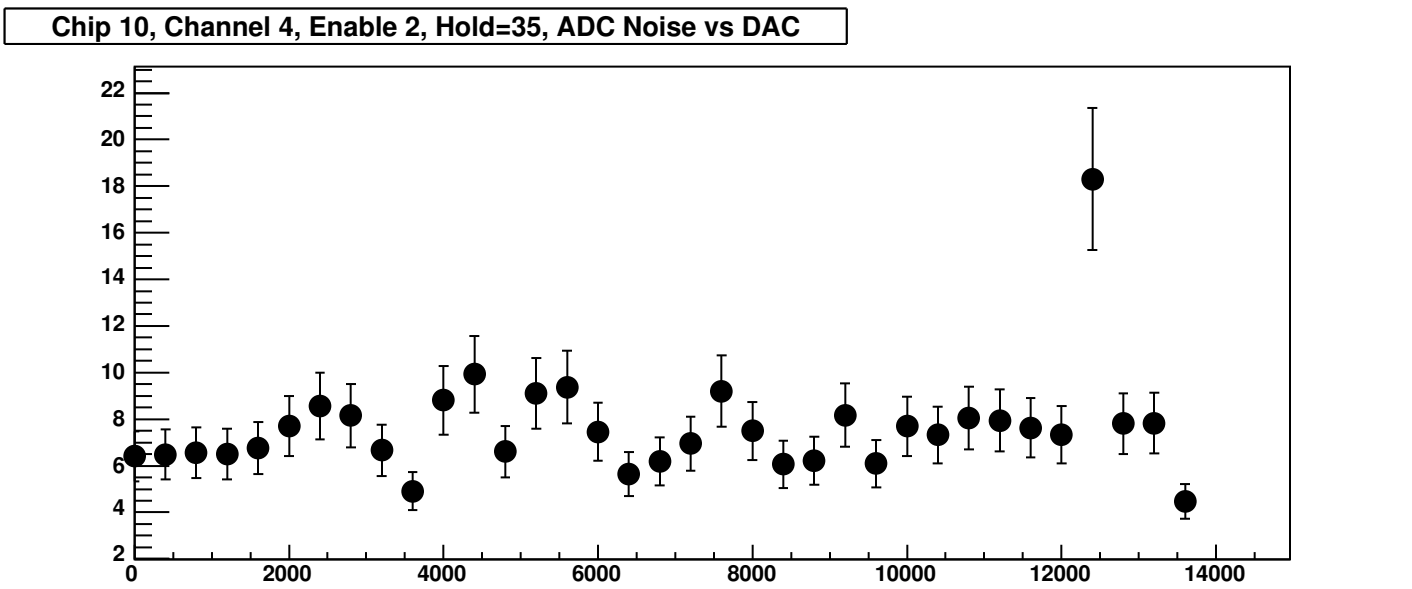

Chip 10, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

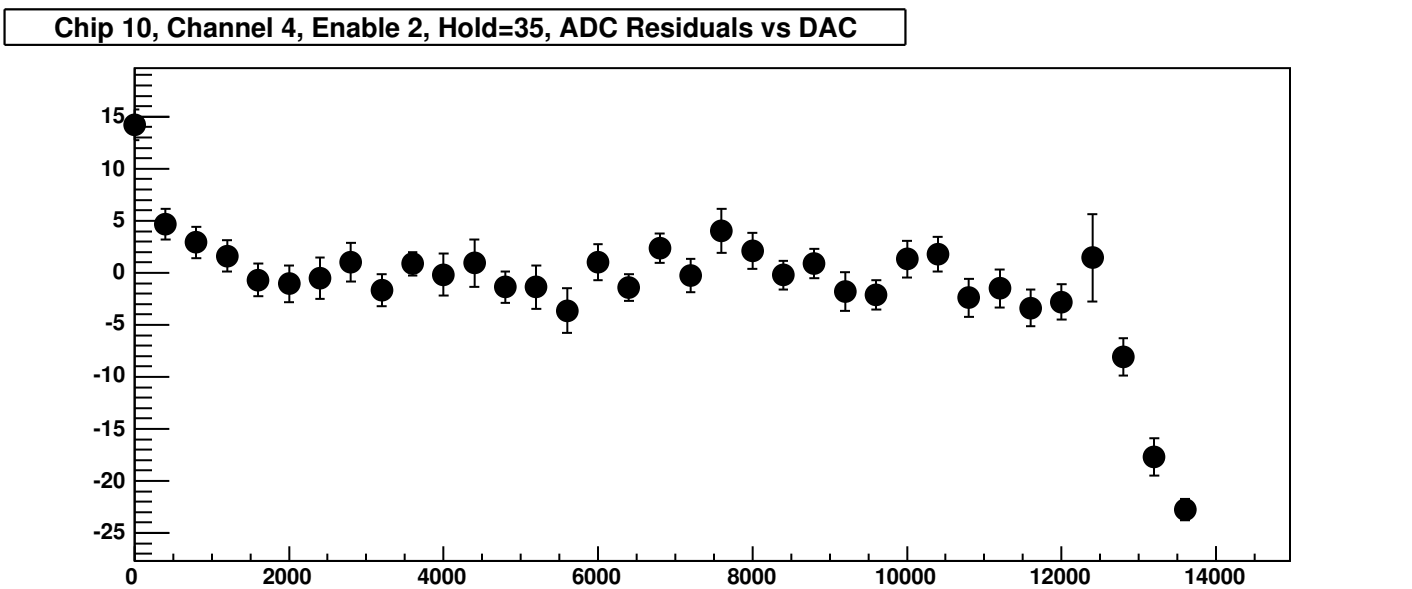

Chip 10, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.88 / 23
p0 108.7 ± 0.7926
p1 0.02443 ± 0.0001191
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

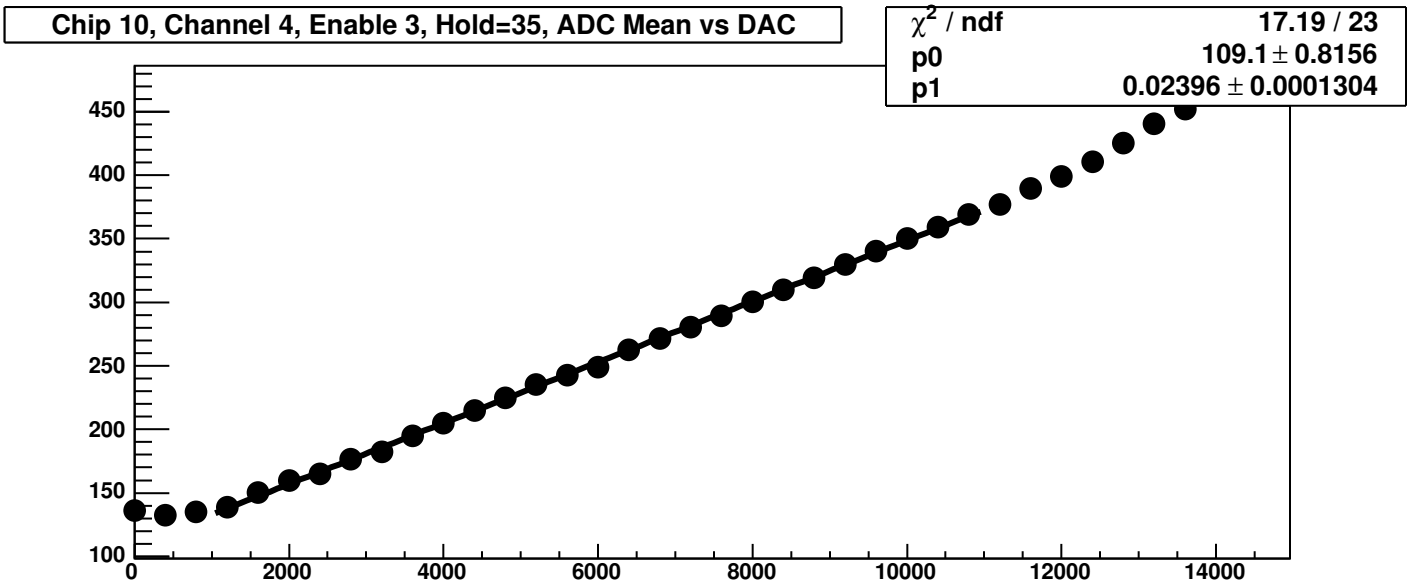

Chip 10, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

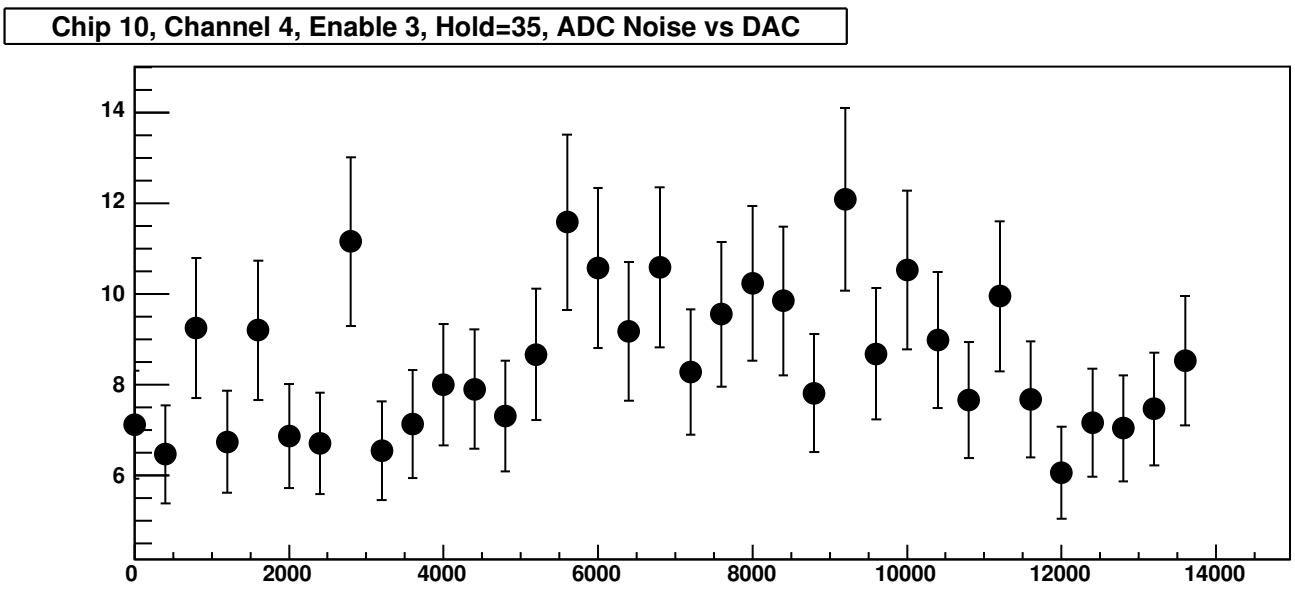

Chip 10, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

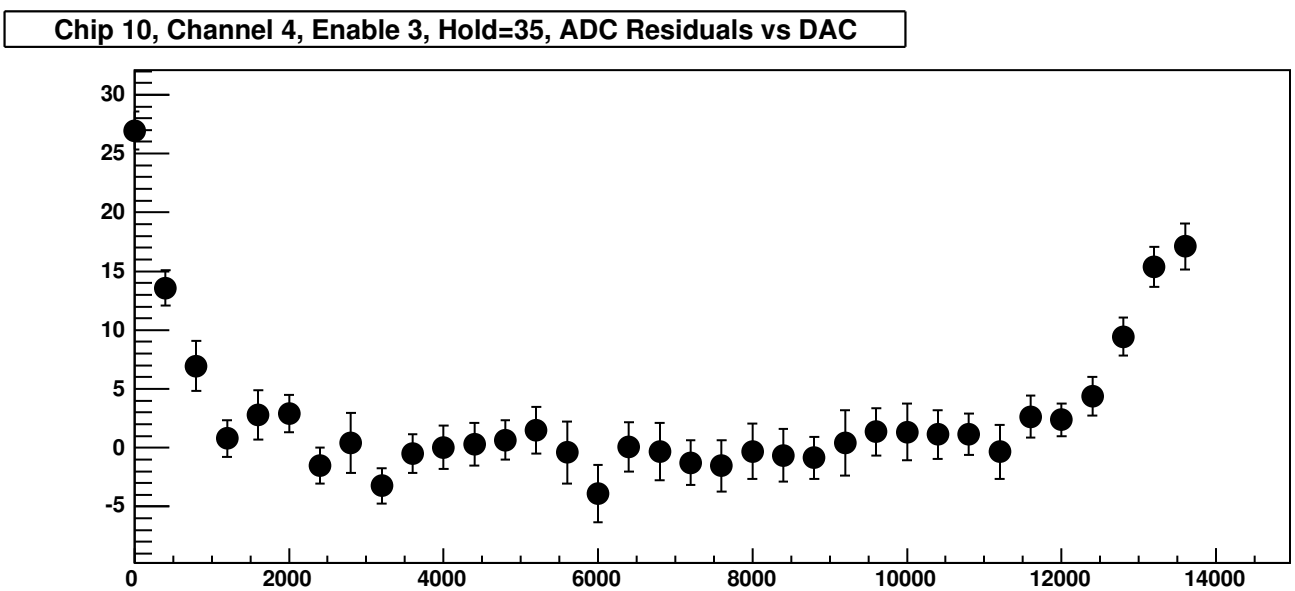

Chip 10, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.65 / 23
p0 113.8 ± 0.7528
p1 0.01499 ± 0.0001132
320
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

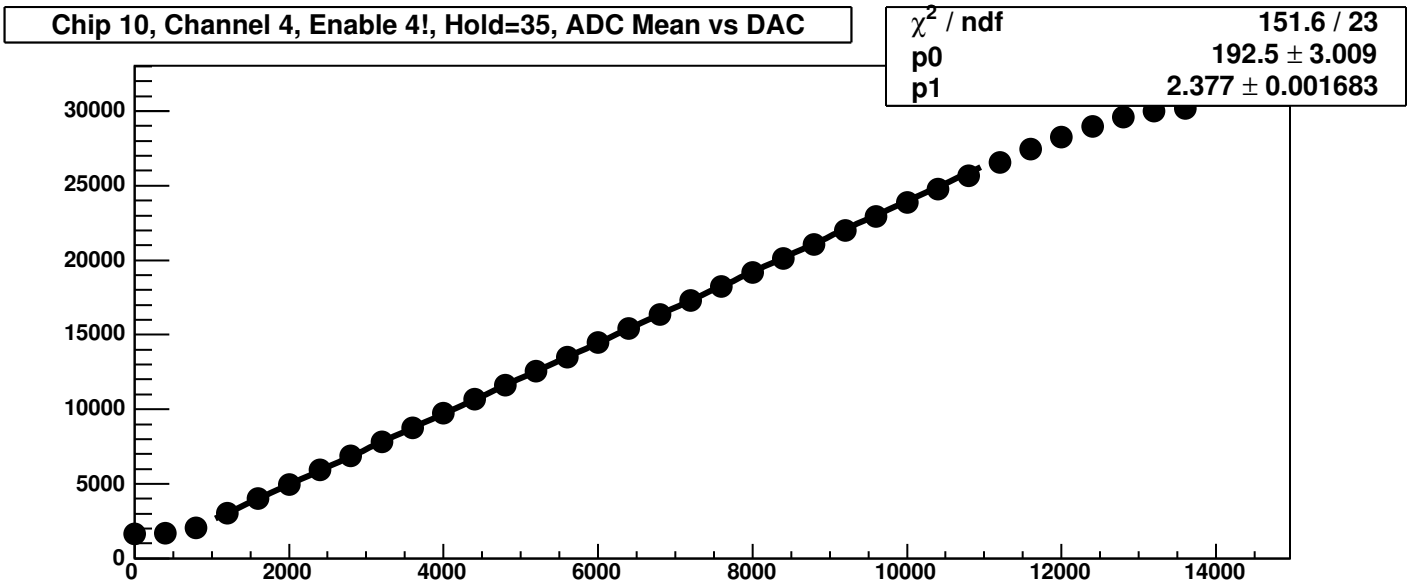

Chip 10, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

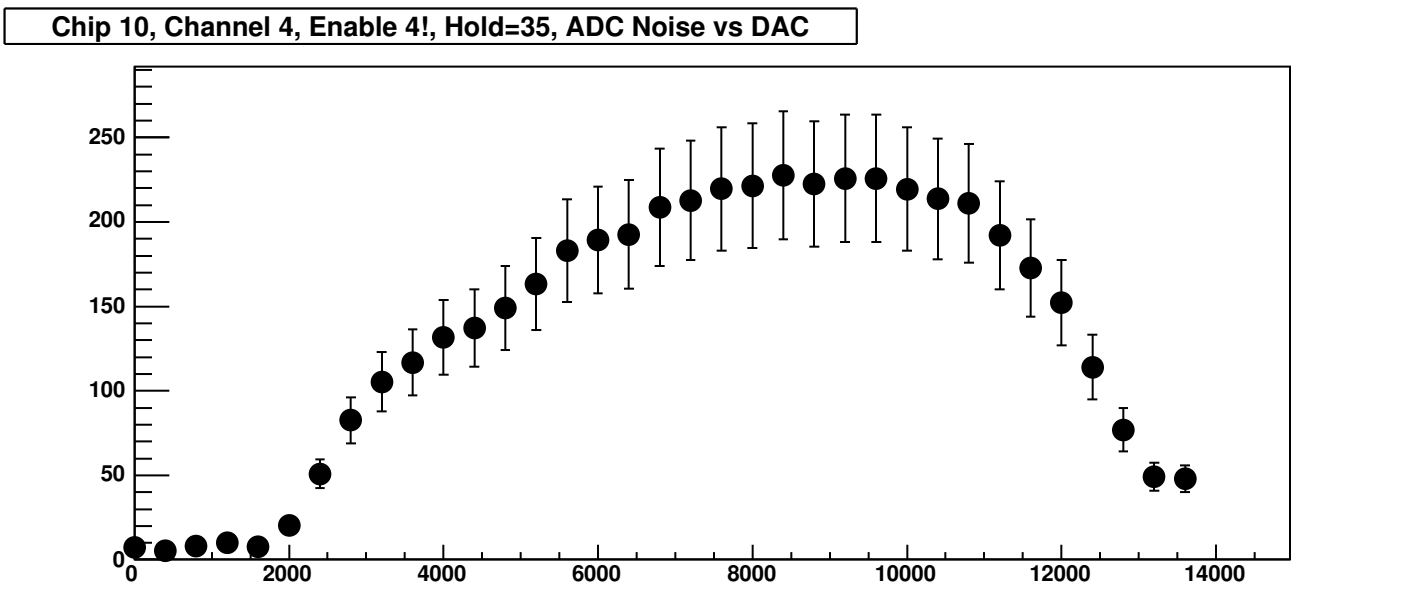

Chip 10, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

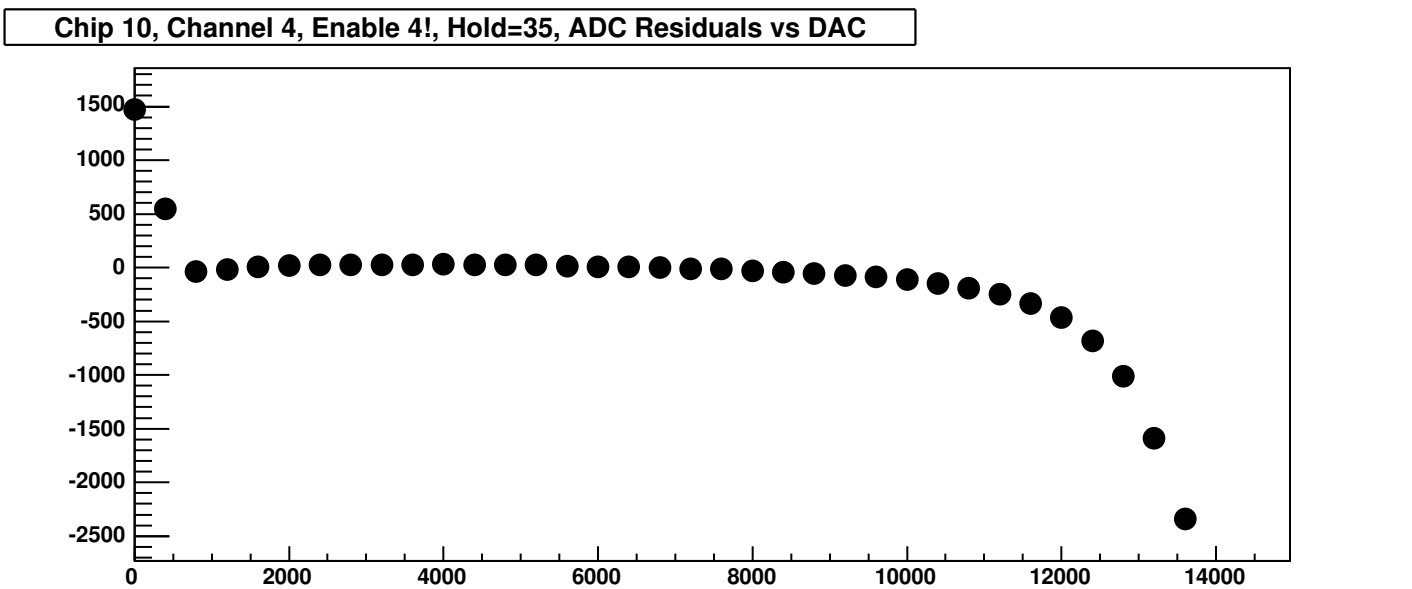

Chip 10, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.19 / 23
p0 109.1 ± 0.8156
p1 0.02396 ± 0.0001304
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

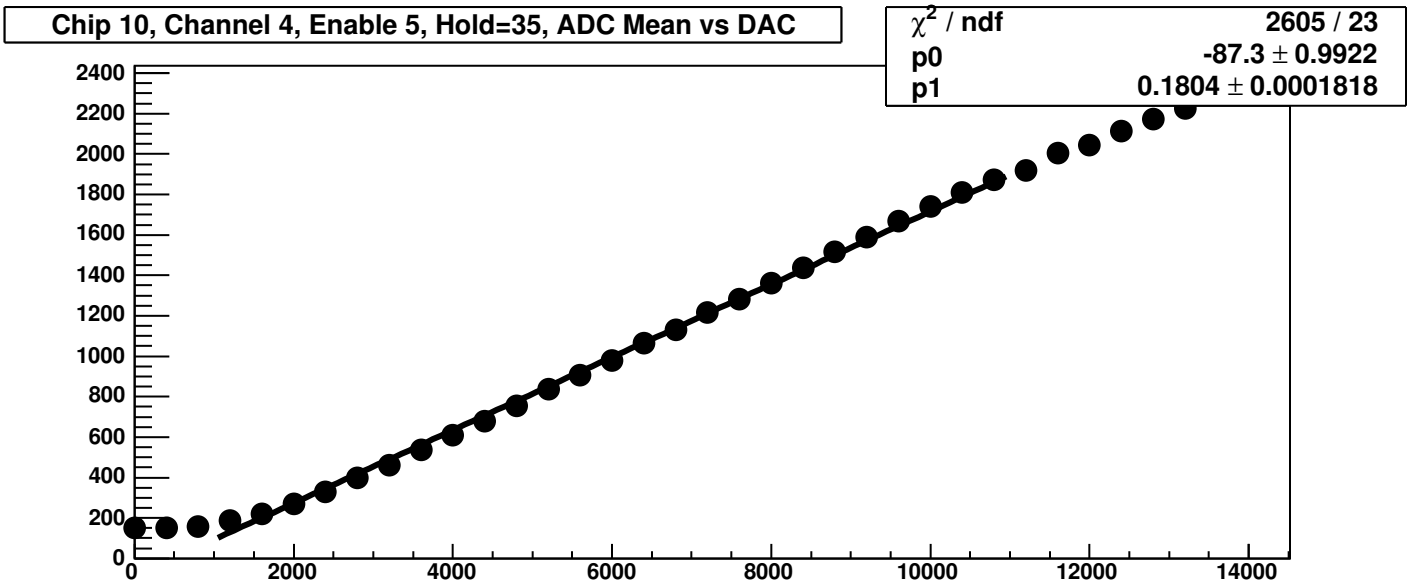

Chip 10, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

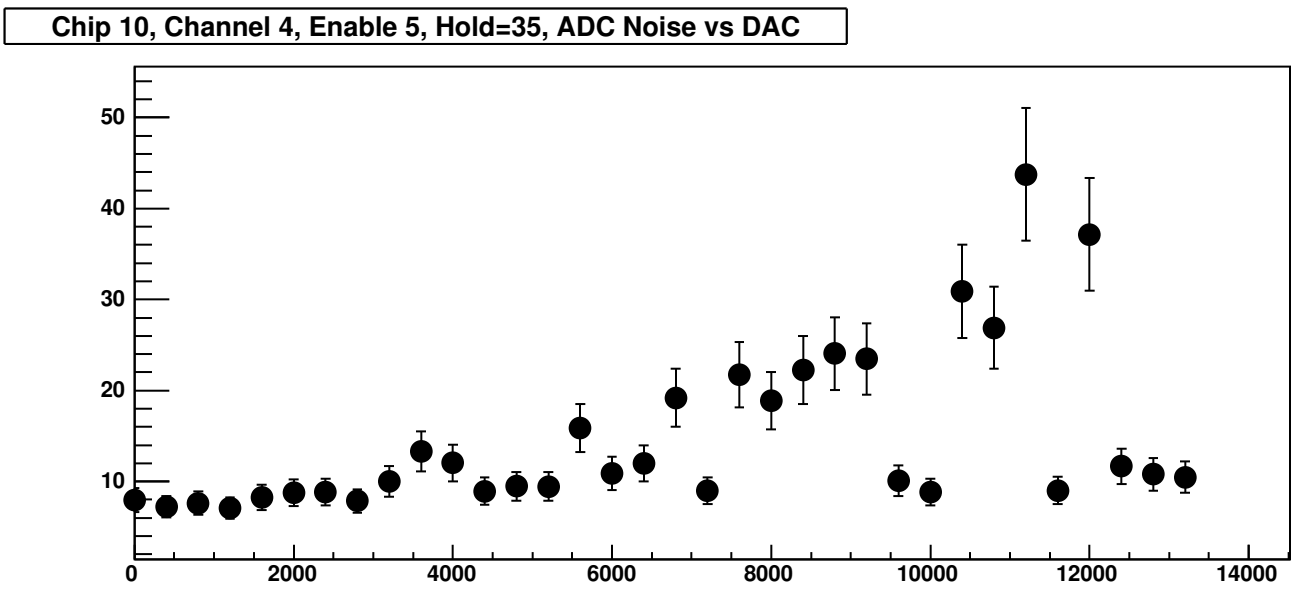

Chip 10, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

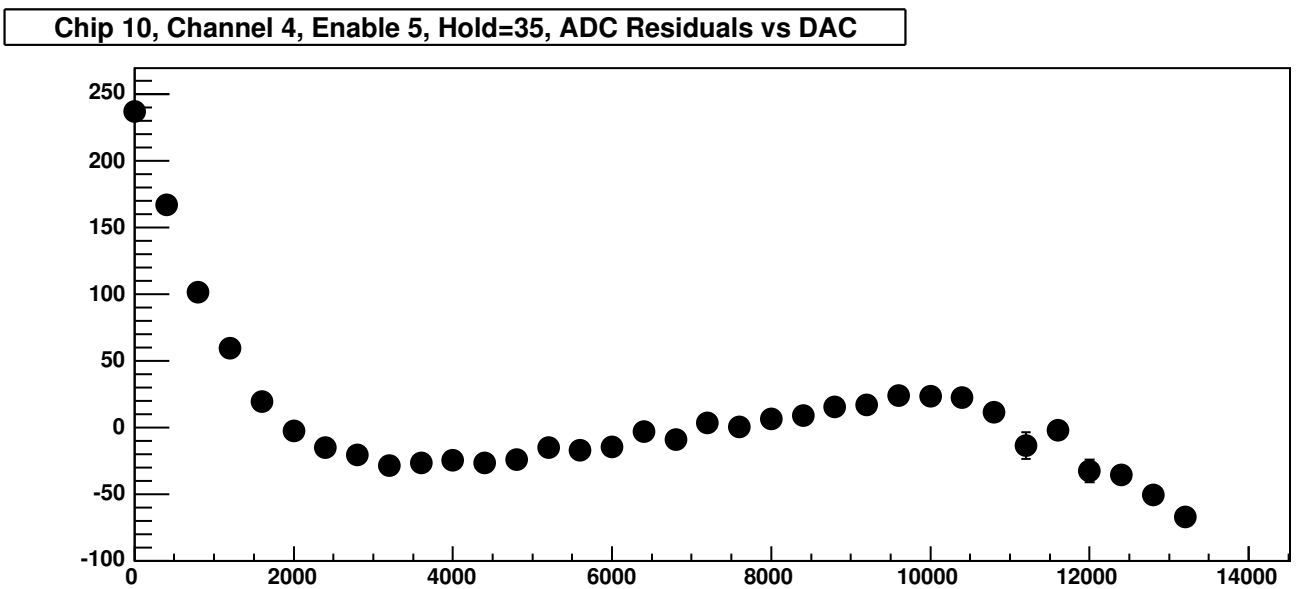

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 151.6 / 23
p0 192.5 ± 3.009
p1 2.377 ± 0.001683
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

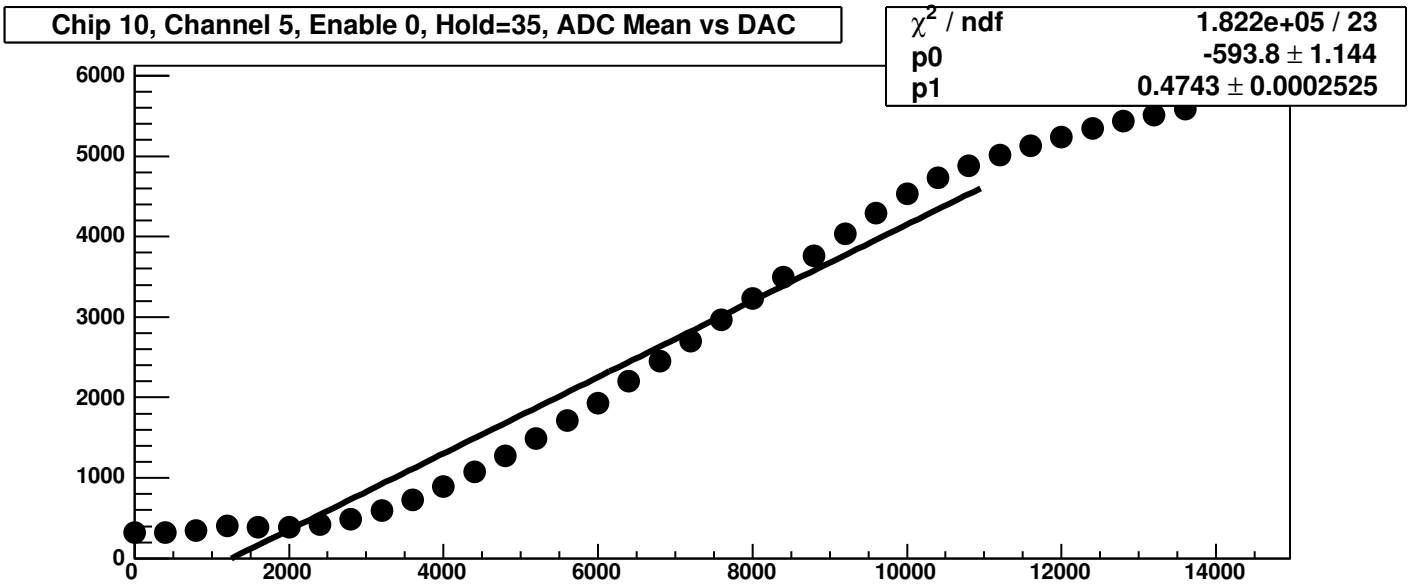

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

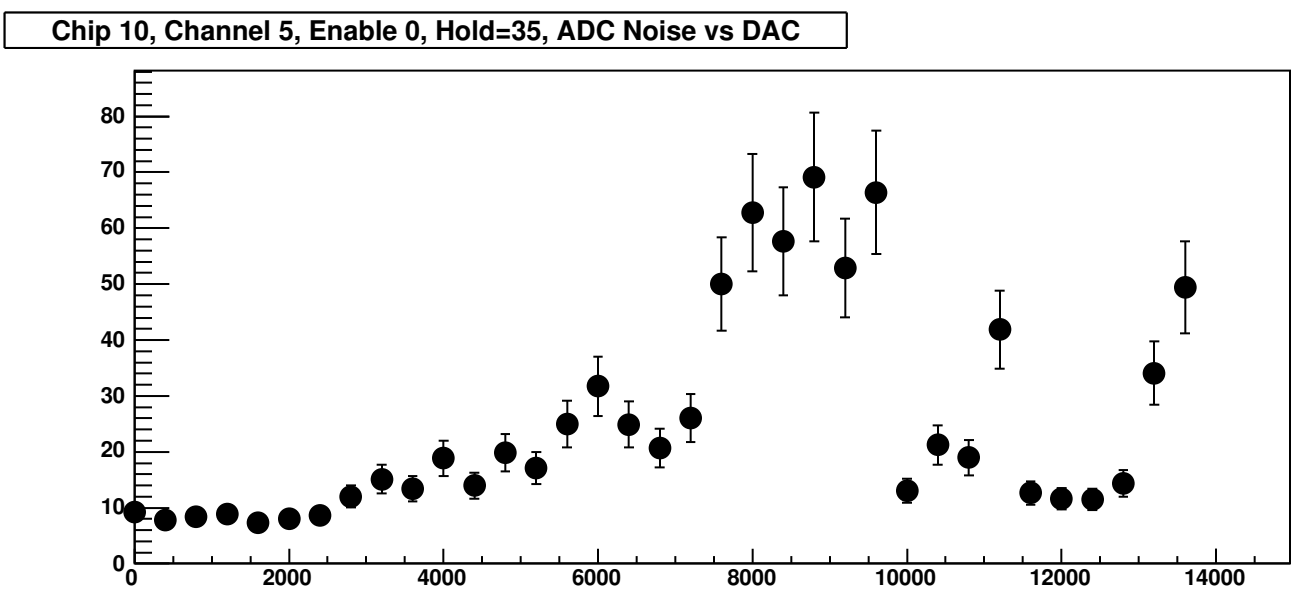

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

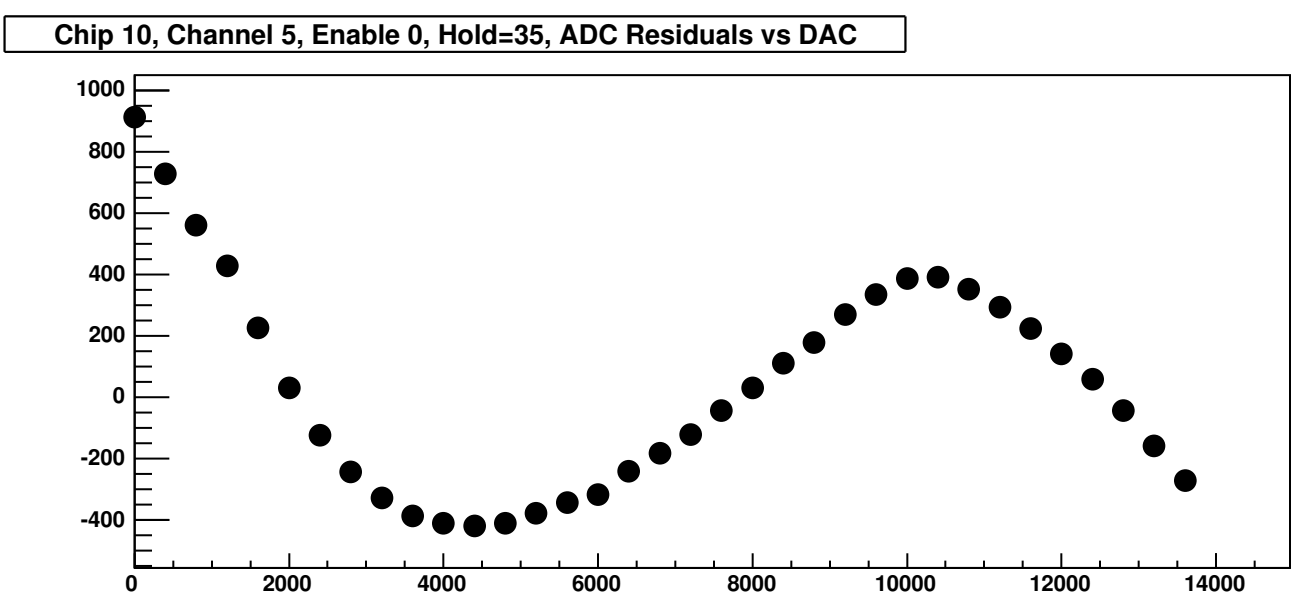

Chip 10, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2605 / 23
p0 -87.3 ± 0.9922
p1 0.1804 ± 0.0001818
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

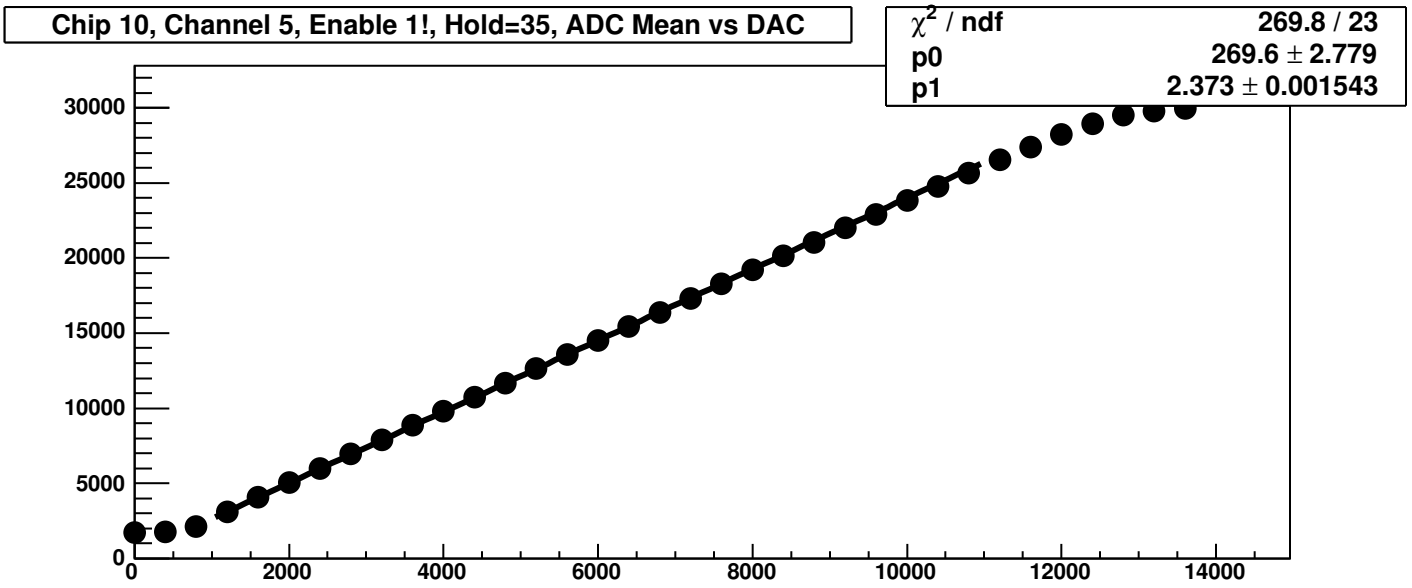

Chip 10, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

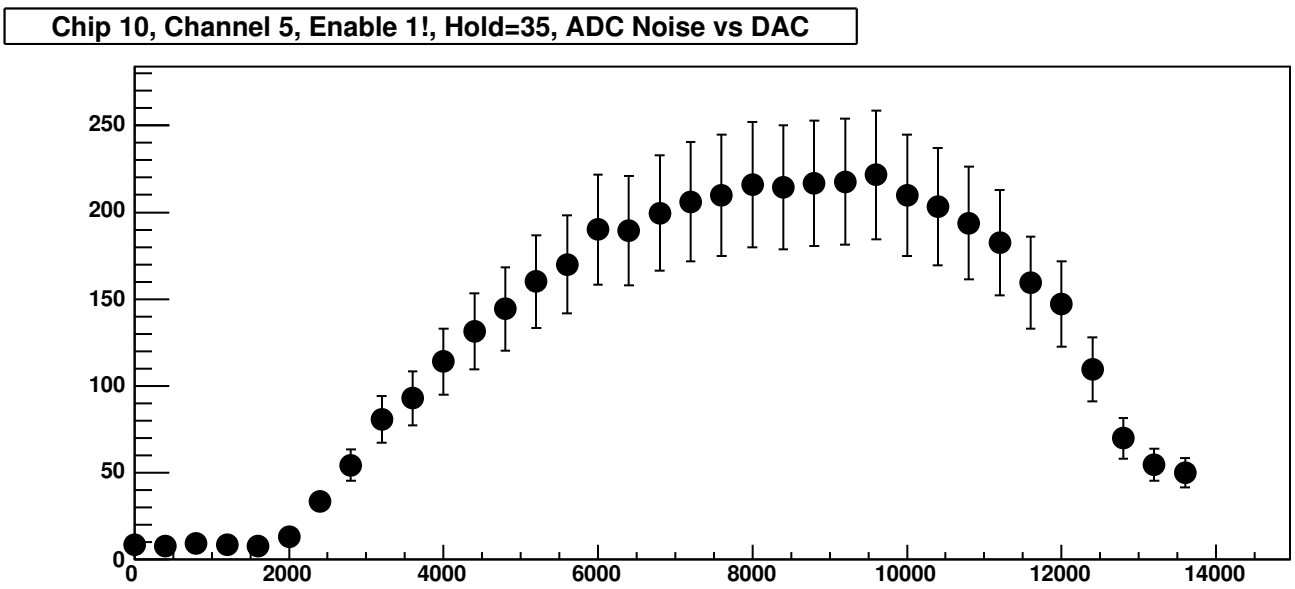

Chip 10, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

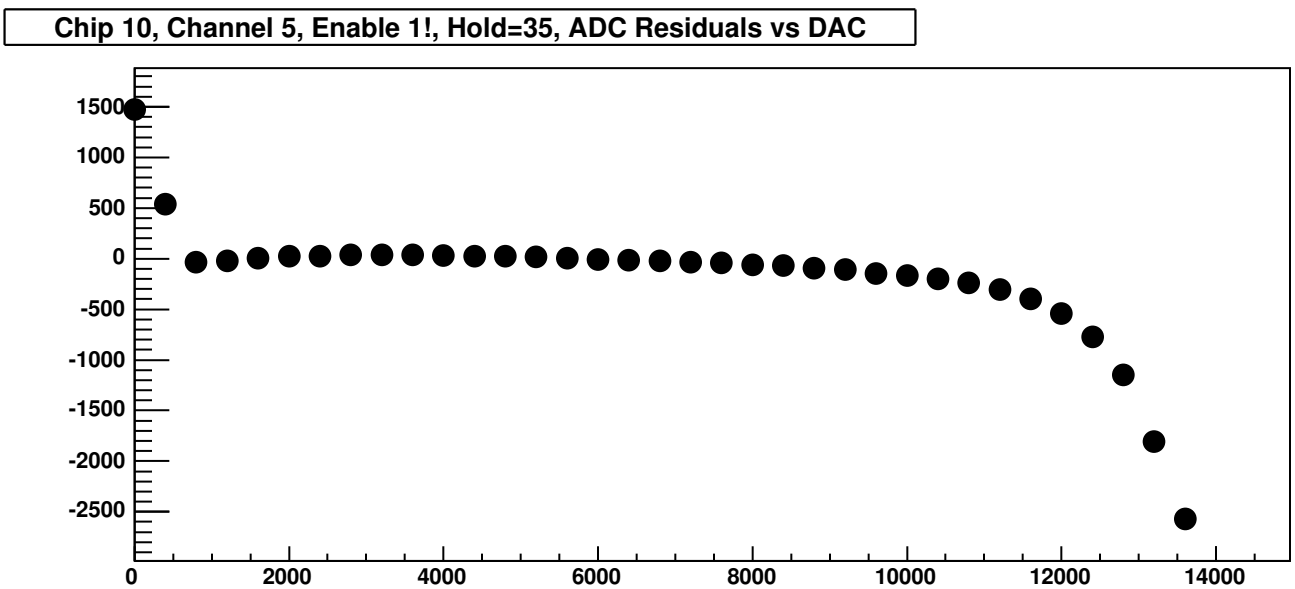

Chip 10, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.822e+05 / 23
p0 -593.8 ± 1.144
p1 0.4743 ± 0.0002525
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

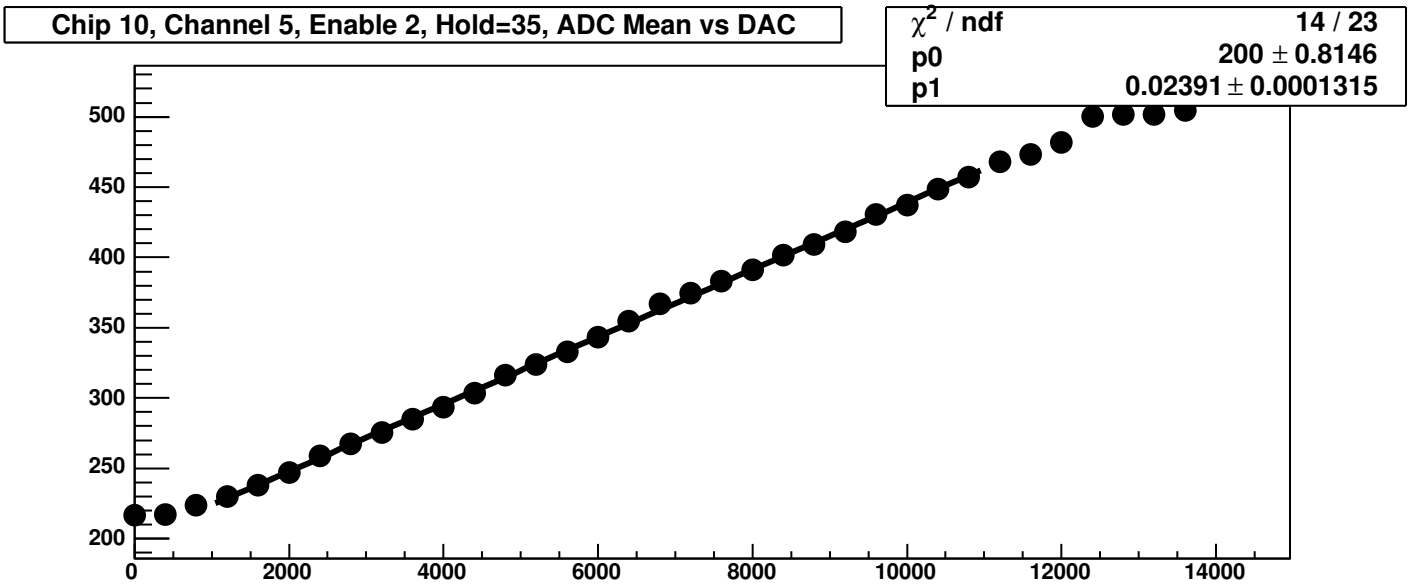

Chip 10, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

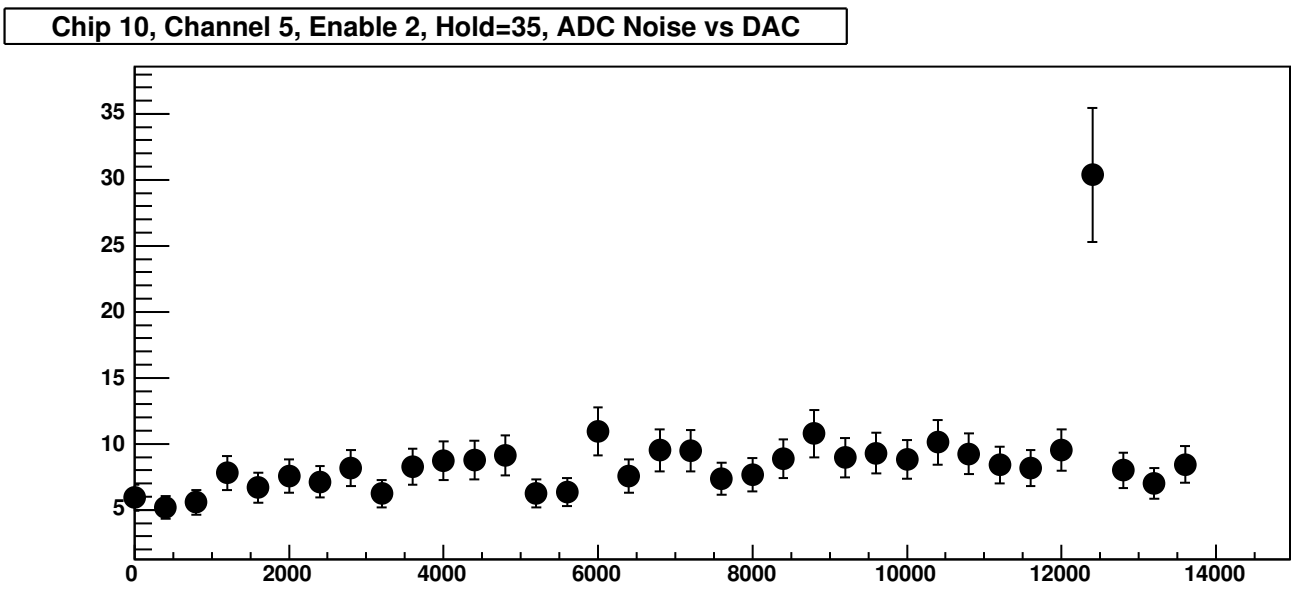

Chip 10, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

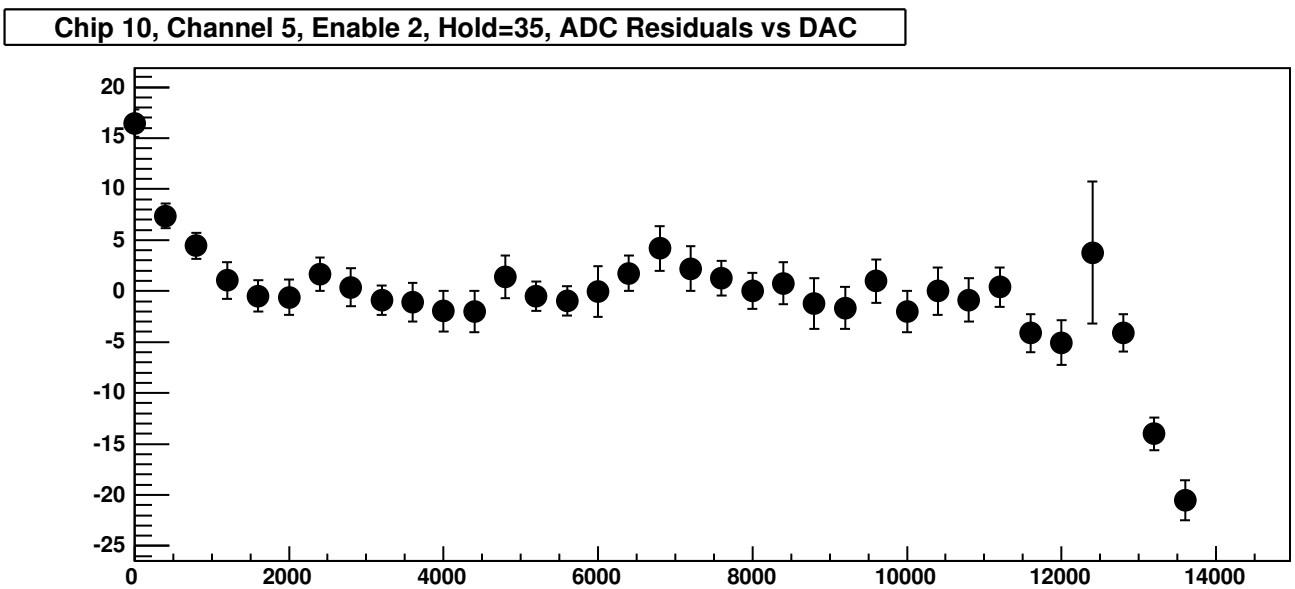

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 269.8 / 23
p0 269.6 ± 2.779
p1 2.373 ± 0.001543
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

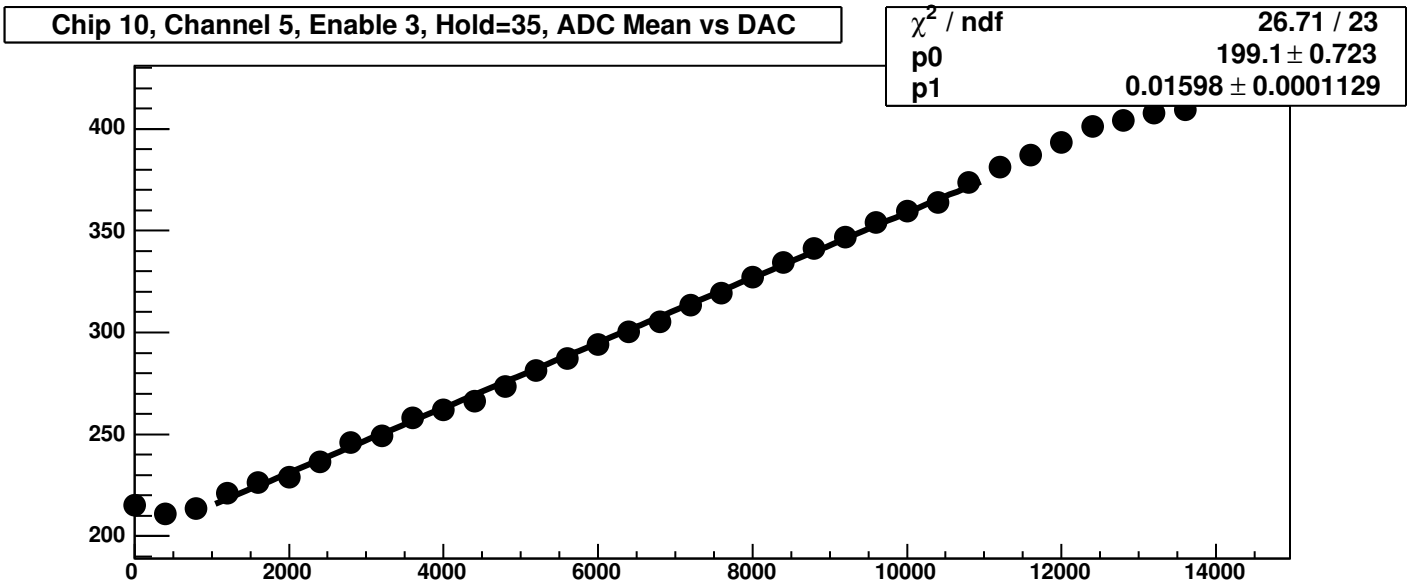

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

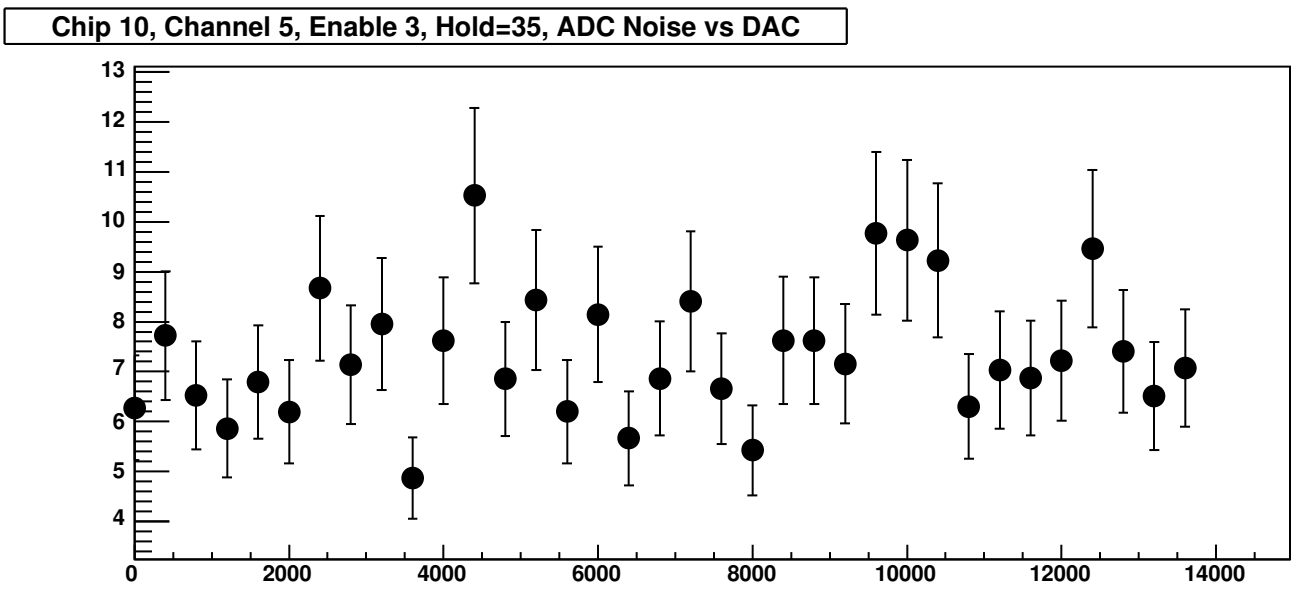

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

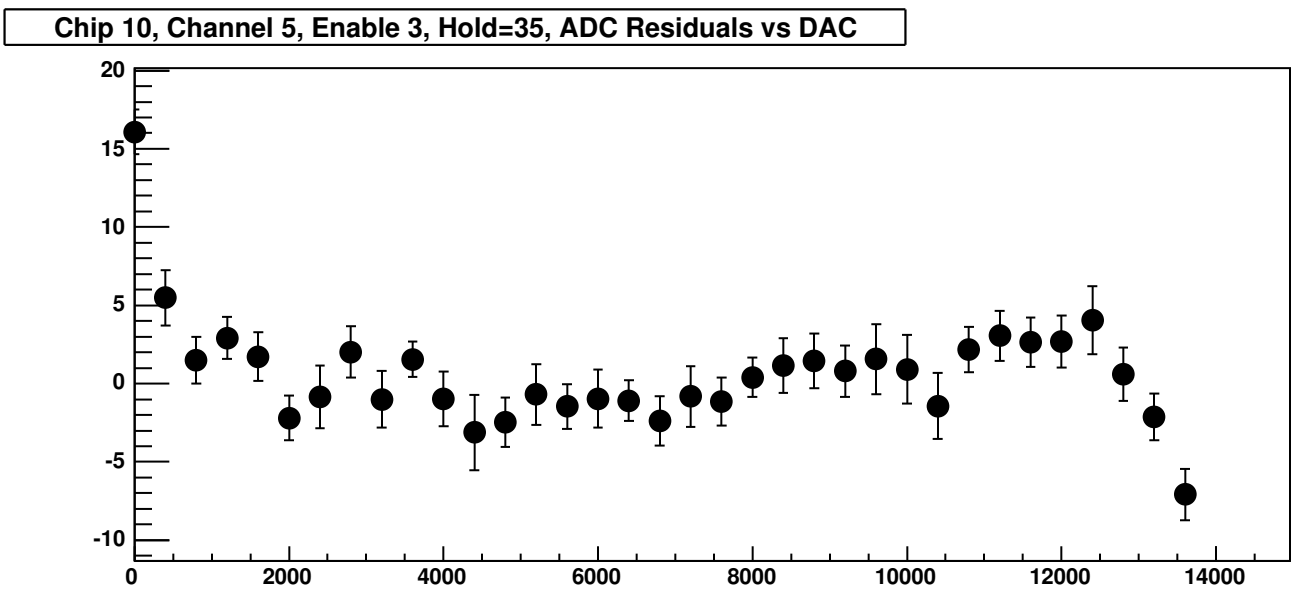

Chip 10, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 14 / 23
p0 200 ± 0.8146
p1 0.02391 ± 0.0001315
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

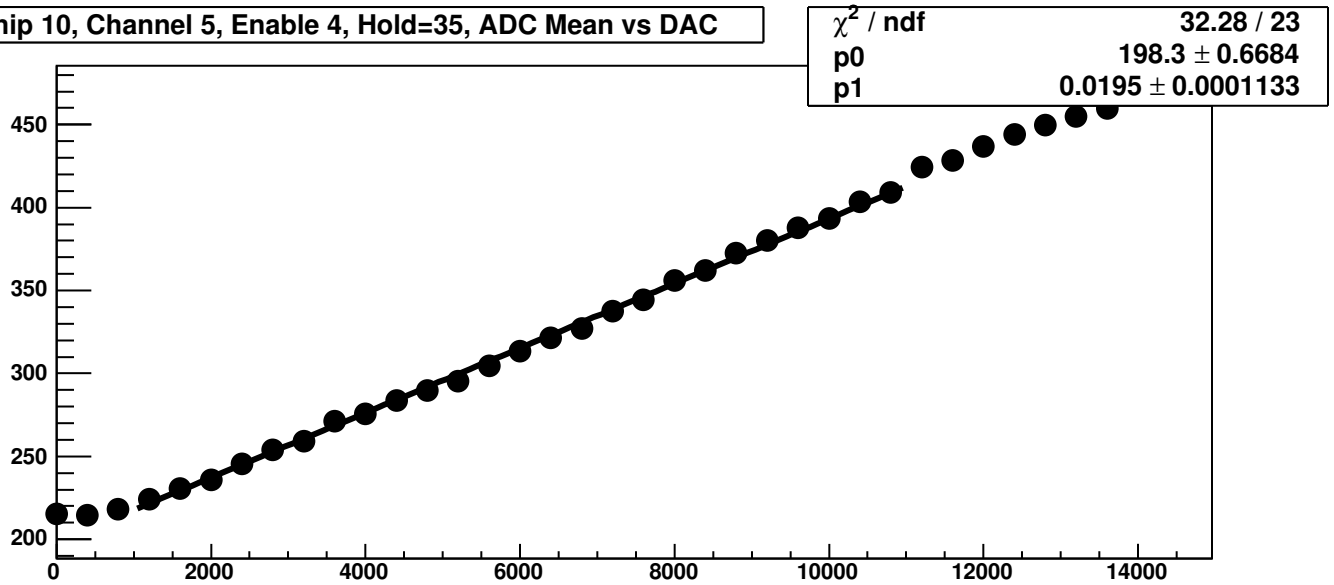

Chip 10, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.71 / 23
p0 199.1 ± 0.723
p1 0.01598 ± 0.0001129
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

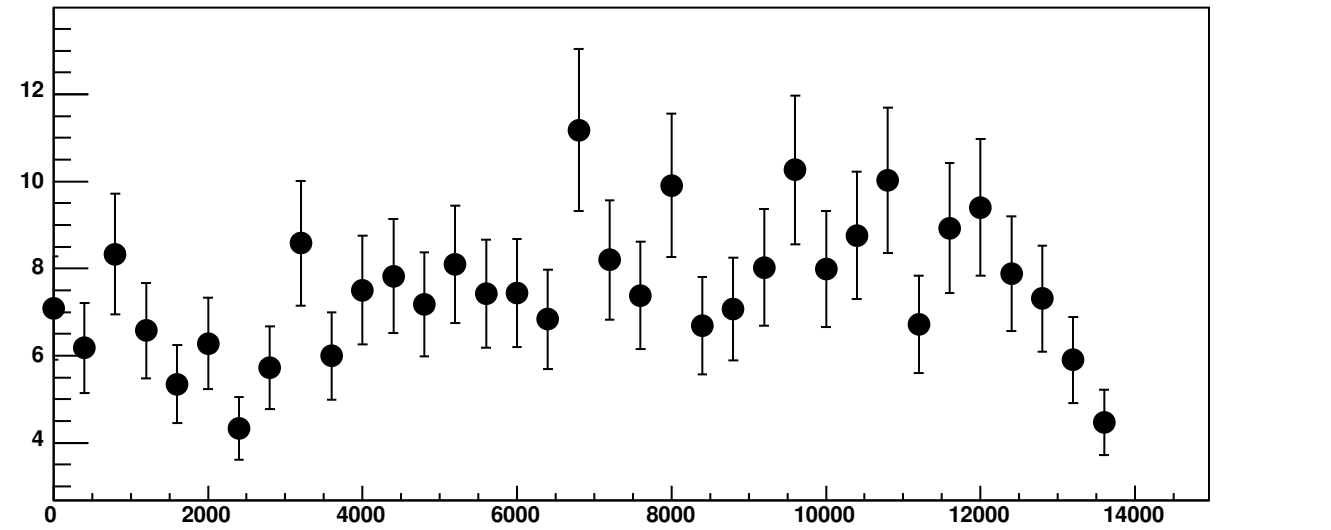

Chip 10, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

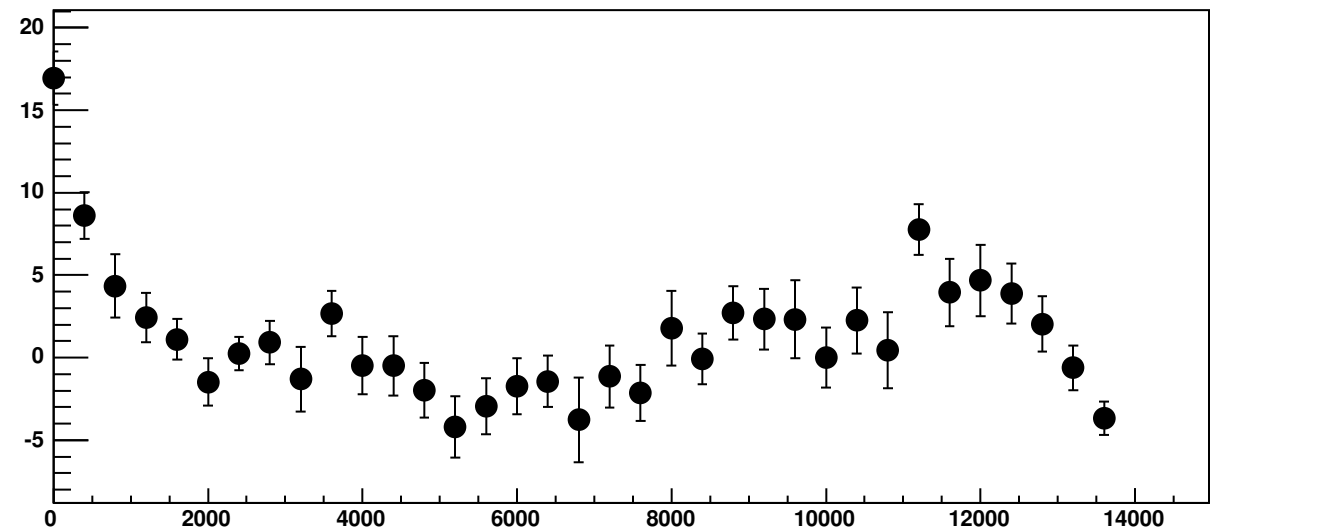
Chip 10, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



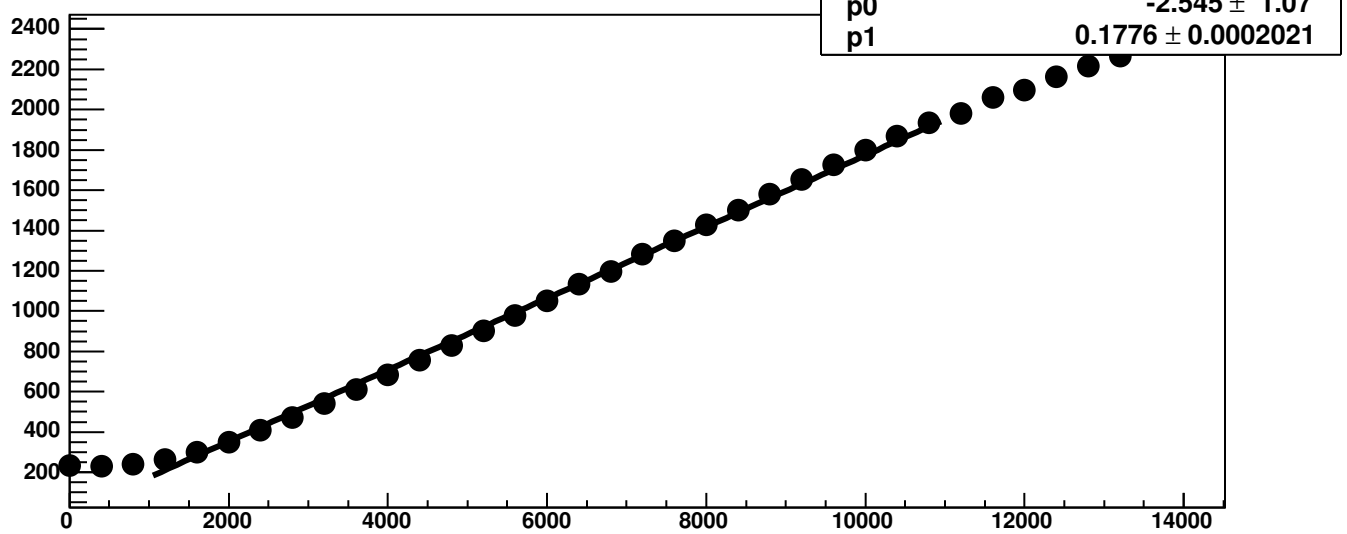
Chip 10, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



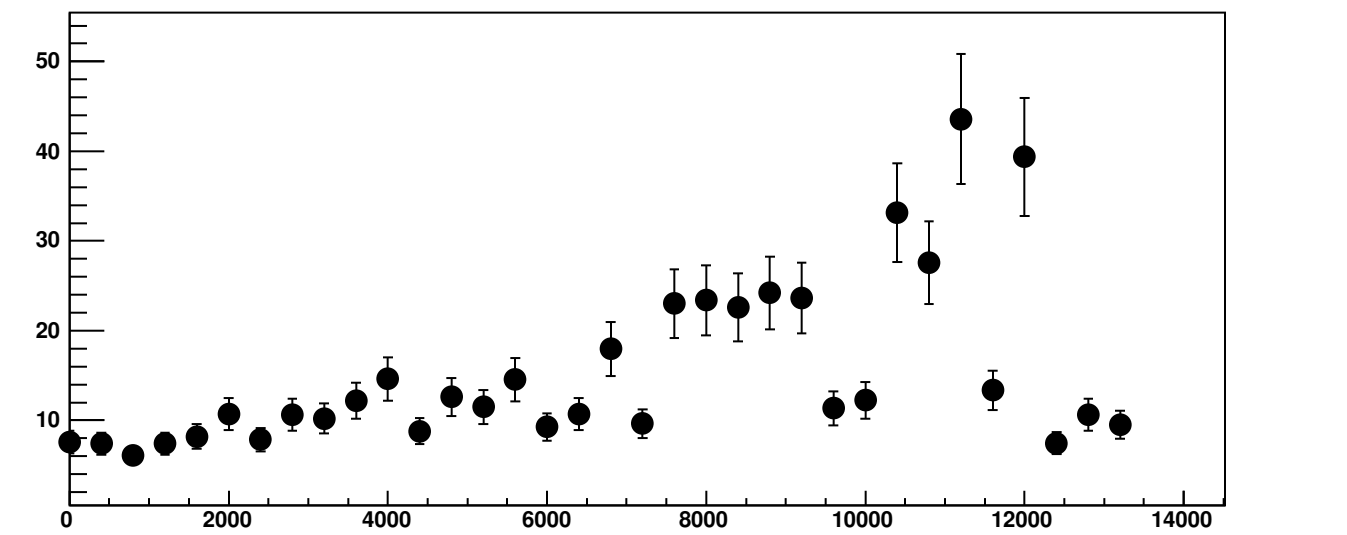
Chip 10, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



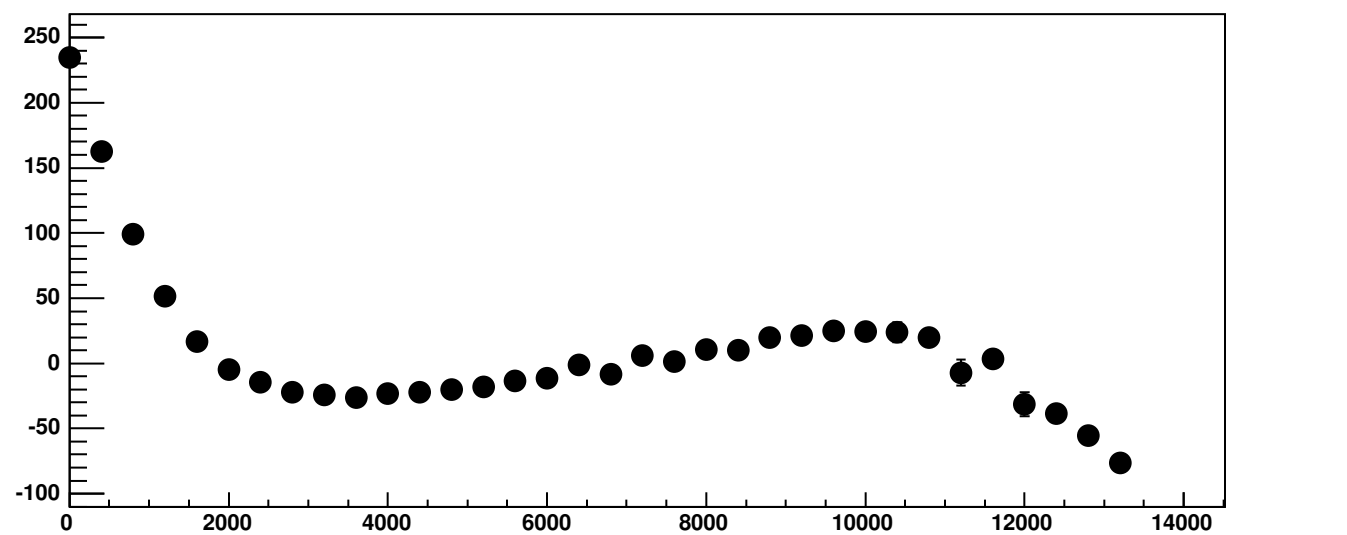
Chip 10, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

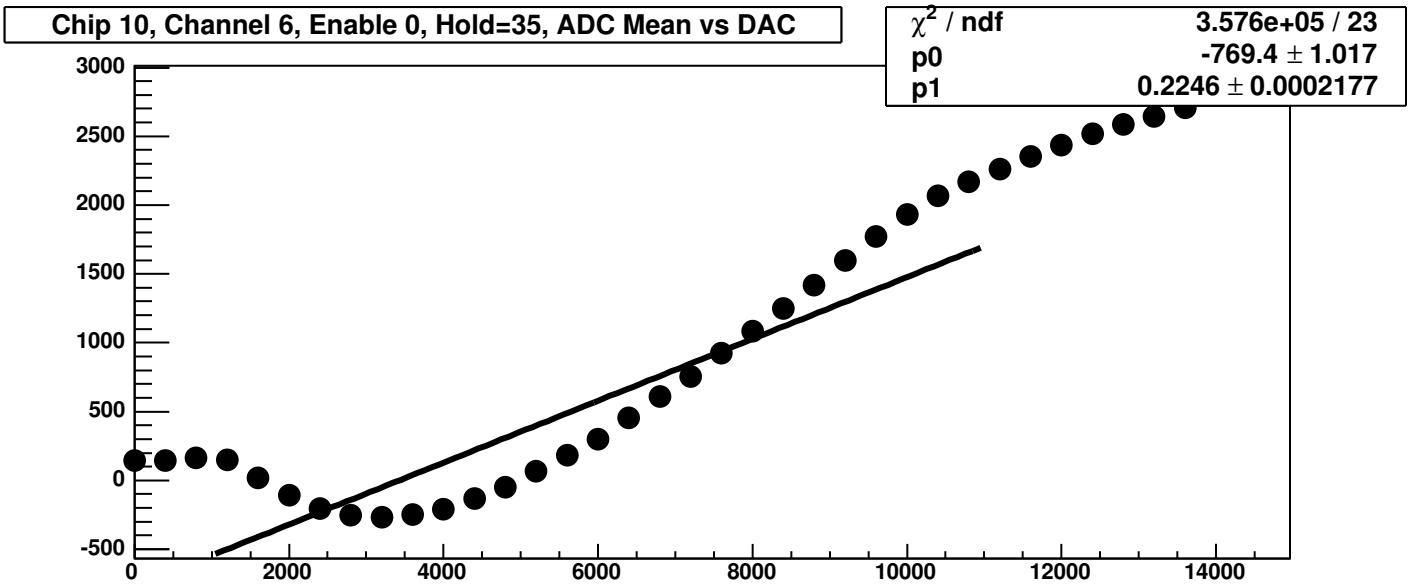


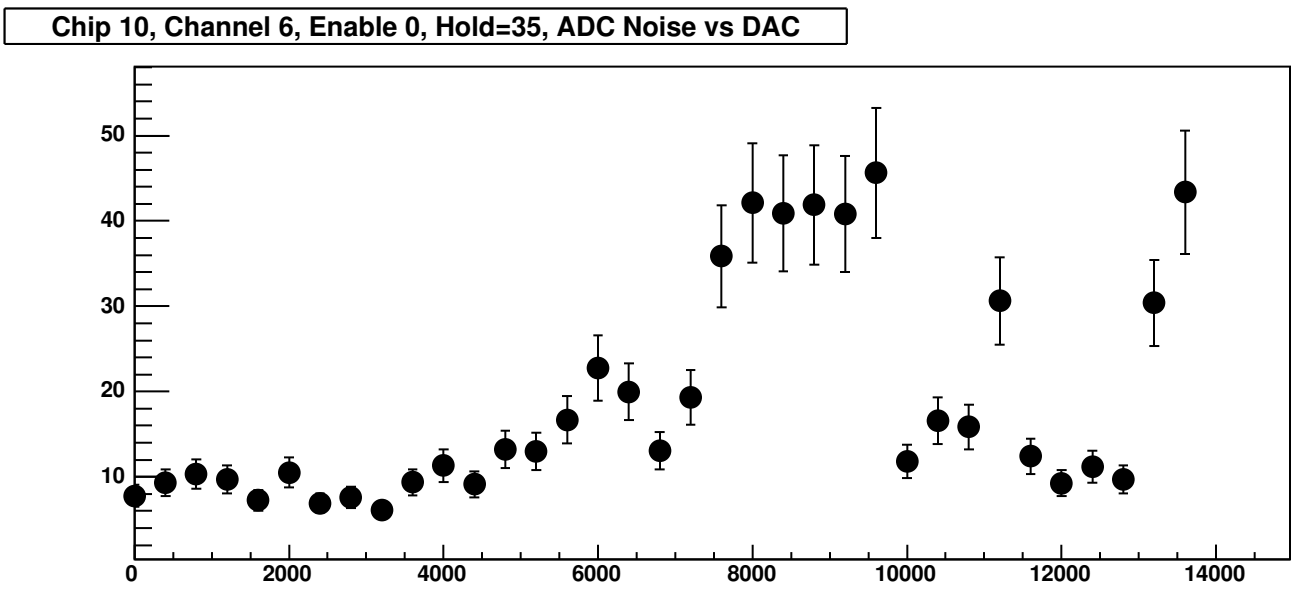
Chip 10, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

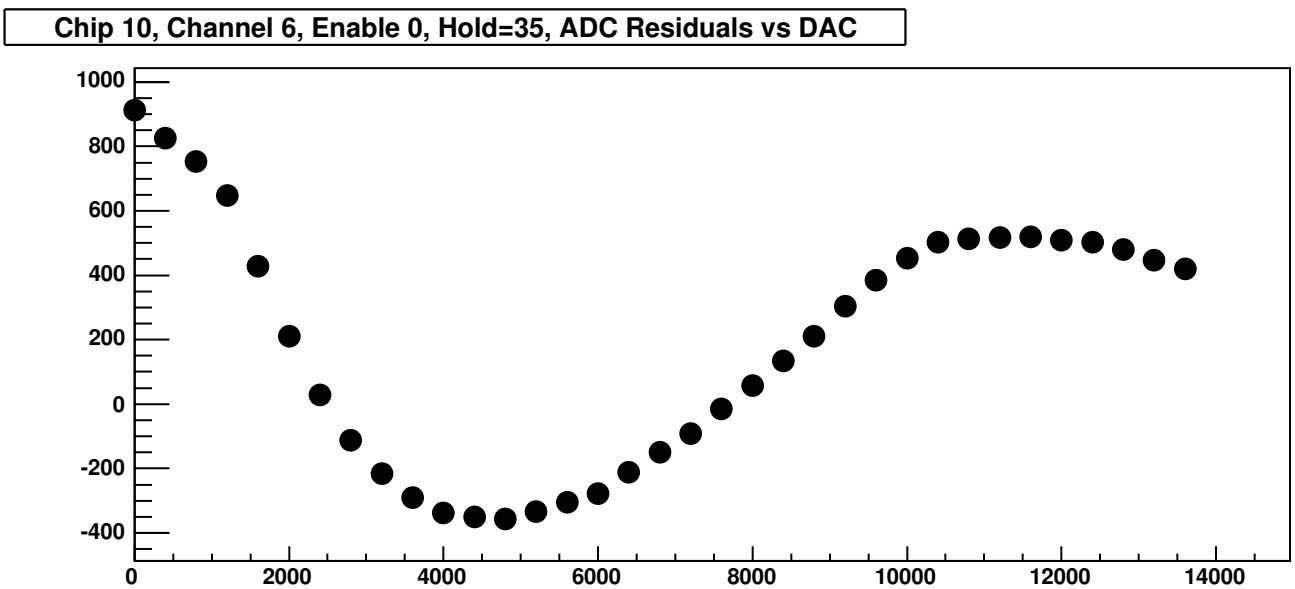


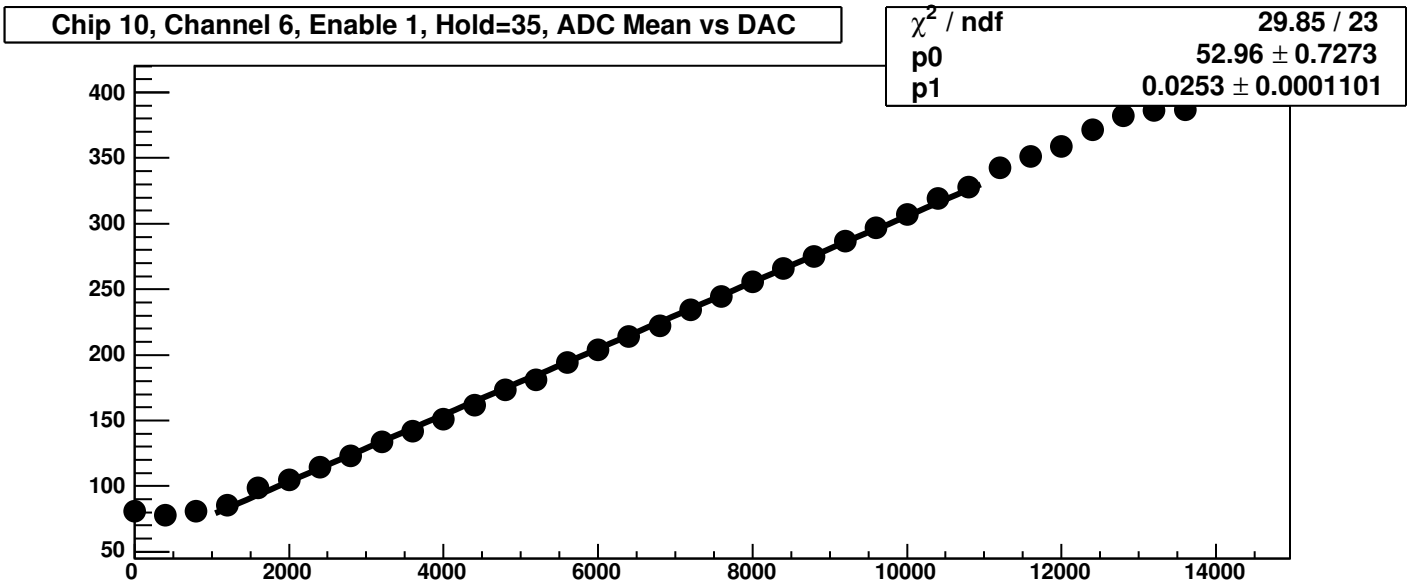
Chip 10, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

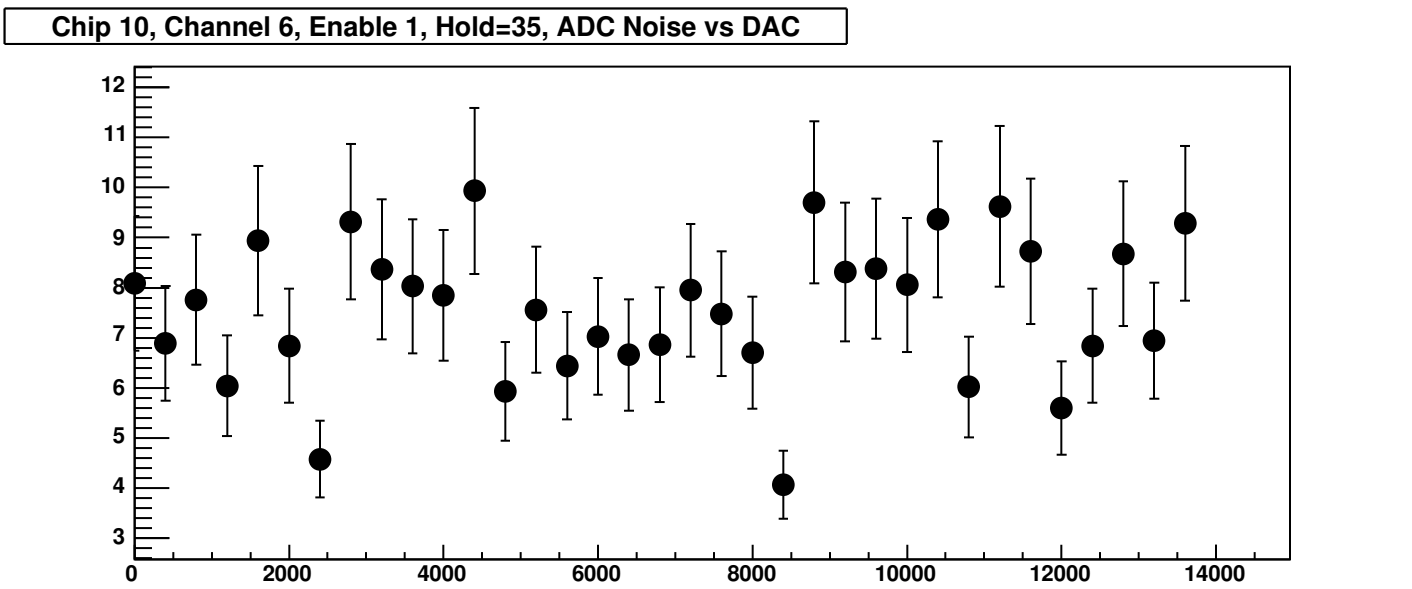


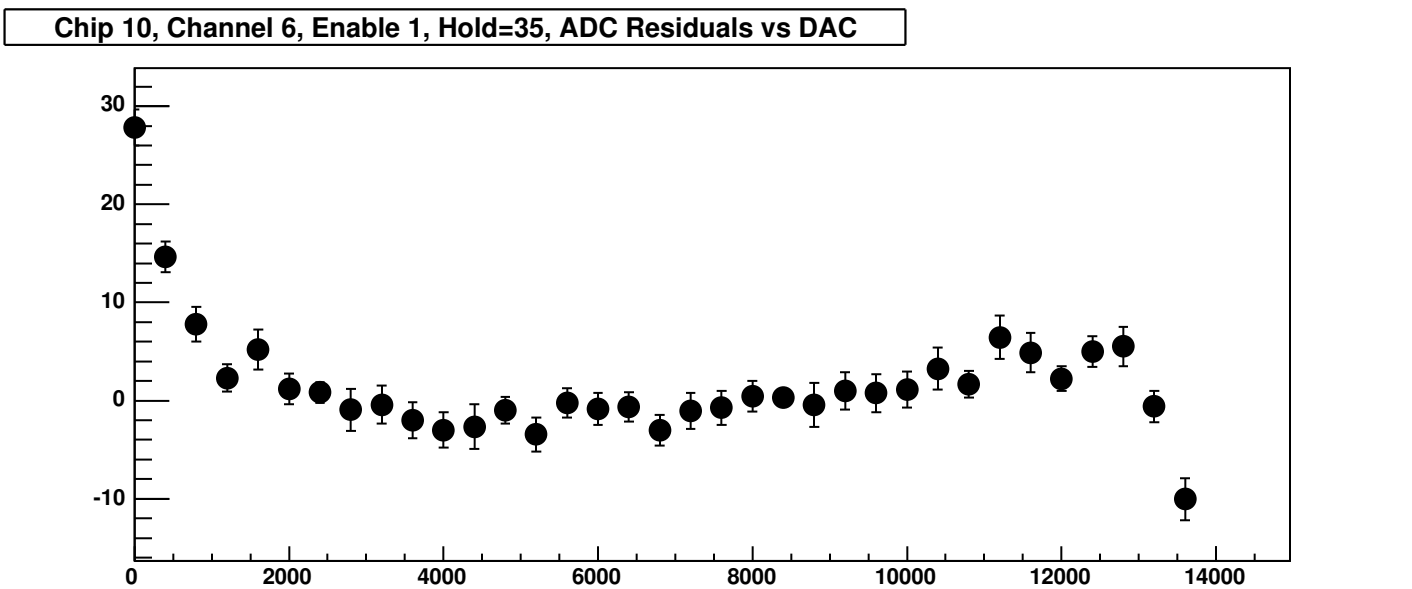

Chip 10, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.576e+05 / 23
p0 -769.4 ± 1.017
p1 0.2246 ± 0.0002177
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

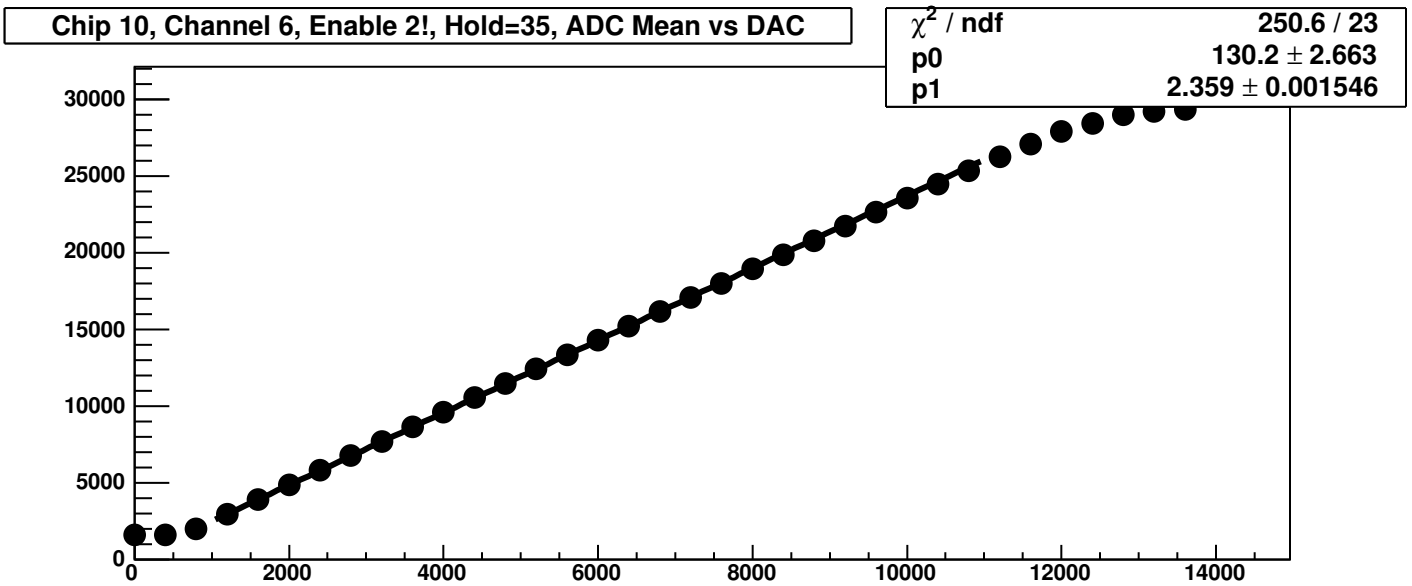

Chip 10, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

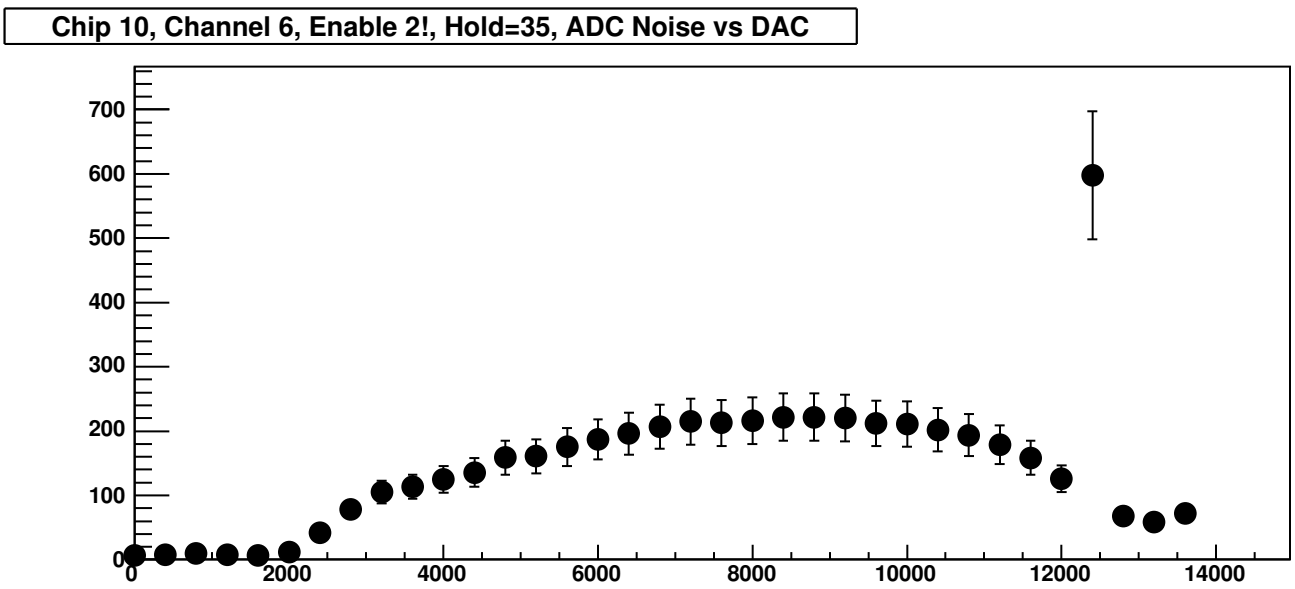

Chip 10, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

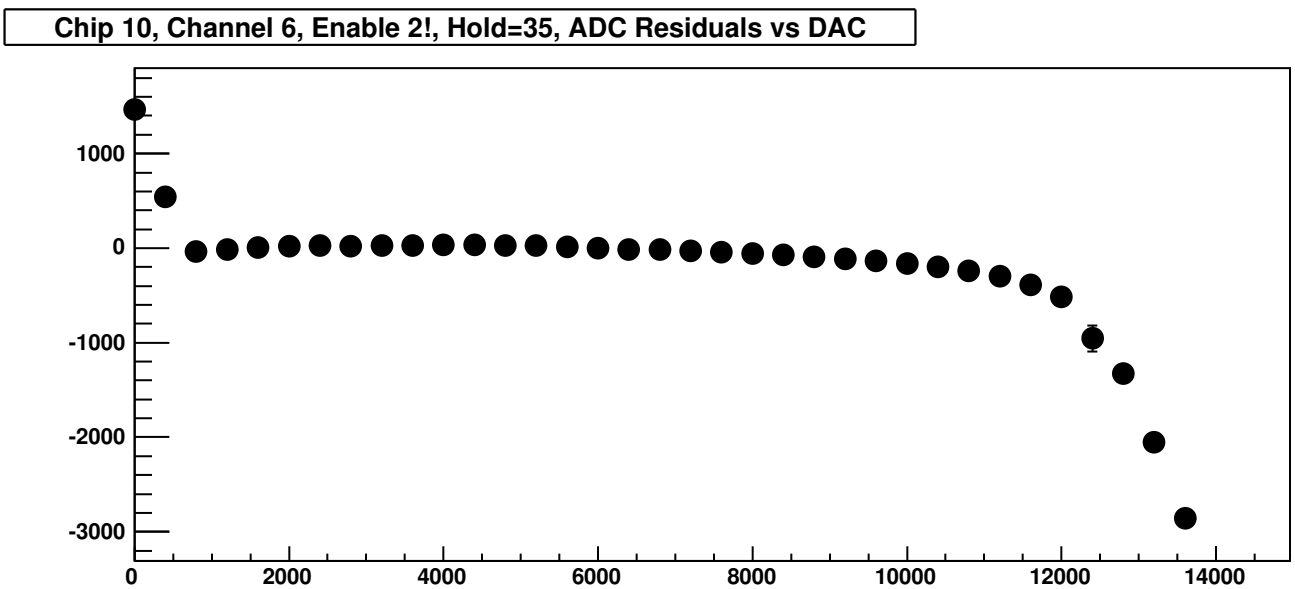

Chip 10, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.85 / 23
p0 52.96 ± 0.7273
p1 0.0253 ± 0.0001101
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

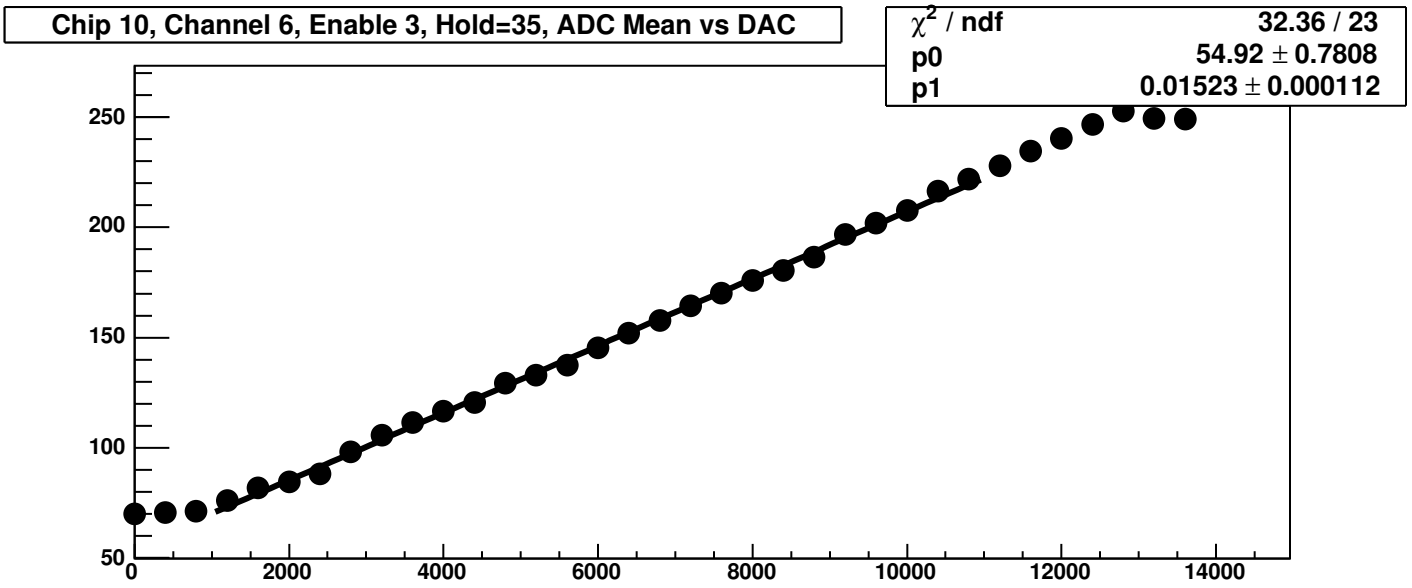

Chip 10, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

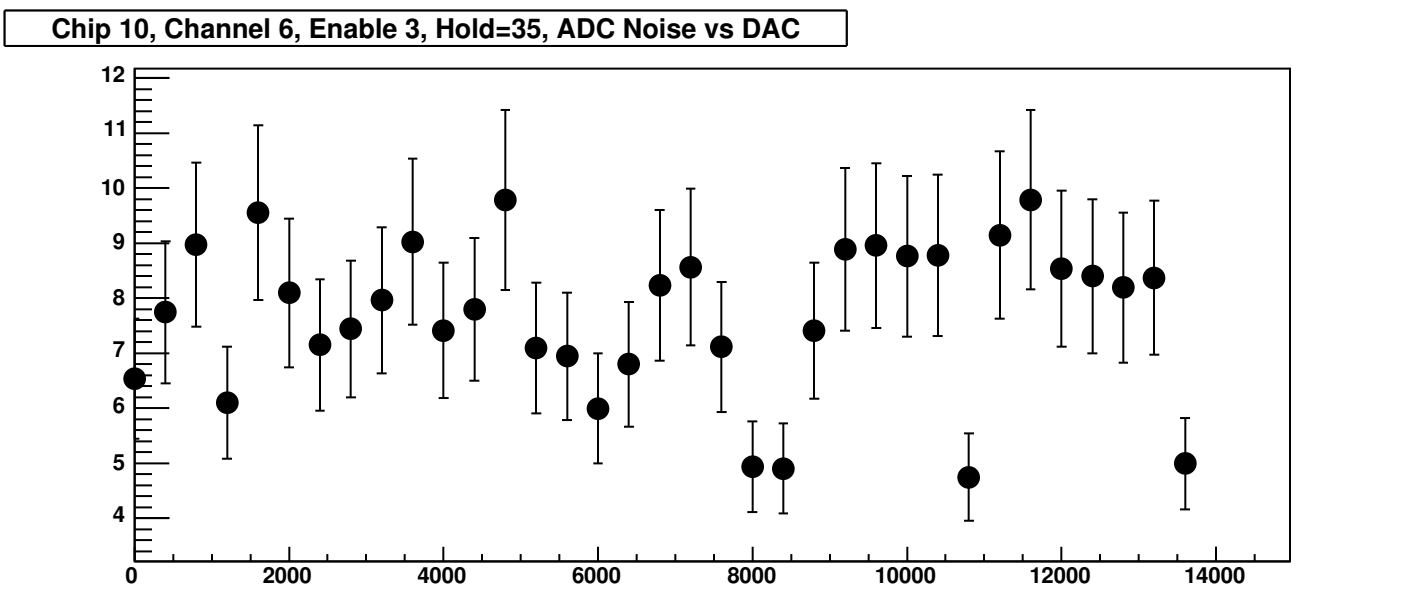

Chip 10, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000

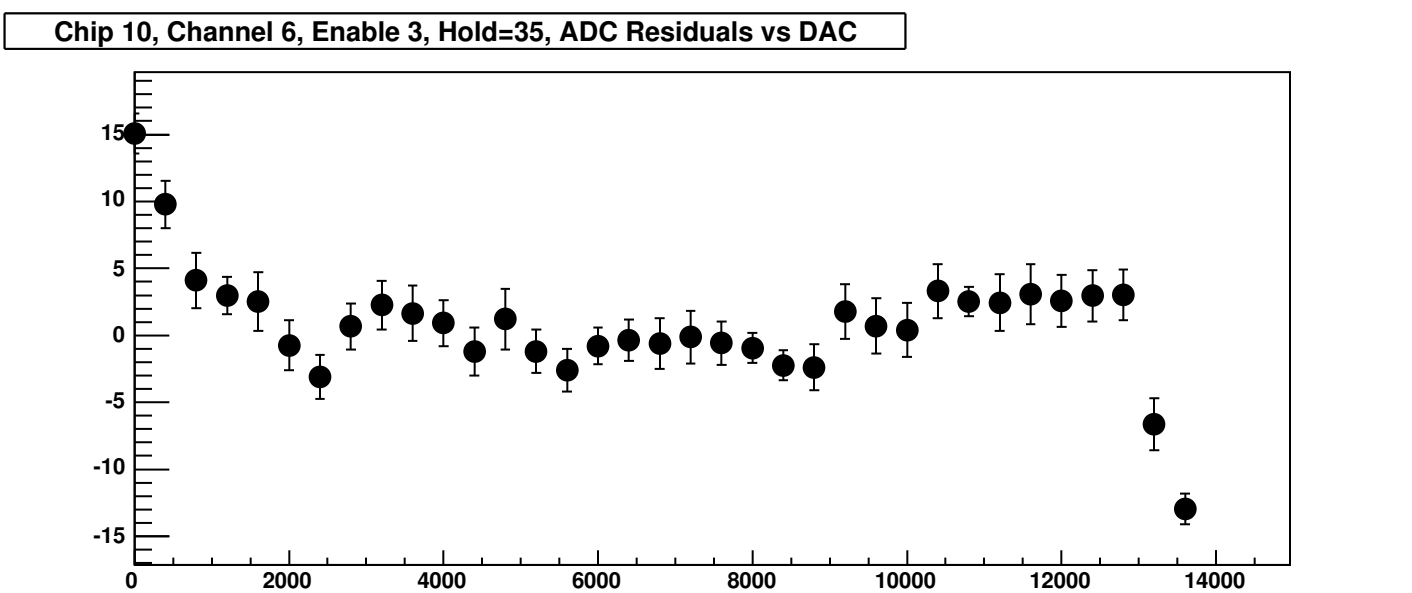

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 250.6 / 23
p0 130.2 ± 2.663
p1 2.359 ± 0.001546
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

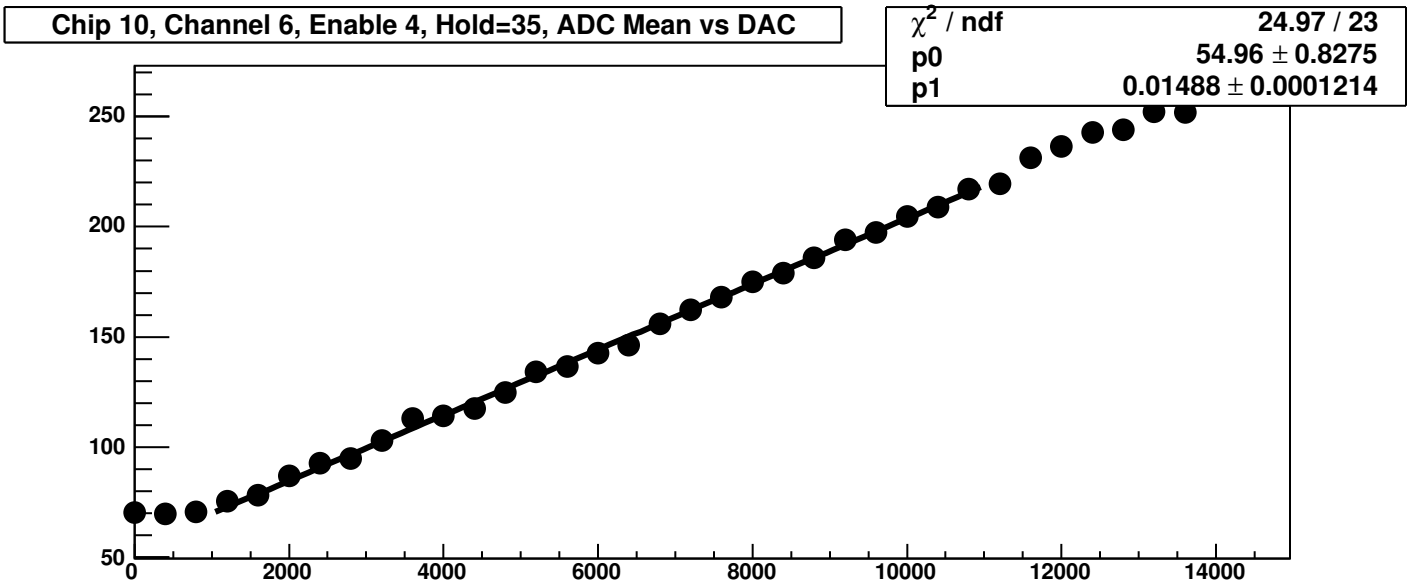

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

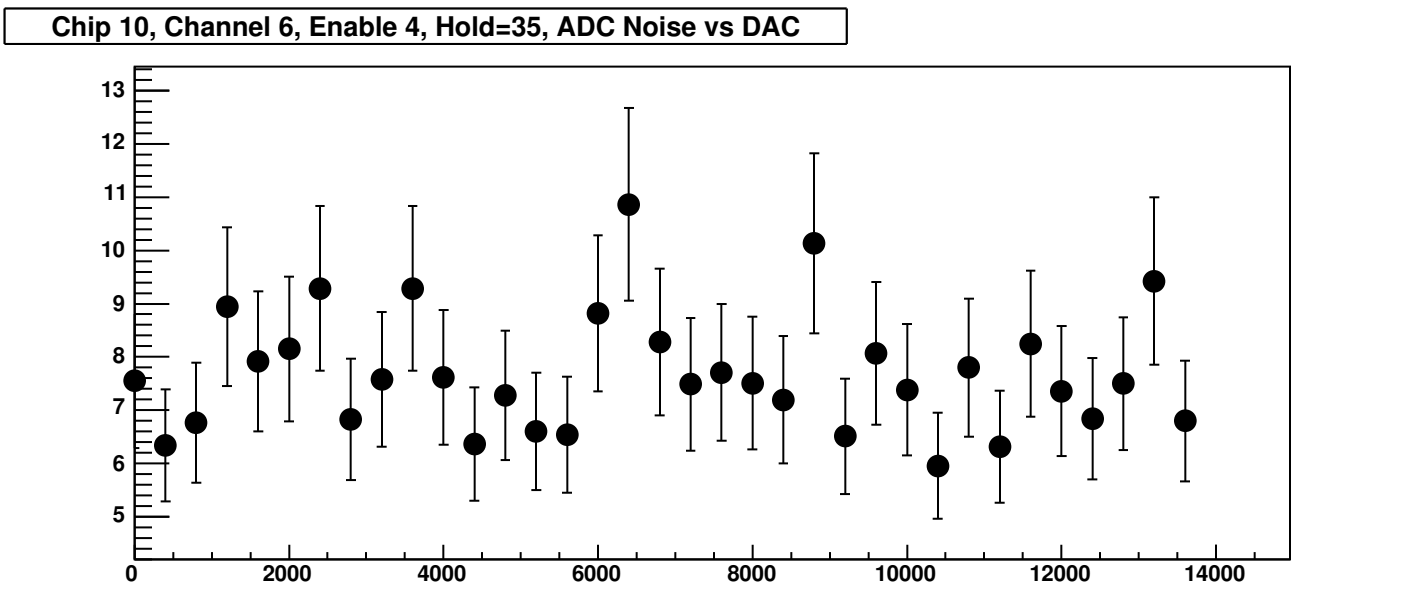

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

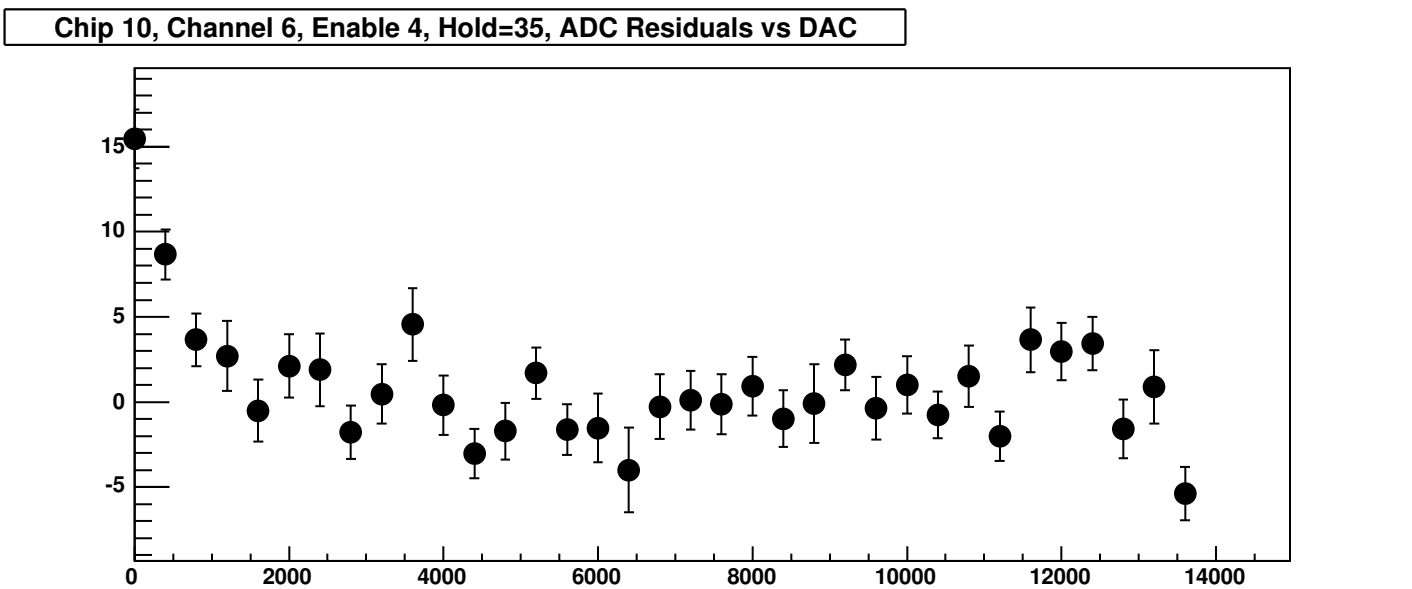

Chip 10, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.36 / 23
p0 54.92 ± 0.7808
p1 0.01523 ± 0.000112
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

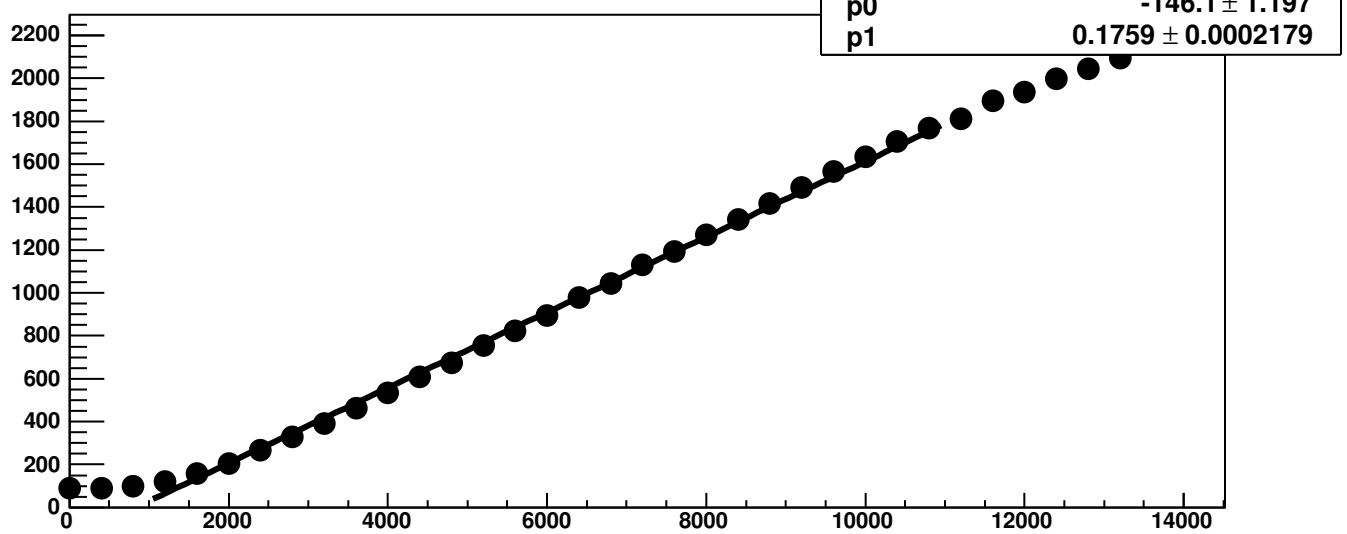

Chip 10, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.97 / 23
p0 54.96 ± 0.8275
p1 0.01488 ± 0.0001214
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

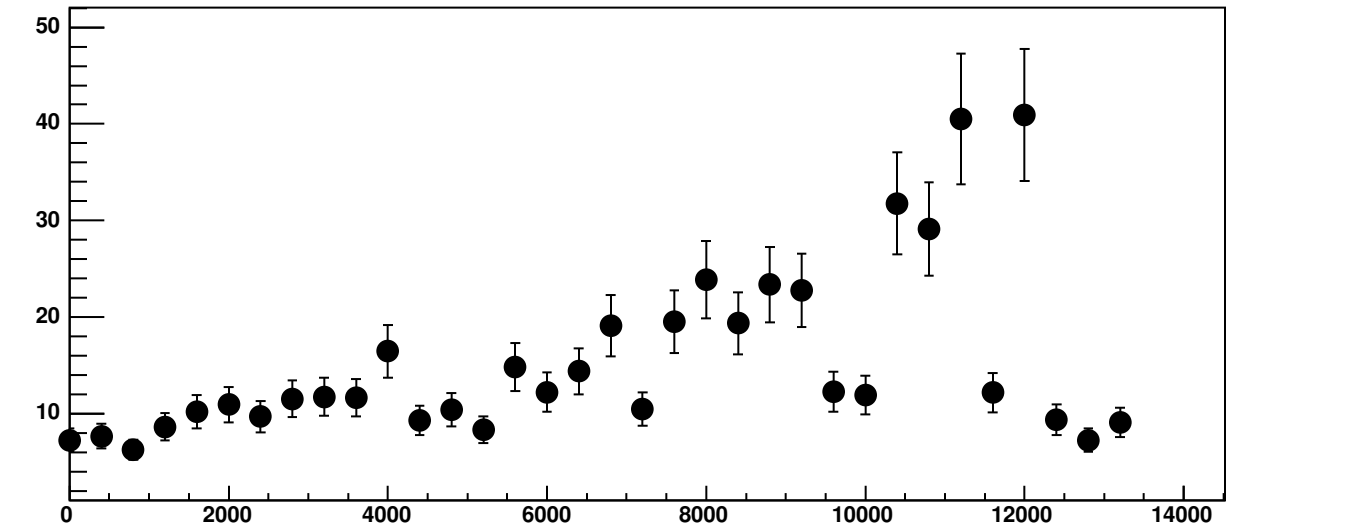

Chip 10, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

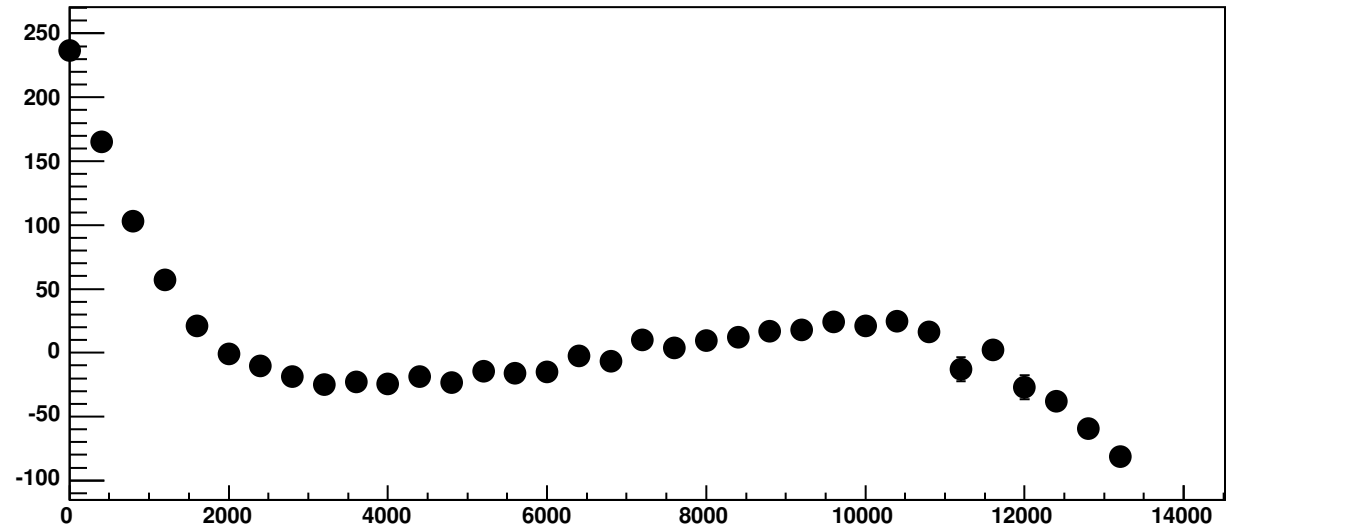
Chip 10, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



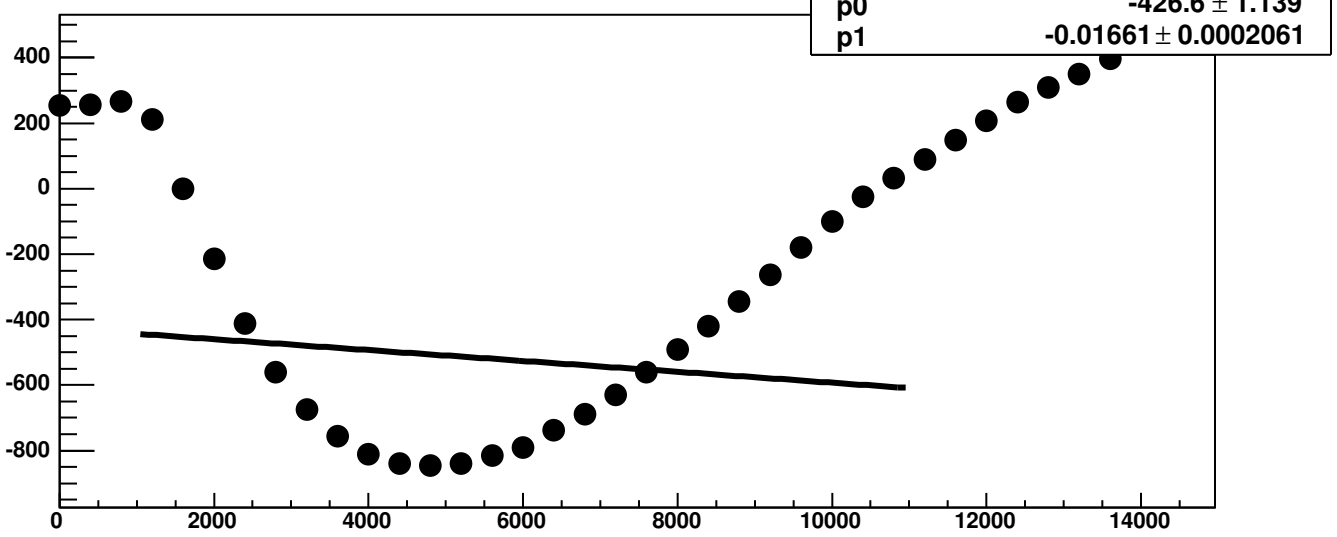
Chip 10, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



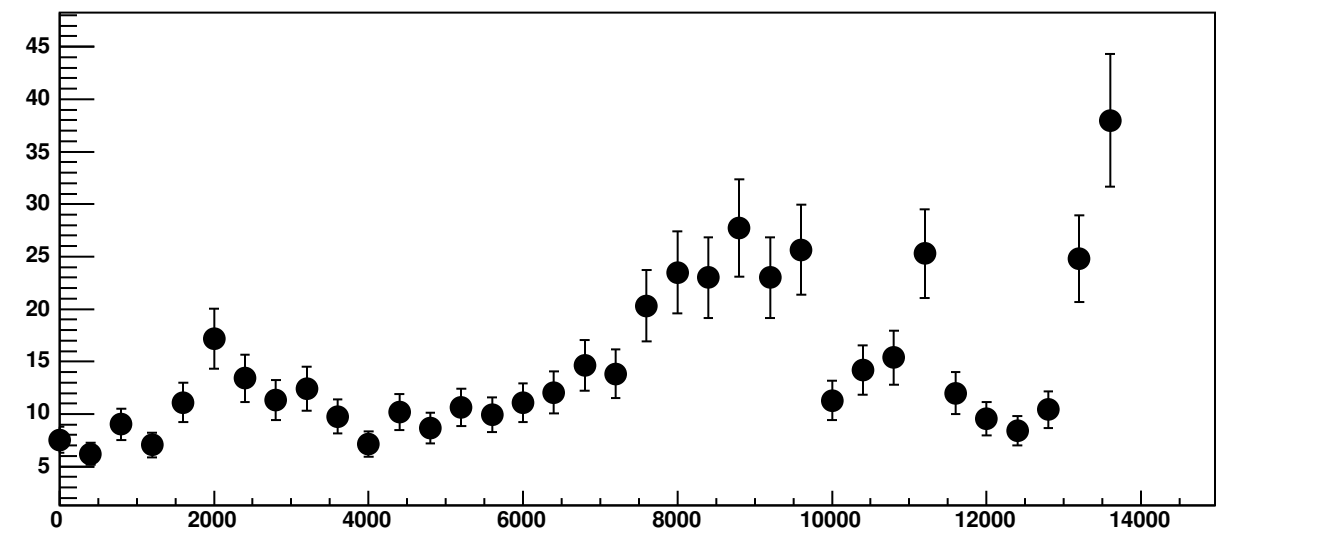
Chip 10, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



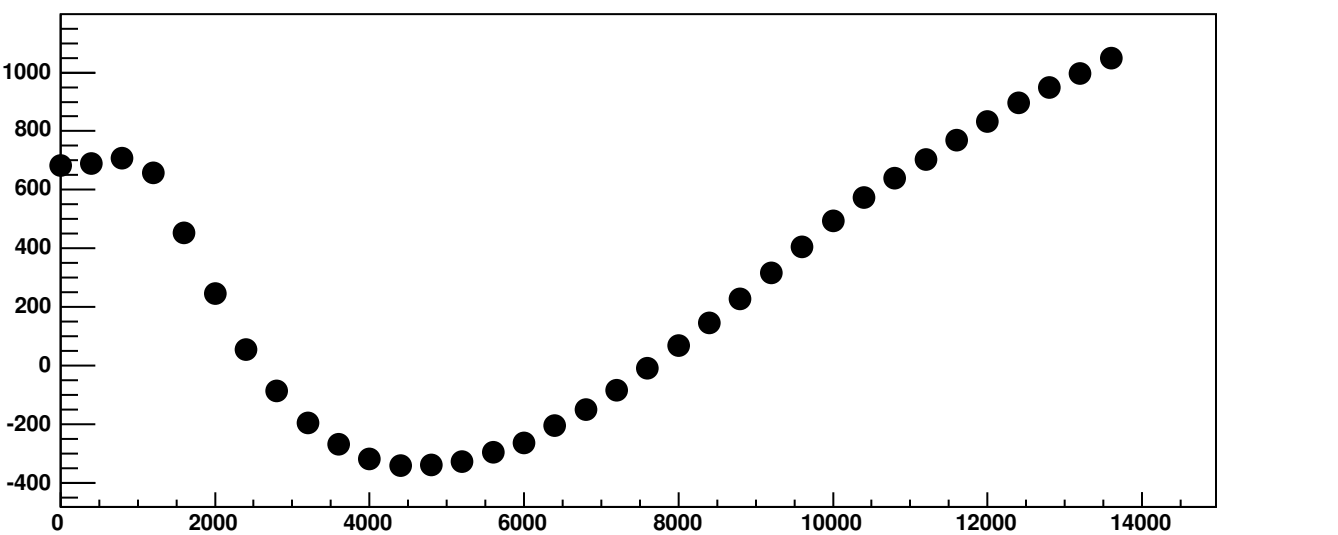
Chip 10, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

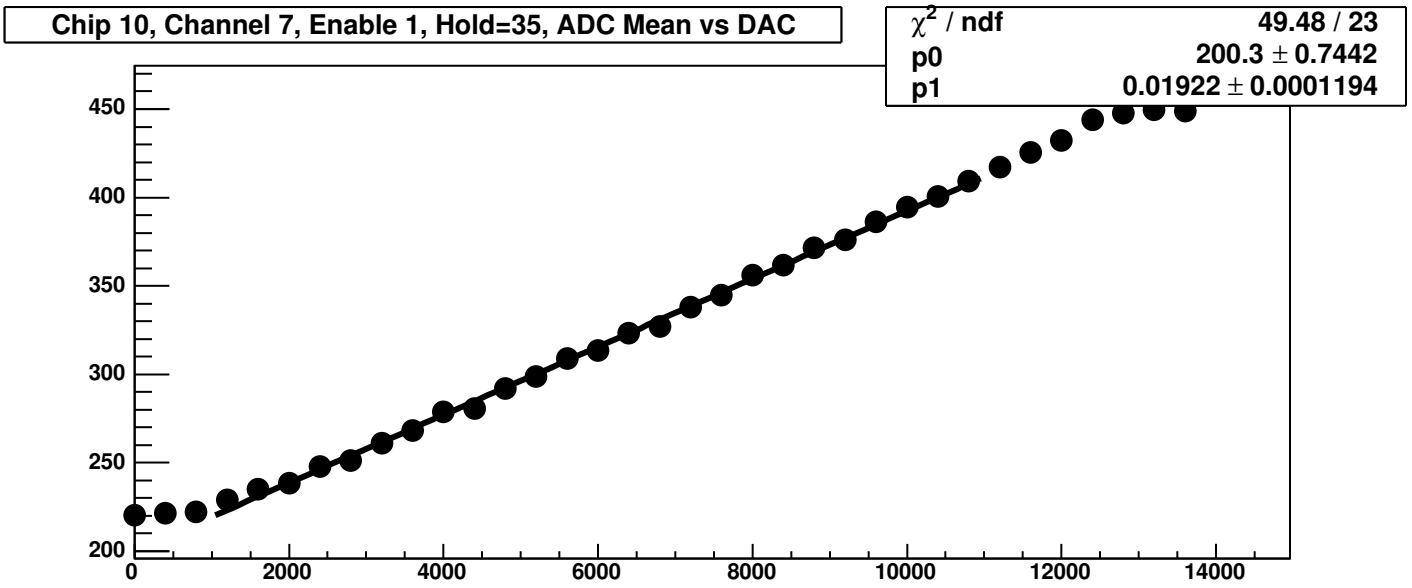


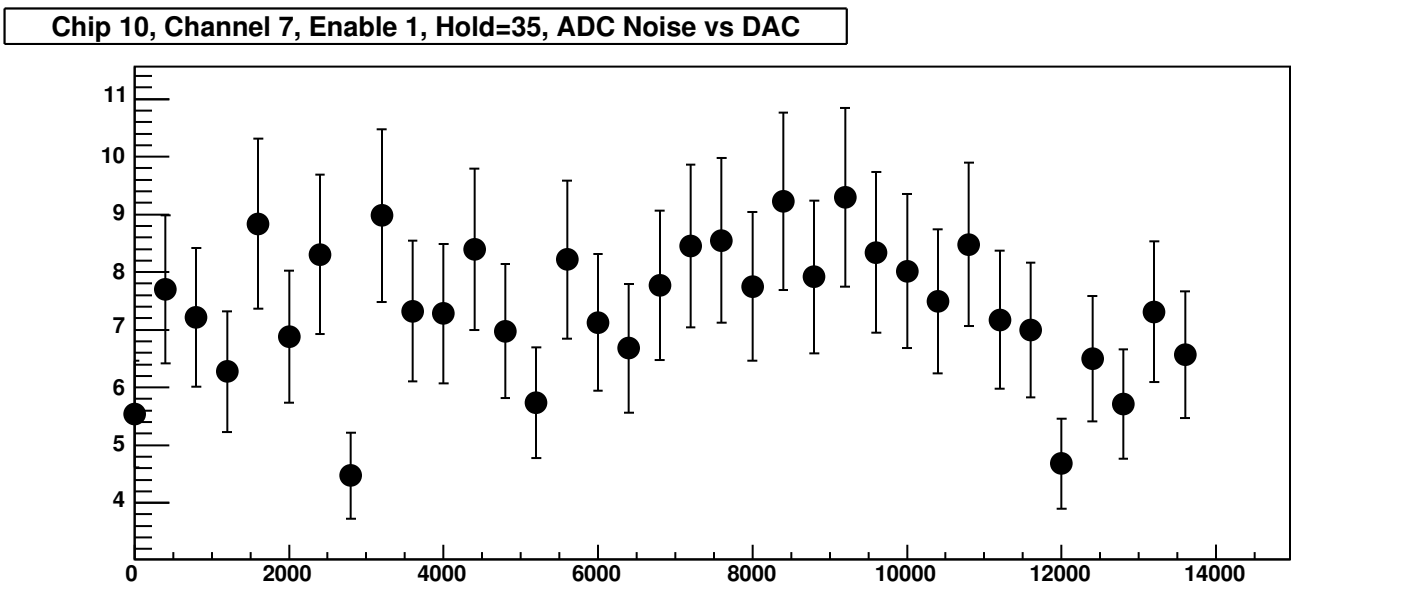
Chip 10, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

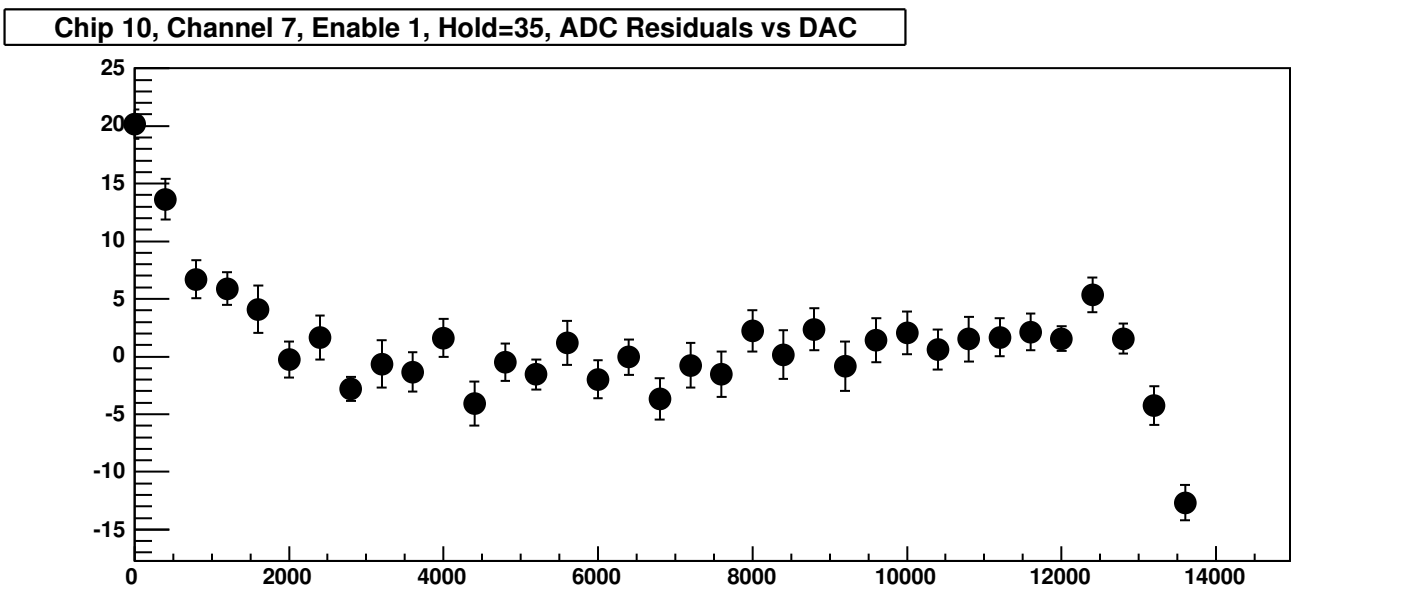


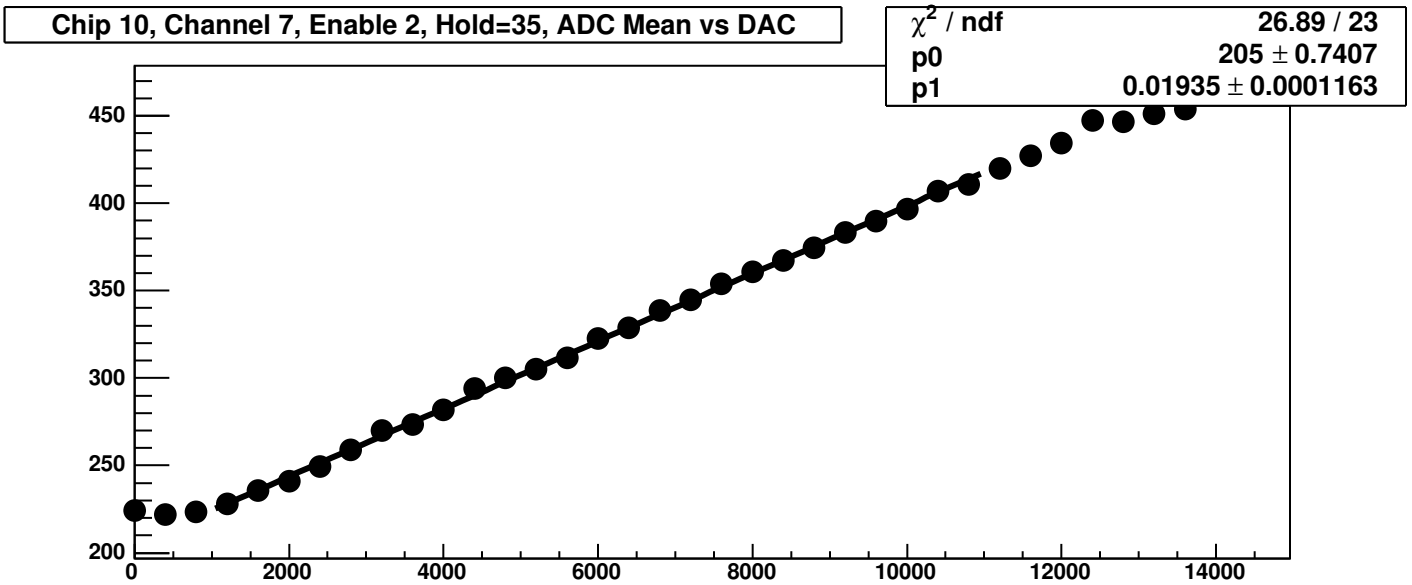
Chip 10, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

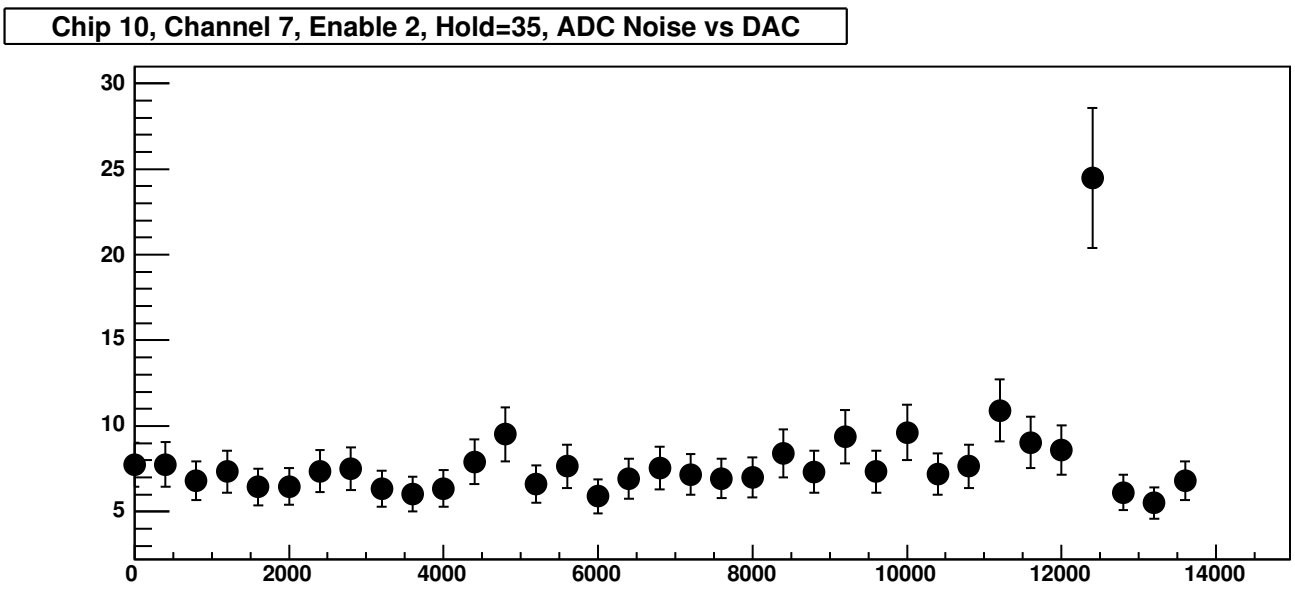


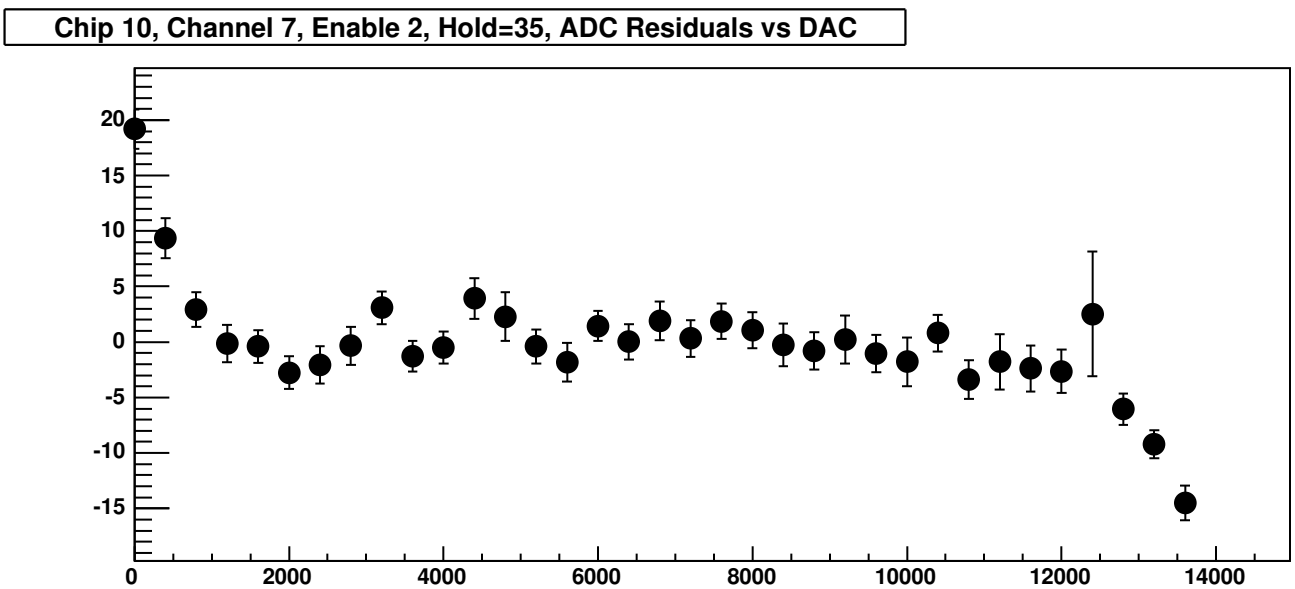

Chip 10, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.48 / 23
p0 200.3 ± 0.7442
p1 0.01922 ± 0.0001194
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

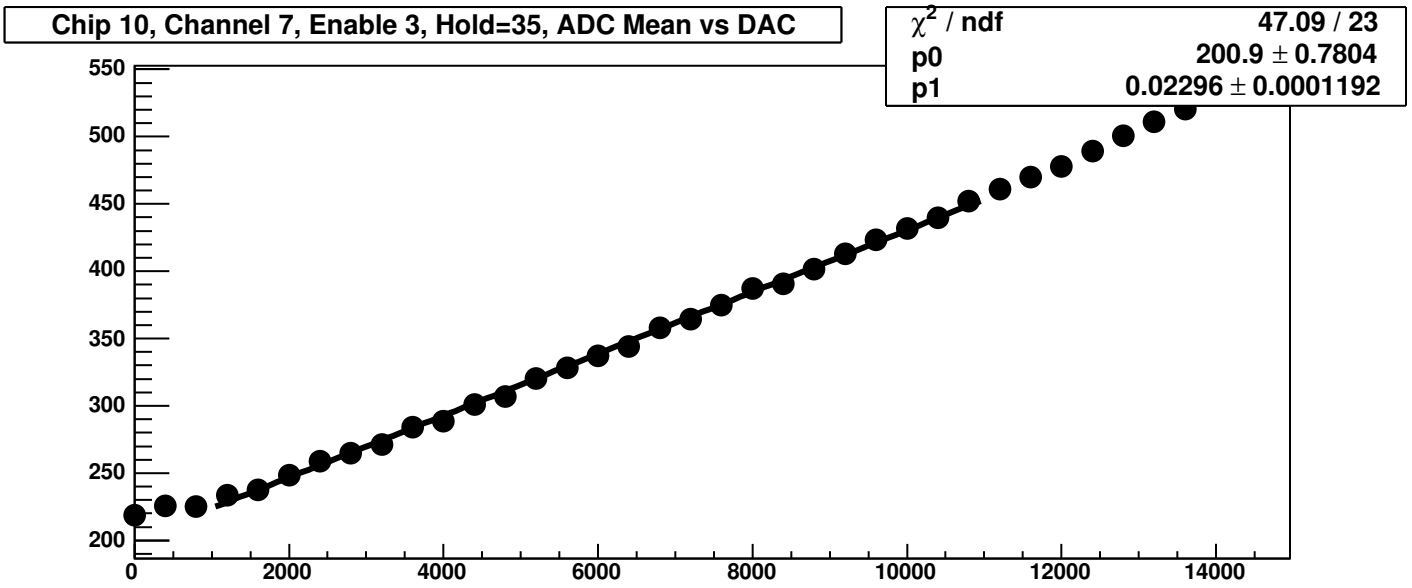

Chip 10, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

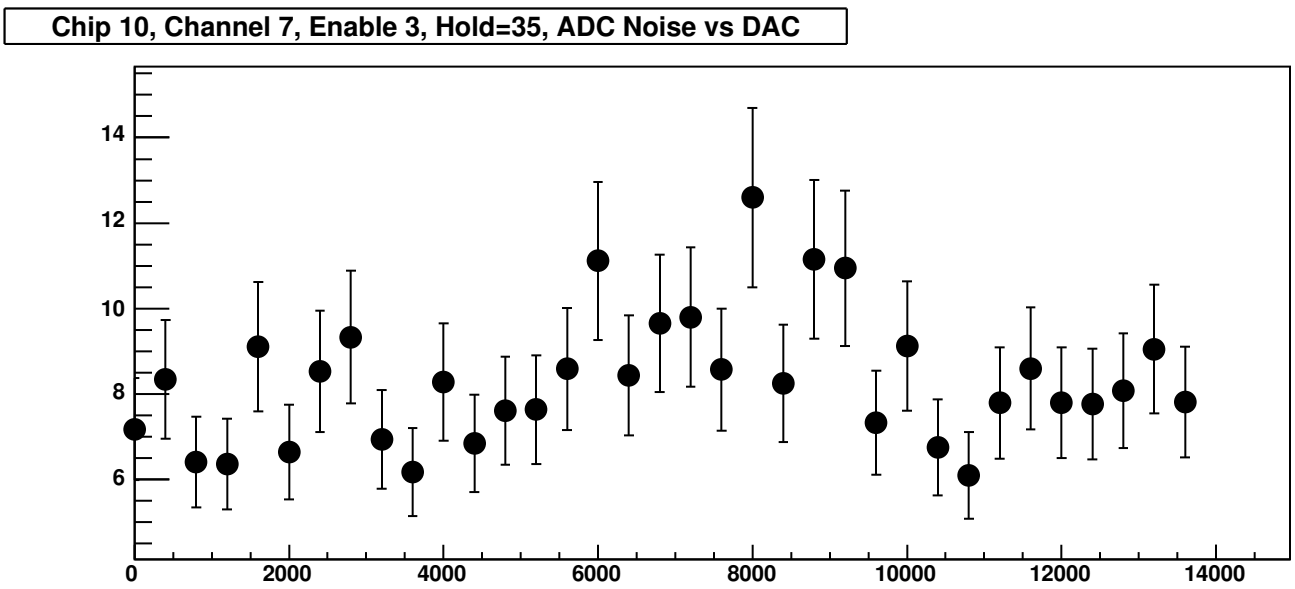

Chip 10, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

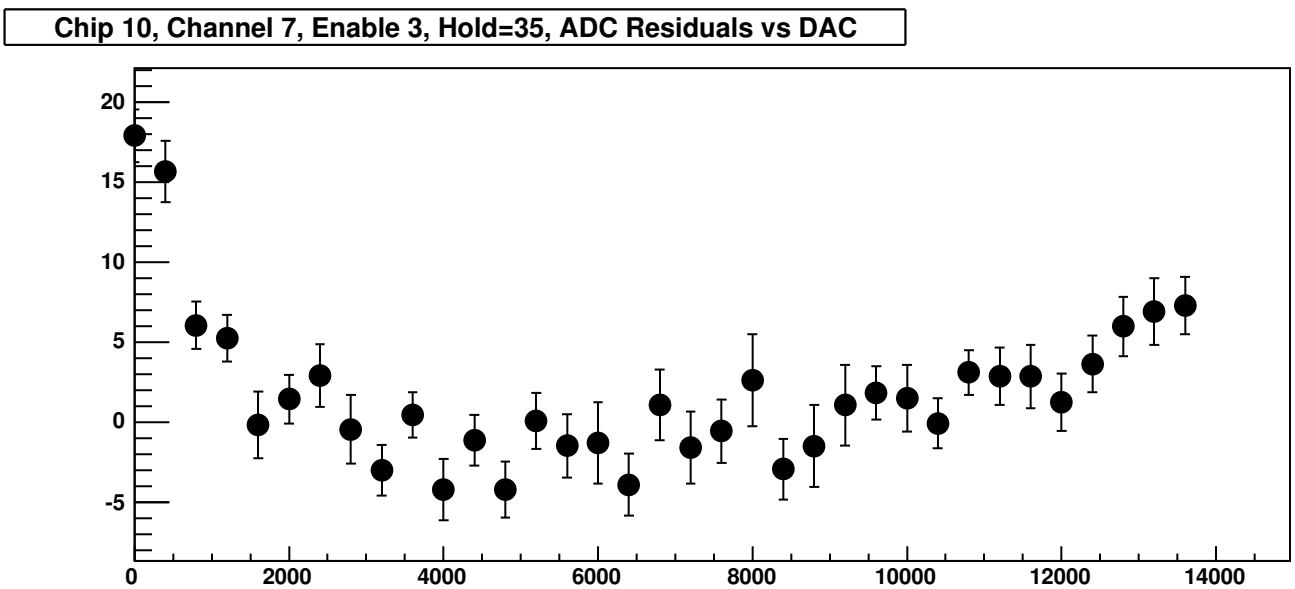

Chip 10, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.89 / 23
p0 205 ± 0.7407
p1 0.01935 ± 0.0001163
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

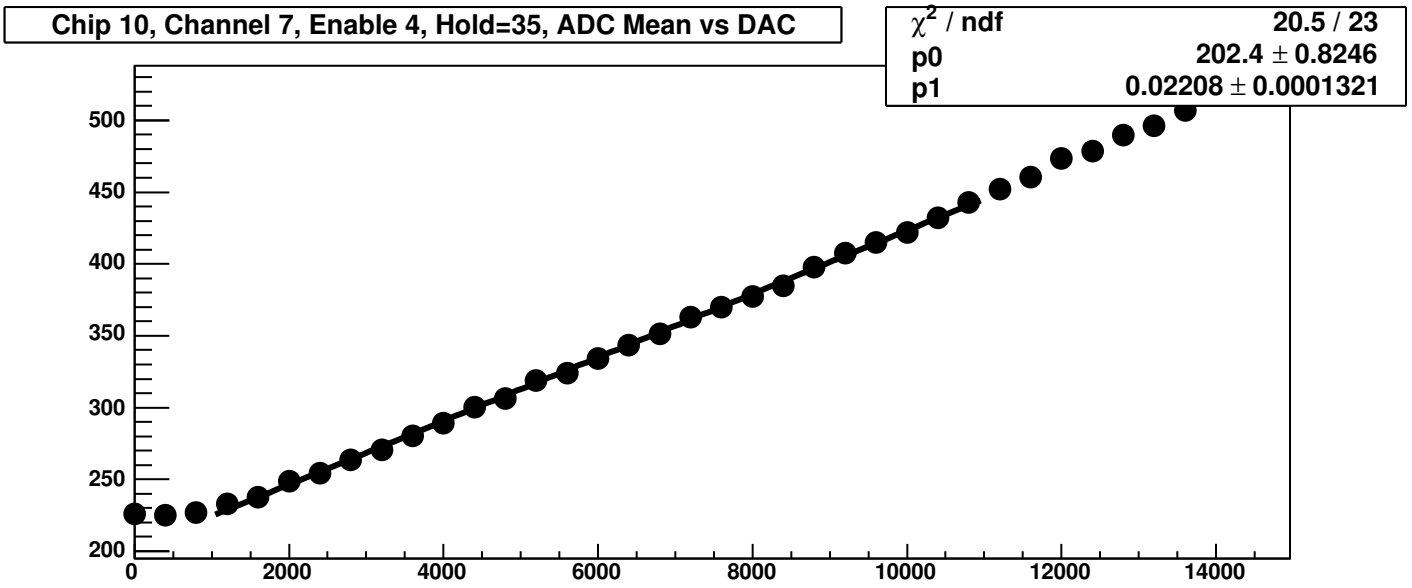

Chip 10, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

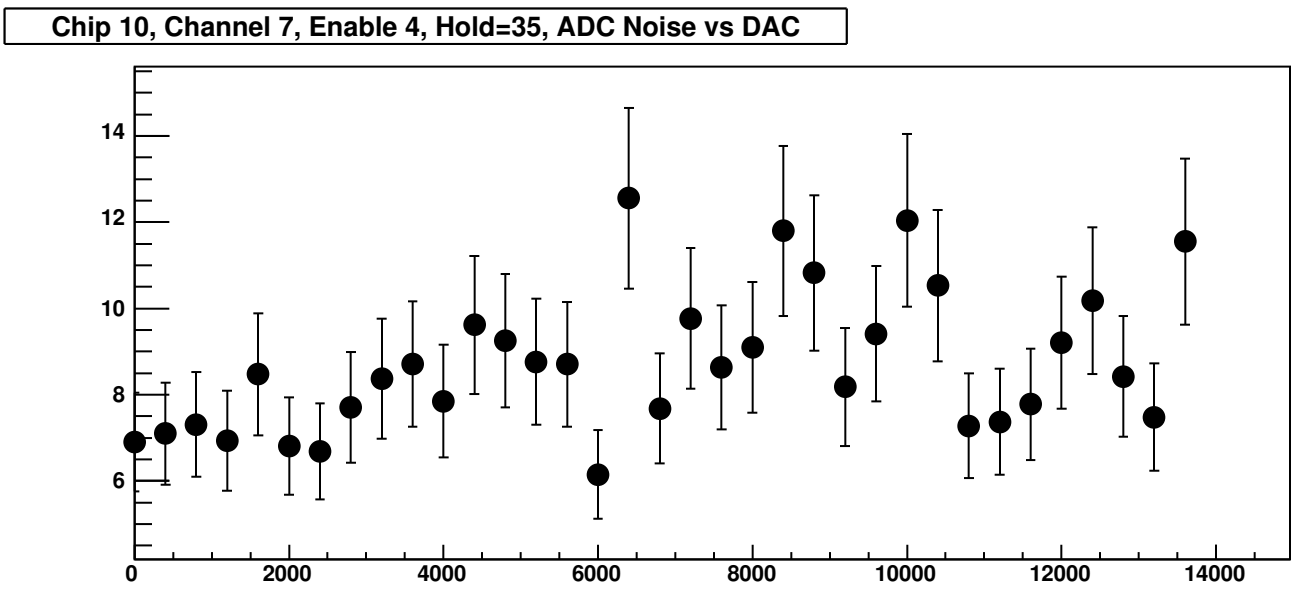

Chip 10, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

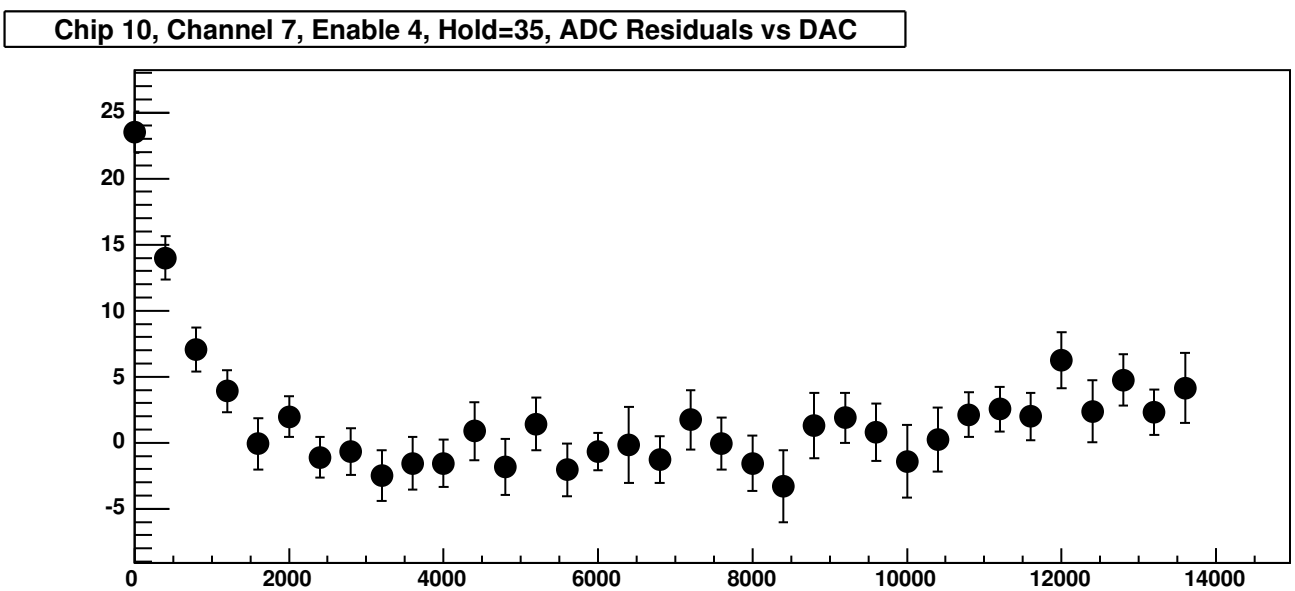

Chip 10, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.09 / 23
p0 200.9 ± 0.7804
p1 0.02296 ± 0.0001192
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

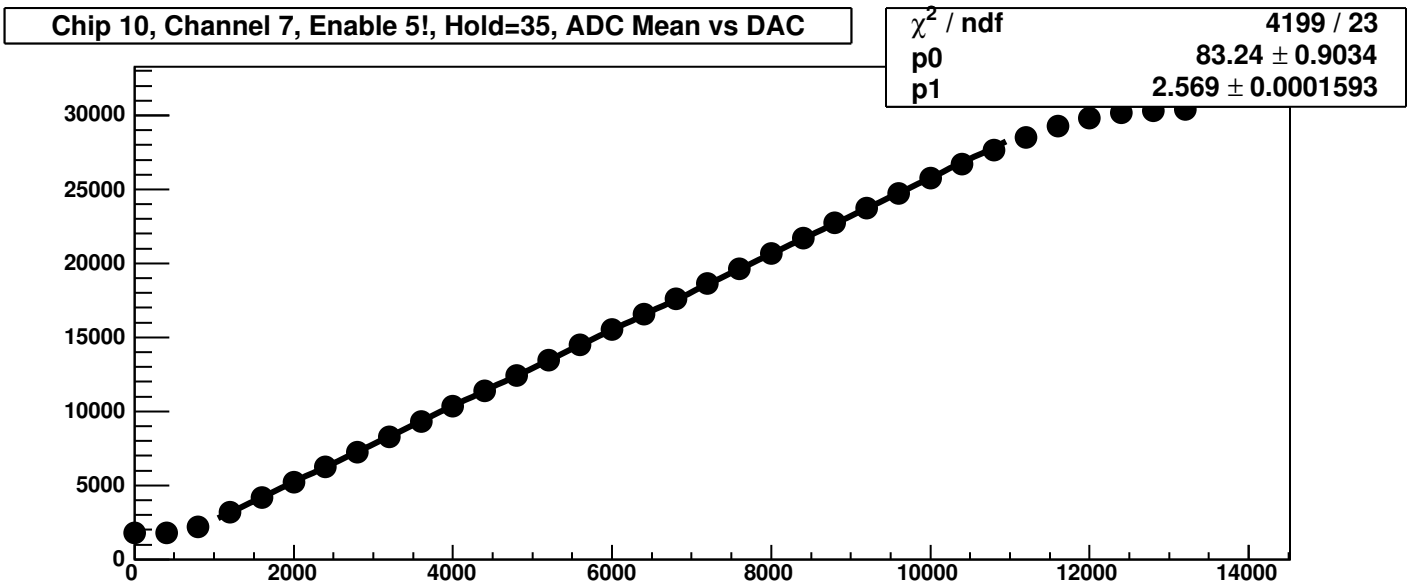

Chip 10, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

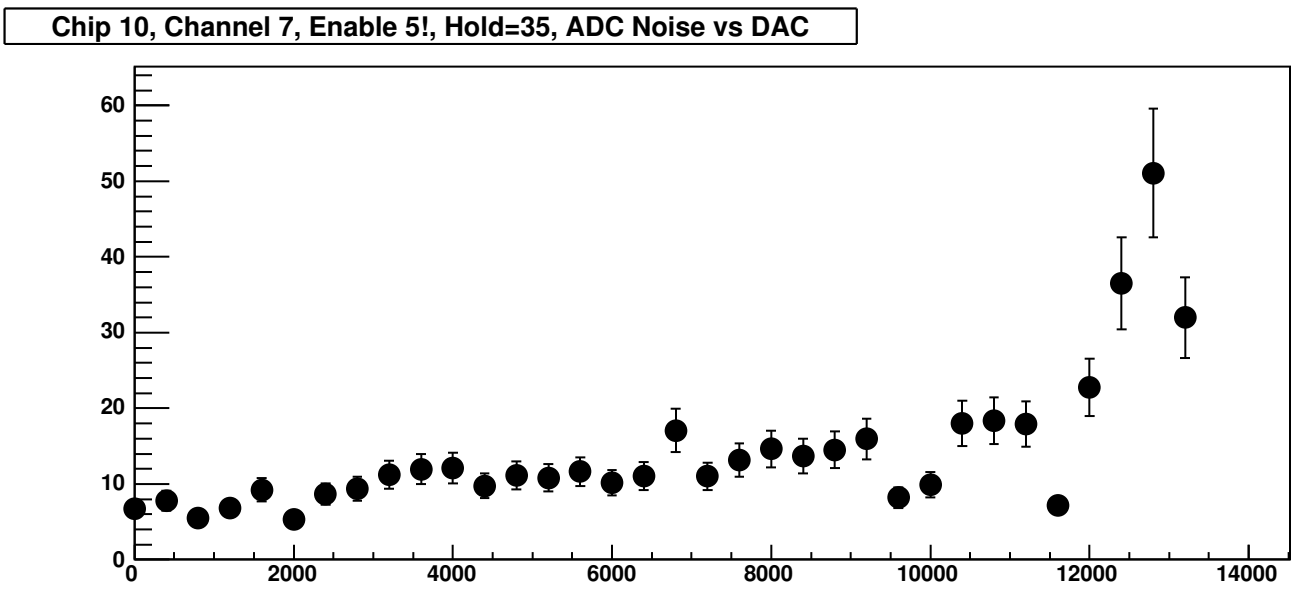

Chip 10, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

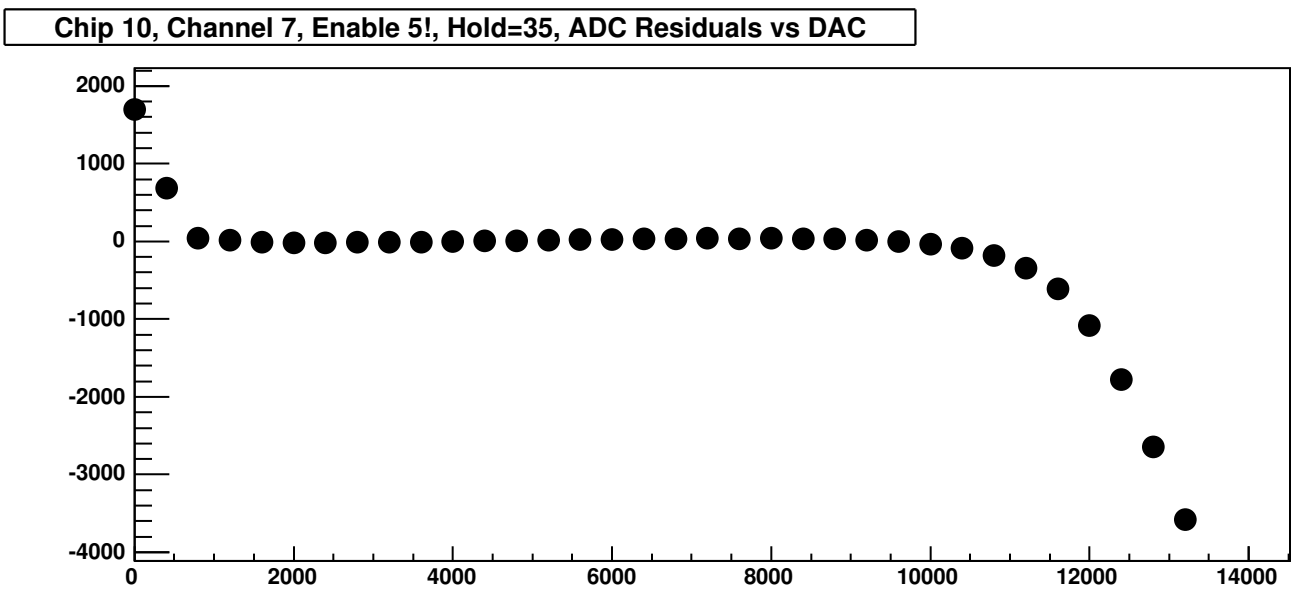

Chip 10, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.5 / 23
p0 202.4 ± 0.8246
p1 0.02208 ± 0.0001321
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

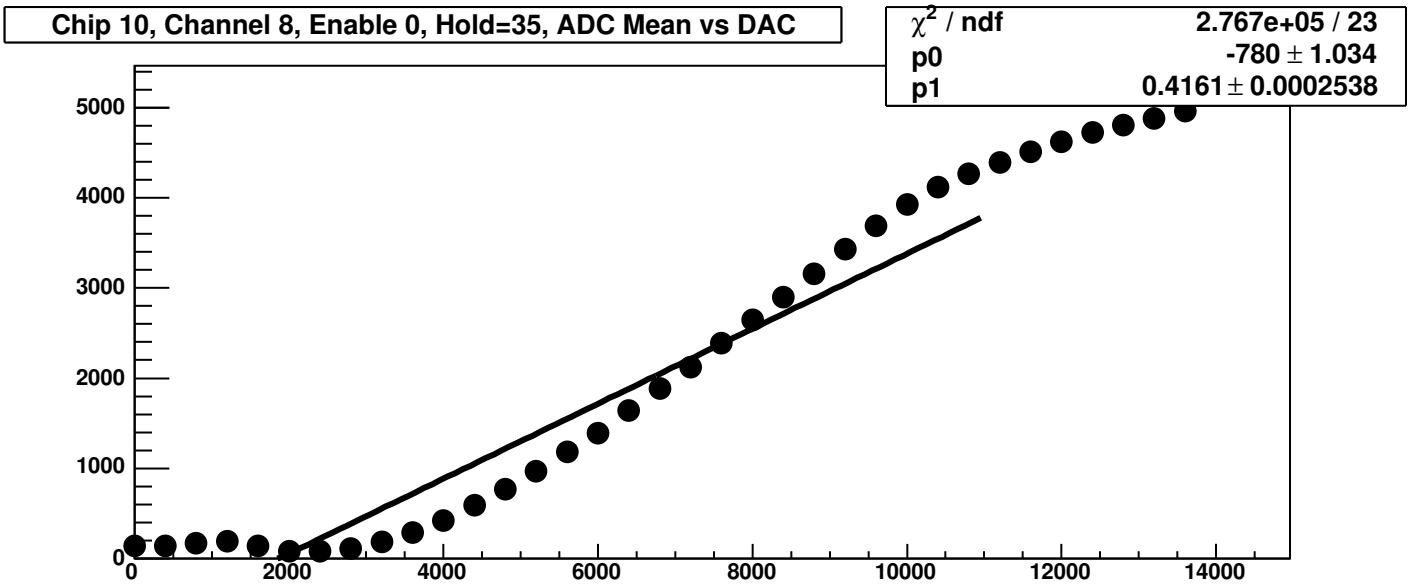

Chip 10, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

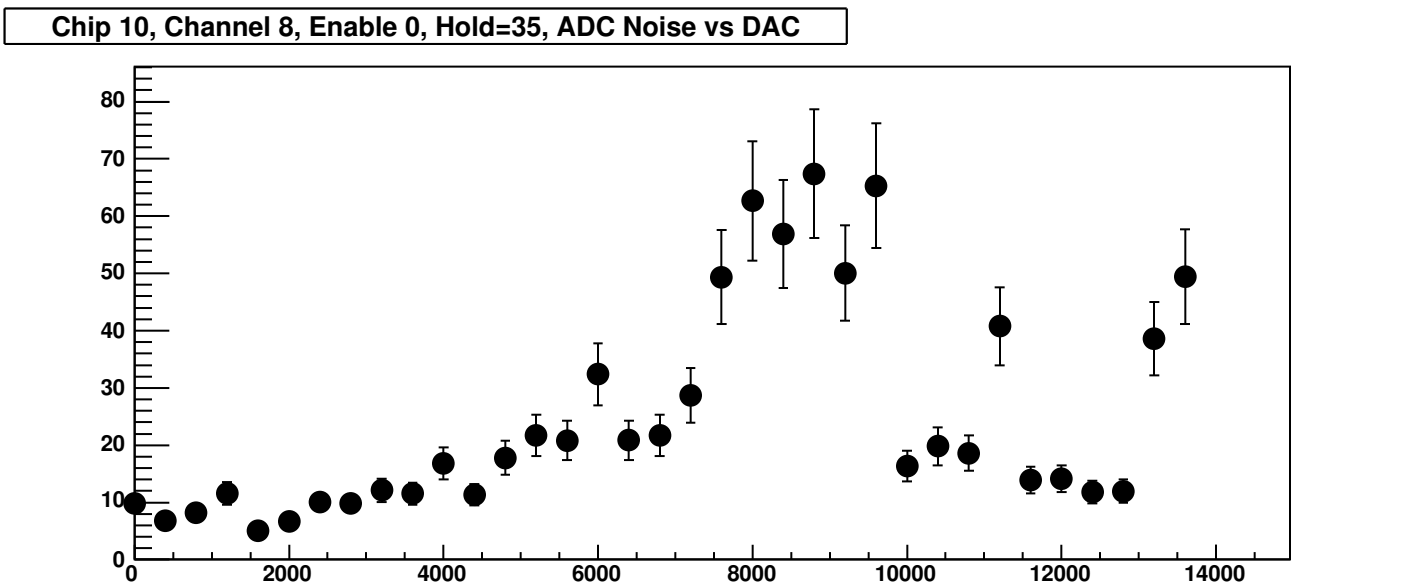

Chip 10, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

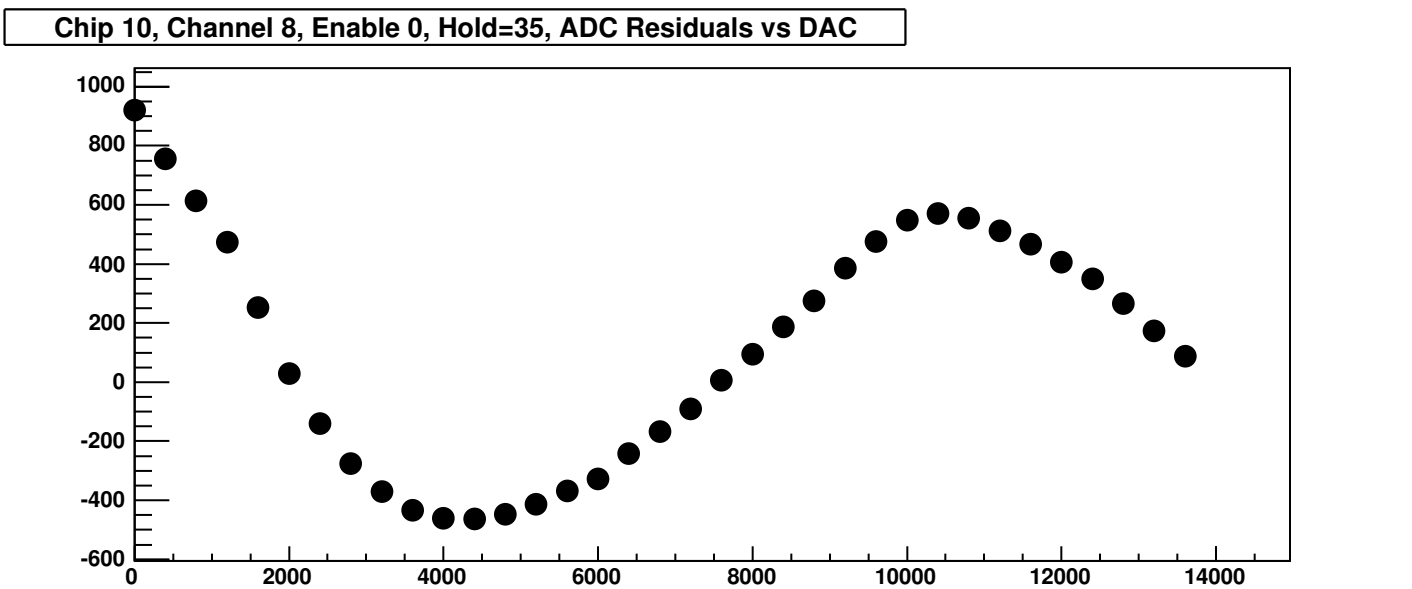

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4199 / 23
p0 83.24 ± 0.9034
p1 2.569 ± 0.0001593
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

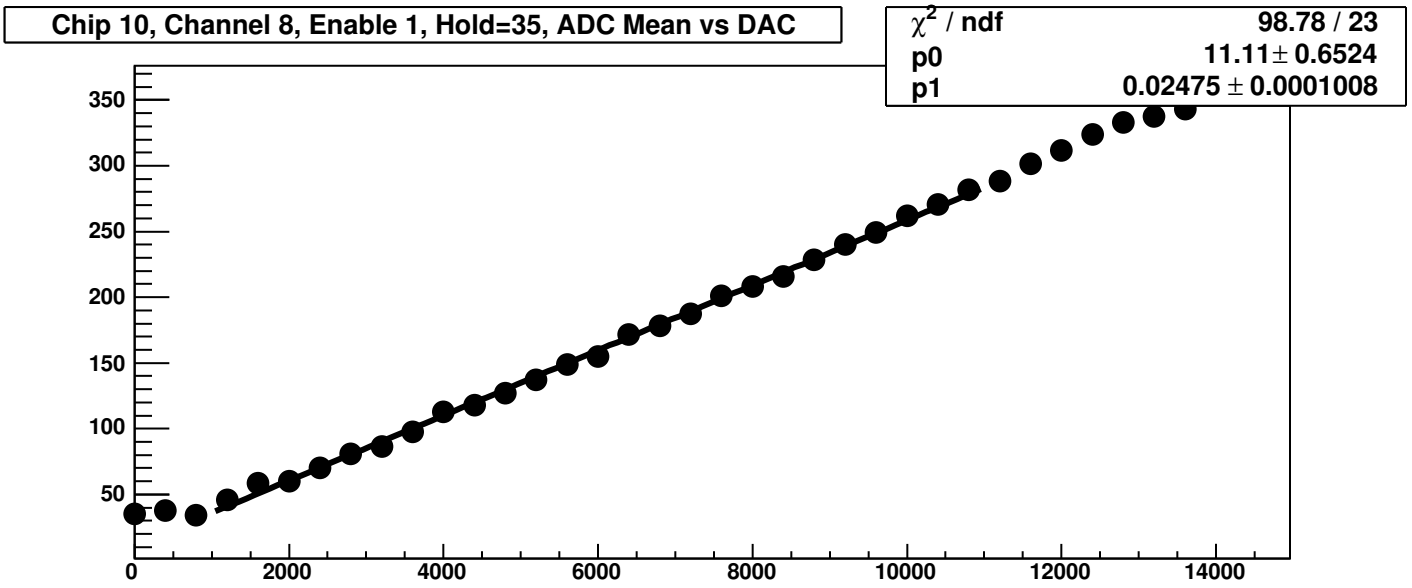

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

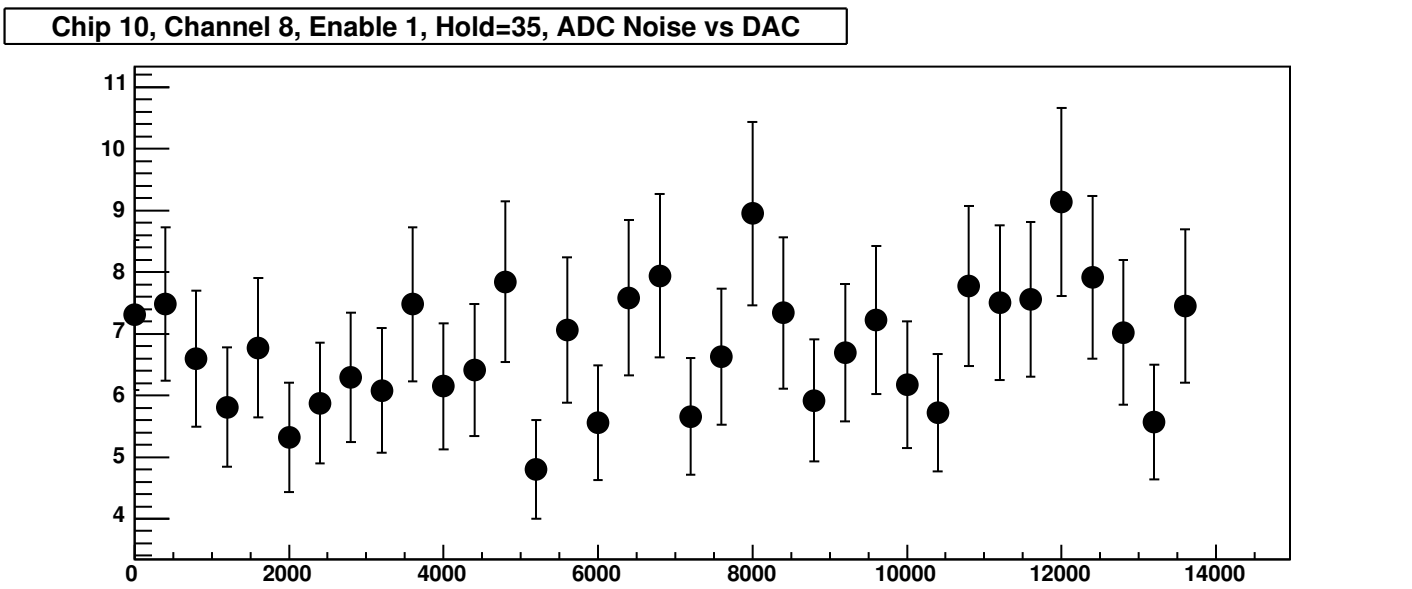

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

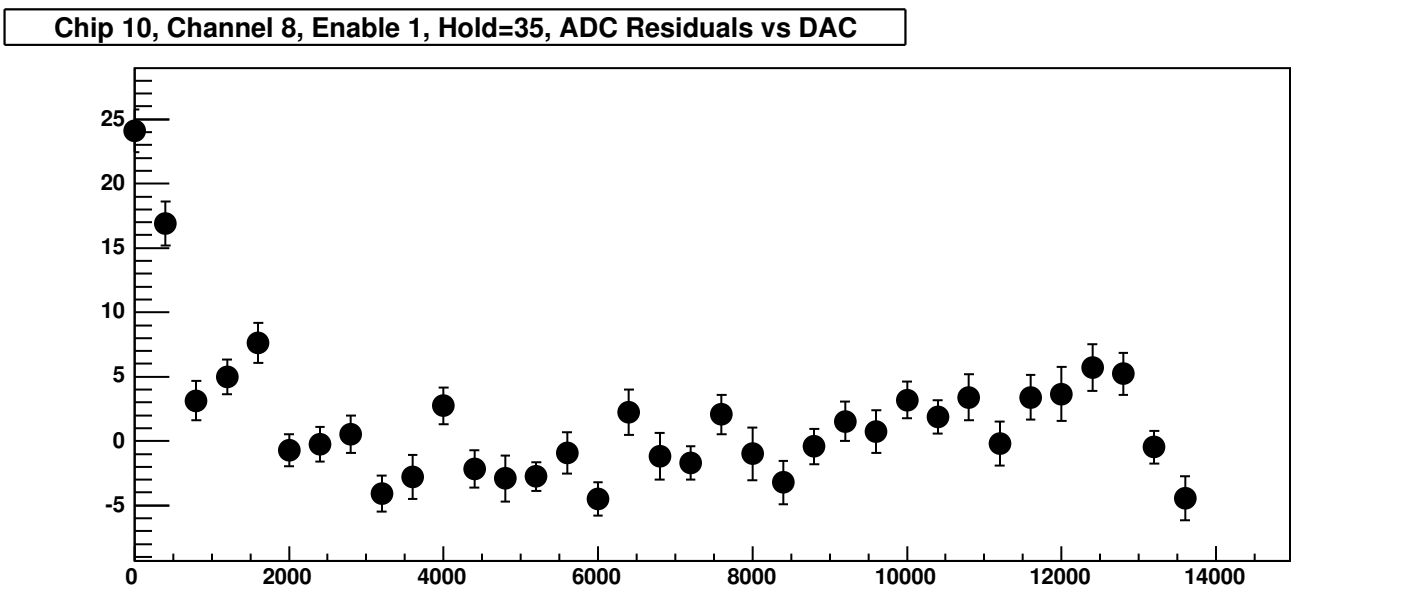

Chip 10, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.767e+05 / 23
p0 -780 ± 1.034
p1 0.4161 ± 0.0002538
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

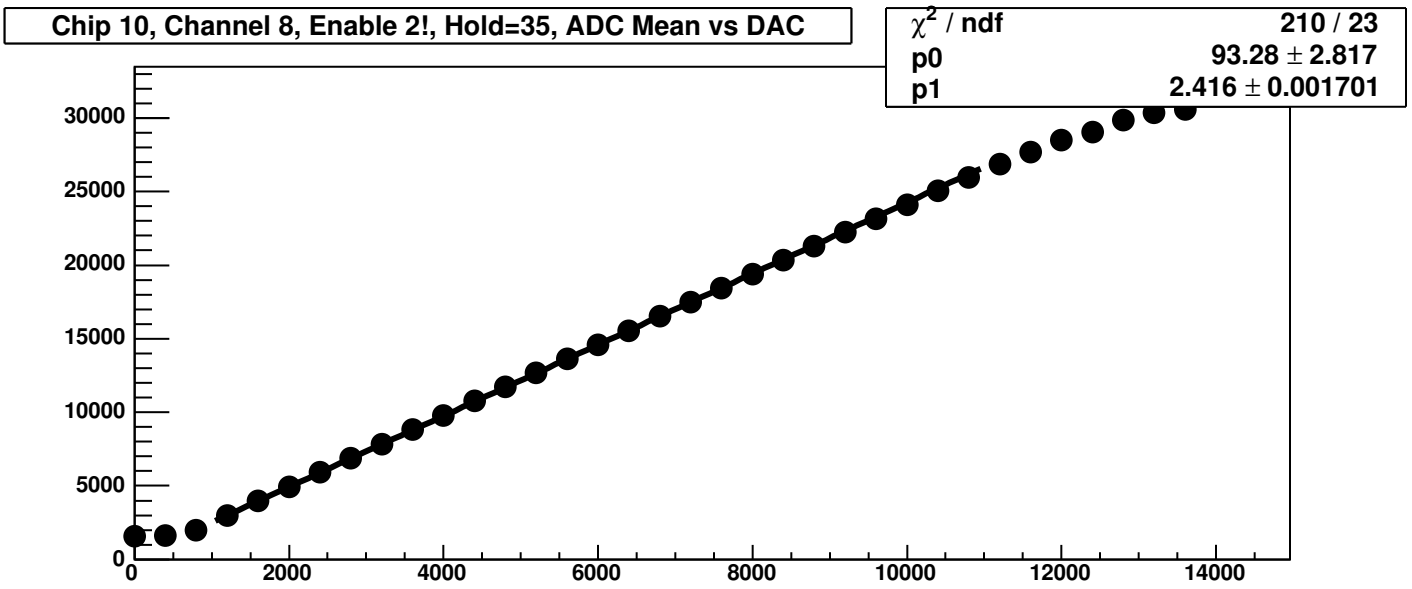

Chip 10, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

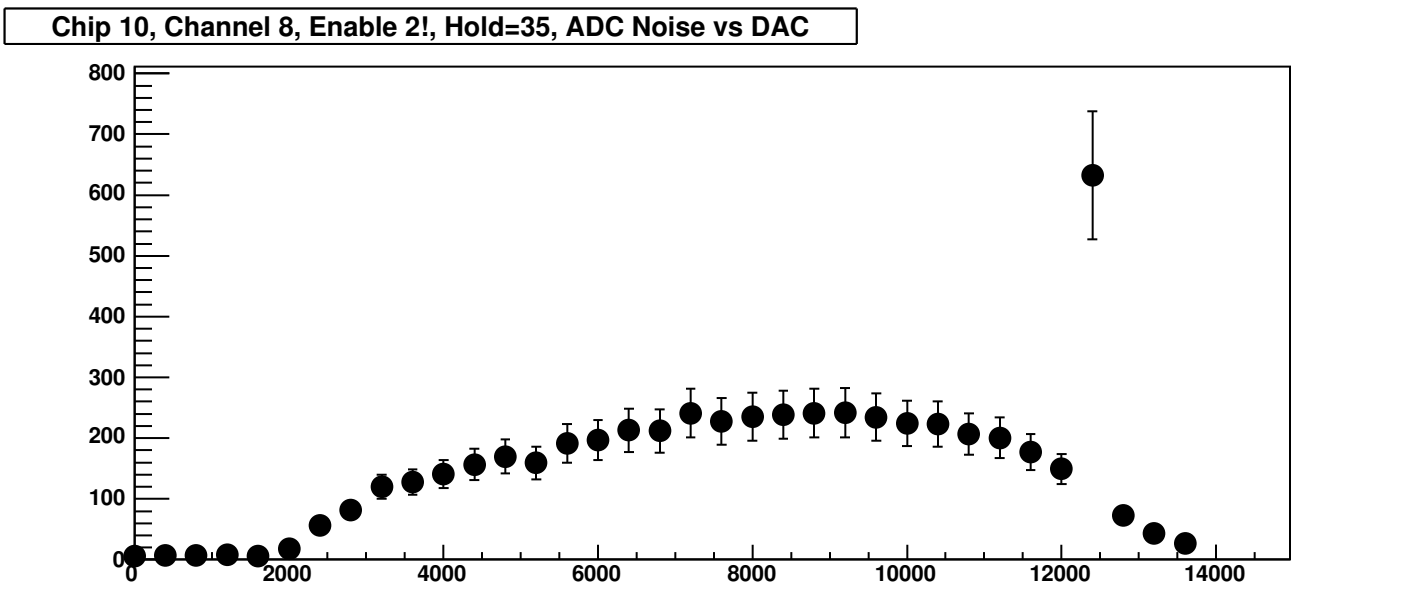

Chip 10, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

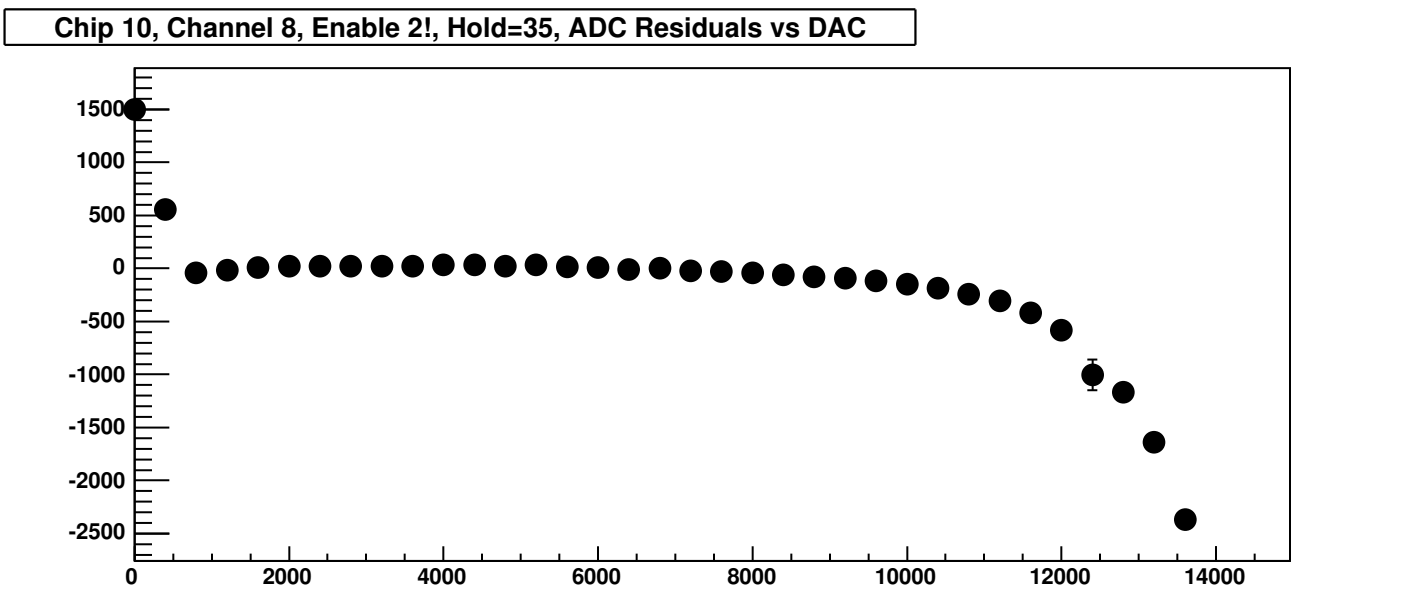

Chip 10, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 98.78 / 23
p0 11.11 ± 0.6524
p1 0.02475 ± 0.0001008
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

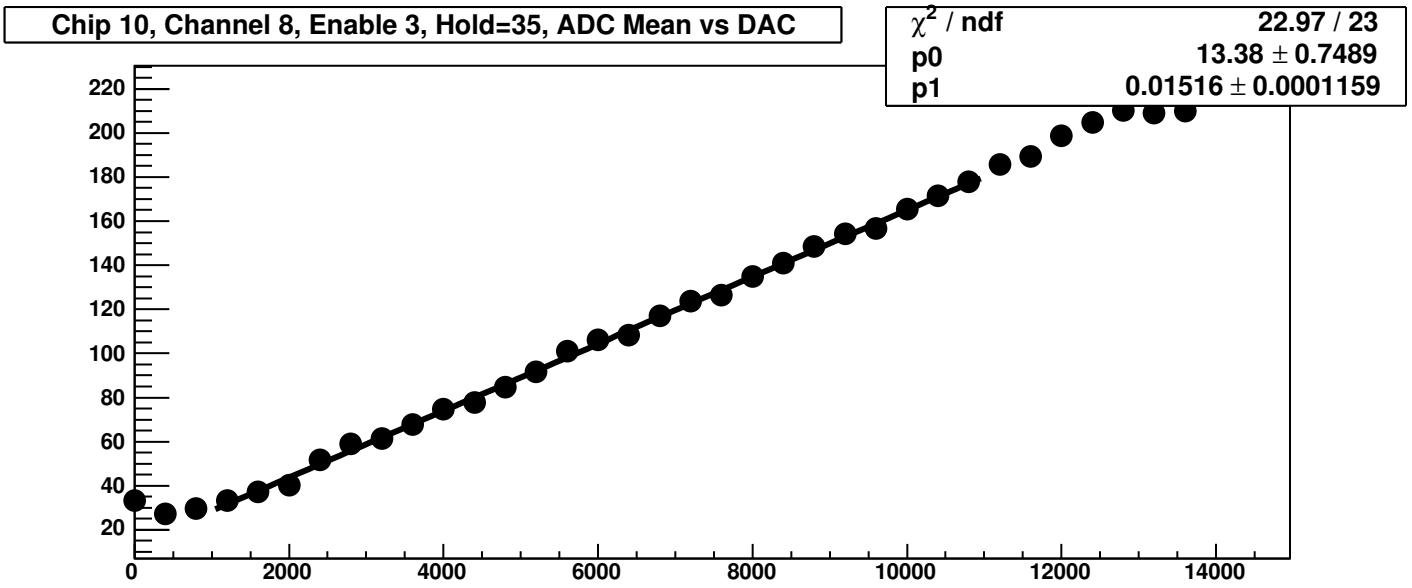

Chip 10, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

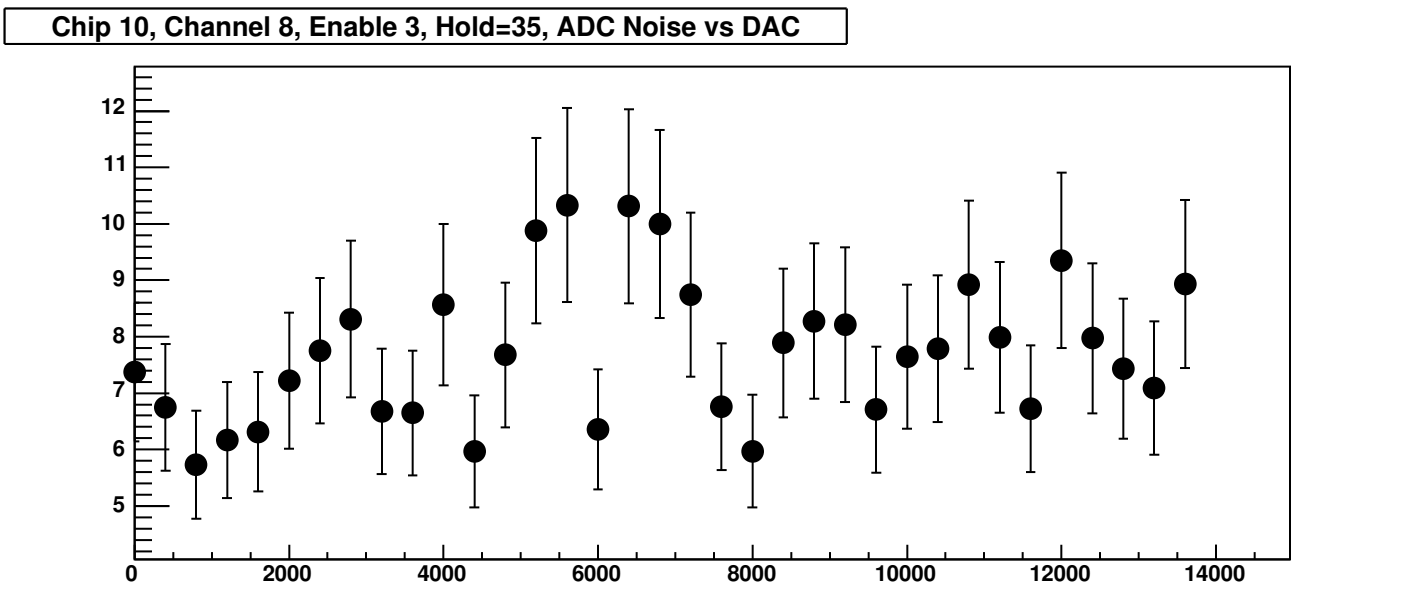

Chip 10, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

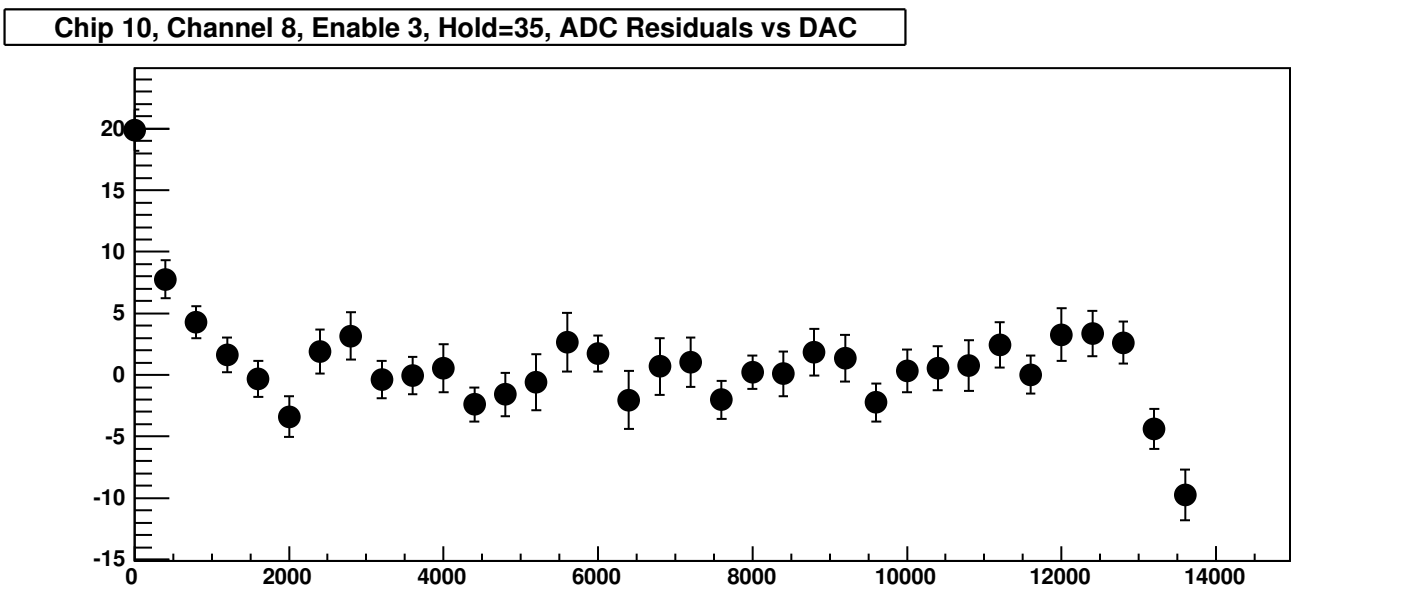

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 210 / 23
p0 93.28 ± 2.817
p1 2.416 ± 0.001701
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

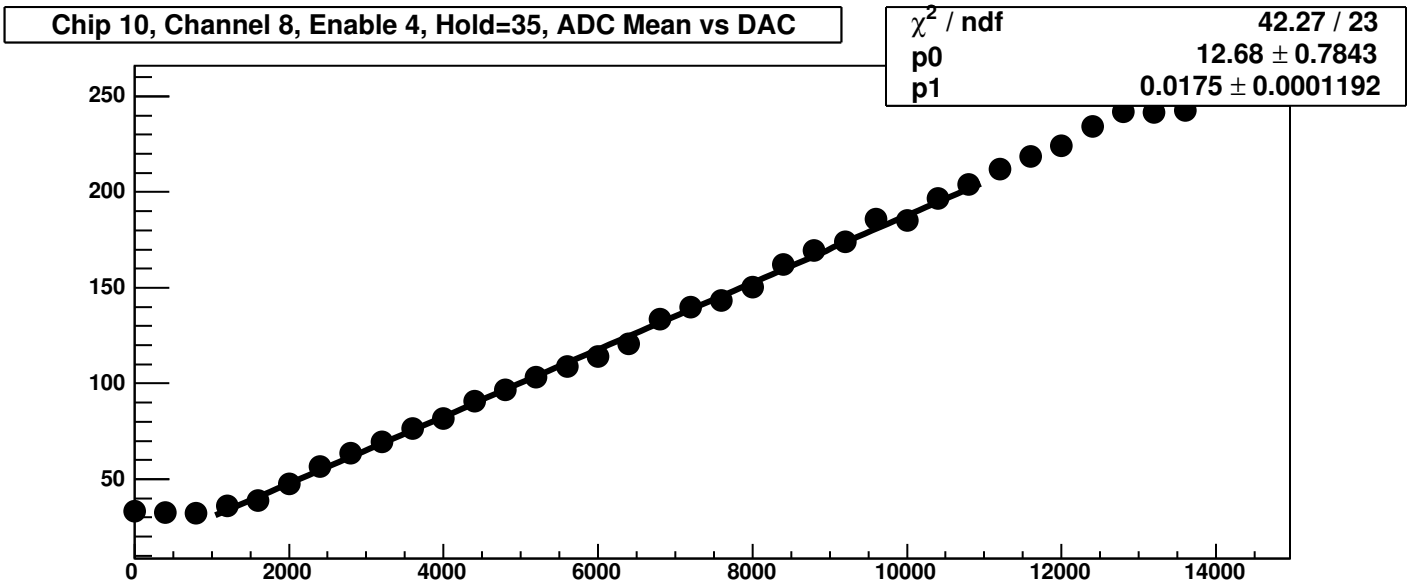

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

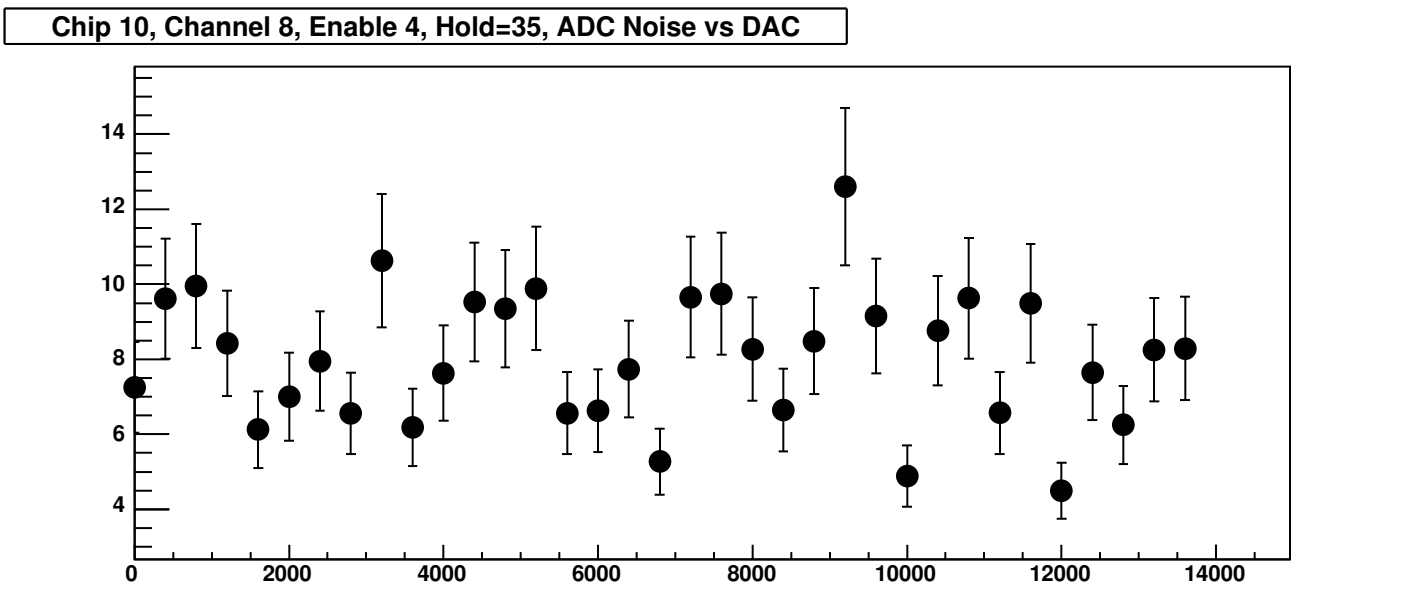

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

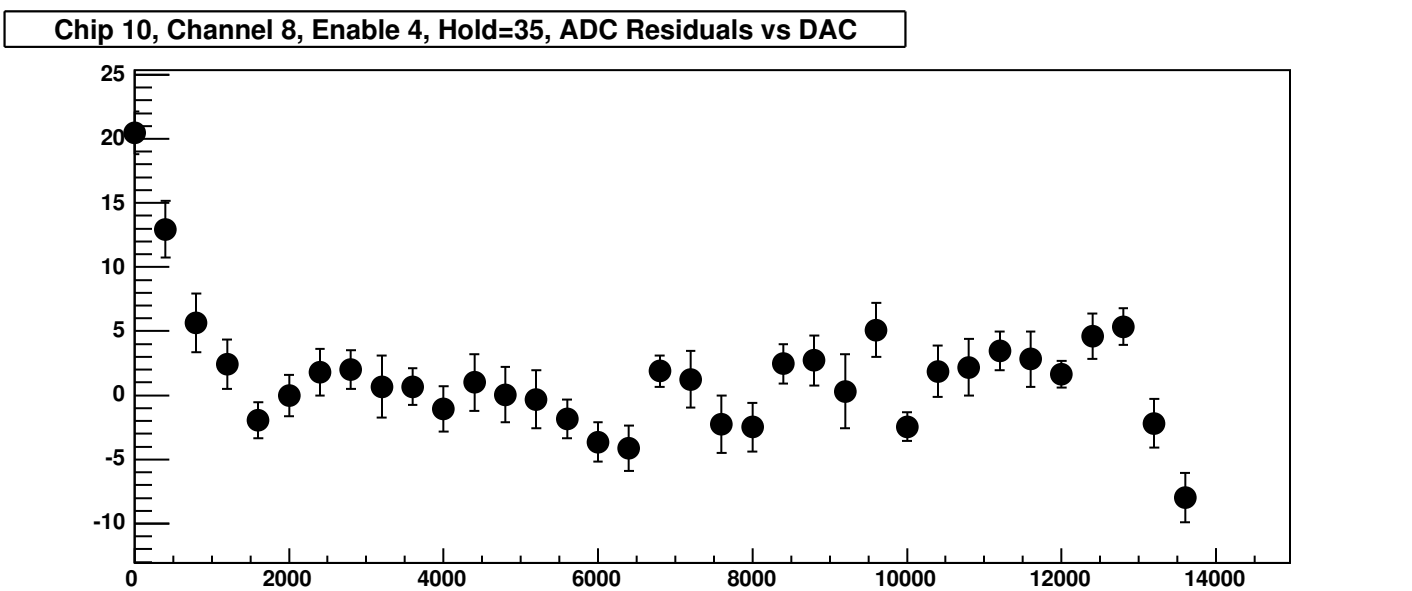

Chip 10, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.97 / 23
p0 13.38 ± 0.7489
p1 0.01516 ± 0.0001159
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

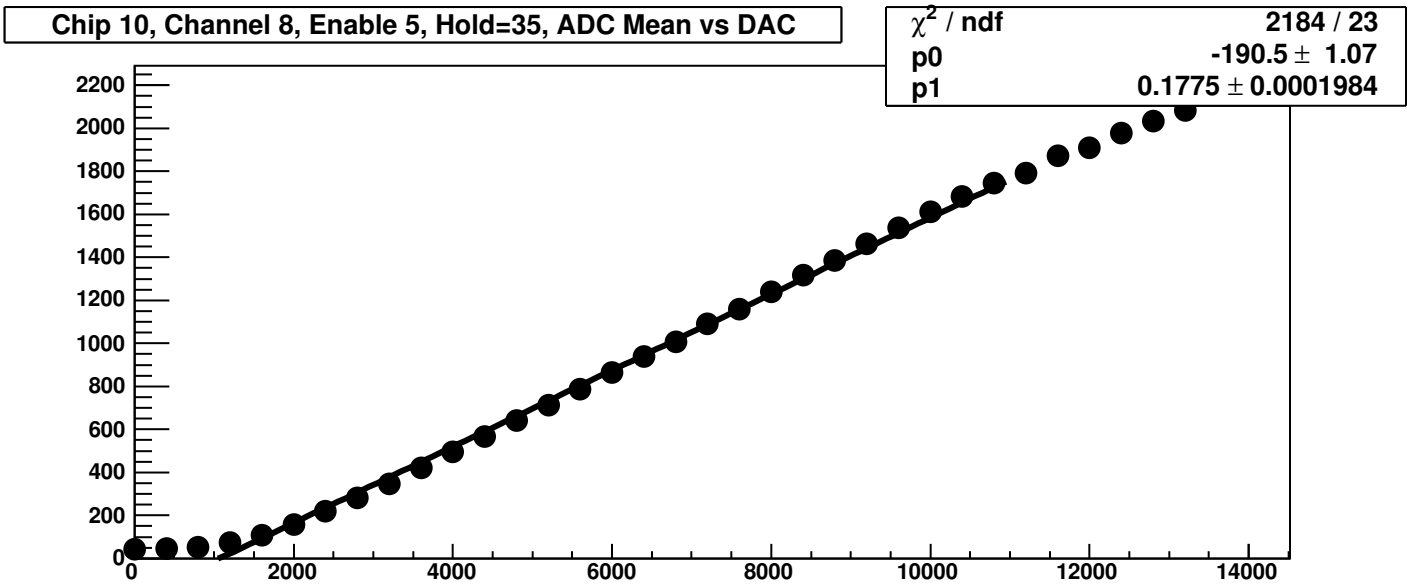

Chip 10, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

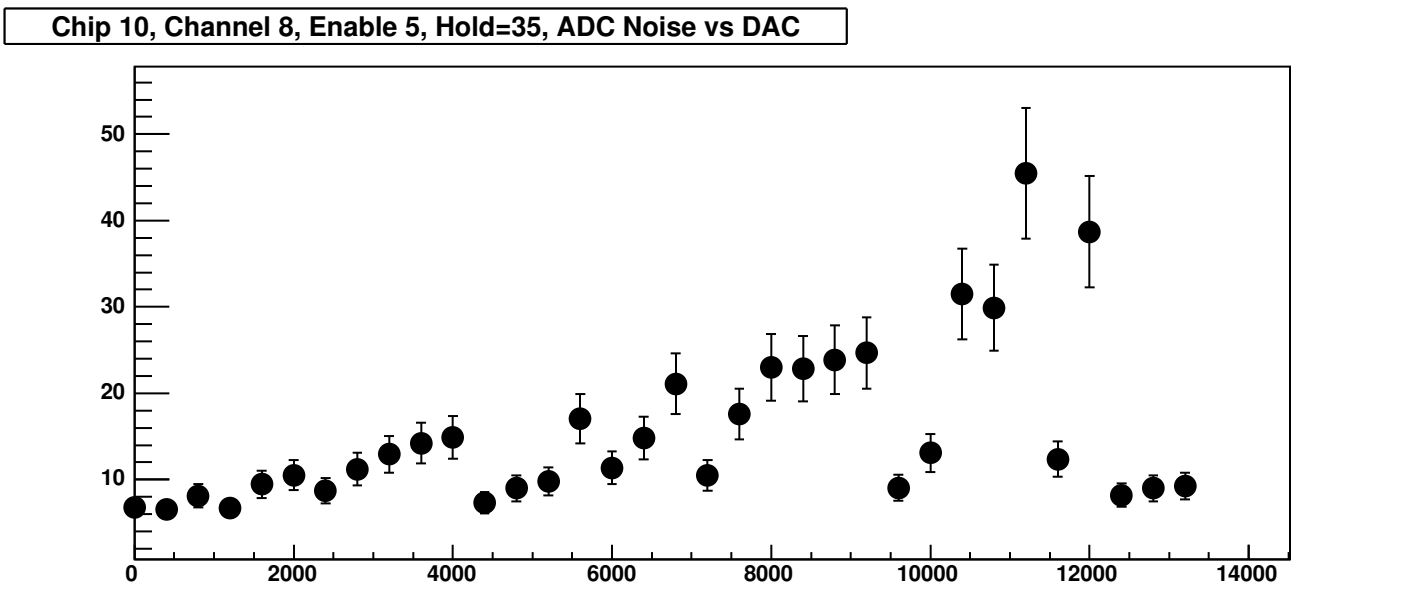

Chip 10, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

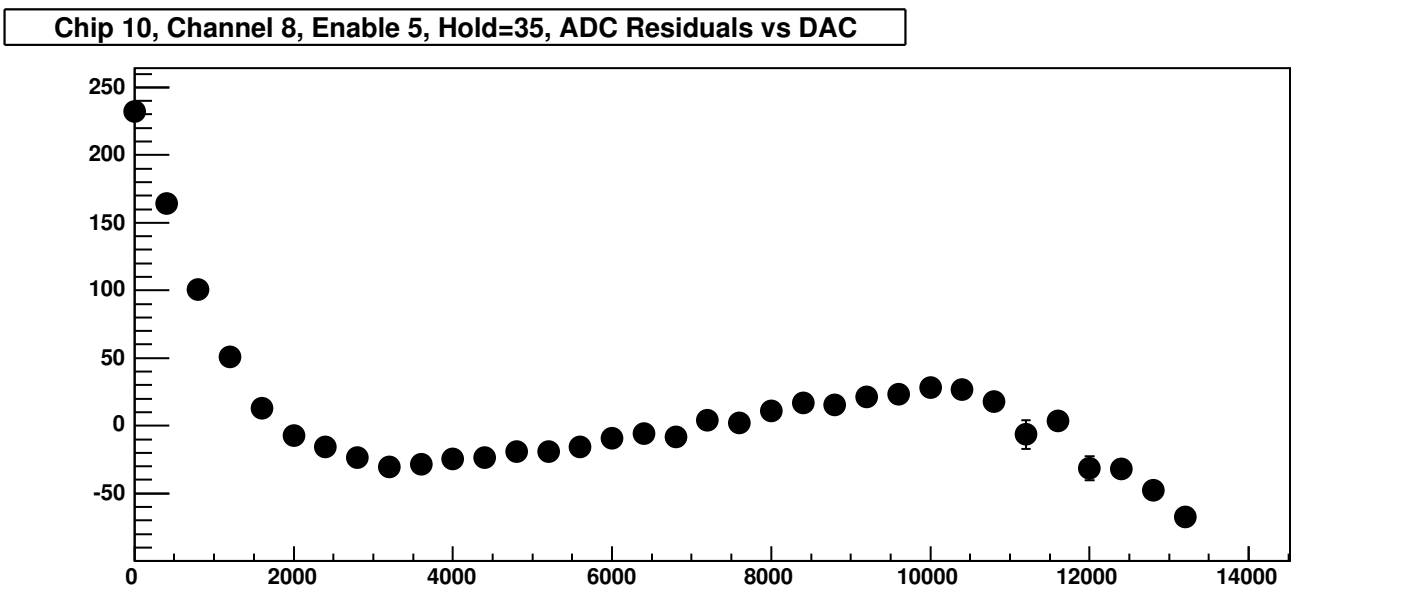

Chip 10, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.27 / 23
p0 12.68 ± 0.7843
p1 0.0175 ± 0.0001192
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

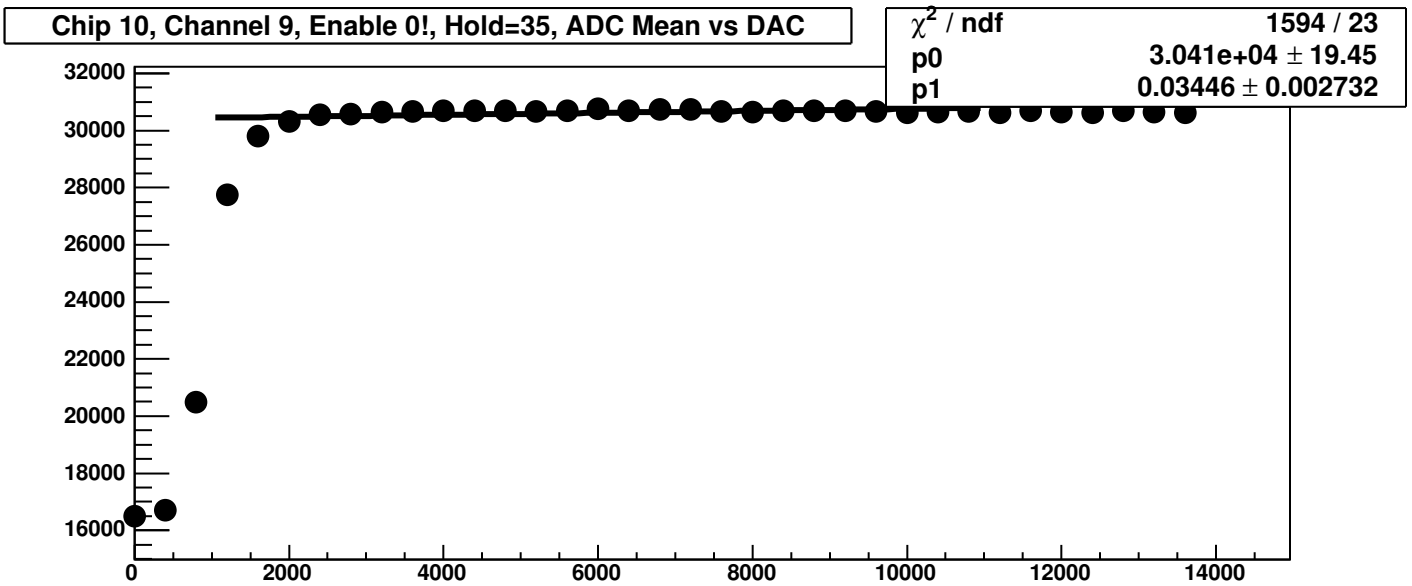

Chip 10, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

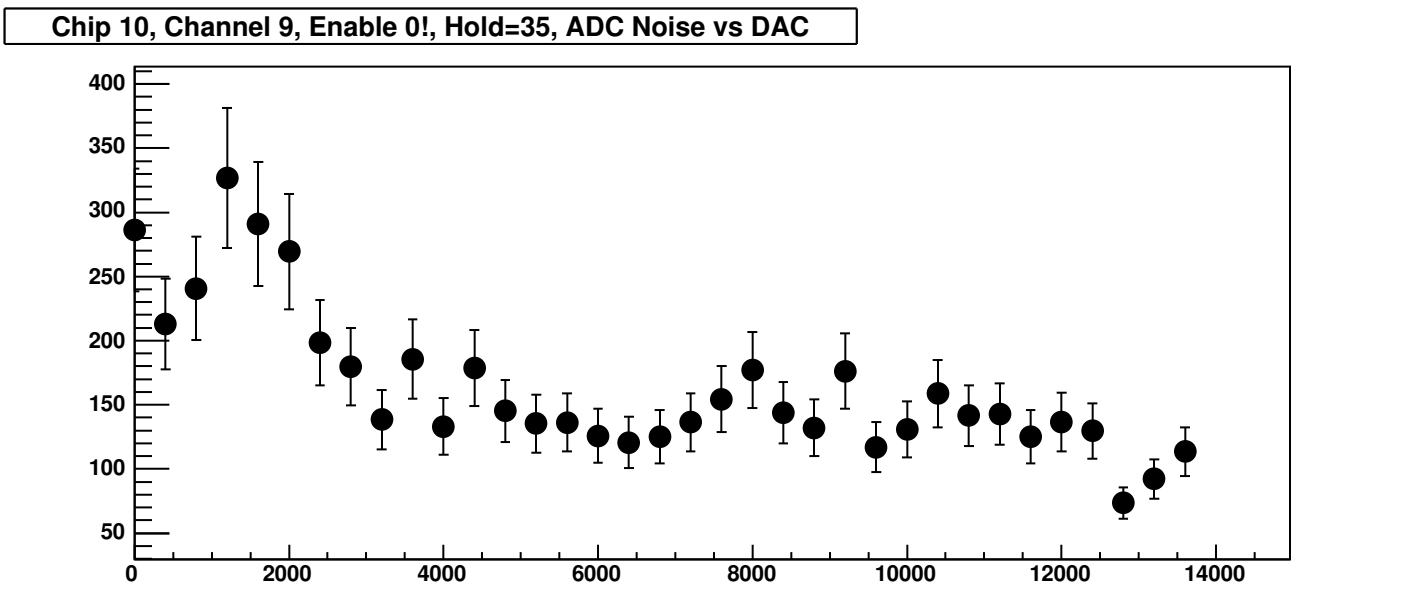

Chip 10, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

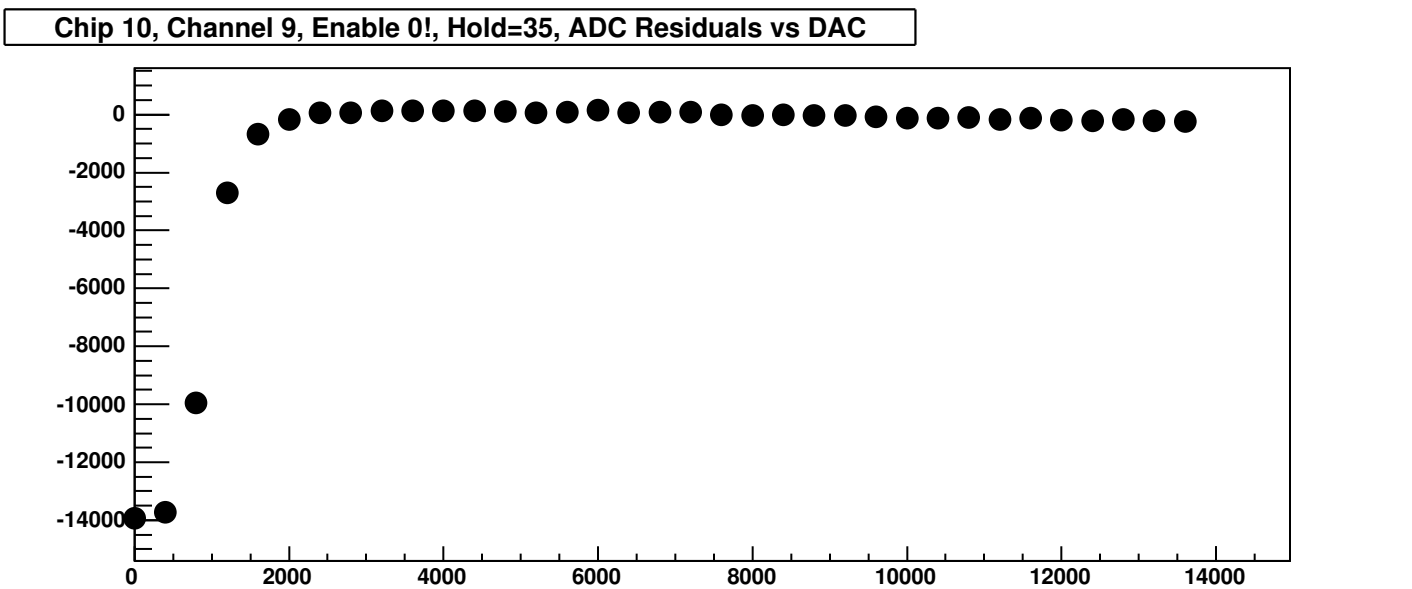

Chip 10, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2184 / 23
p0 -190.5 ± 1.07
p1 0.1775 ± 0.0001984
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

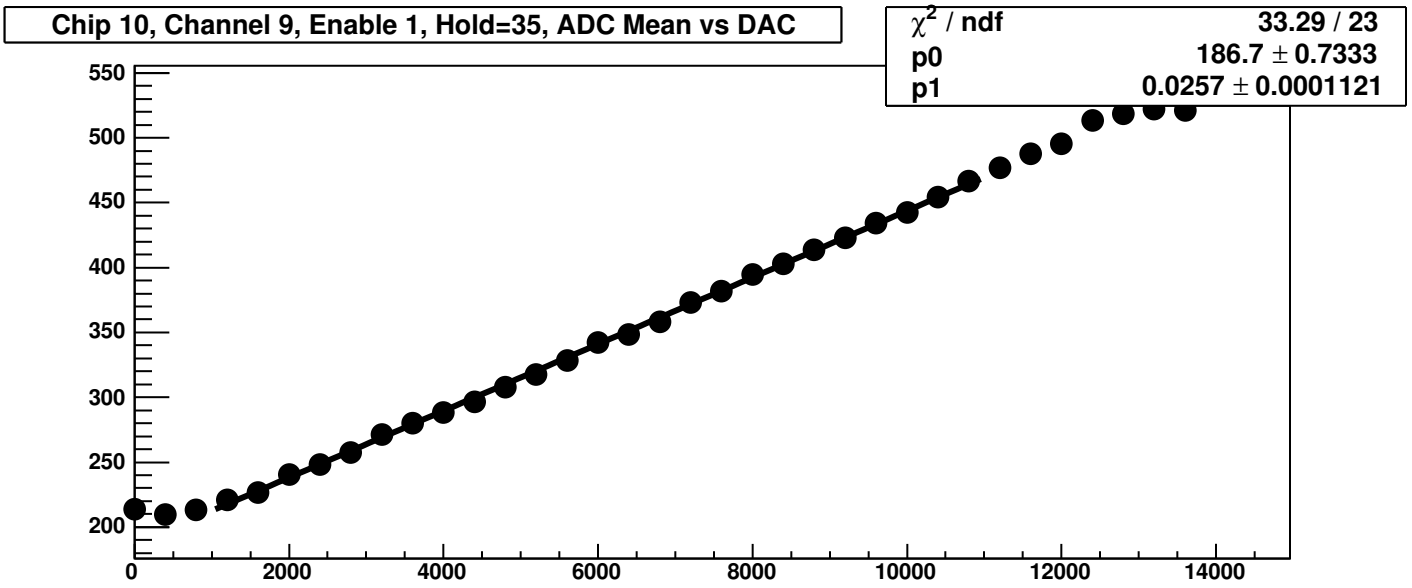

Chip 10, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

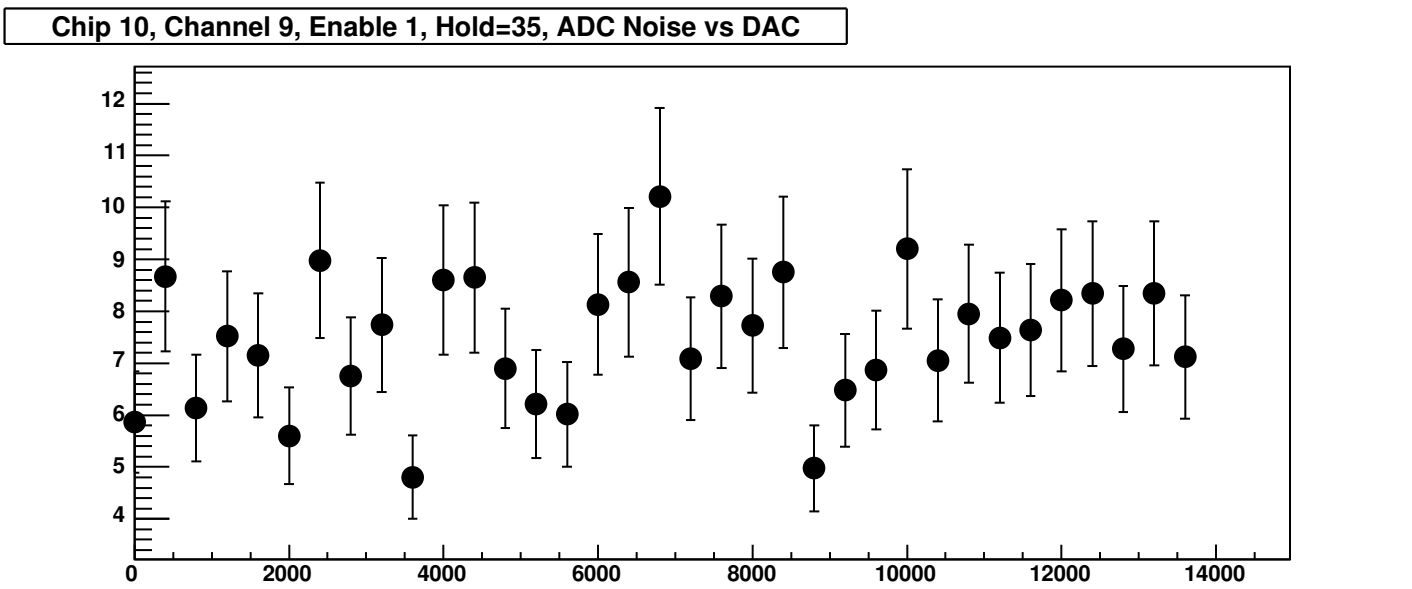

Chip 10, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

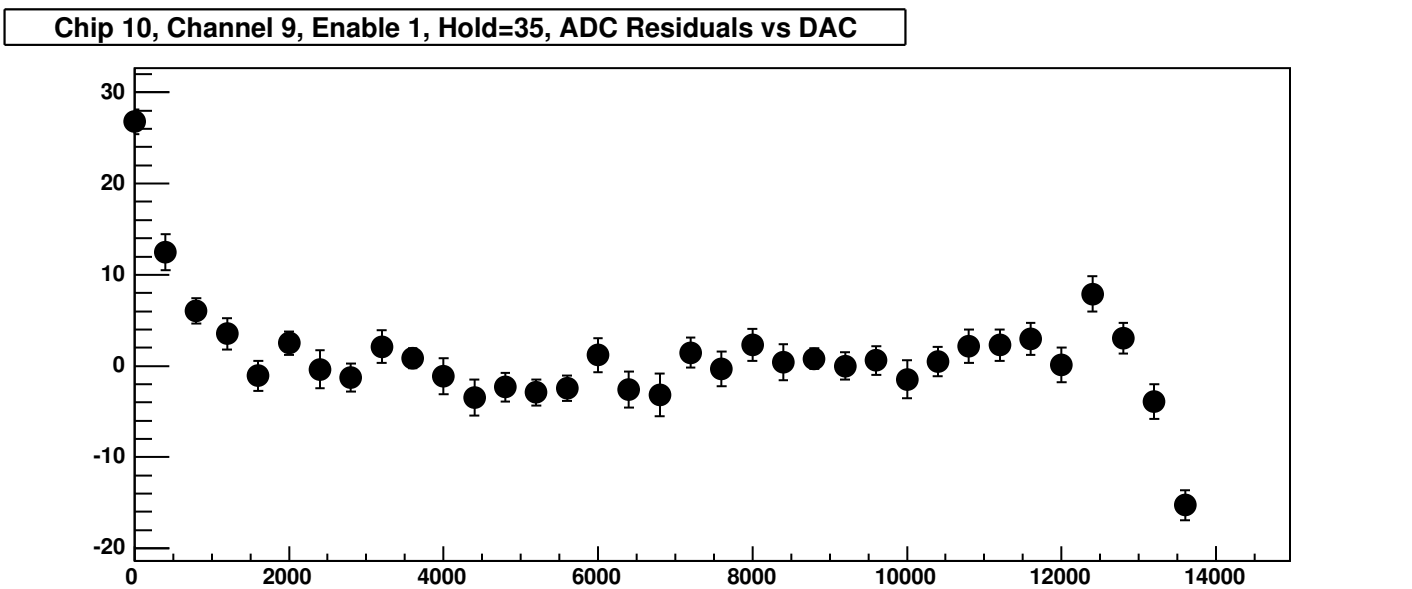

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1594 / 23
p0 3.041e+04 ± 19.45
p1 0.03446 ± 0.002732
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

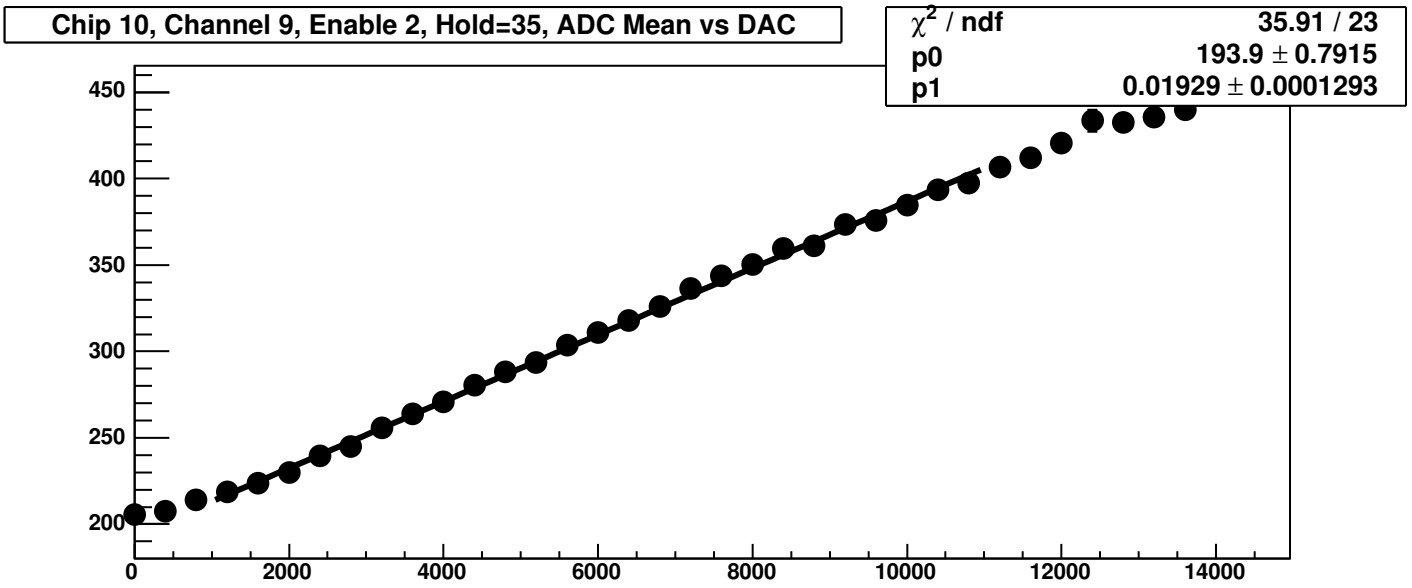

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

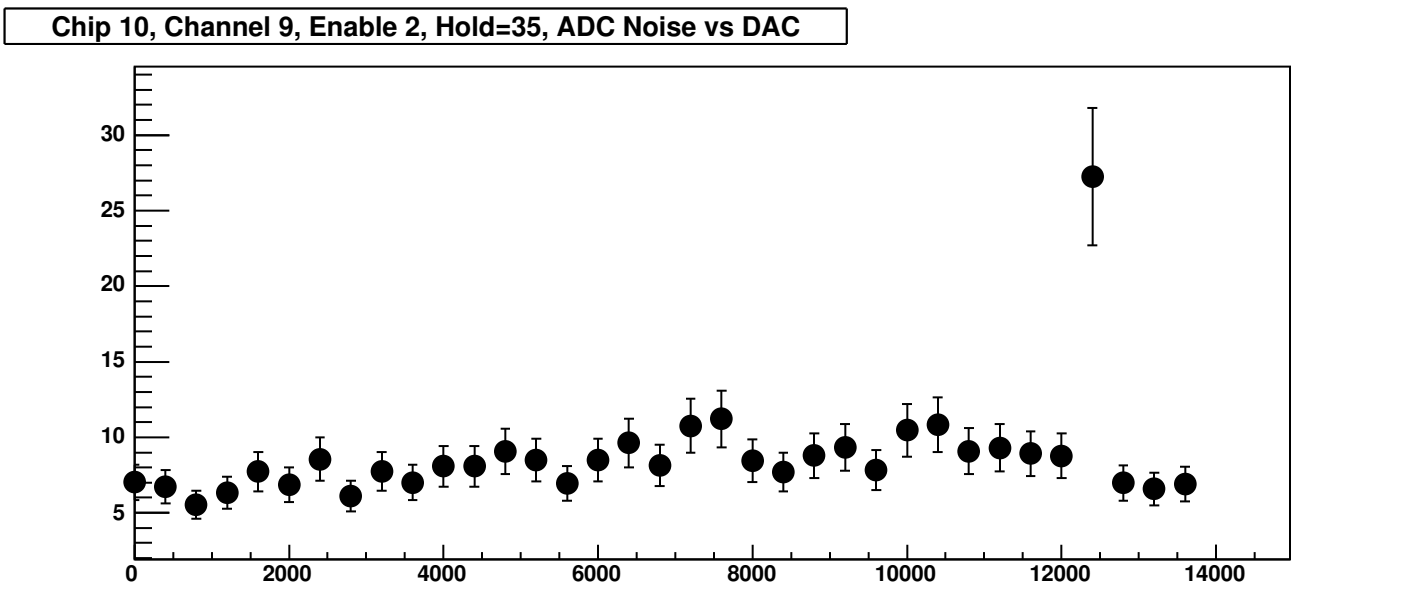

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

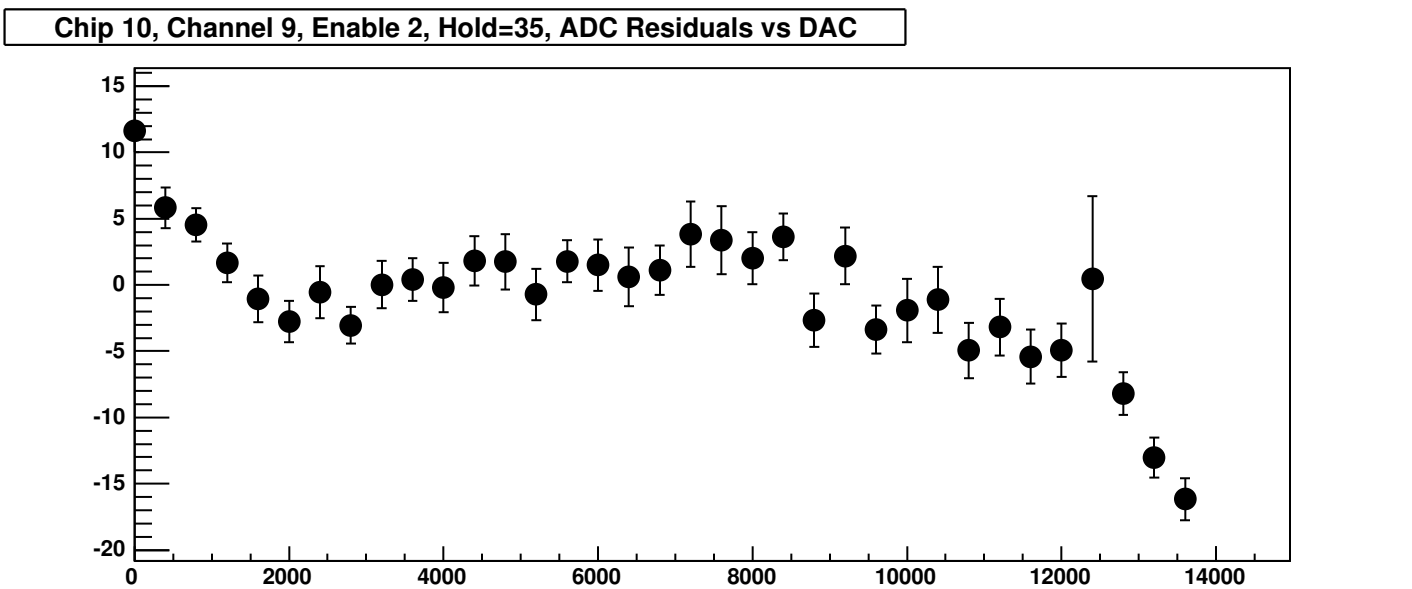

Chip 10, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.29 / 23
p0 186.7 ± 0.7333
p1 0.0257 ± 0.0001121
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

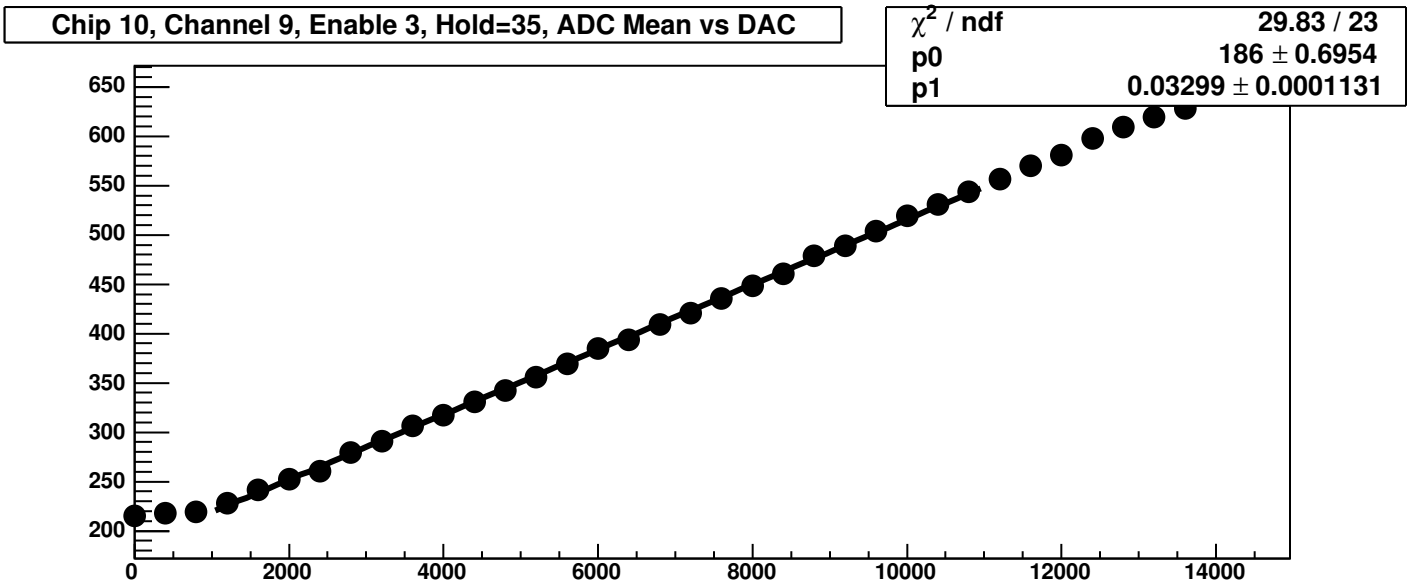

Chip 10, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

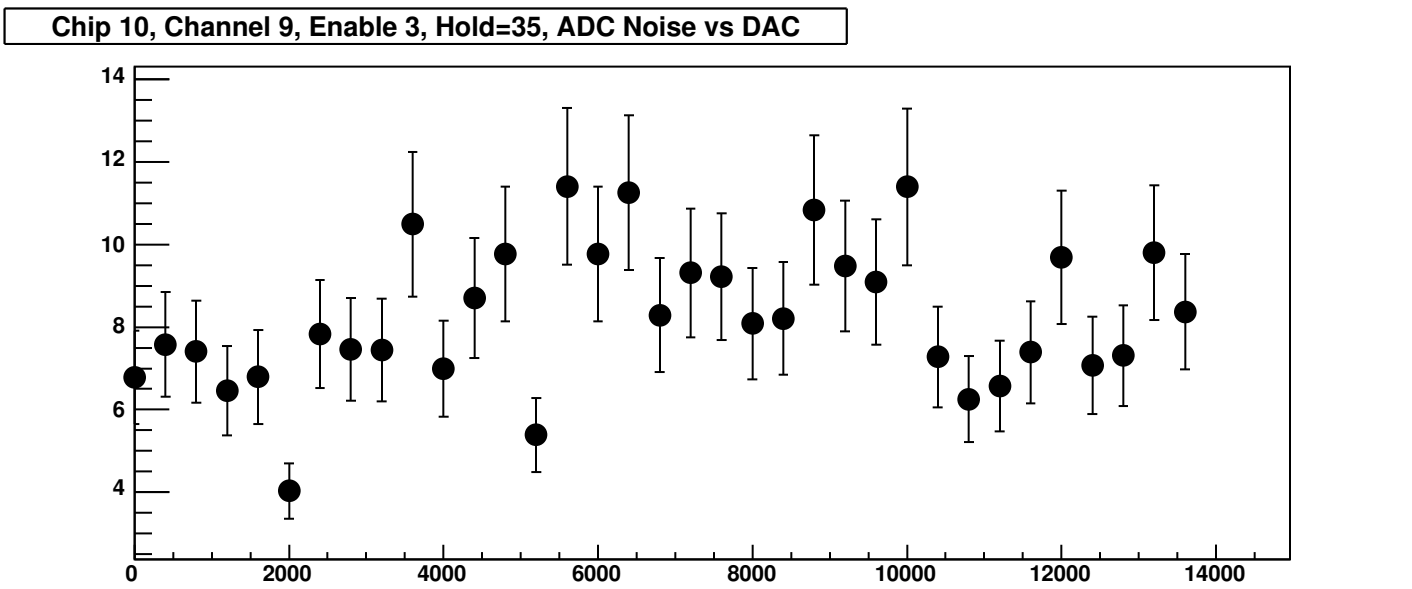

Chip 10, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

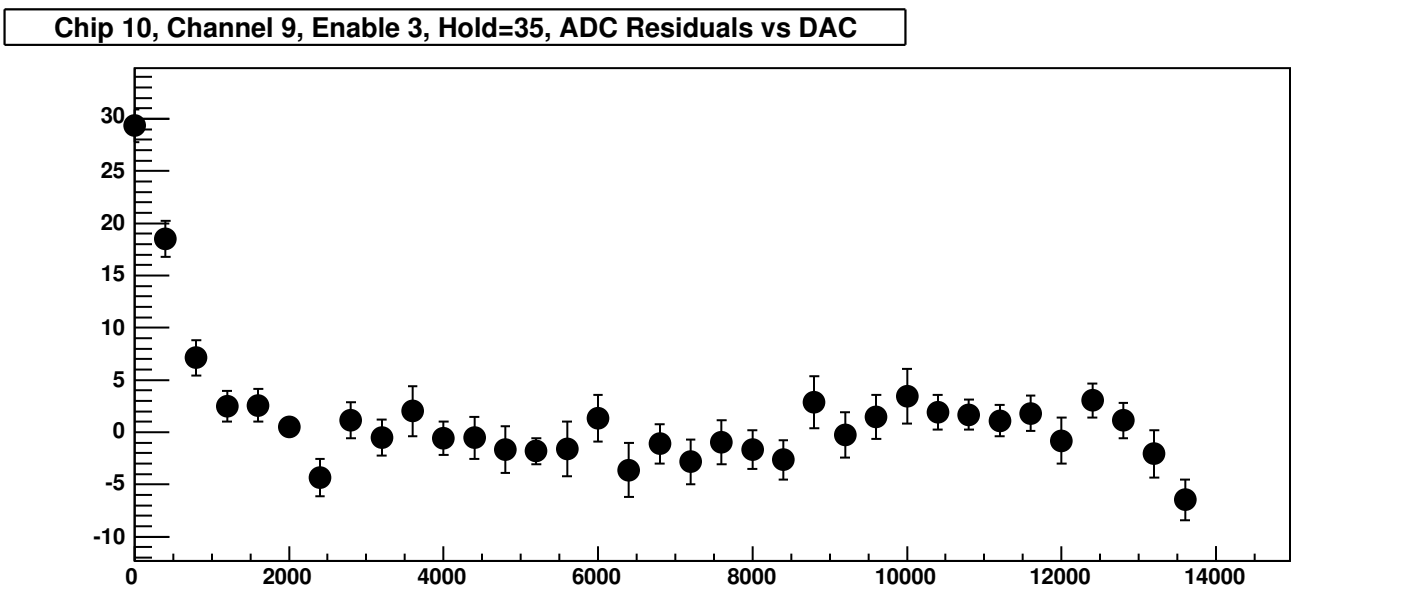

Chip 10, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.91 / 23
p0 193.9 ± 0.7915
p1 0.01929 ± 0.0001293
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

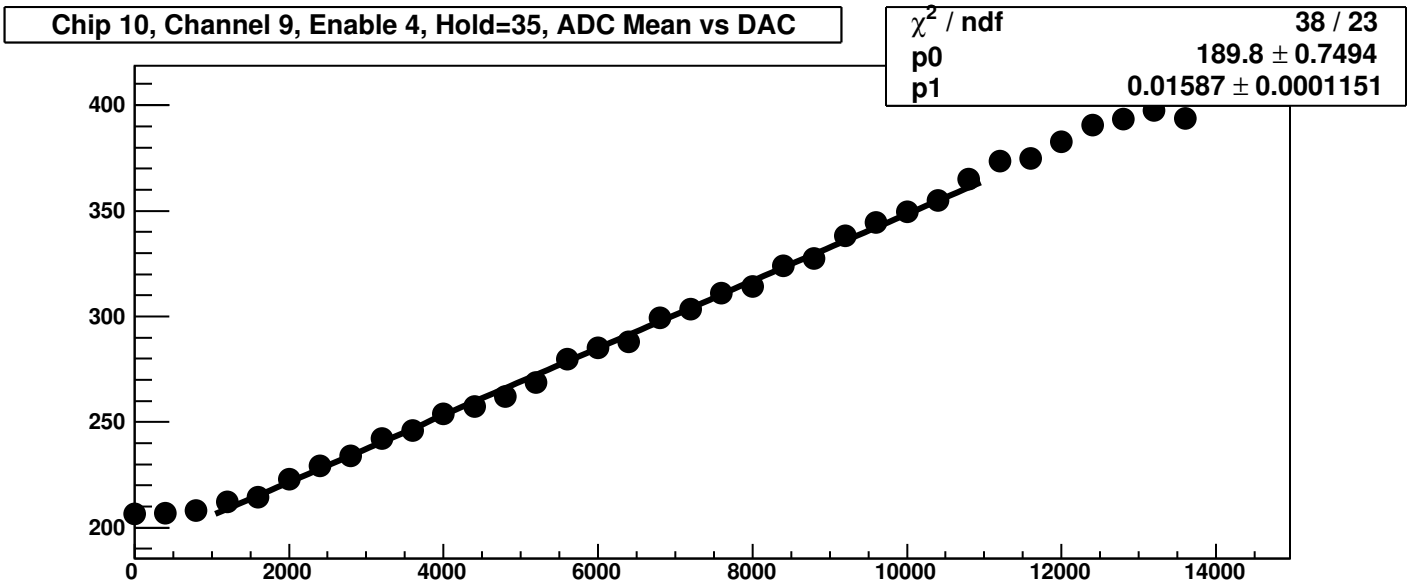

Chip 10, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

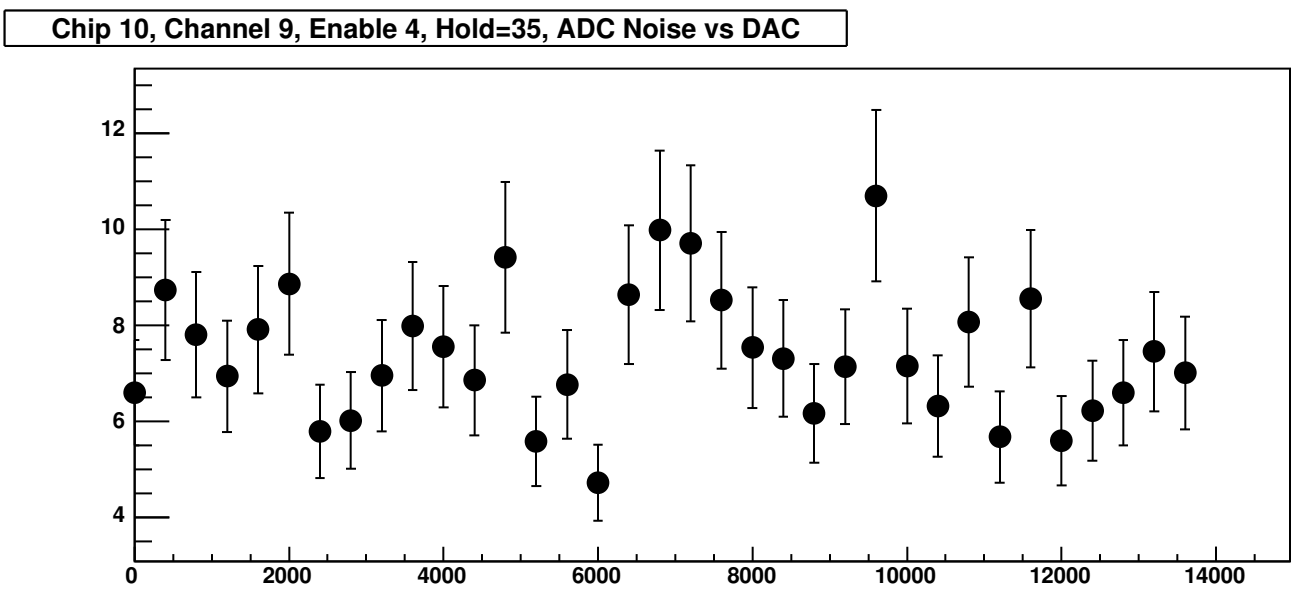

Chip 10, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

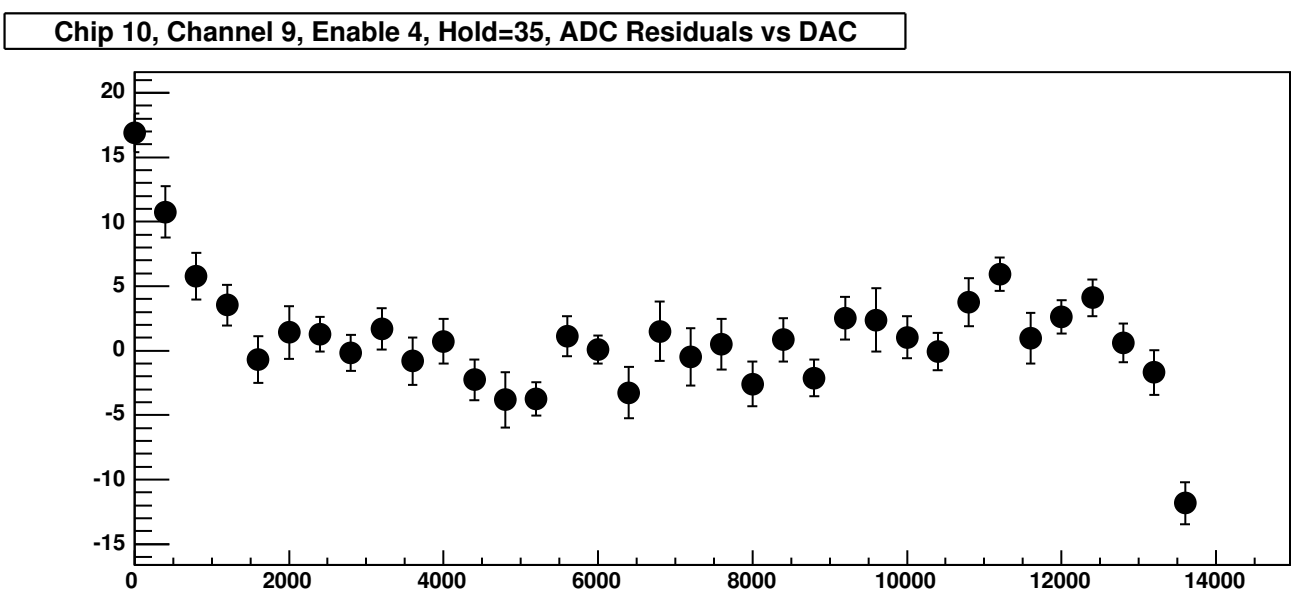

Chip 10, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.83 / 23
p0 186 ± 0.6954
p1 0.03299 ± 0.0001131
650
600
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

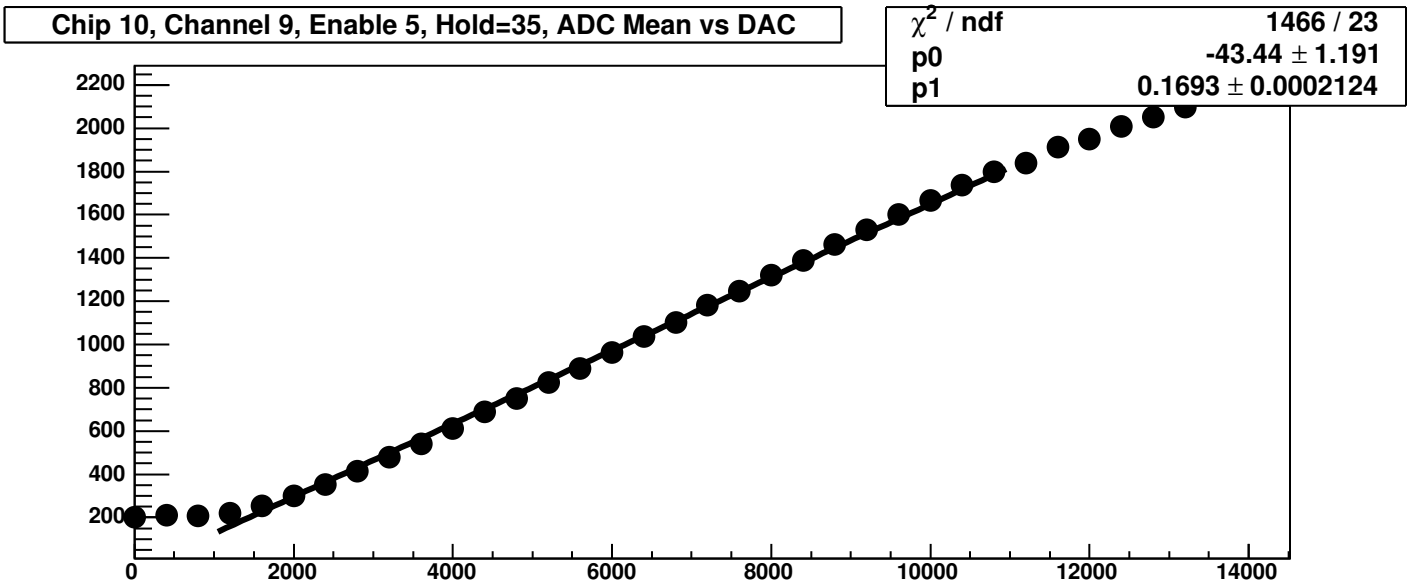

Chip 10, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

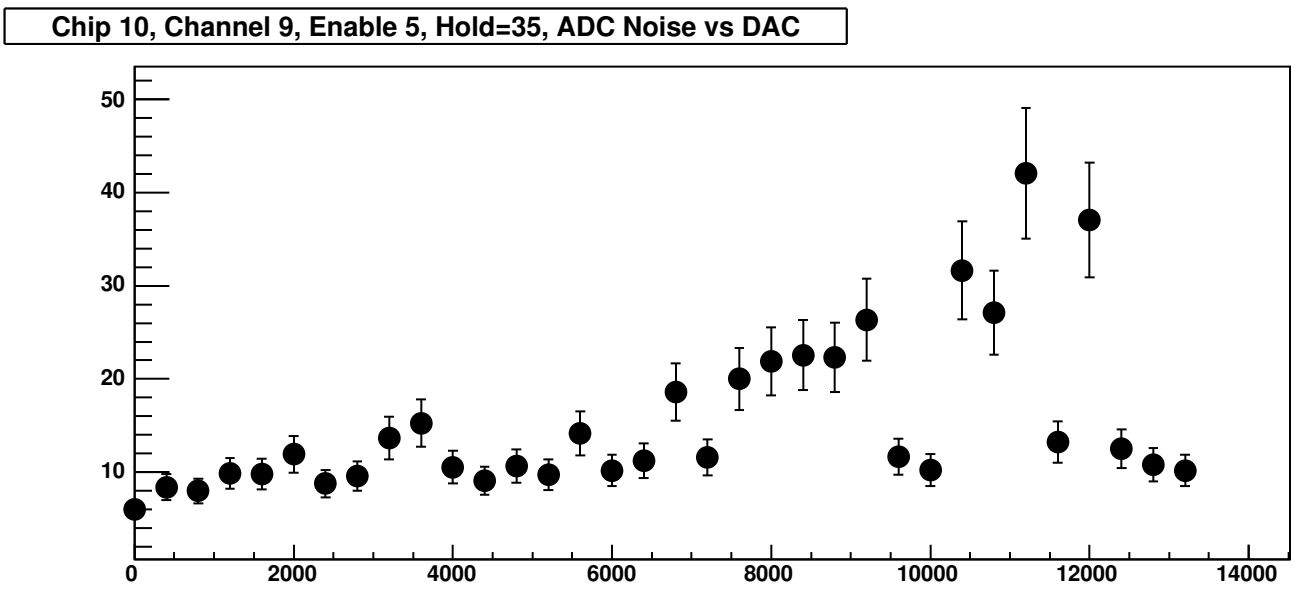

Chip 10, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

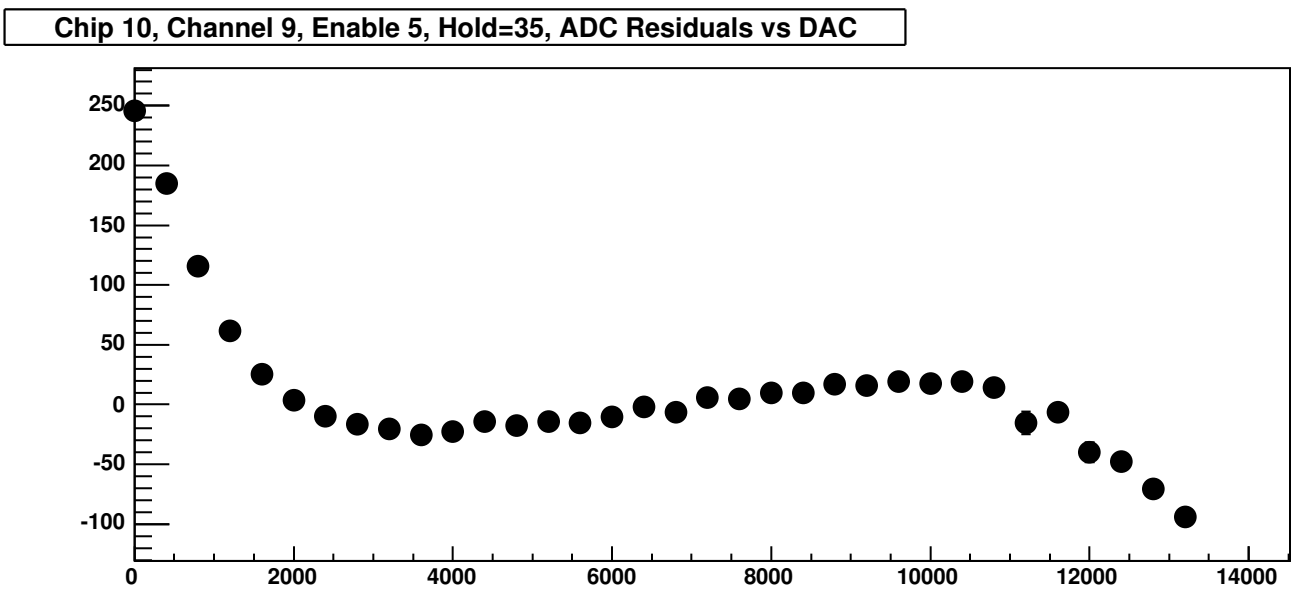

Chip 10, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38 / 23
p0 189.8 ± 0.7494
p1 0.01587 ± 0.0001151
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

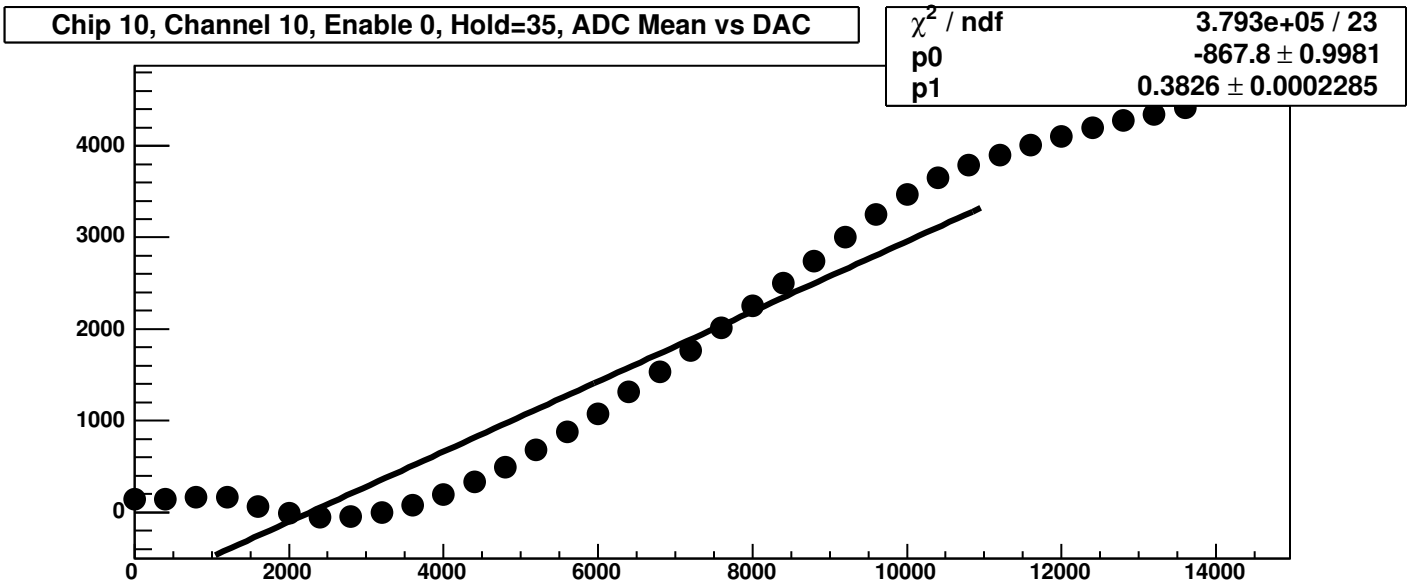

Chip 10, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

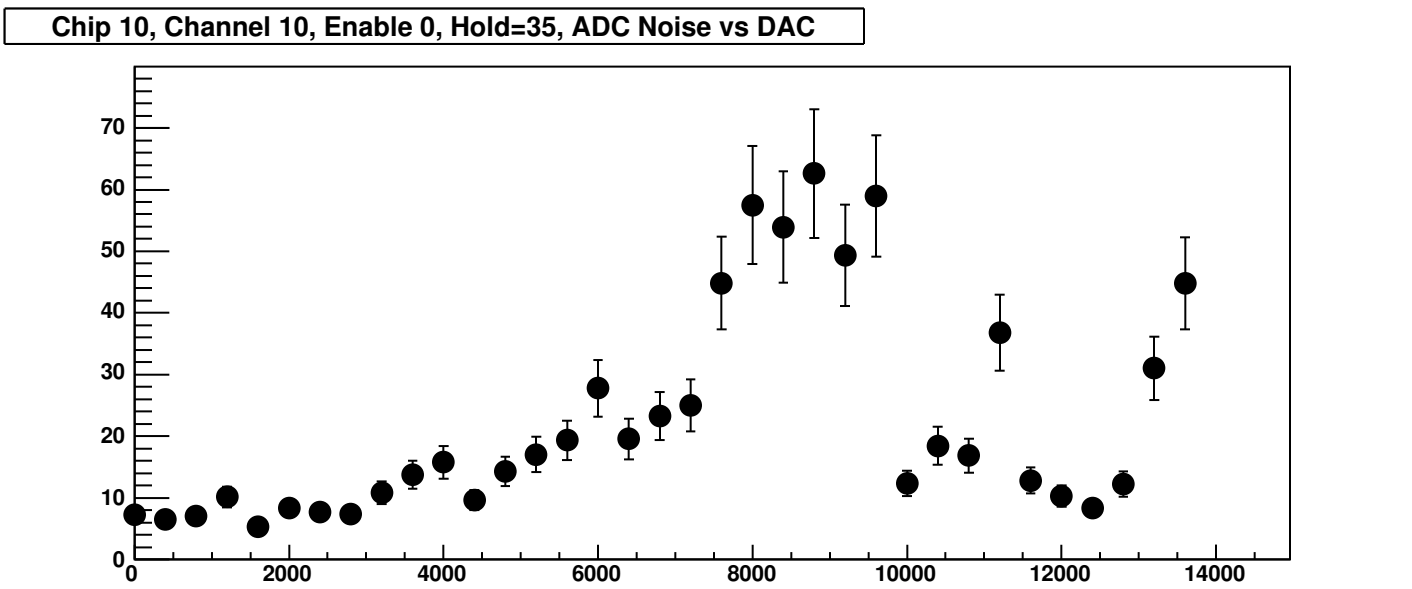

Chip 10, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

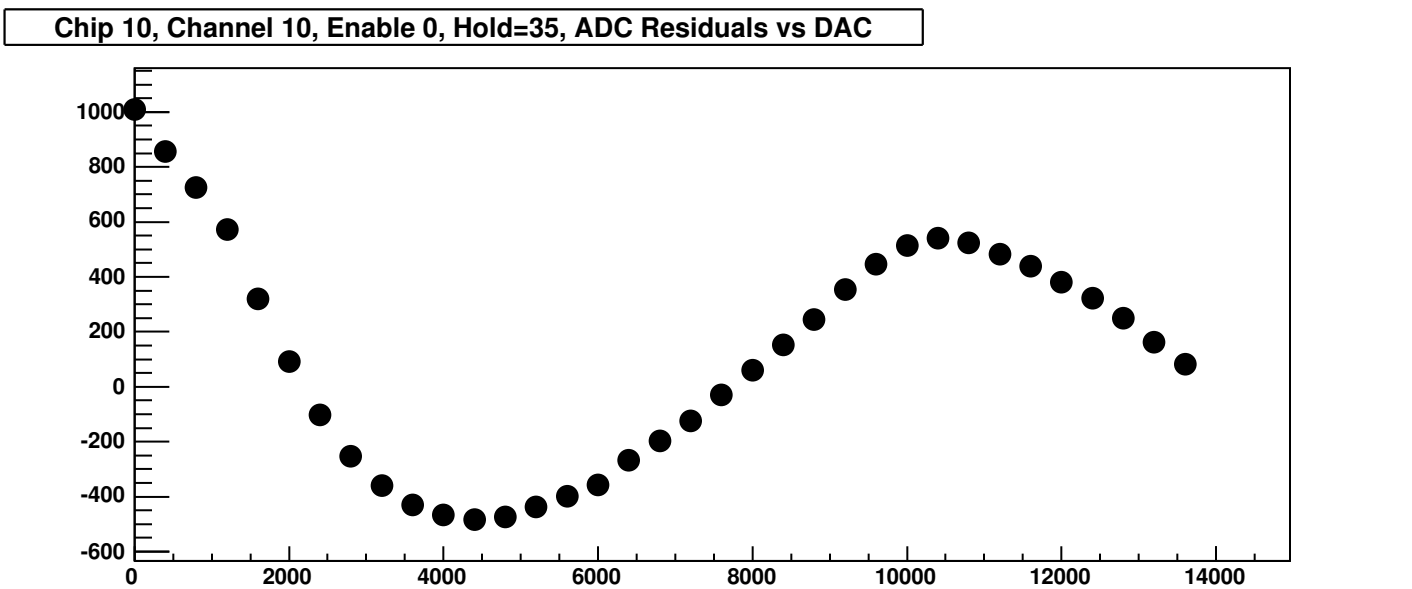

Chip 10, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1466 / 23
p0 -43.44 ± 1.191
p1 0.1693 ± 0.0002124
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

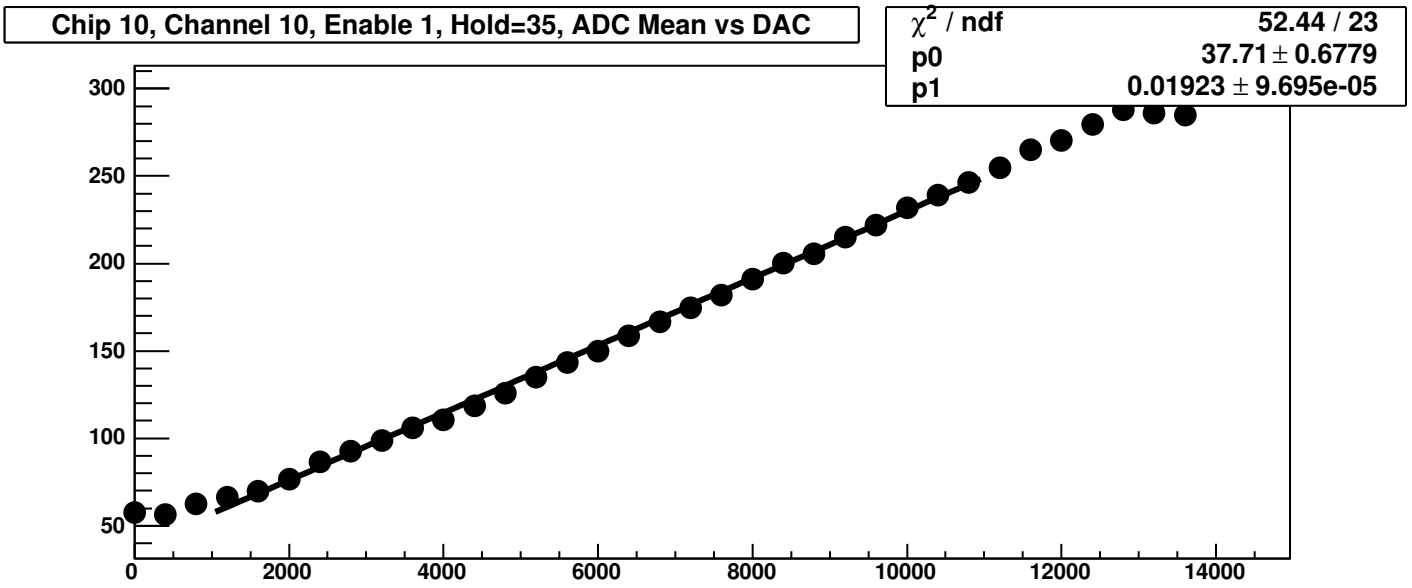

Chip 10, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

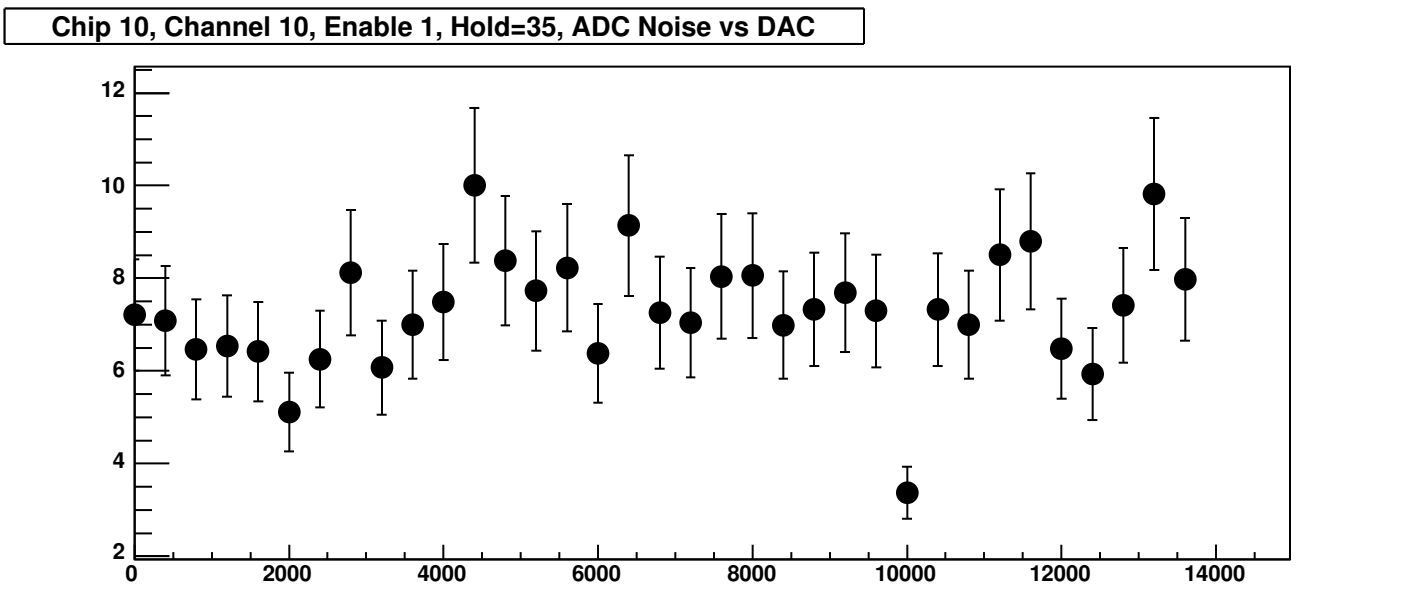

Chip 10, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

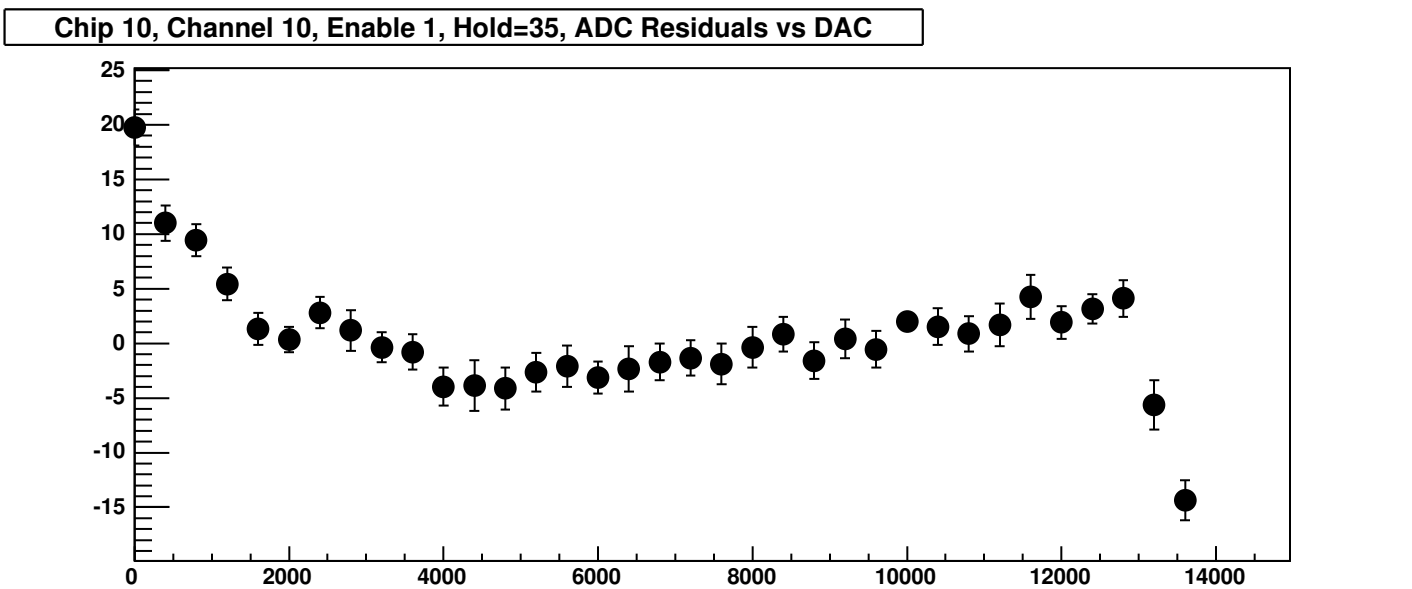

Chip 10, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.793e+05 / 23
p0 -867.8 ± 0.9981
p1 0.3826 ± 0.0002285
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

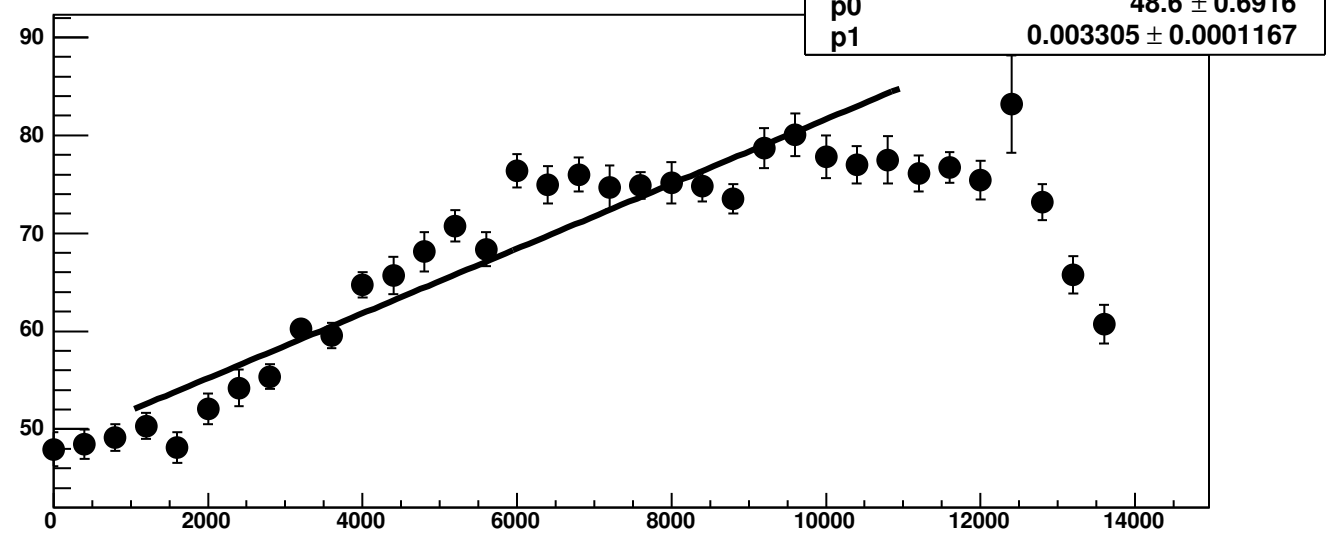

Chip 10, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.44 / 23
p0 37.71 ± 0.6779
p1 0.01923 ± 9.695e-05
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

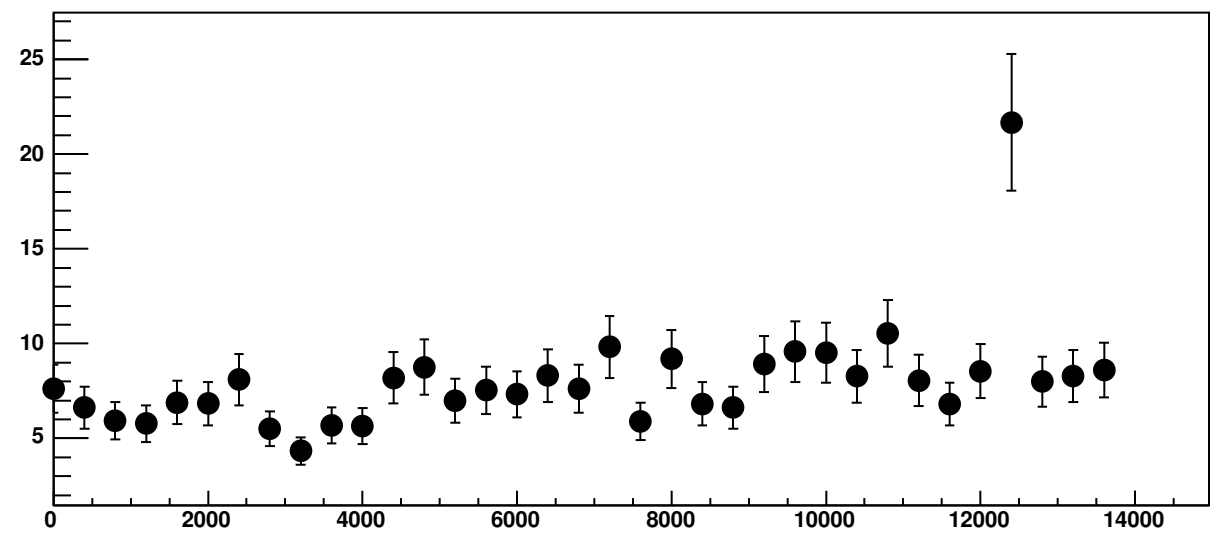

Chip 10, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

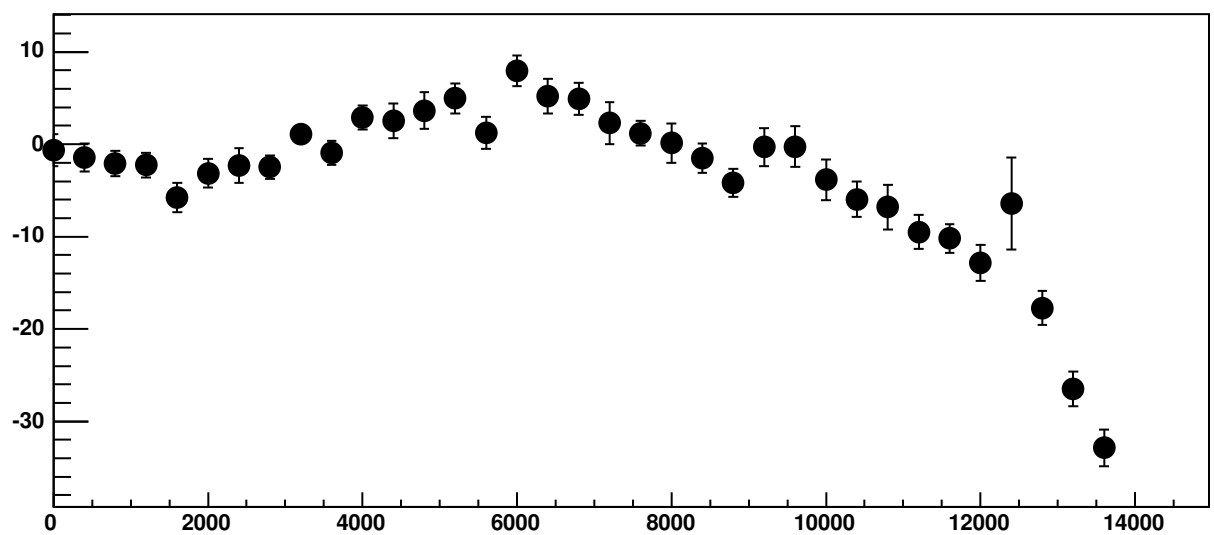
Chip 10, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

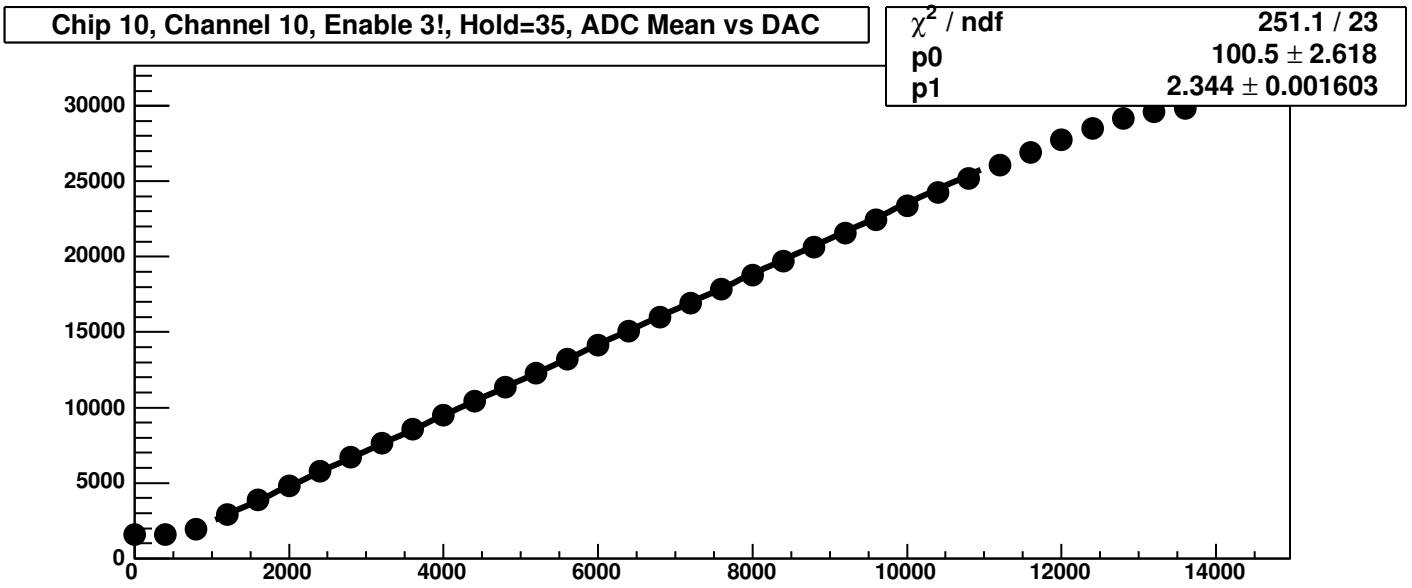


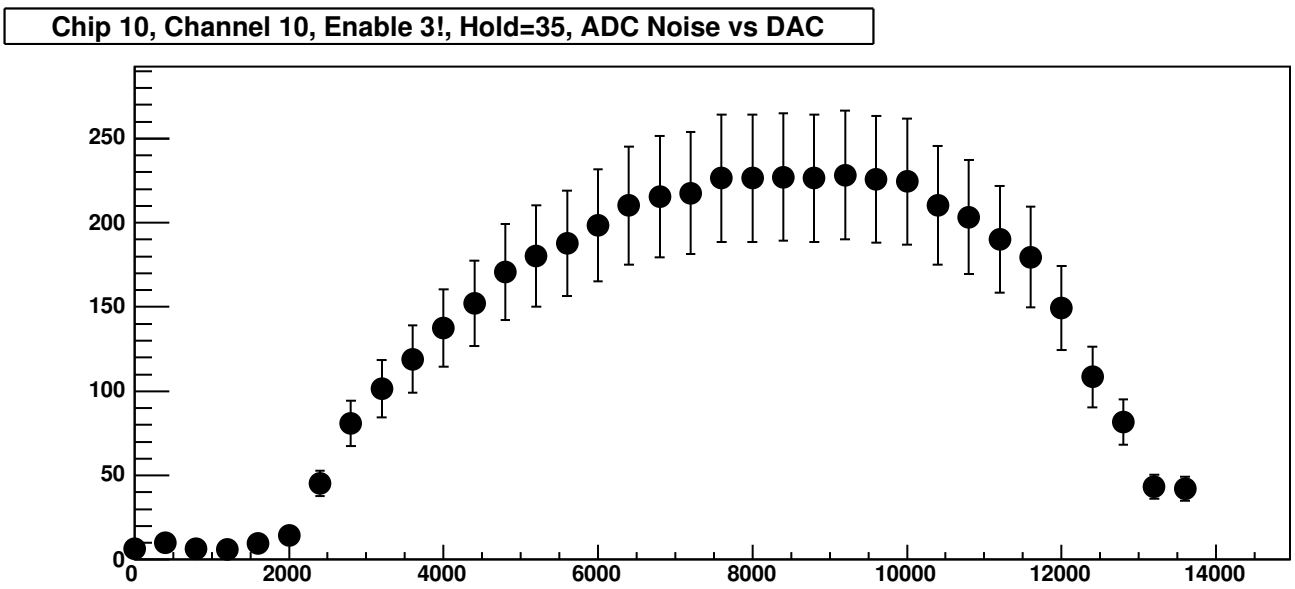
Chip 10, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

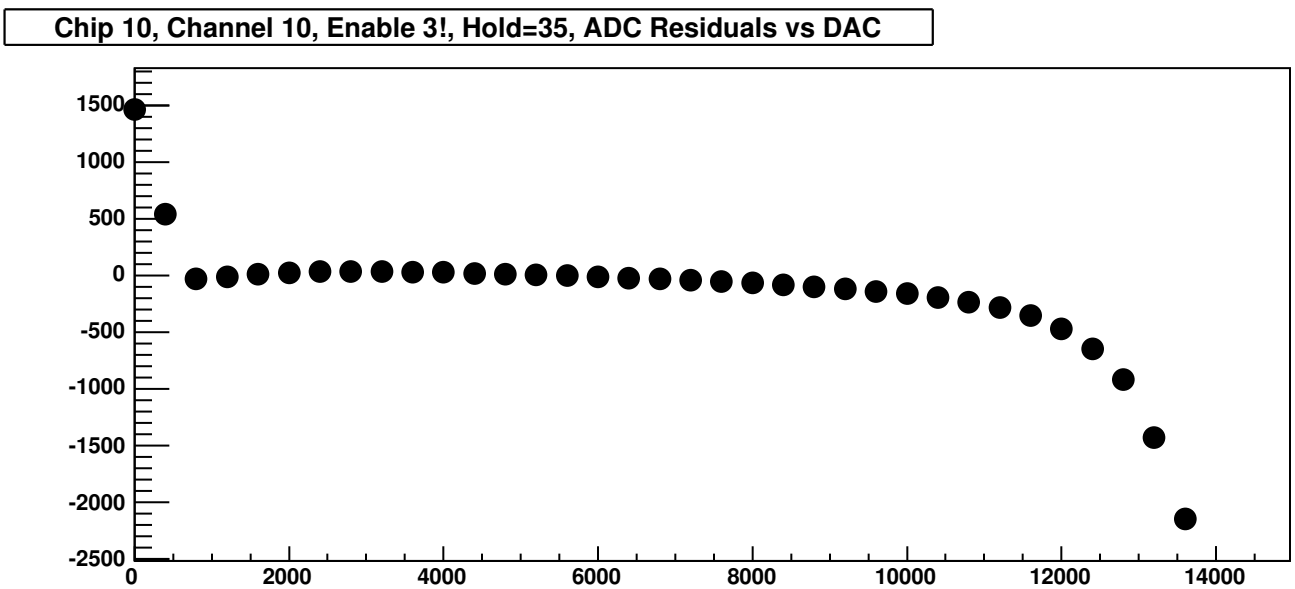


Chip 10, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

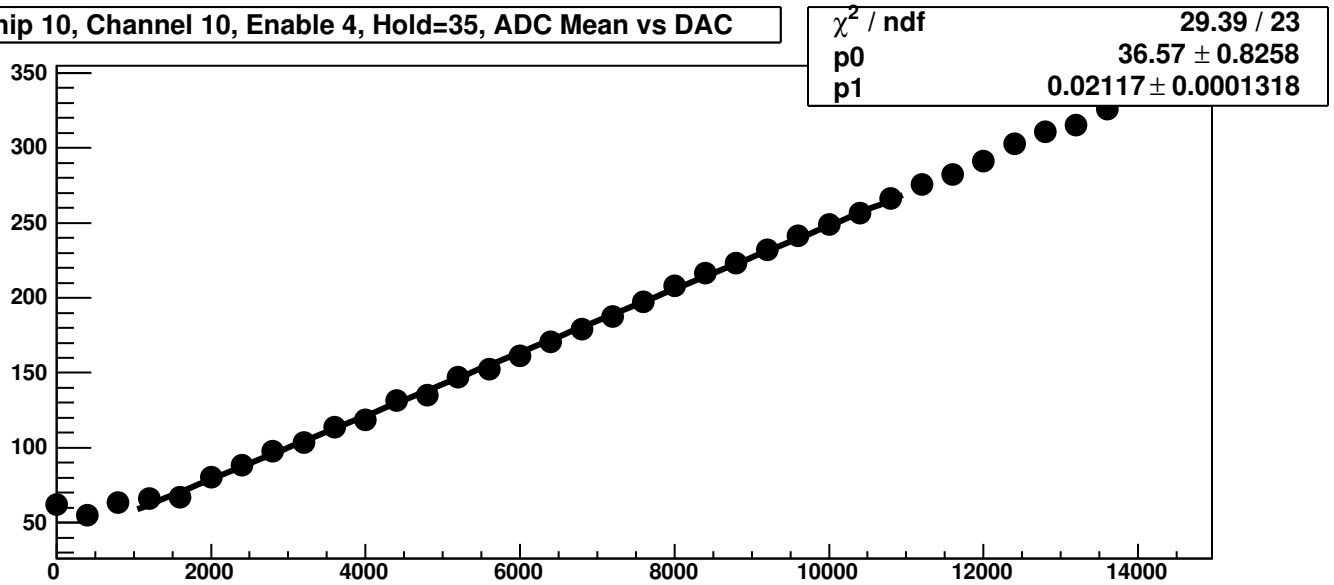



Chip 10, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 251.1 / 23
p0 100.5 ± 2.618
p1 2.344 ± 0.001603
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

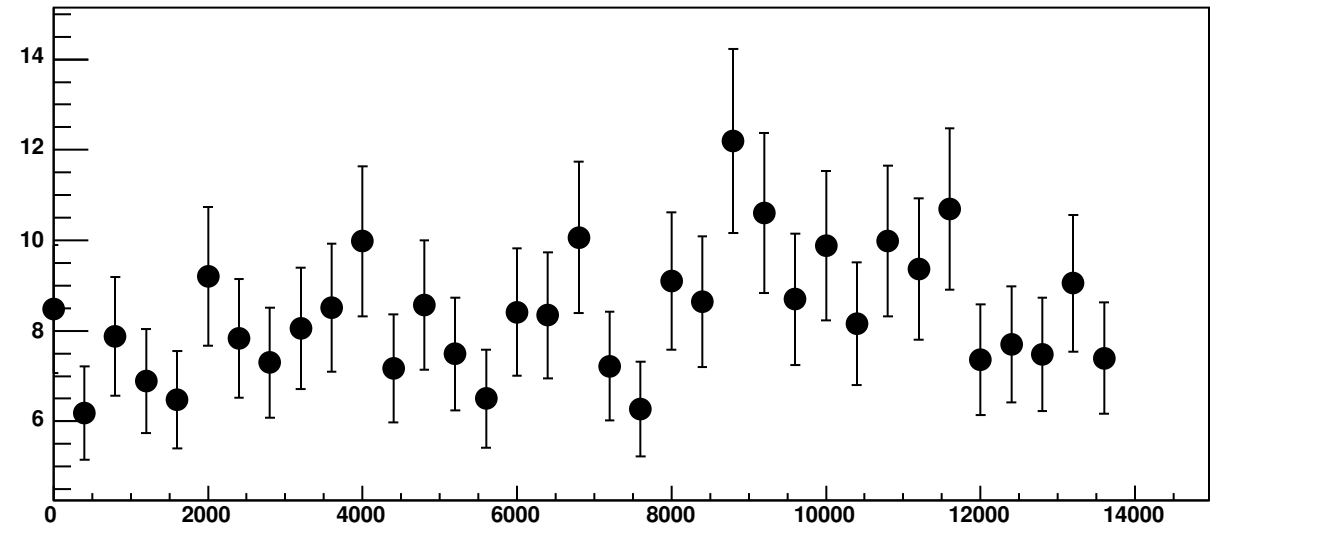

Chip 10, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

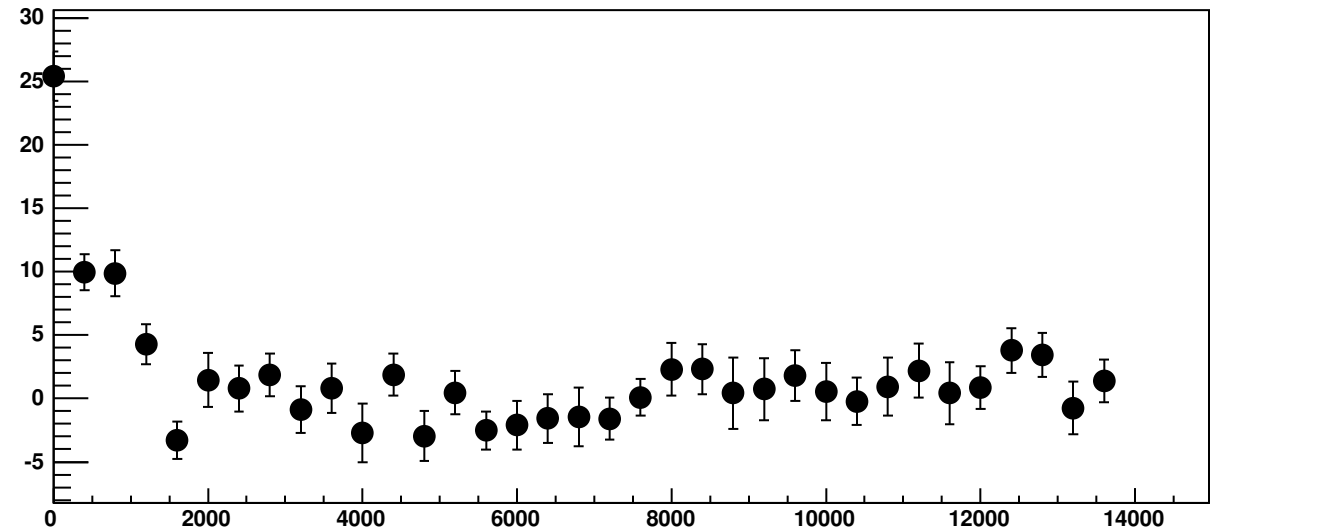
Chip 10, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

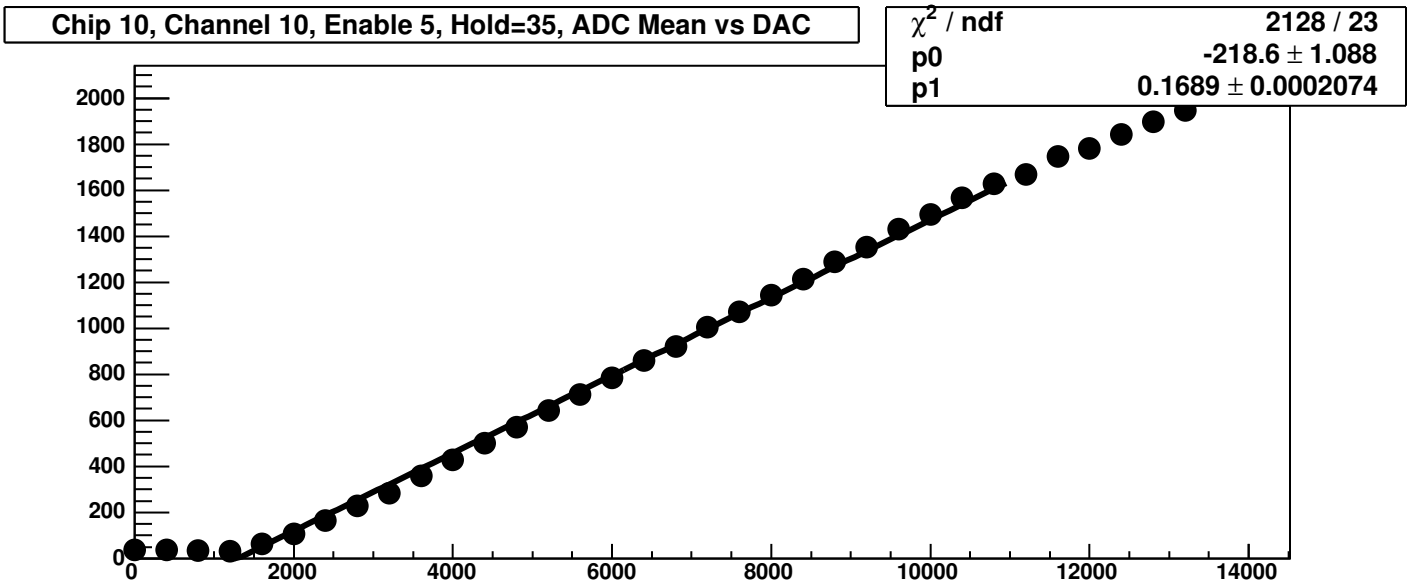


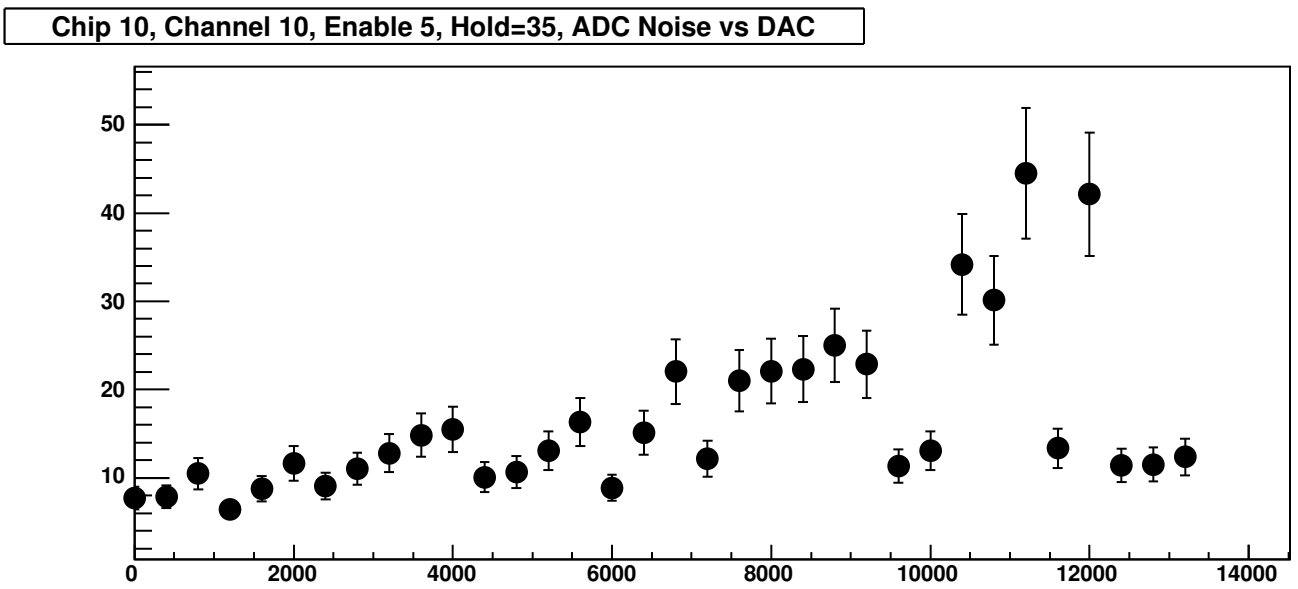
Chip 10, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

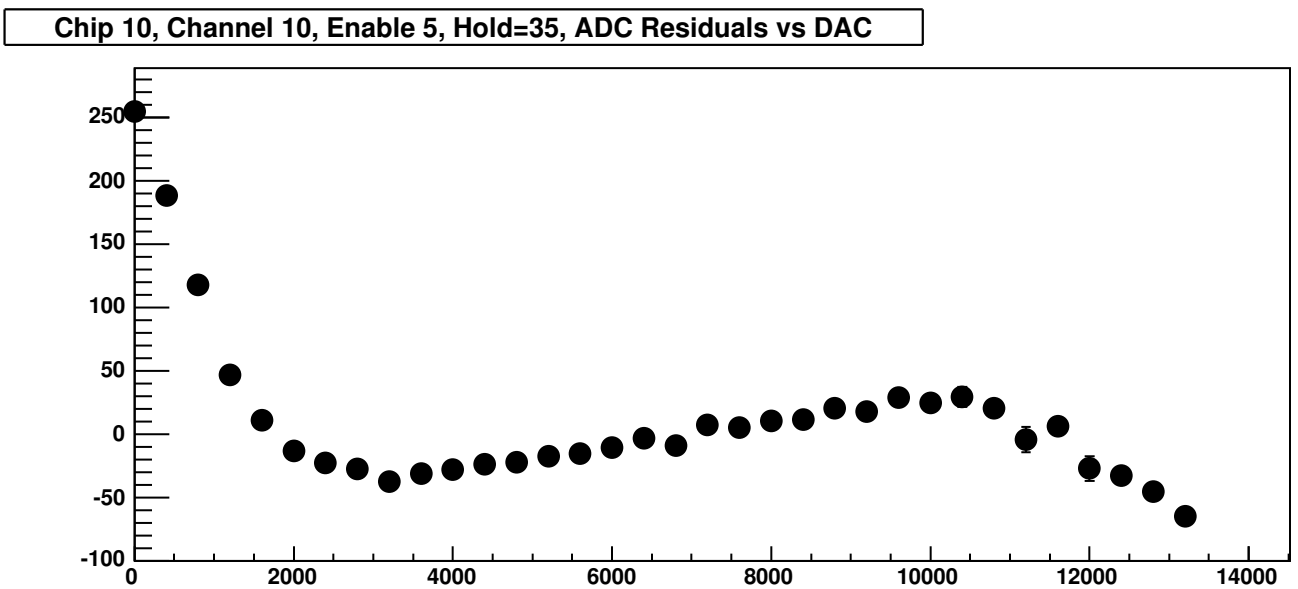


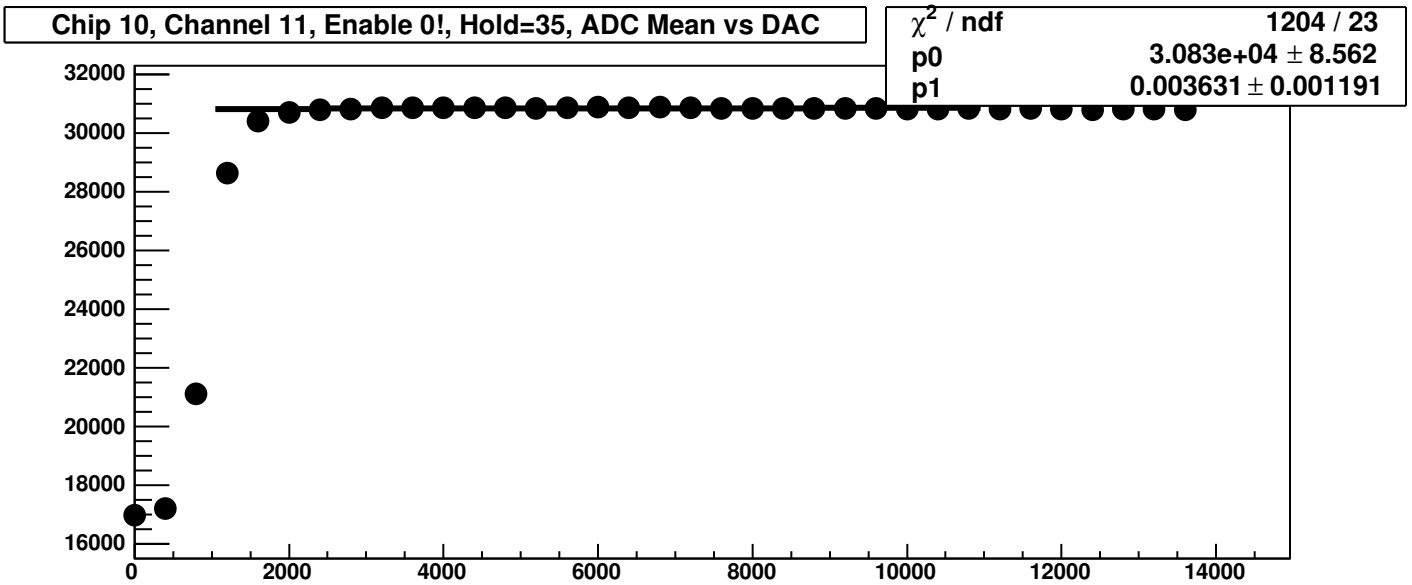
Chip 10, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

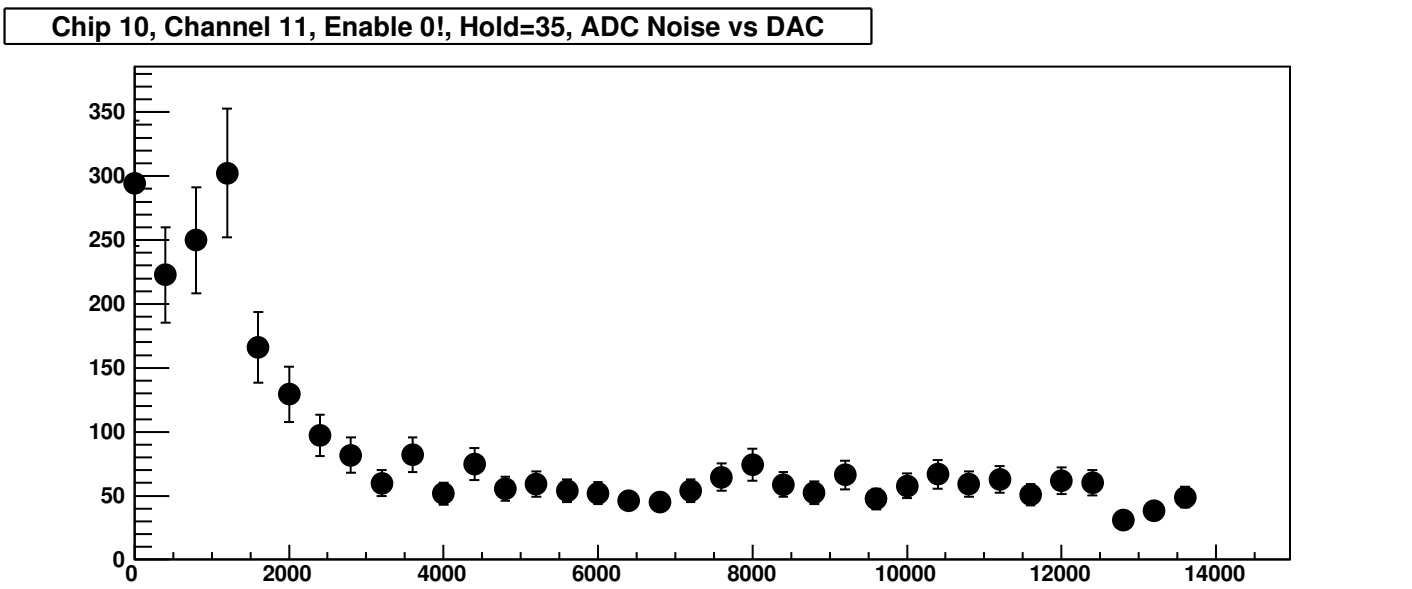


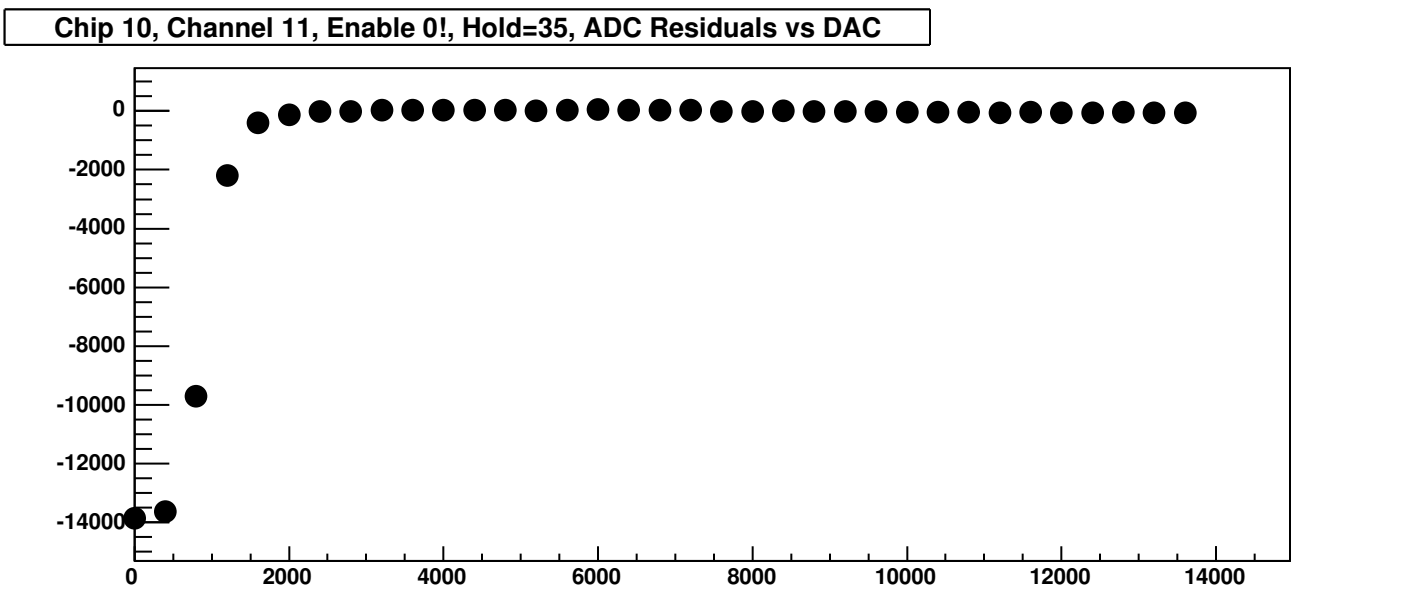

Chip 10, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2128 / 23
p0 -218.6 ± 1.088
p1 0.1689 ± 0.0002074
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

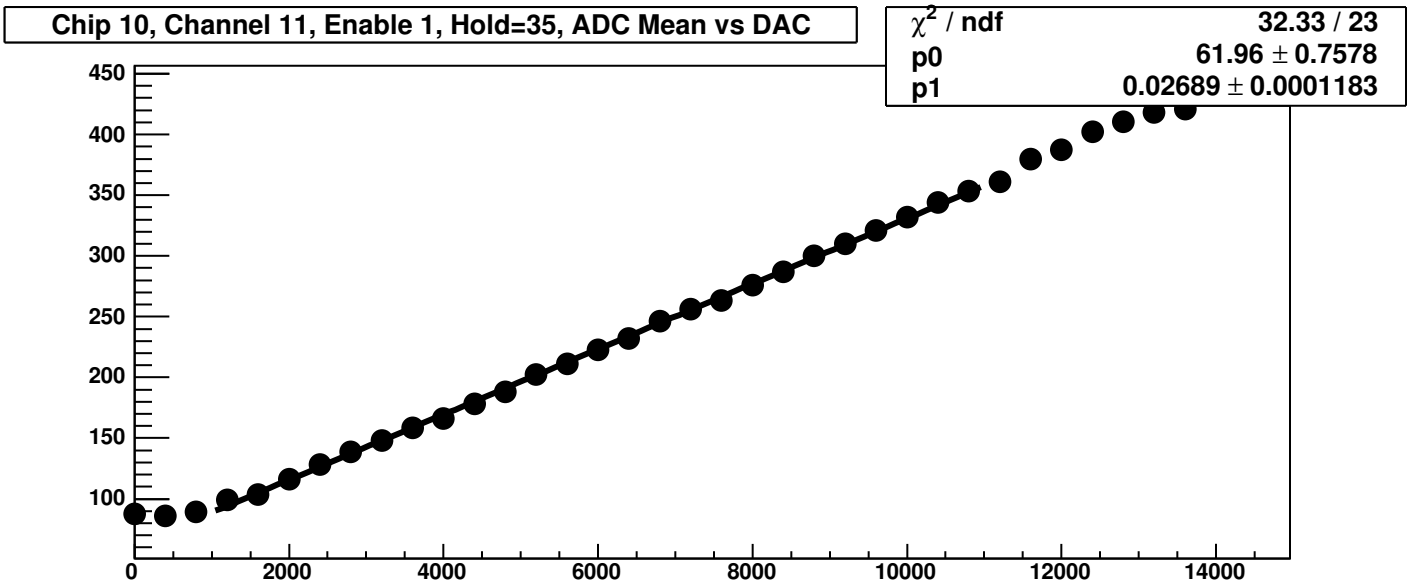

Chip 10, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

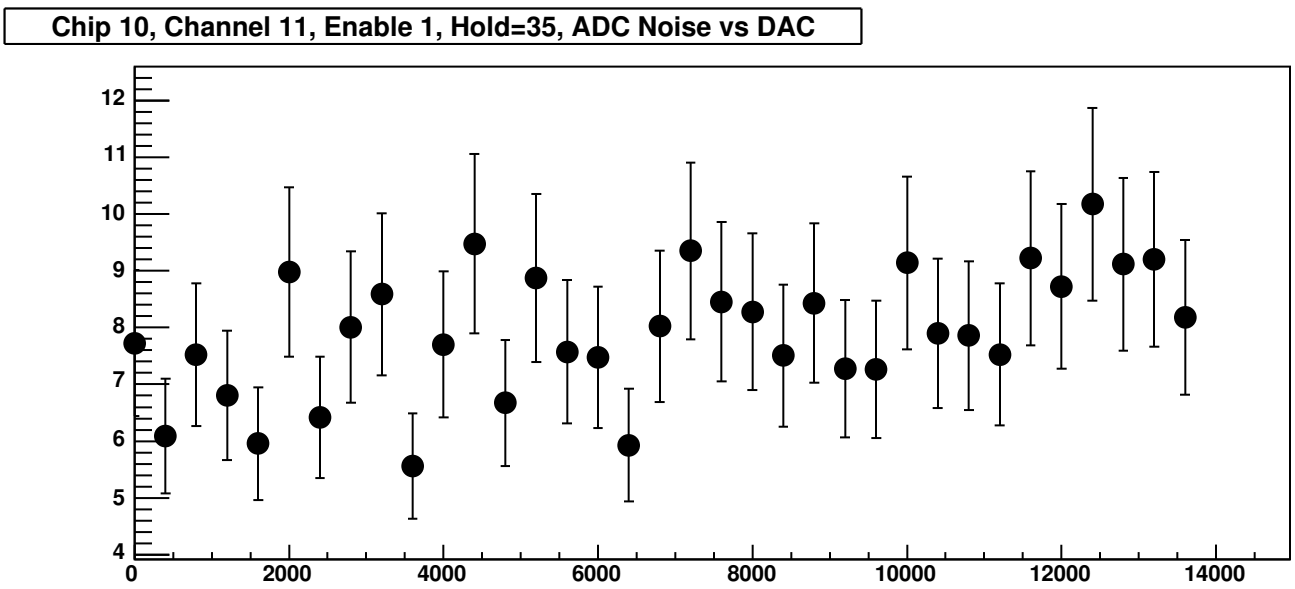

Chip 10, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

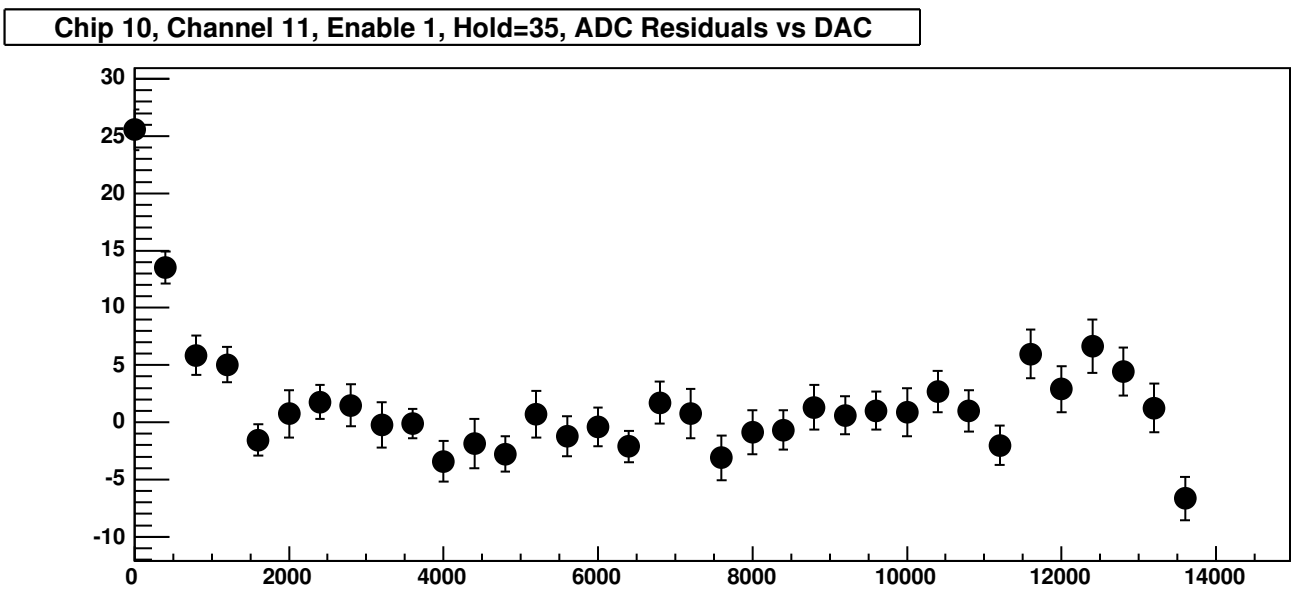

Chip 10, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1204 / 23
p0 3.083e+04 ± 8.562
p1 0.003631 ± 0.001191
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

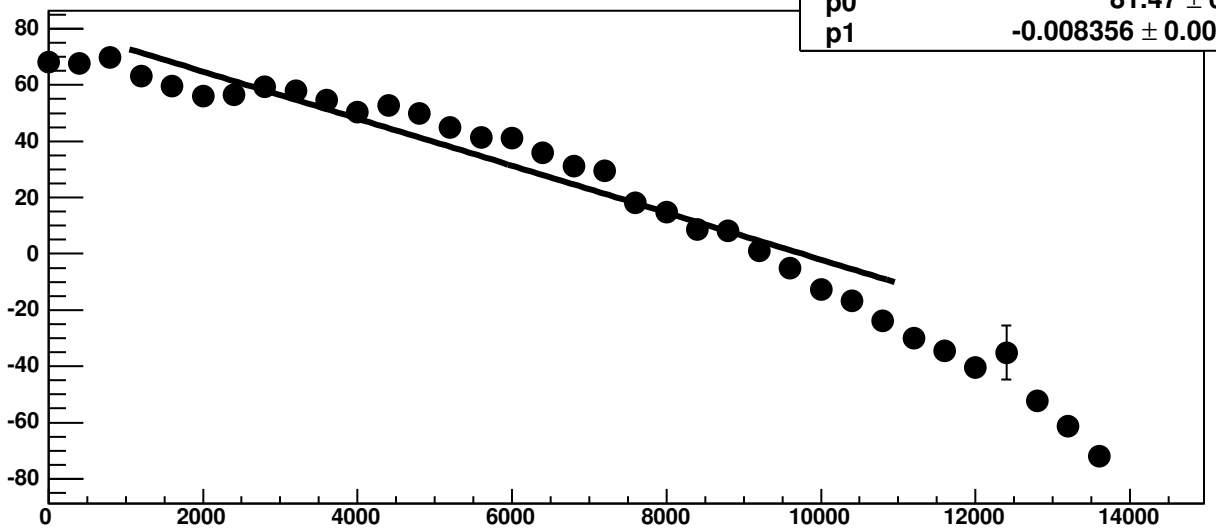

Chip 10, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.33 / 23
p0 61.96 ± 0.7578
p1 0.02689 ± 0.0001183
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

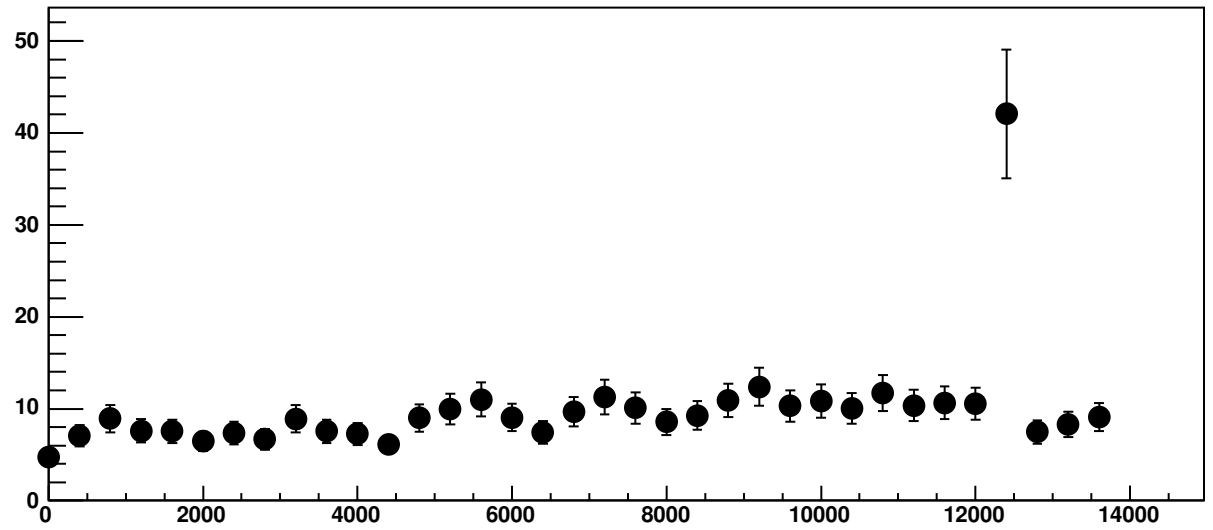

Chip 10, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

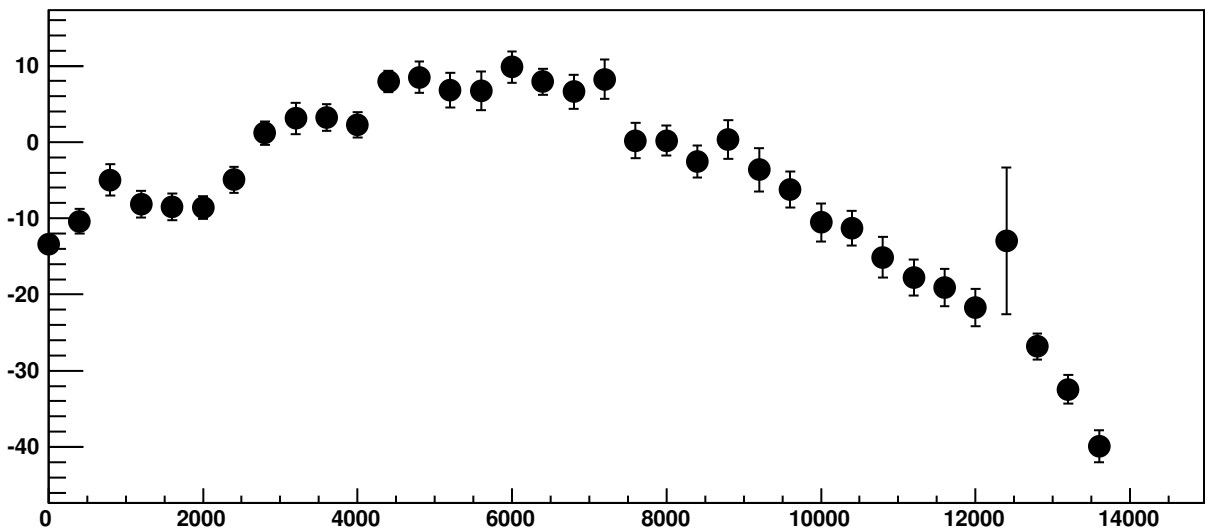
χ^2 / ndf	308 / 23
p0	81.47 ± 0.8275
p1	-0.008356 ± 0.0001416



Chip 10, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

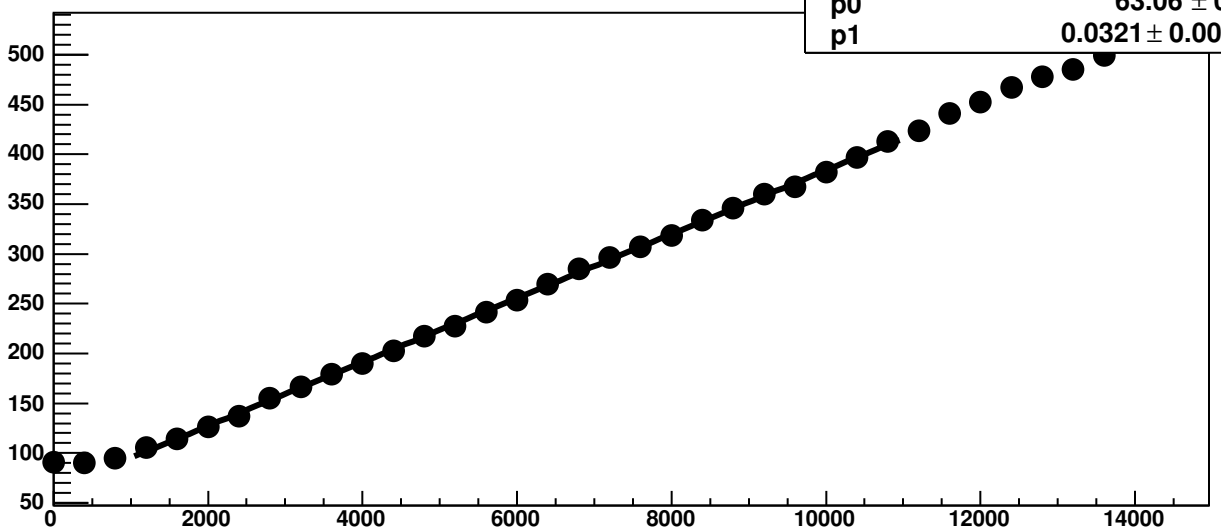


Chip 10, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

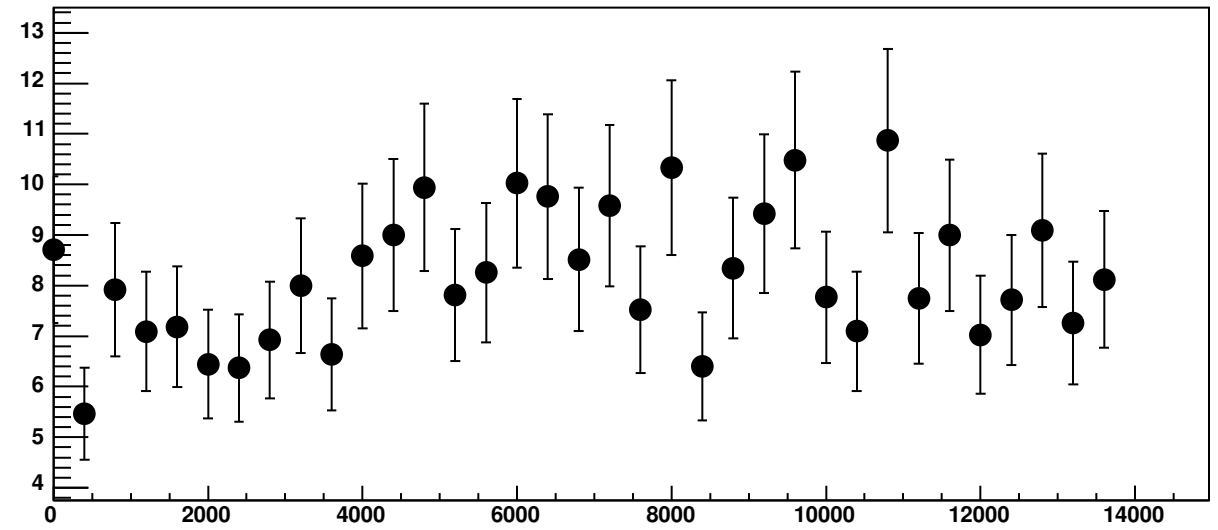


Chip 10, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

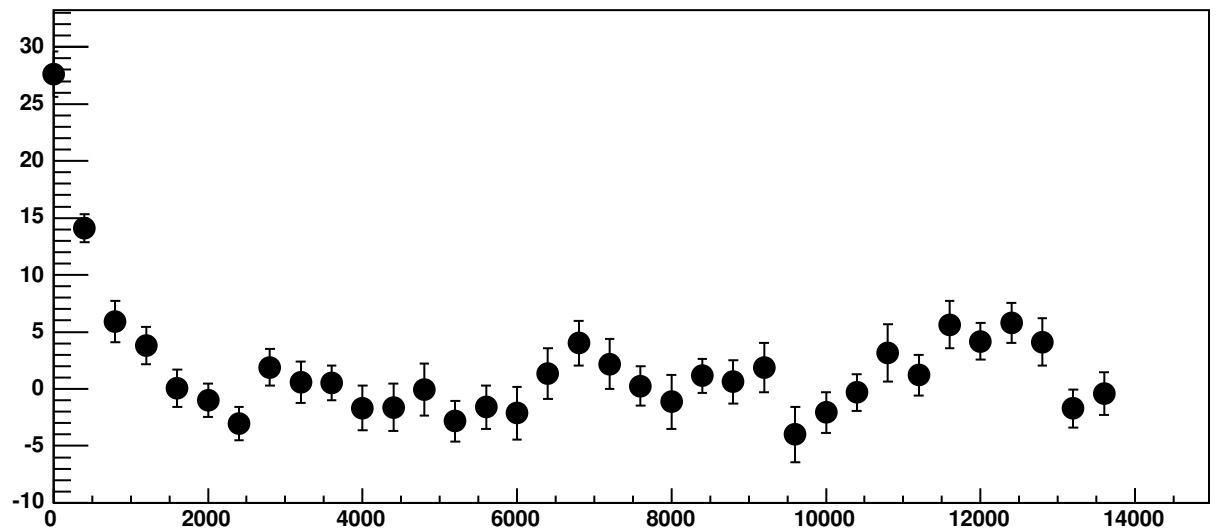
χ^2 / ndf	30.36 / 23
p0	63.06 ± 0.7759
p1	0.0321 ± 0.0001229

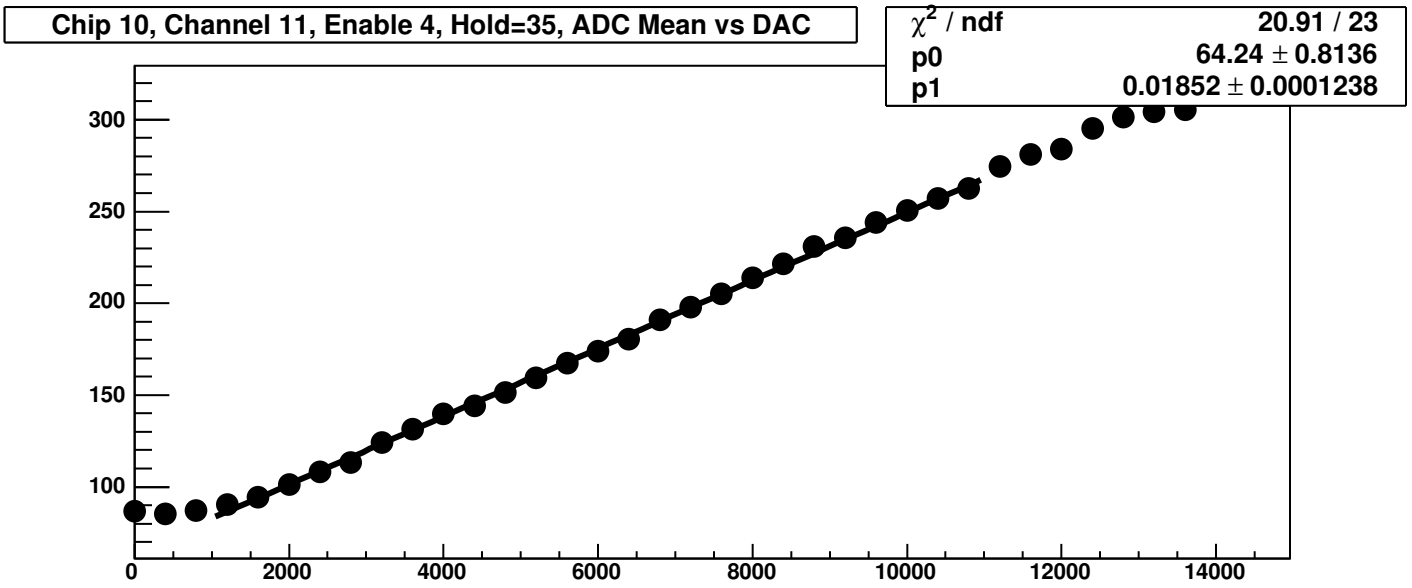


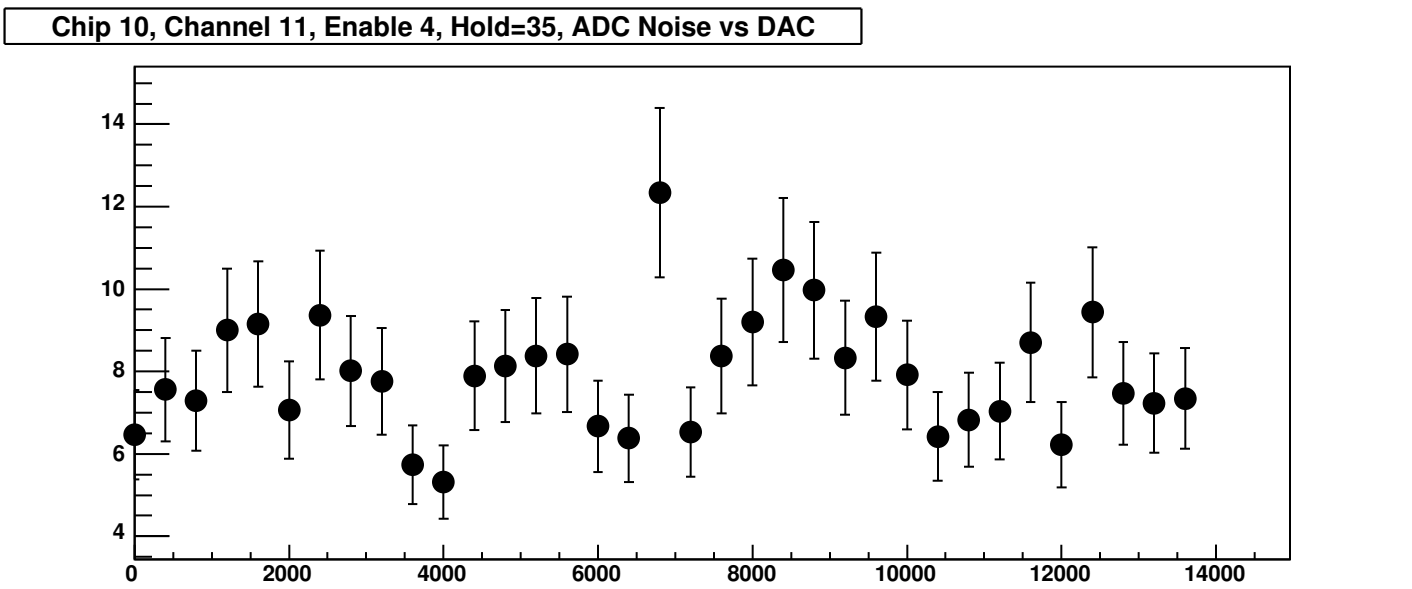
Chip 10, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC

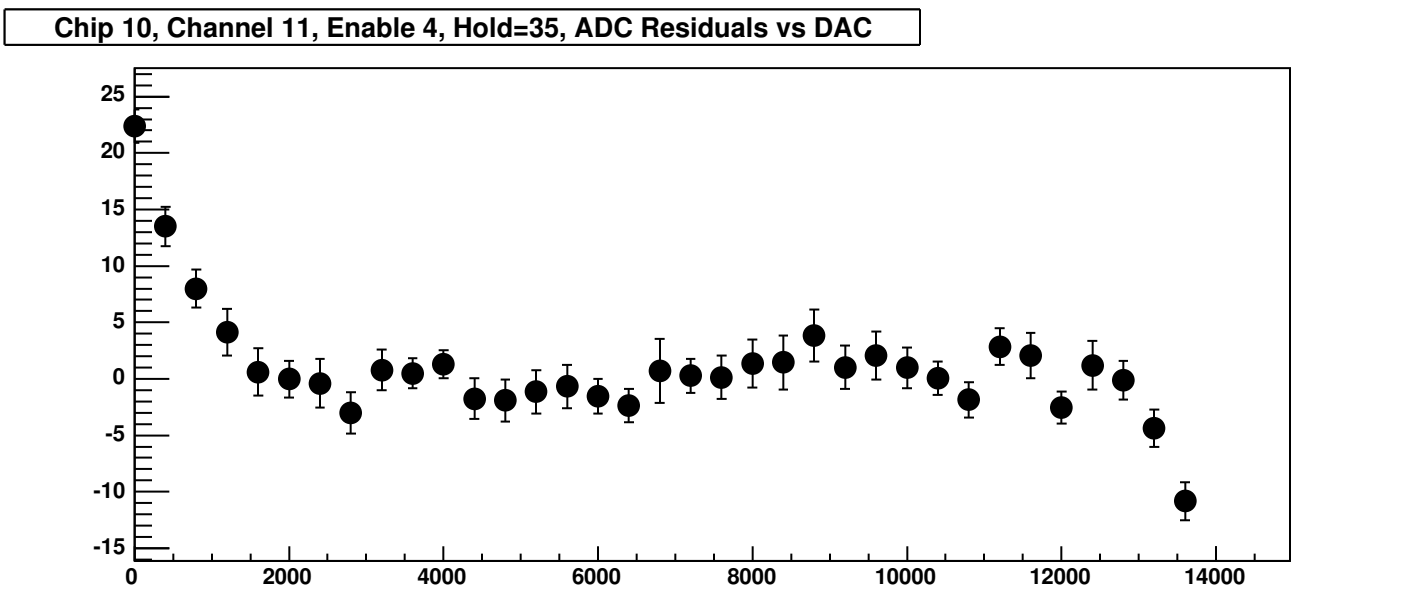


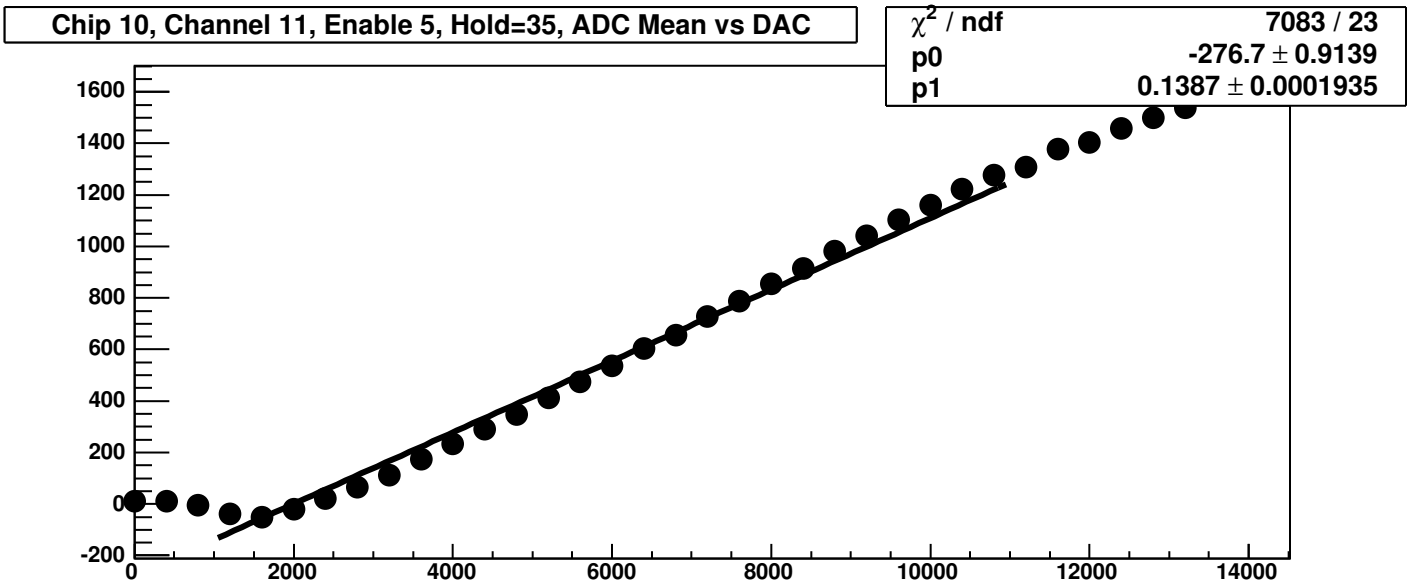
Chip 10, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

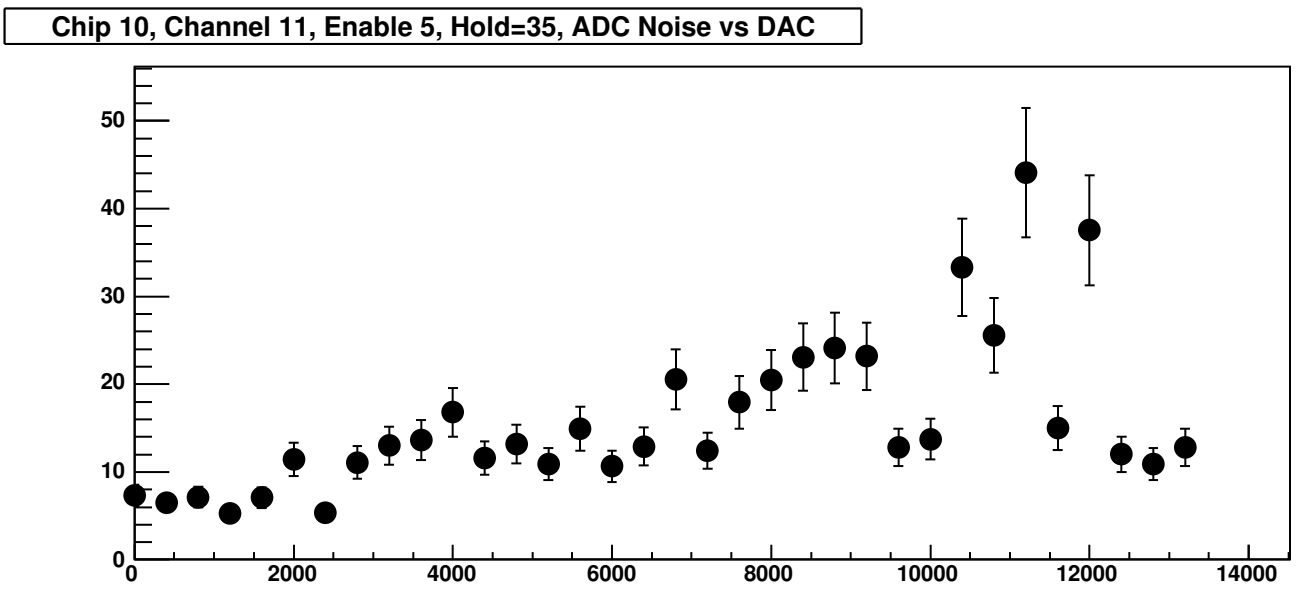


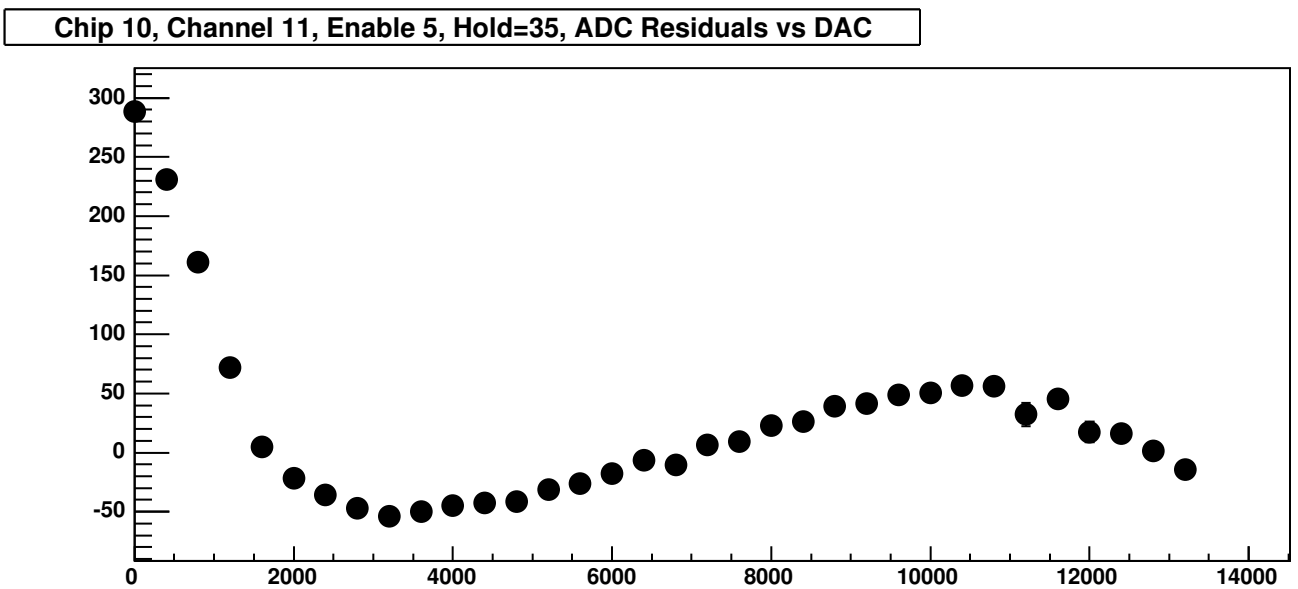

Chip 10, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.91 / 23
p0 64.24 ± 0.8136
p1 0.01852 ± 0.0001238
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

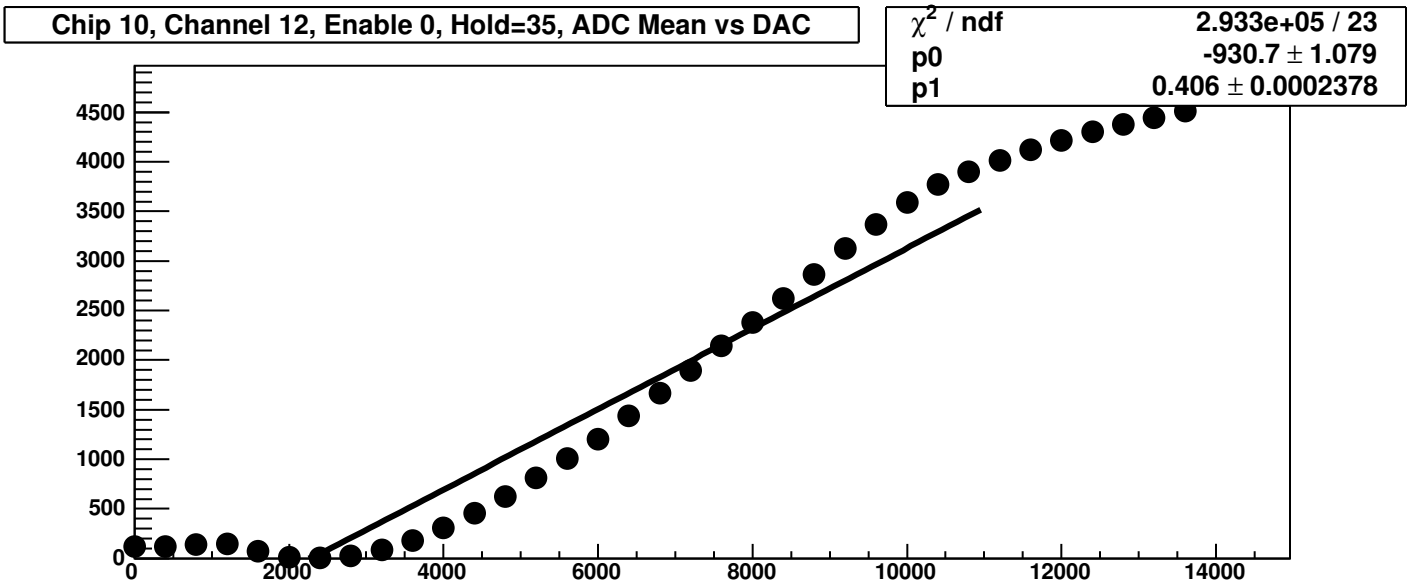

Chip 10, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

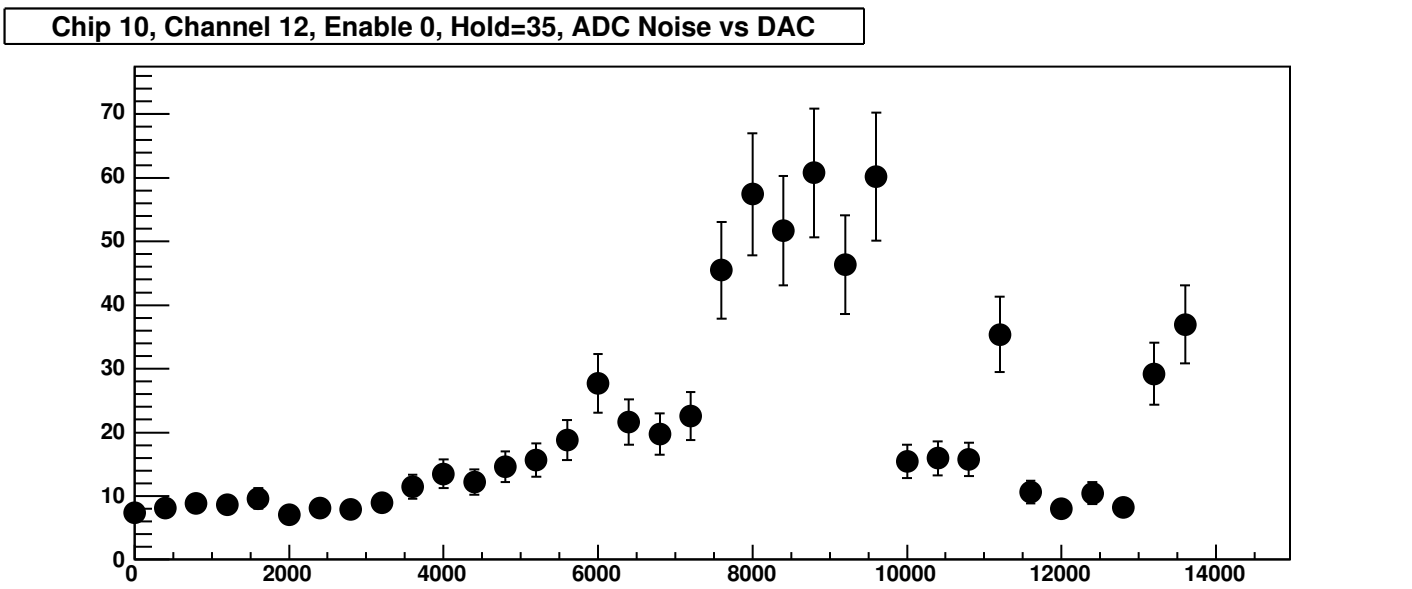

Chip 10, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

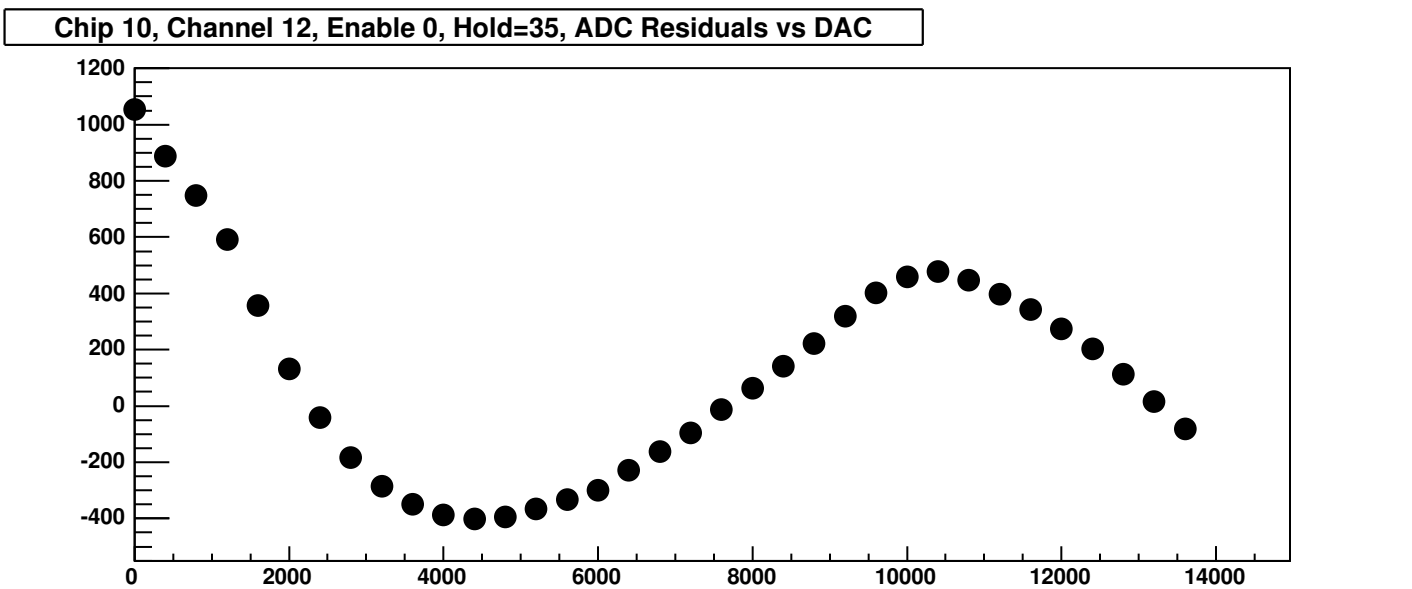

Chip 10, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7083 / 23
p0 -276.7 ± 0.9139
p1 0.1387 ± 0.0001935
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

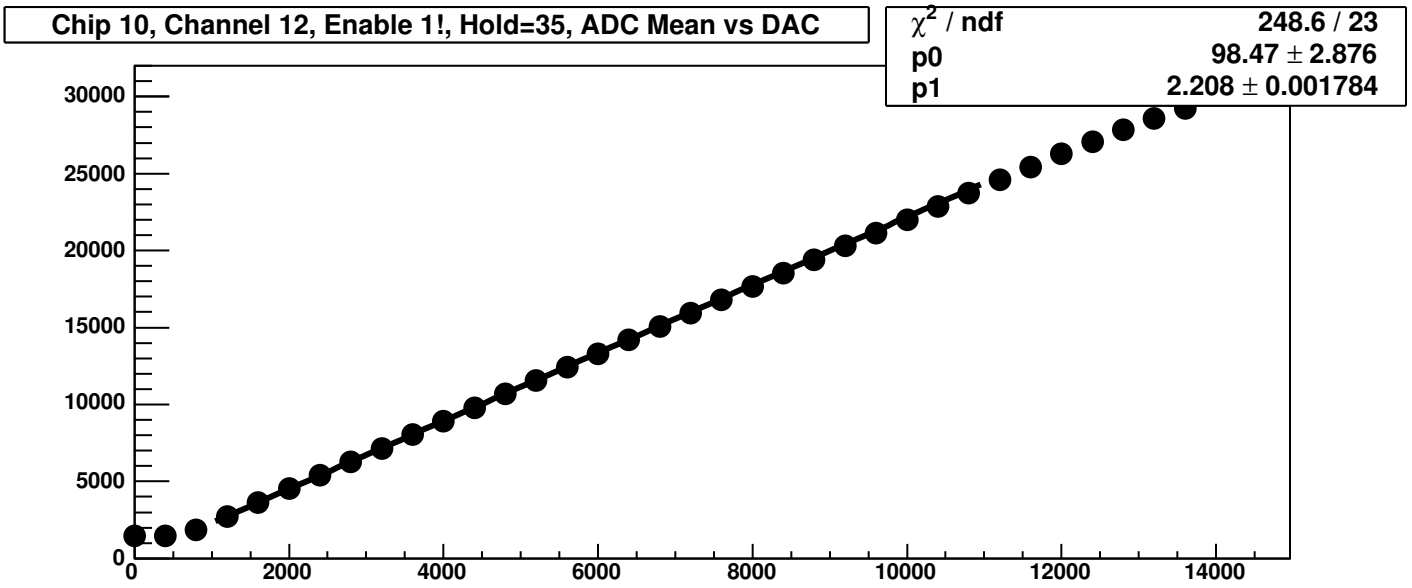

Chip 10, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

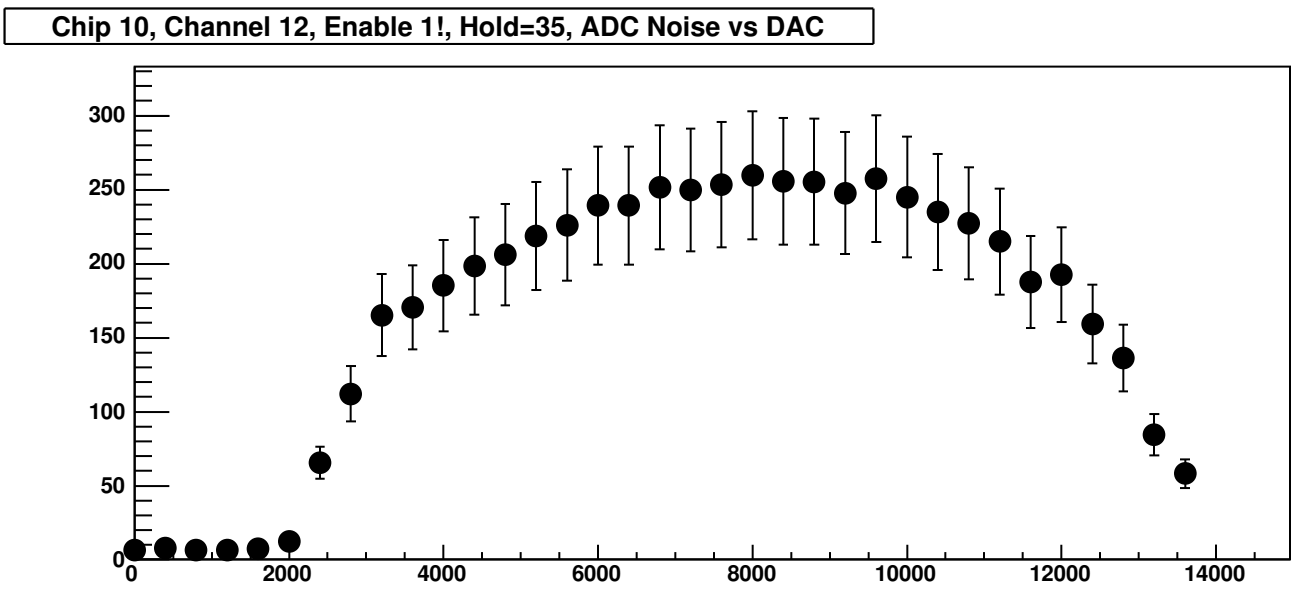

Chip 10, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

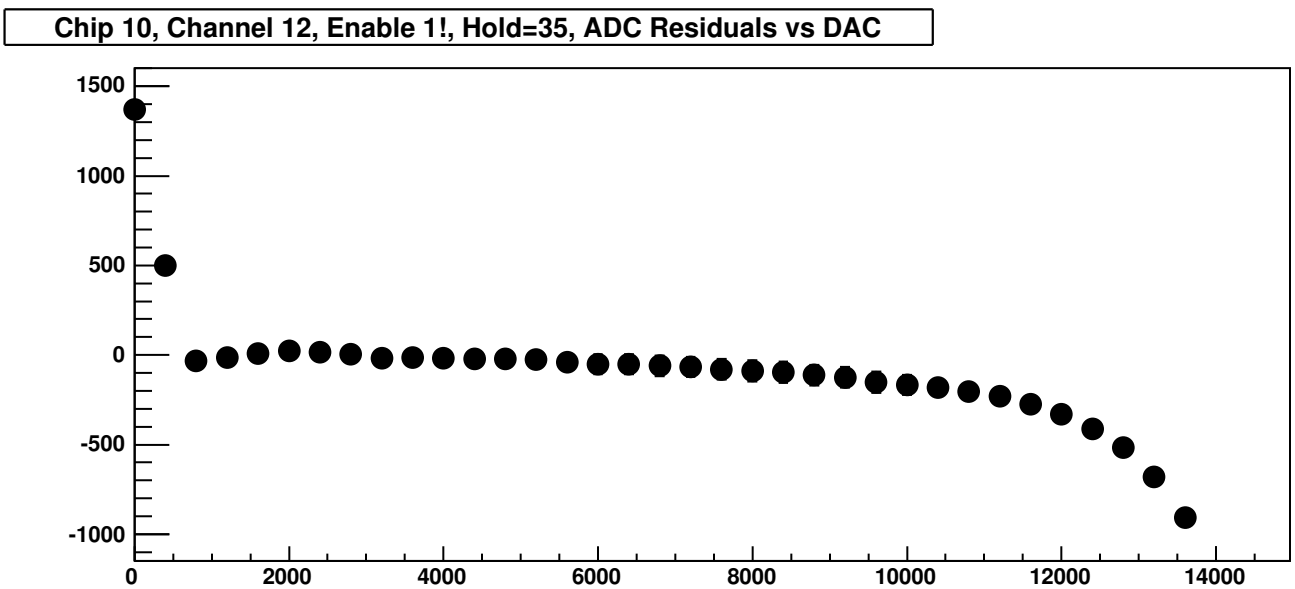

Chip 10, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.933e+05 / 23
p0 -930.7 ± 1.079
p1 0.406 ± 0.0002378
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

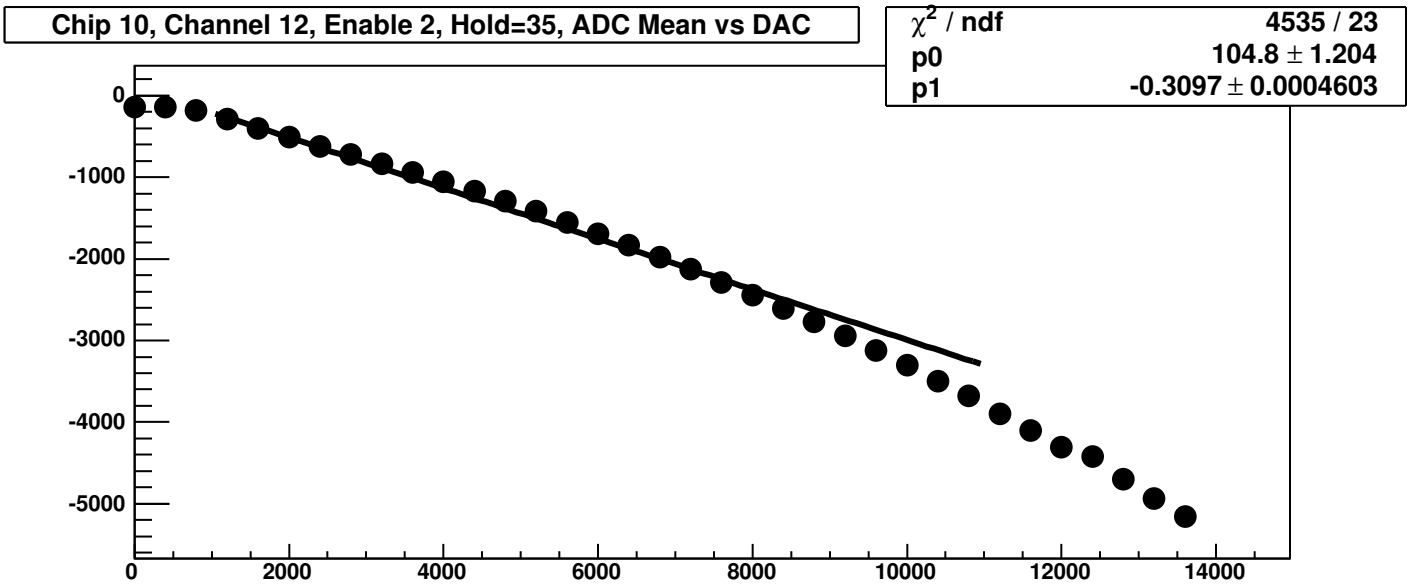

Chip 10, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

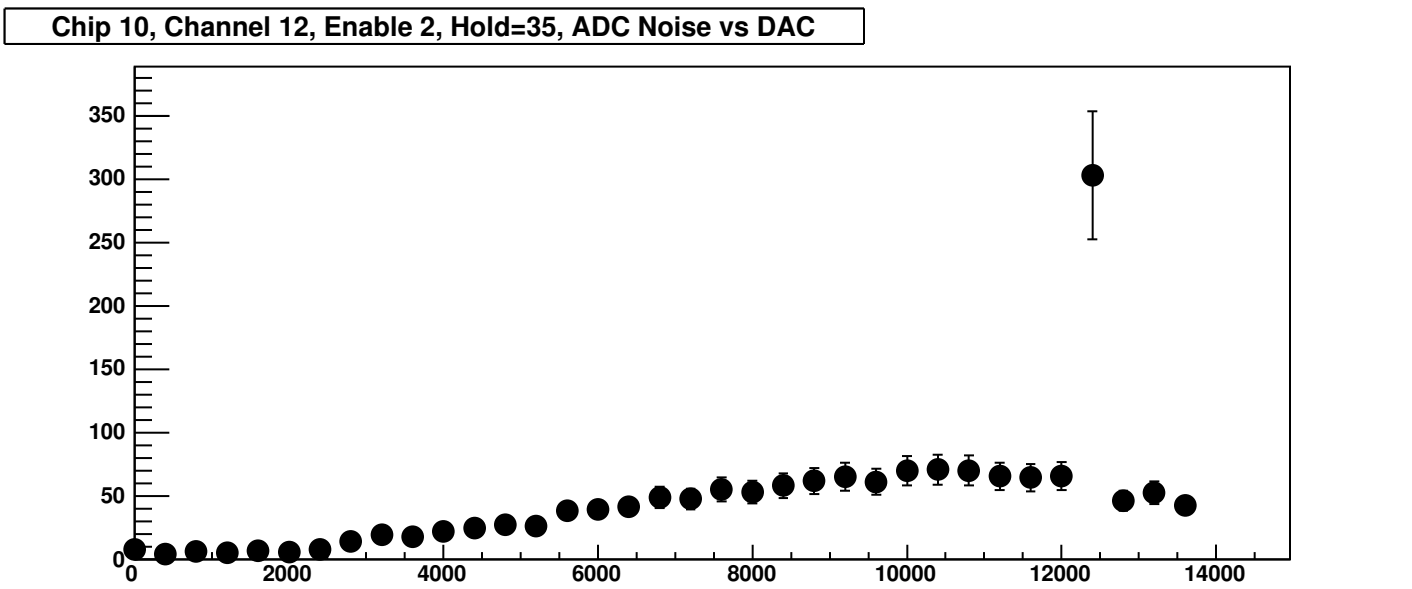

Chip 10, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

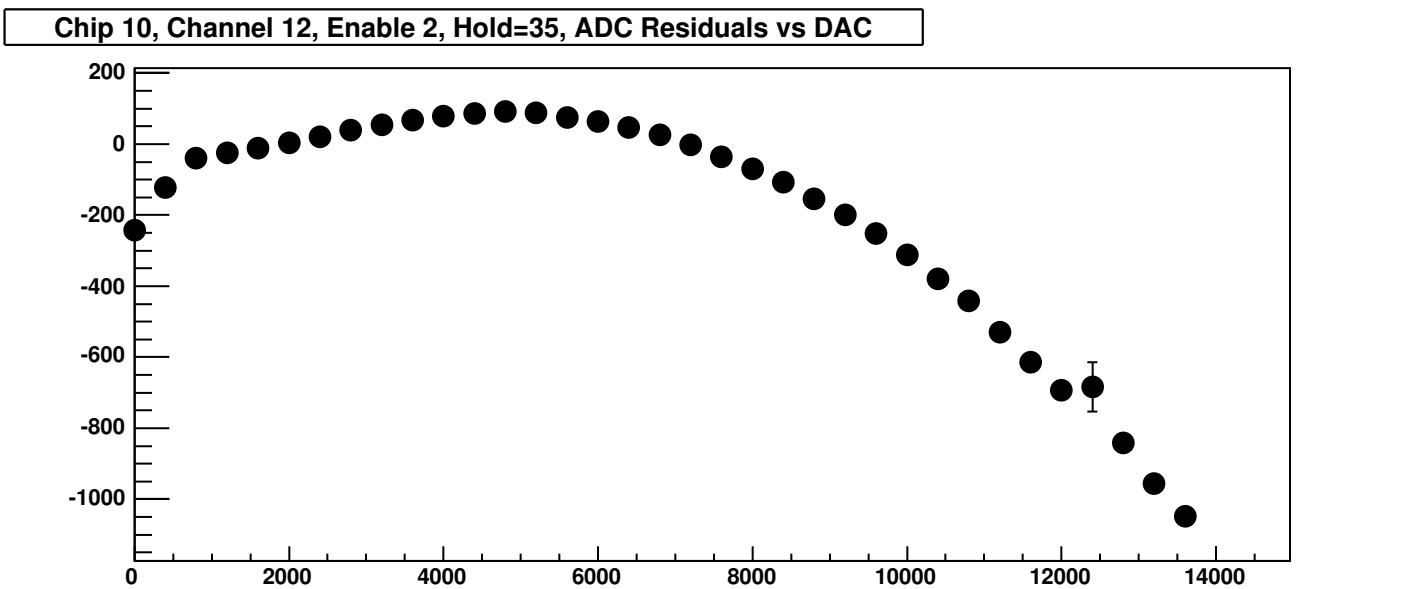

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 248.6 / 23
p0 98.47 ± 2.876
p1 2.208 ± 0.001784
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

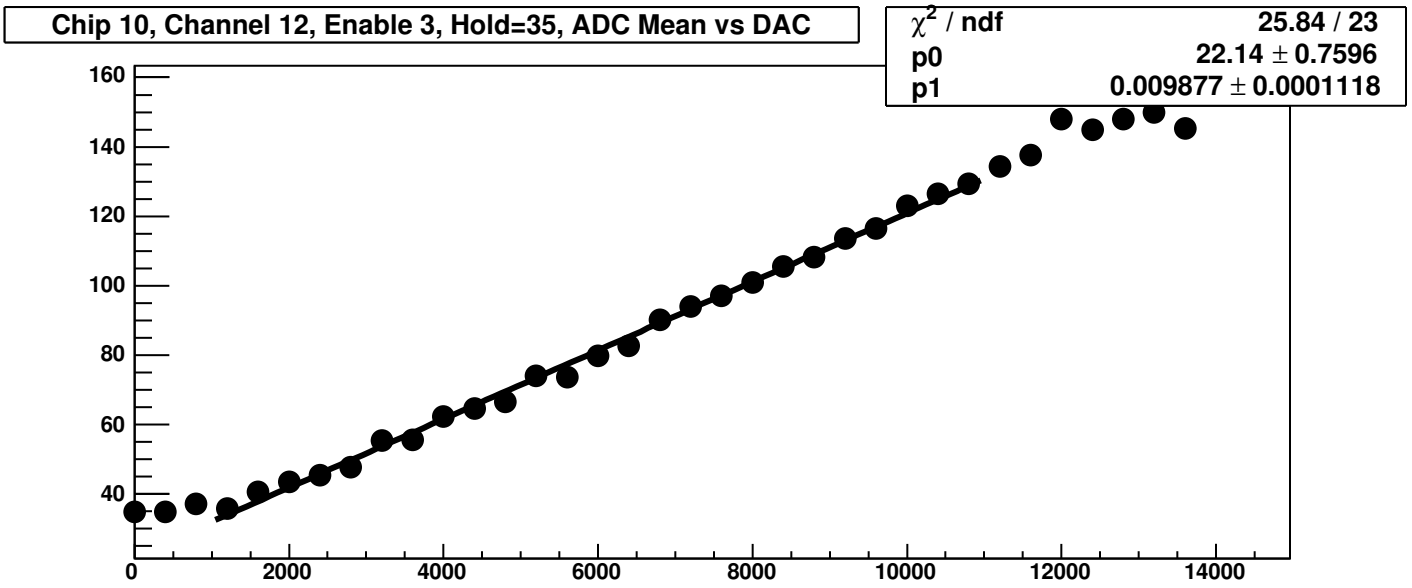

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

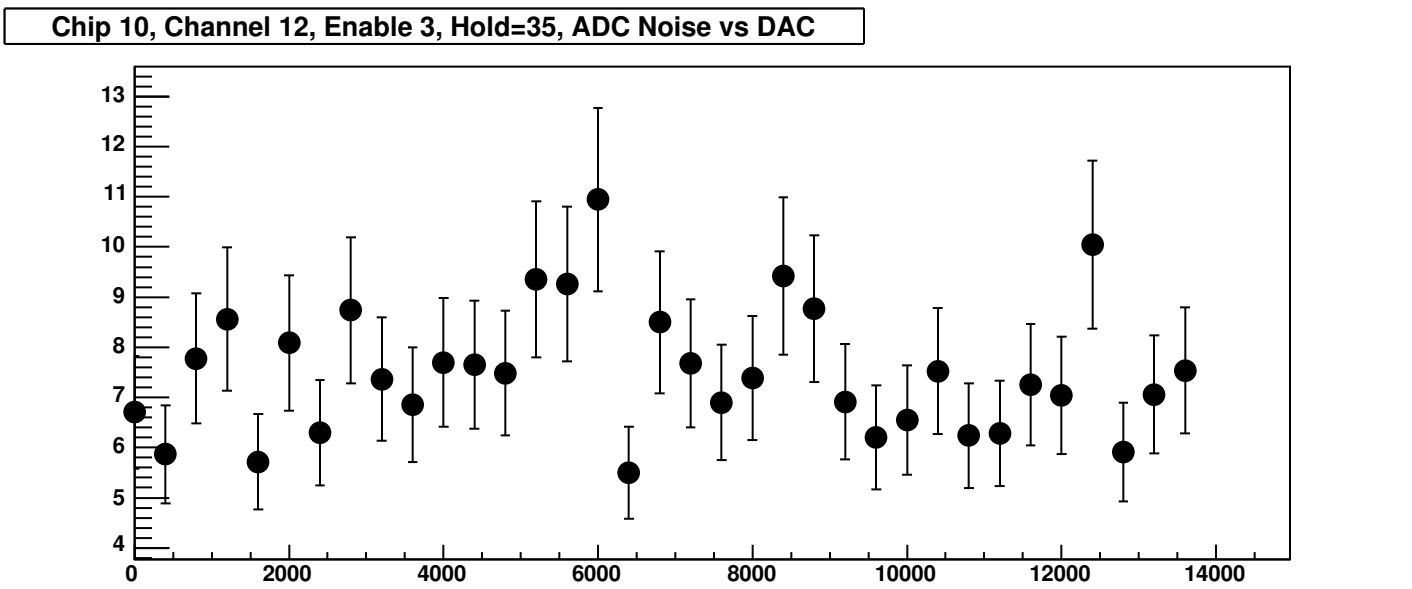

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

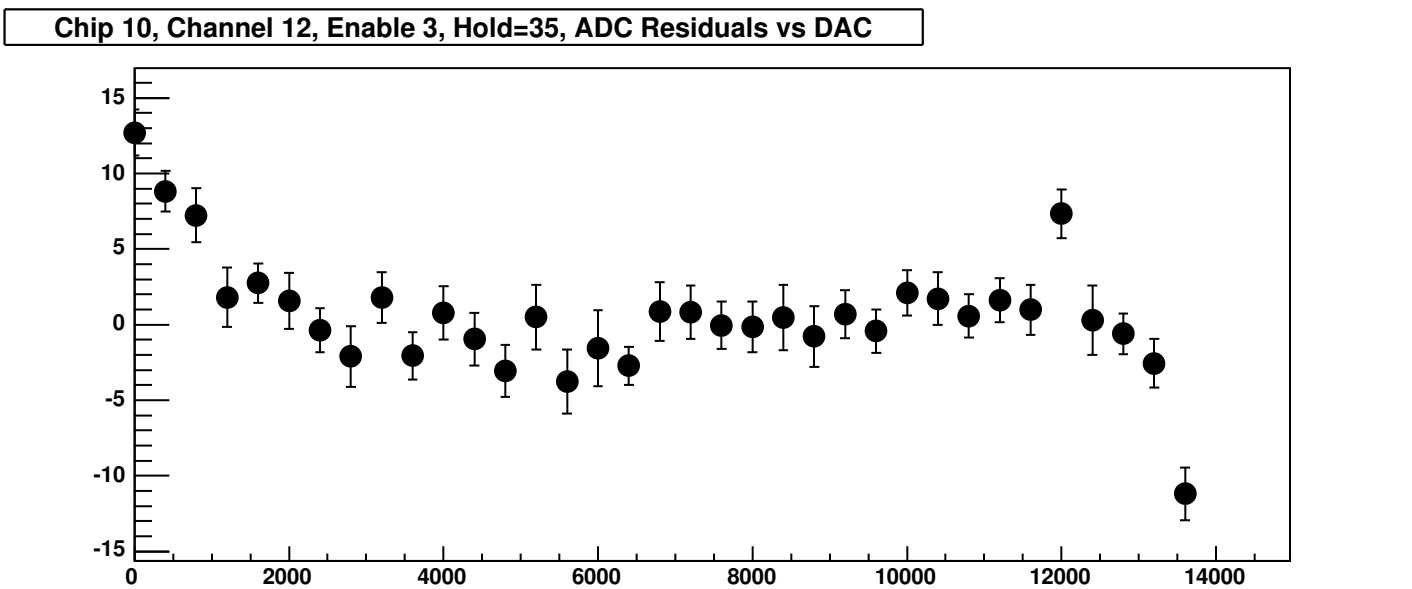

Chip 10, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4535 / 23
p0 104.8 ± 1.204
p1 -0.3097 ± 0.0004603
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

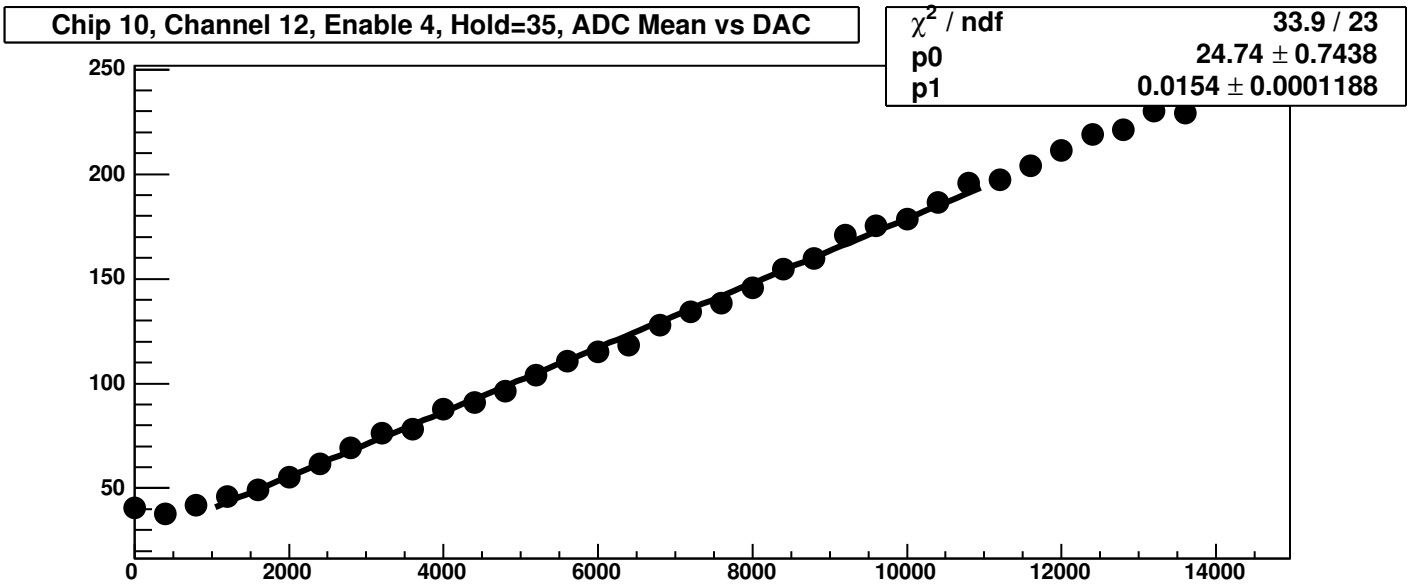

Chip 10, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

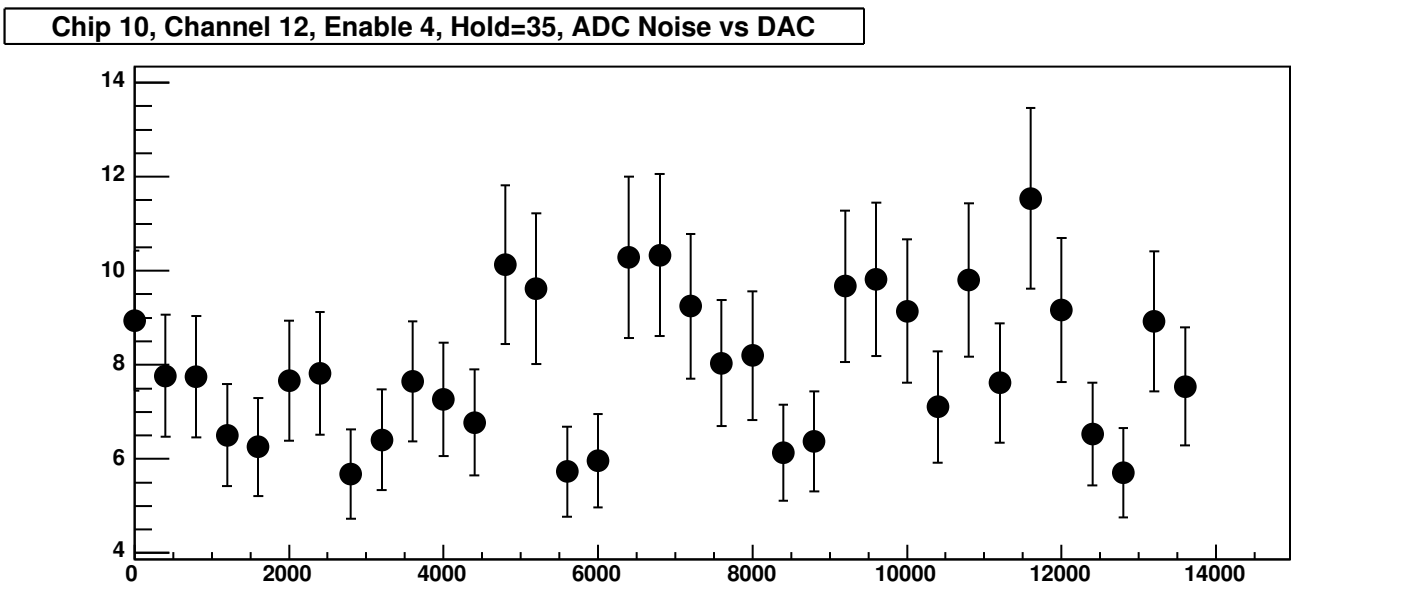

Chip 10, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

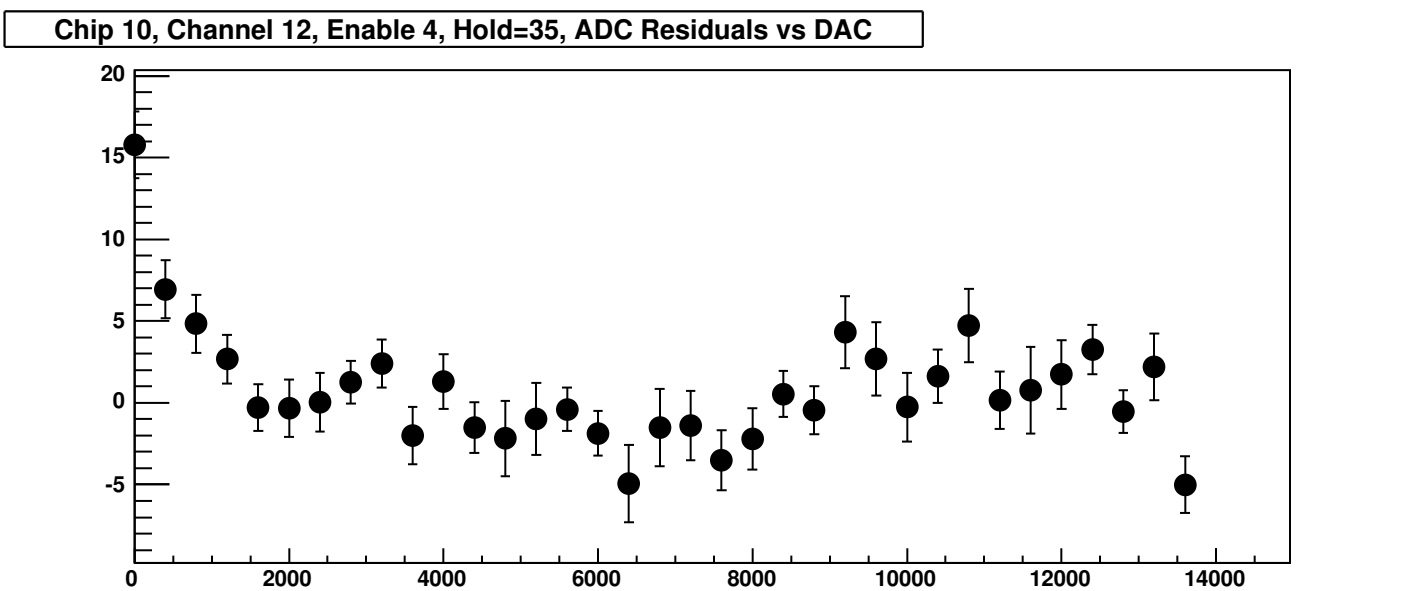

Chip 10, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.84 / 23
p0 22.14 ± 0.7596
p1 0.009877 ± 0.0001118
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

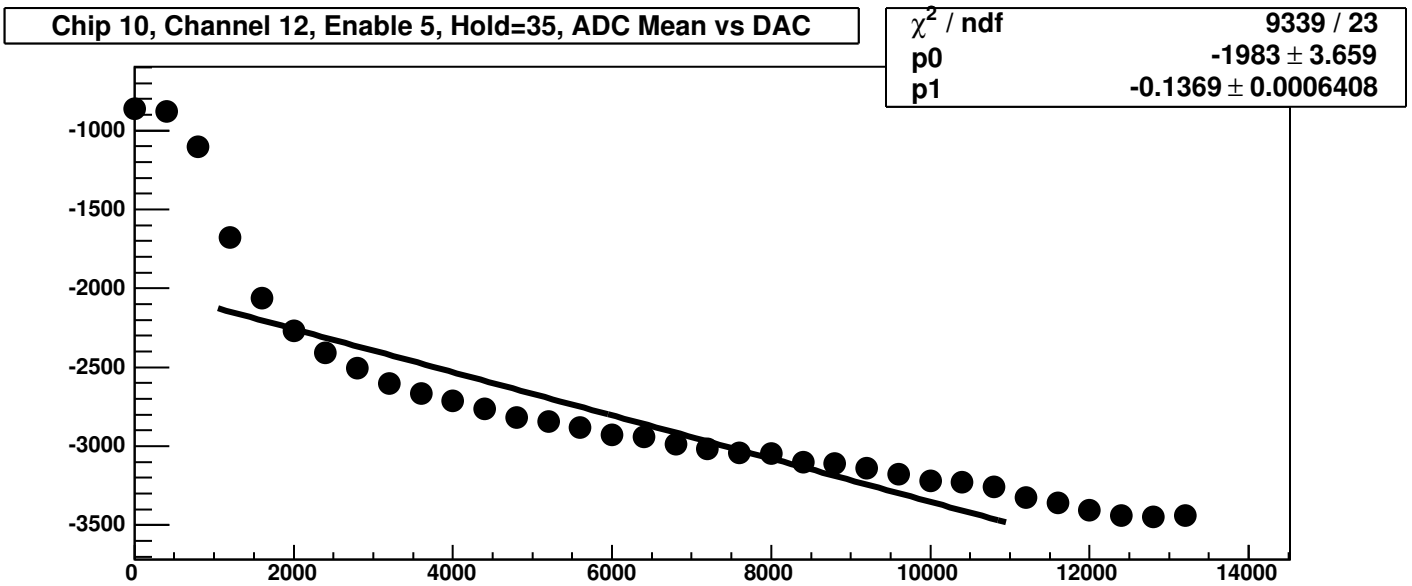

Chip 10, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

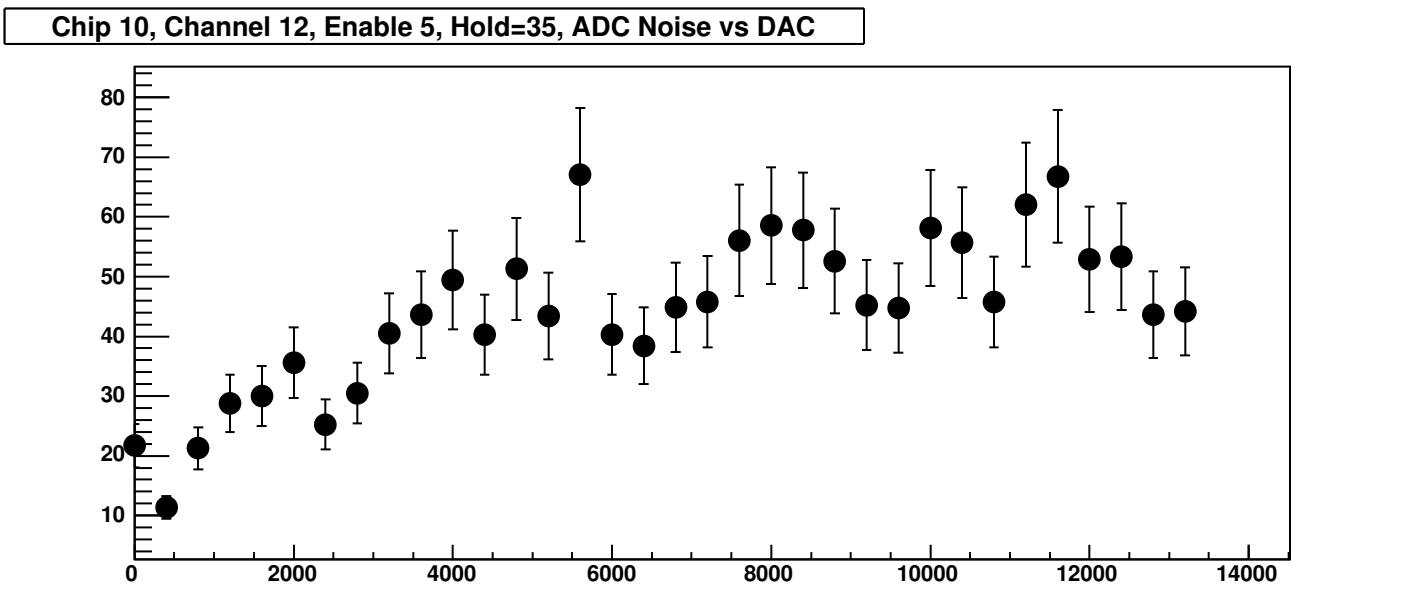

Chip 10, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

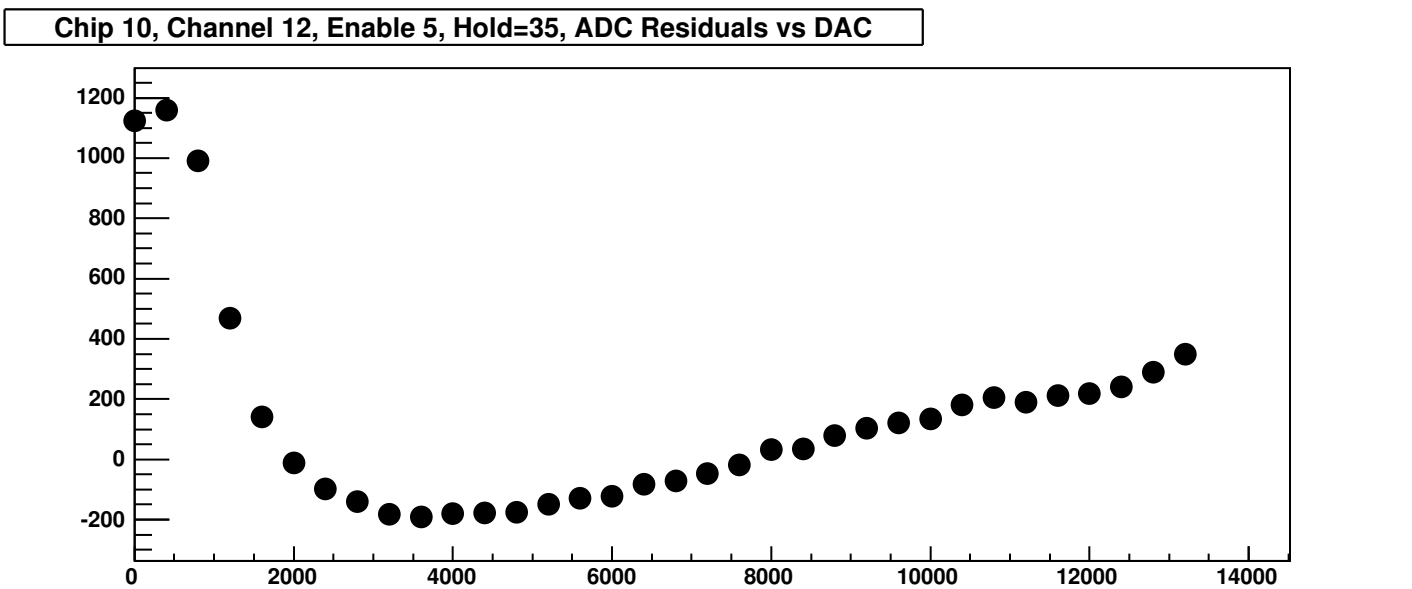

Chip 10, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.9 / 23
p0 24.74 ± 0.7438
p1 0.0154 ± 0.0001188
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

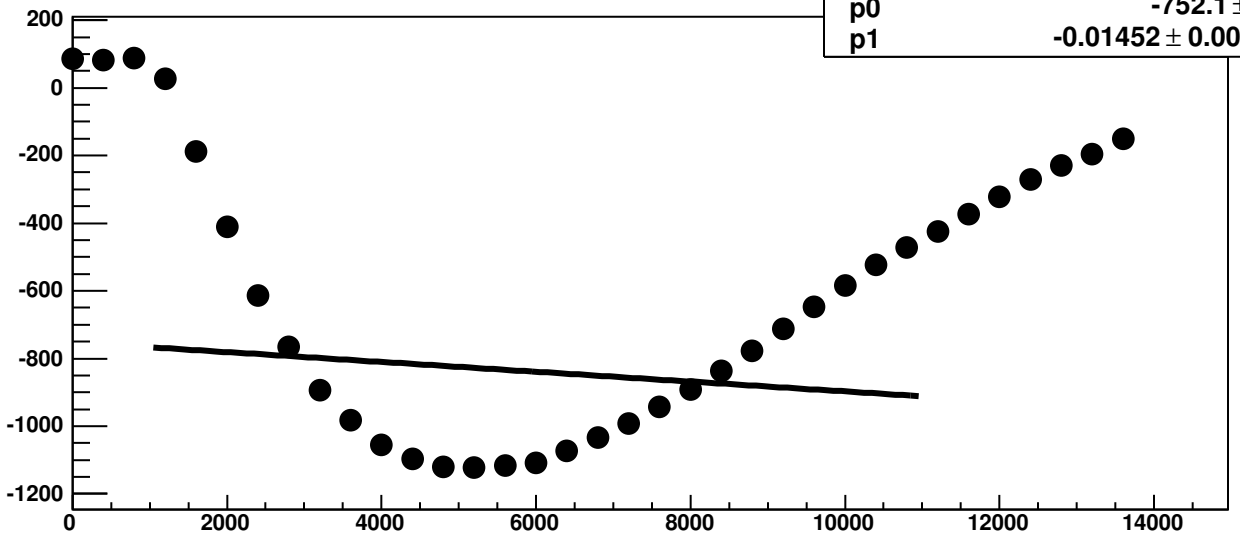

Chip 10, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9339 / 23
p0 -1983 ± 3.659
p1 -0.1369 ± 0.0006408
-1000
-1500
-2000
-2500
-3000
-3500
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

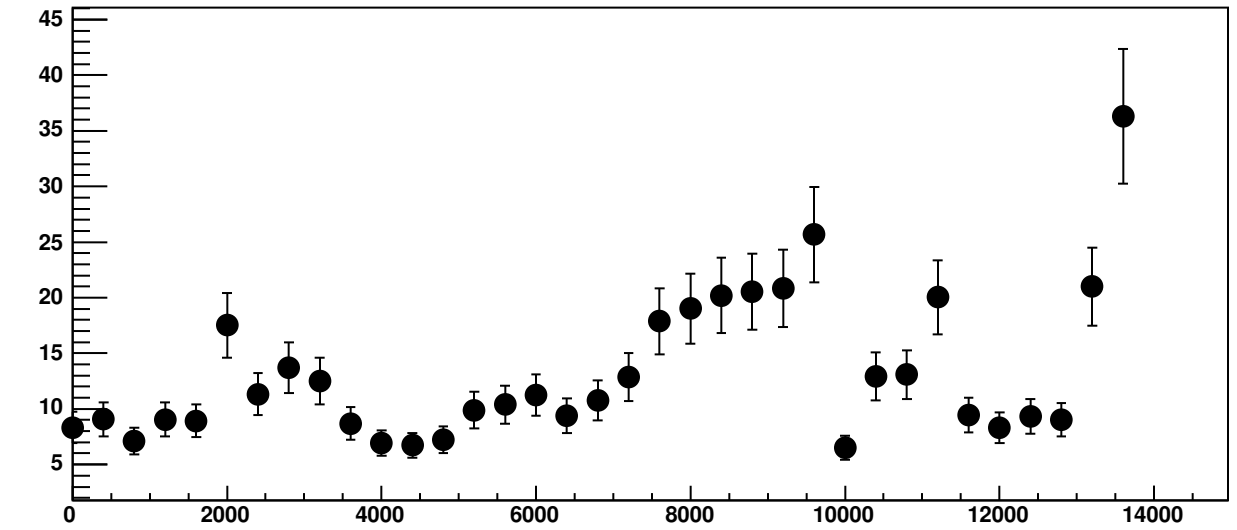

Chip 10, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

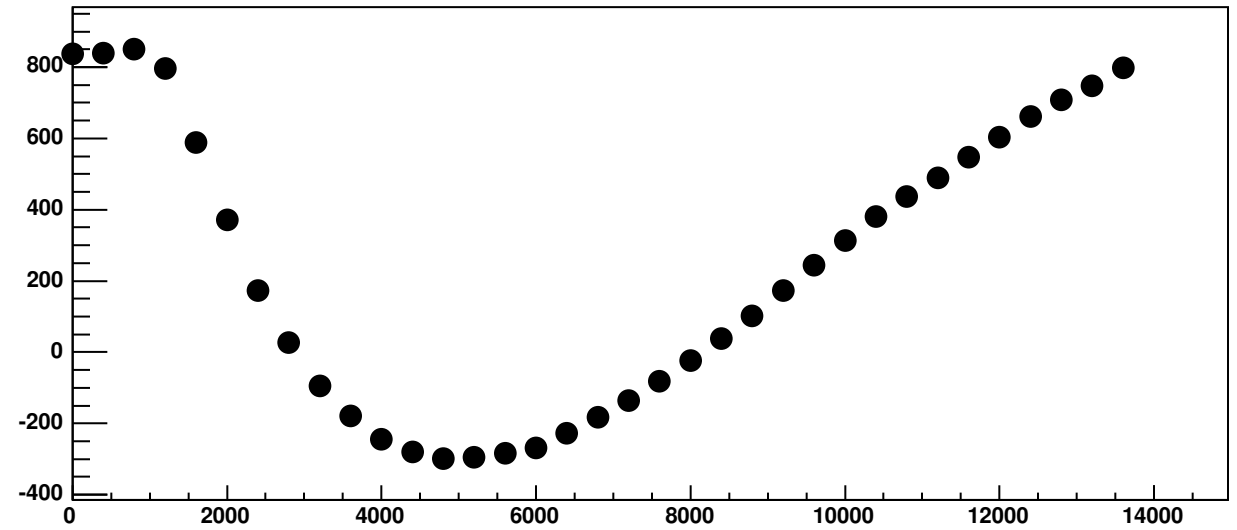
χ^2 / ndf	4.86e+05 / 23
p0	-752.1 ± 1.07
p1	-0.01452 ± 0.0001762

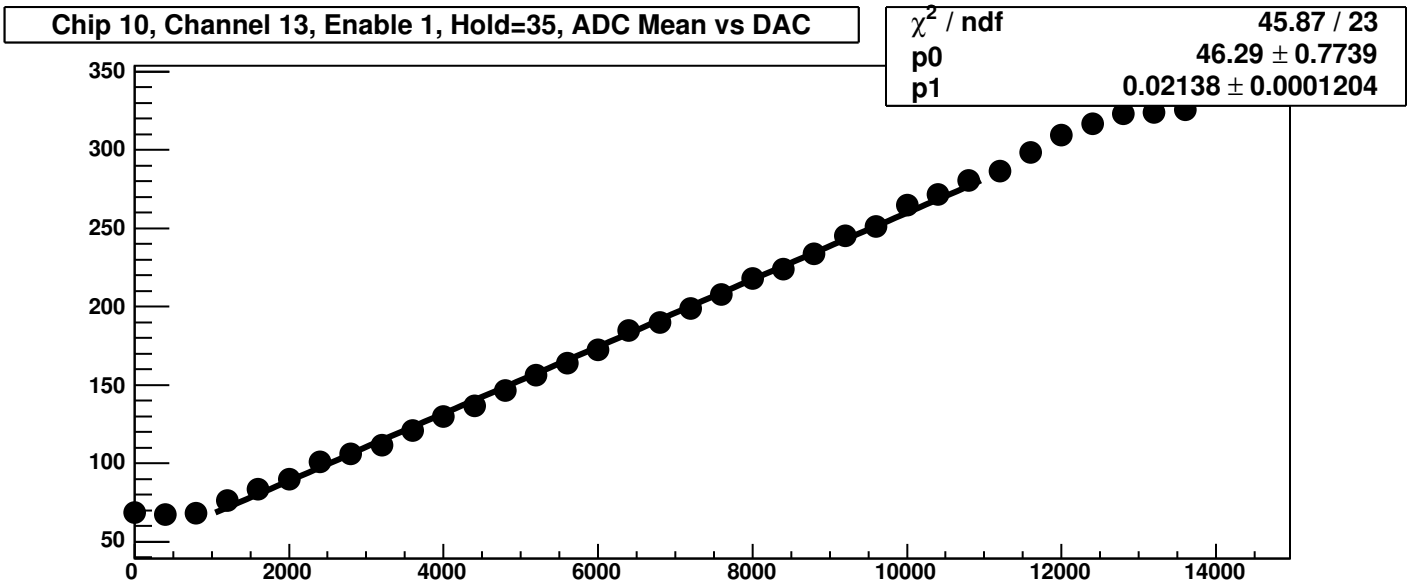


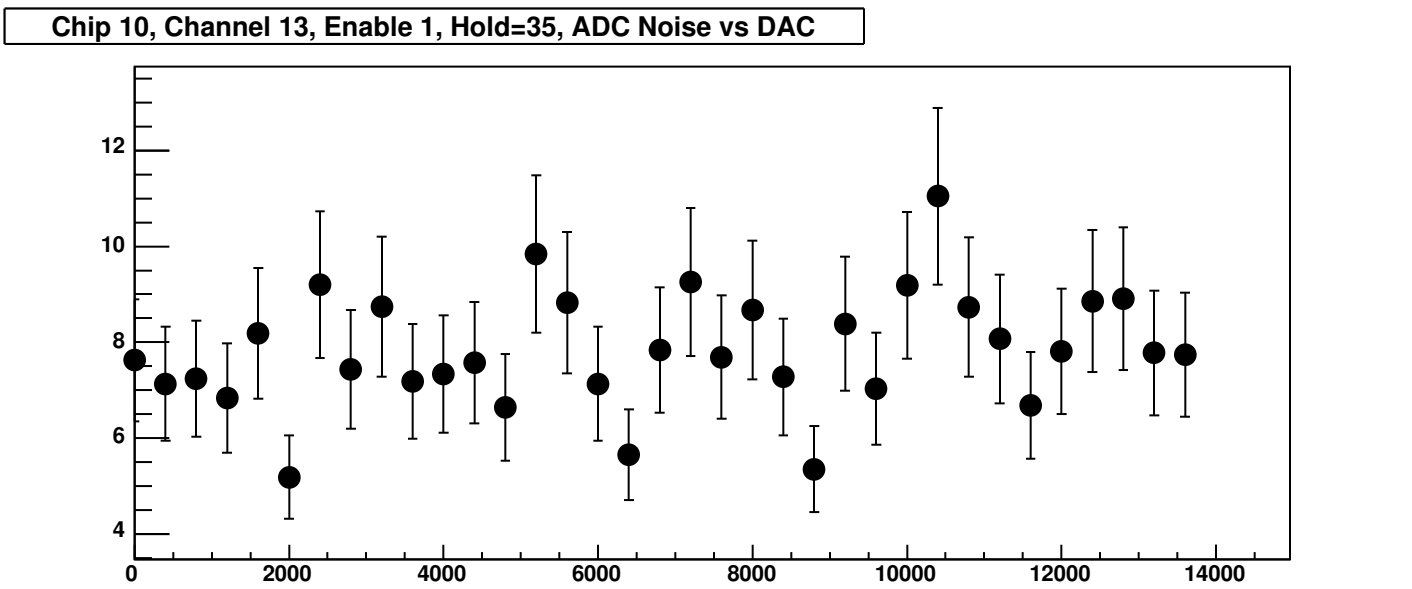
Chip 10, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

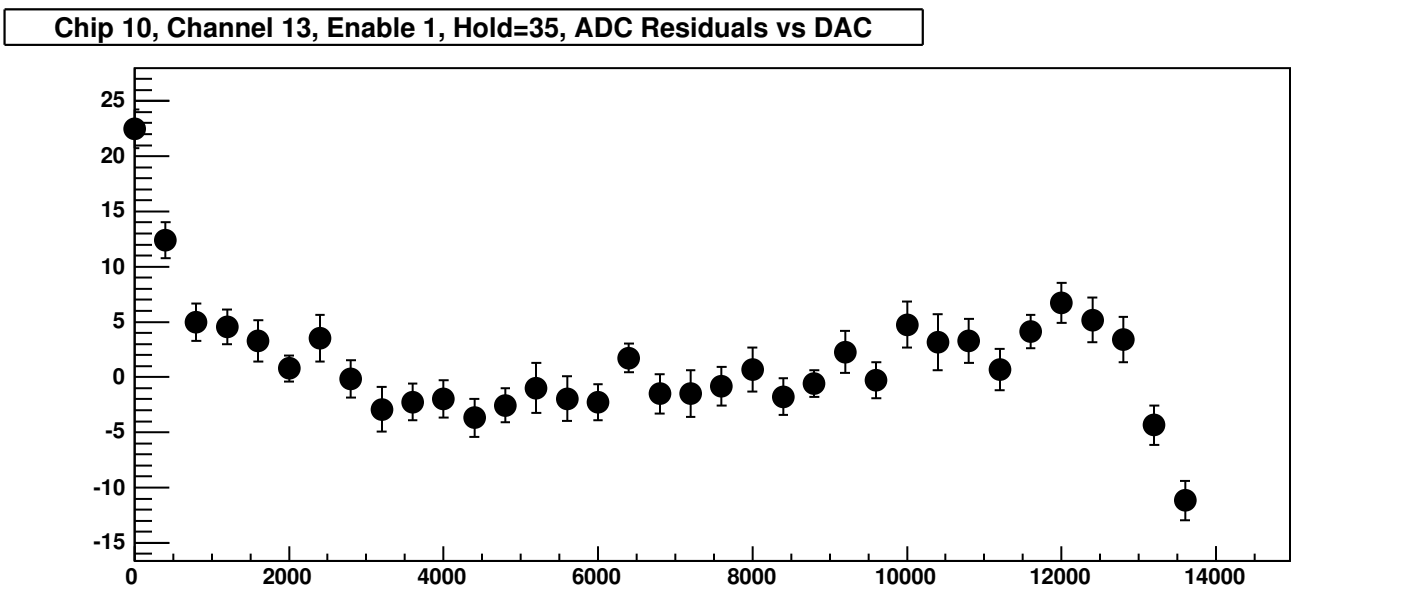


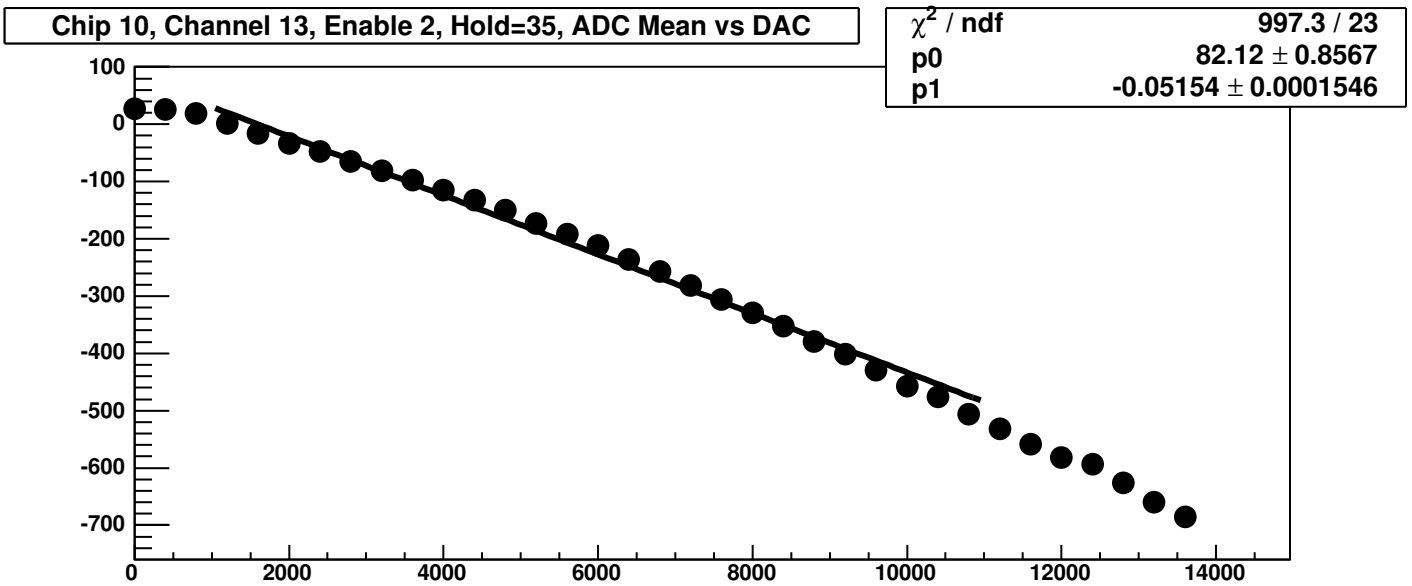
Chip 10, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

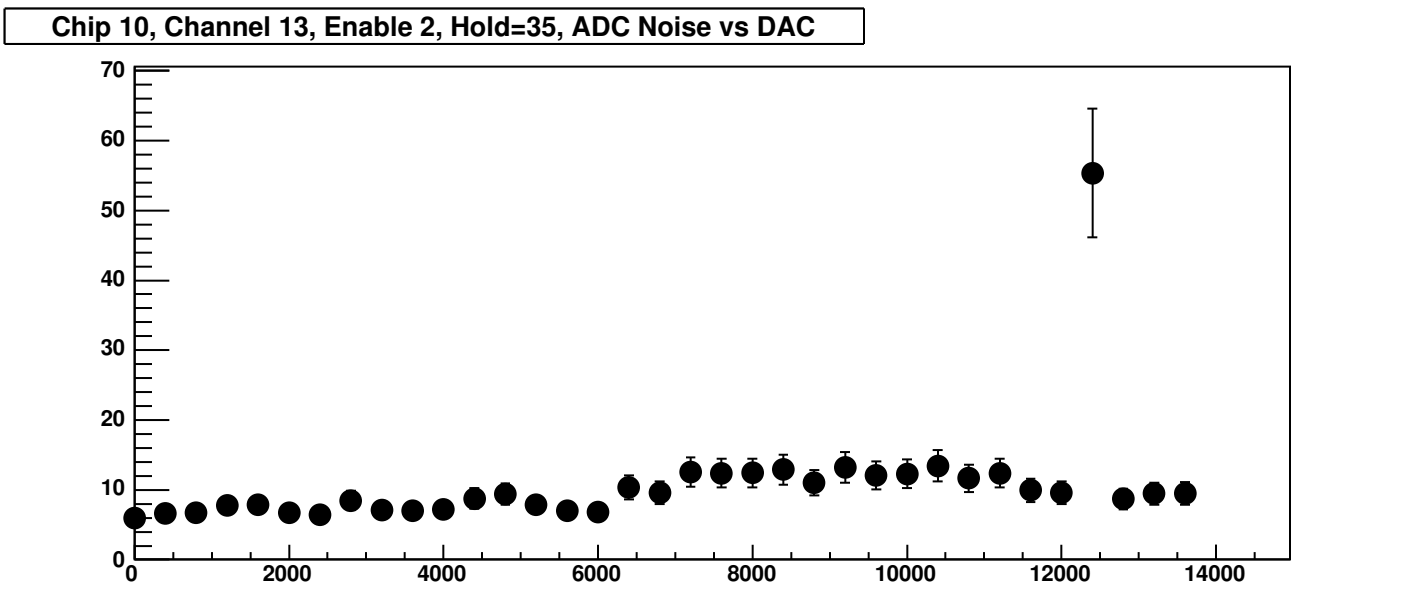


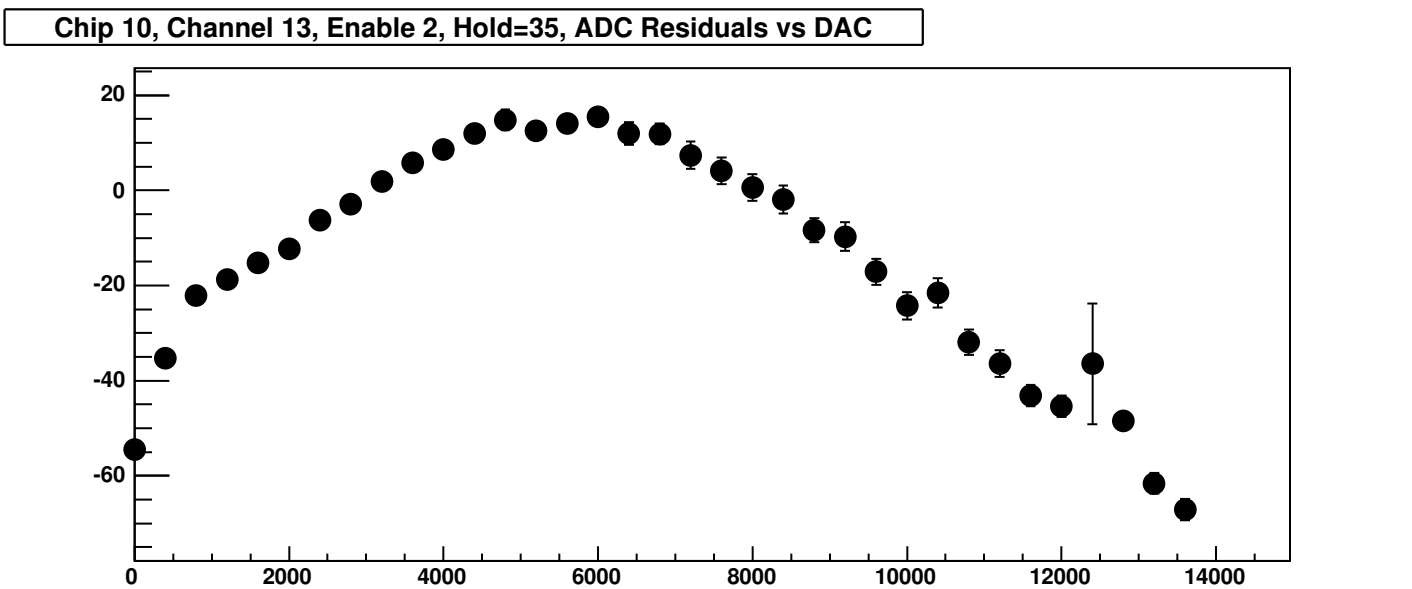

Chip 10, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.87 / 23
p0 46.29 ± 0.7739
p1 0.02138 ± 0.0001204
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

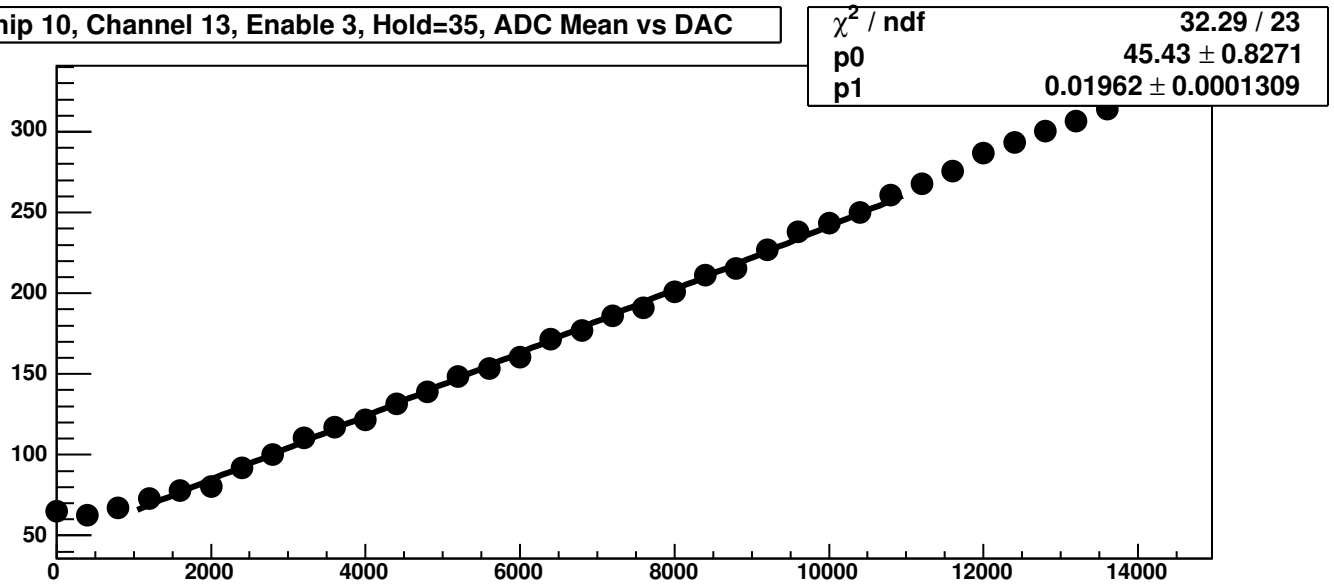

Chip 10, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 997.3 / 23
p0 82.12 ± 0.8567
p1 -0.05154 ± 0.0001546
100
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

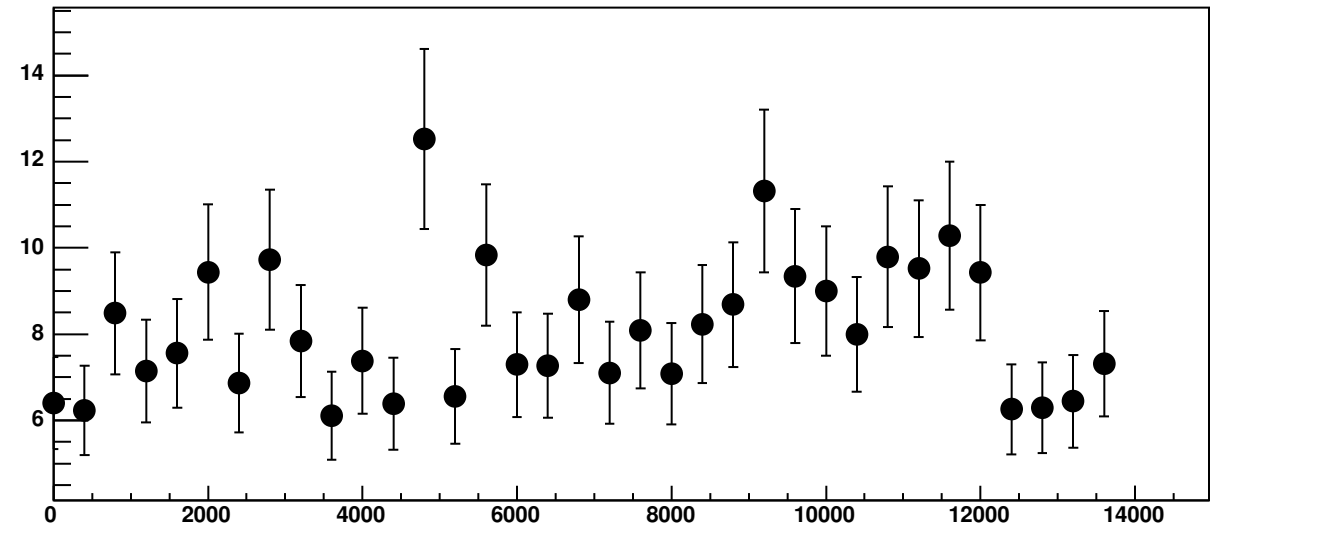

Chip 10, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

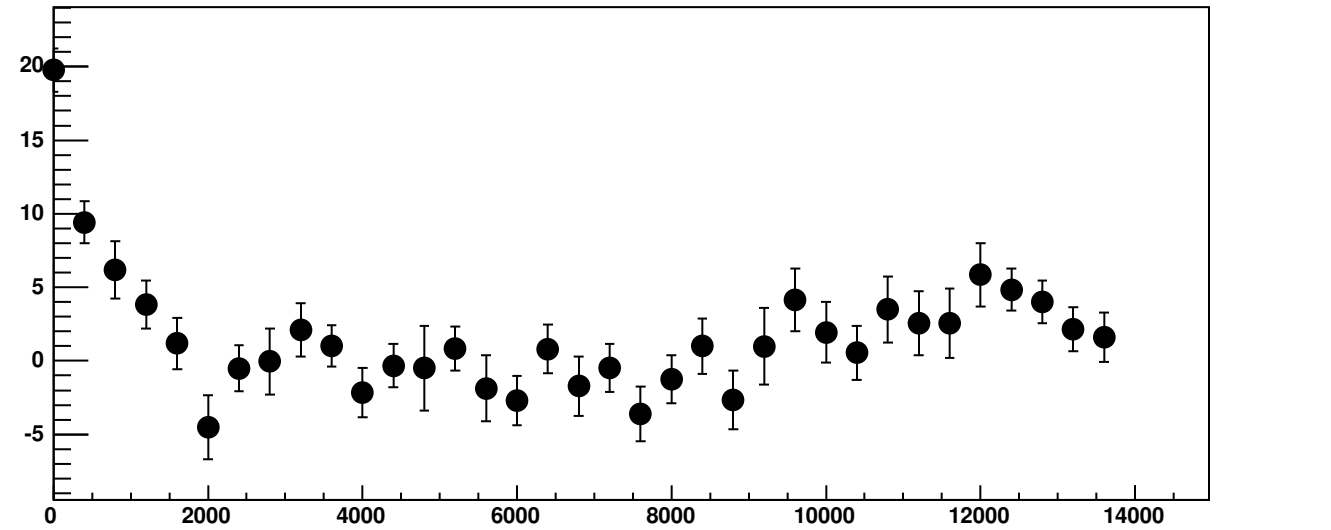
Chip 10, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

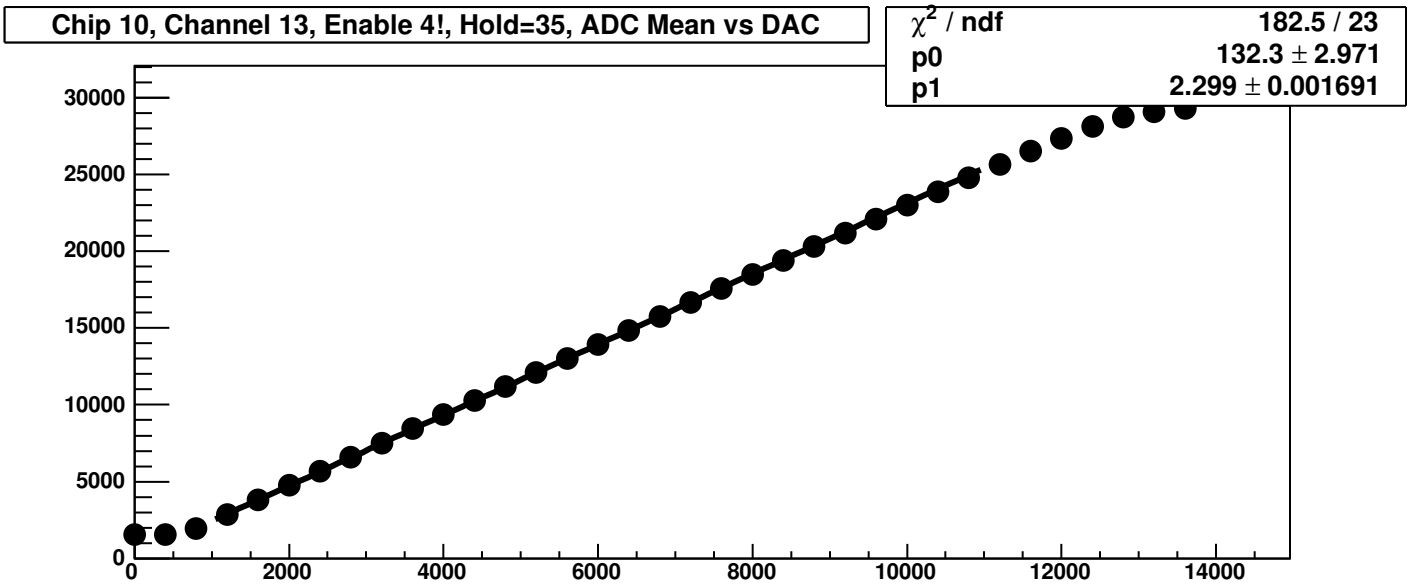


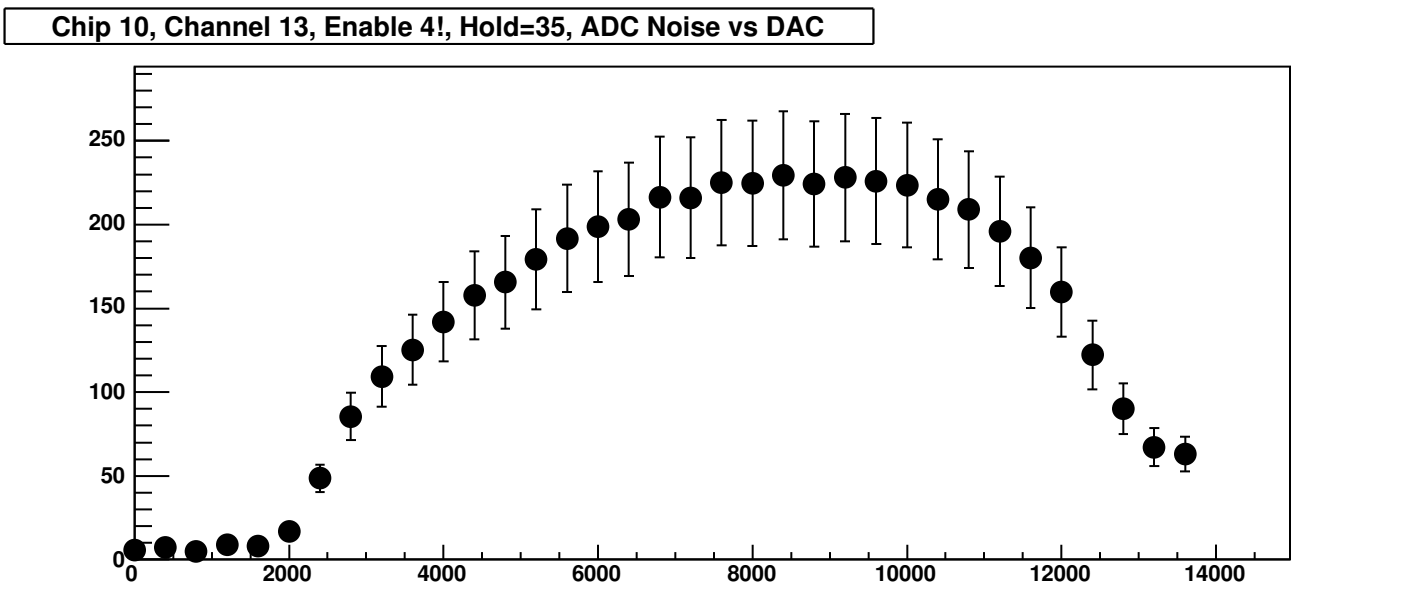
Chip 10, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

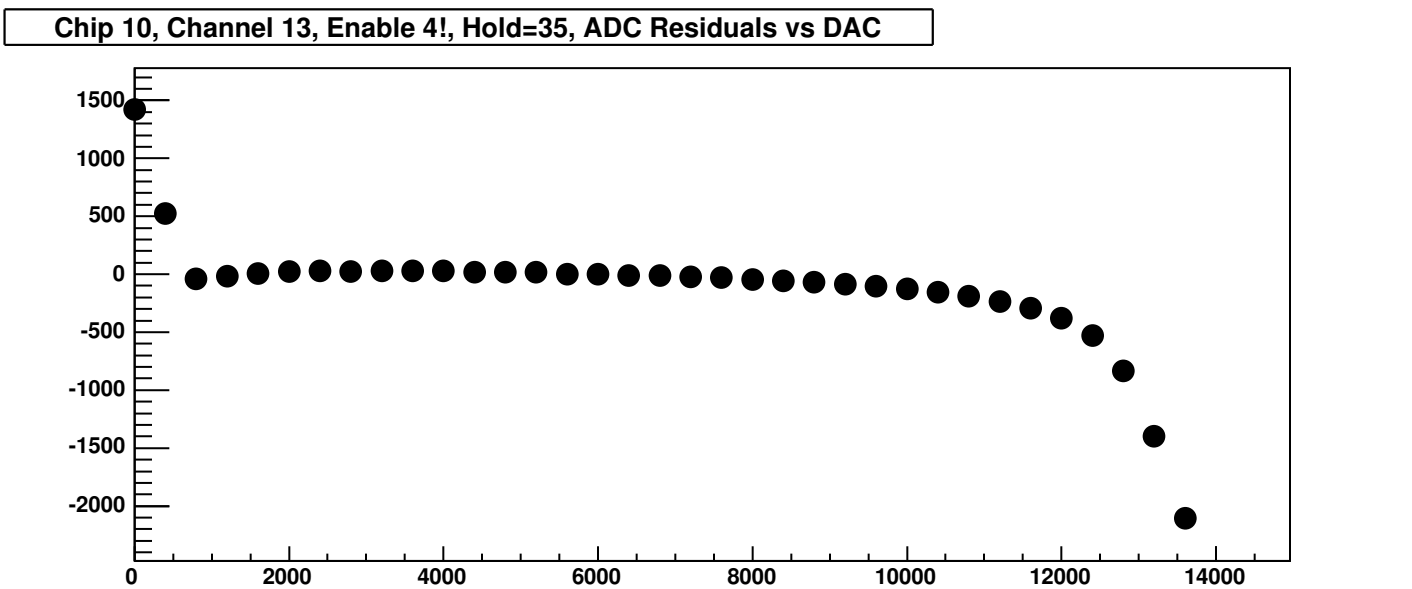


Chip 10, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



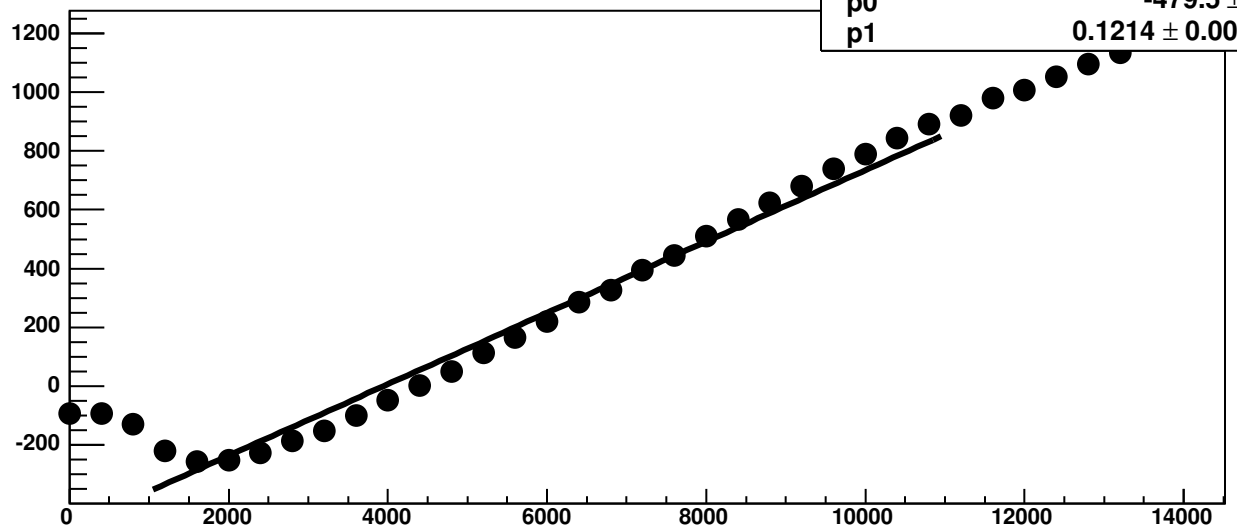

Chip 10, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 182.5 / 23
p0 132.3 ± 2.971
p1 2.299 ± 0.001691
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

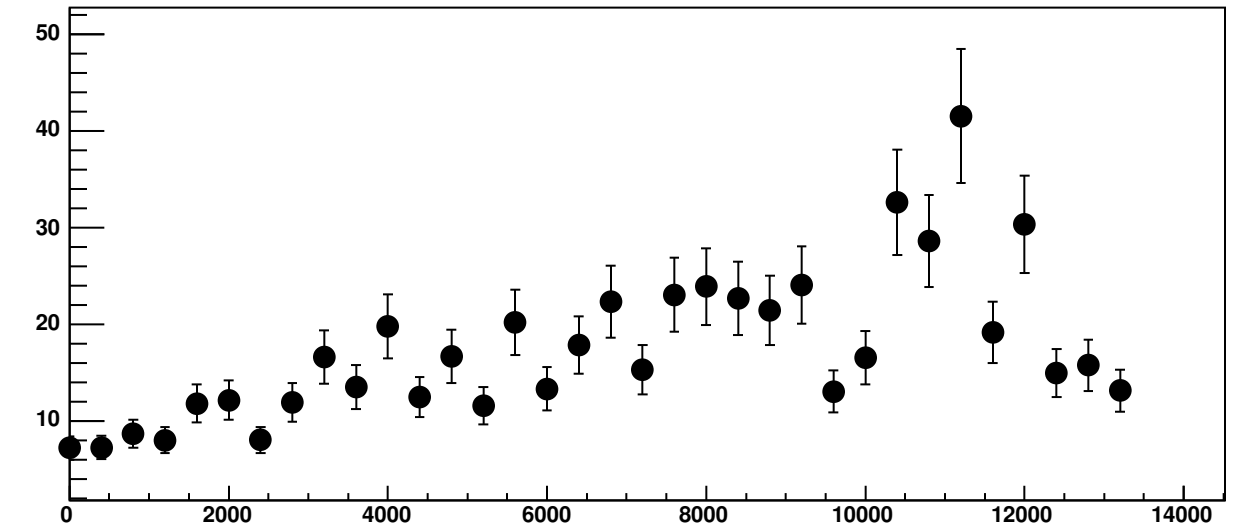

Chip 10, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

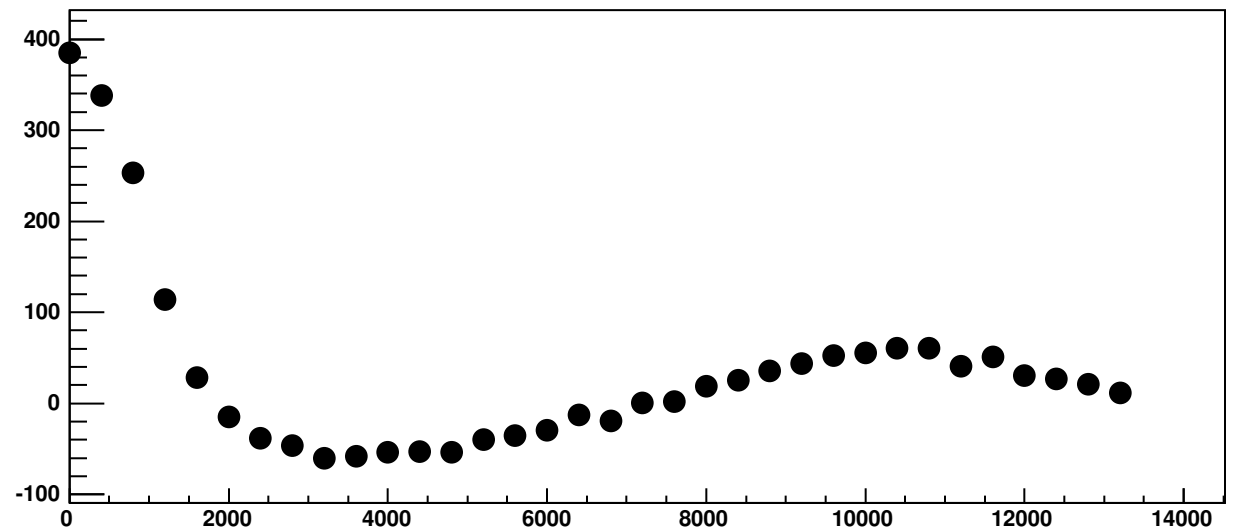
χ^2 / ndf	7175 / 23
p0	-479.5 ± 1.251
p1	0.1214 ± 0.0002375

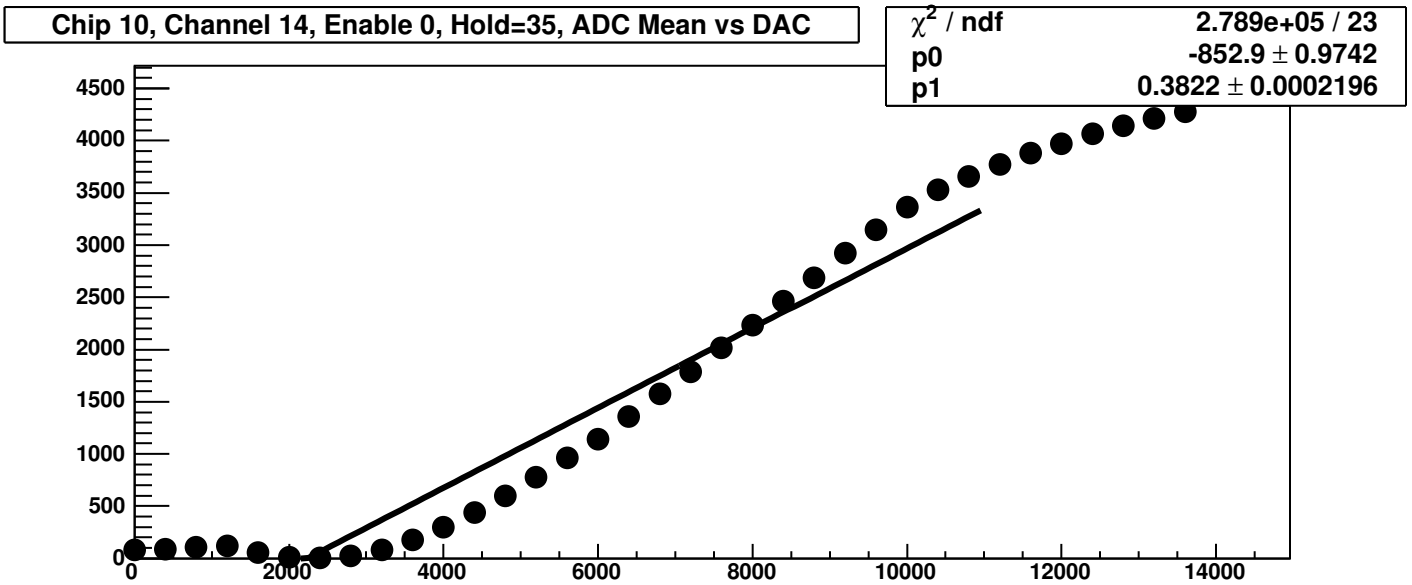


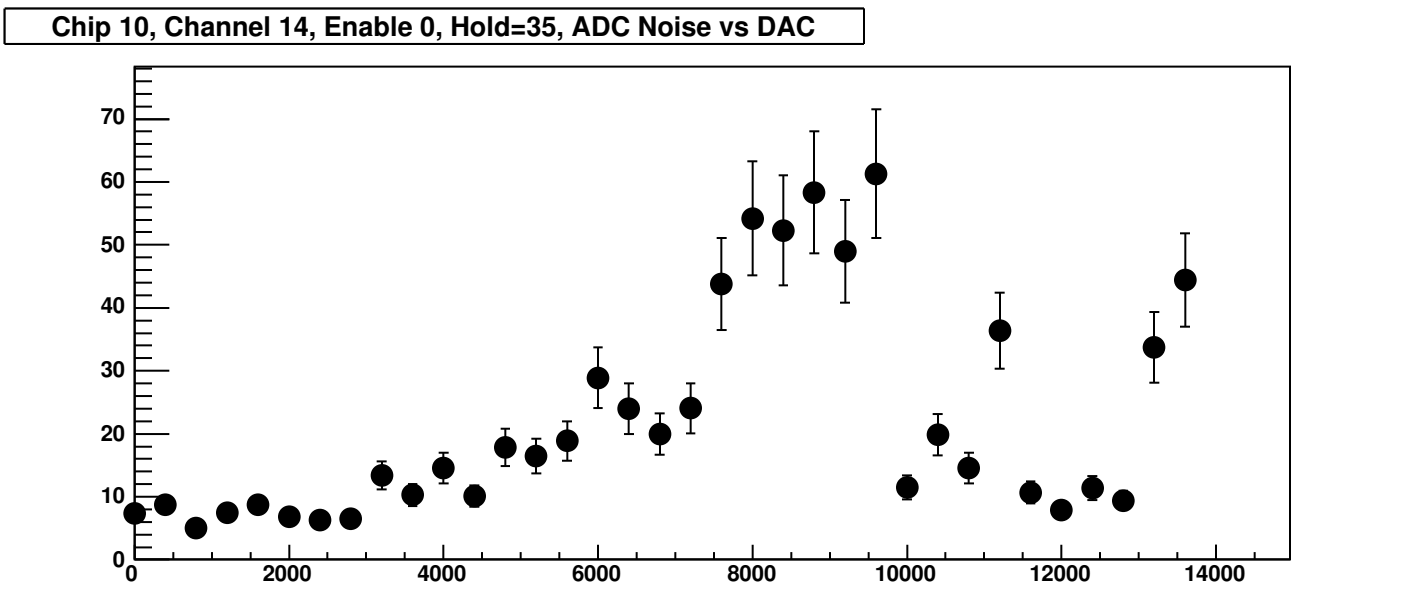
Chip 10, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

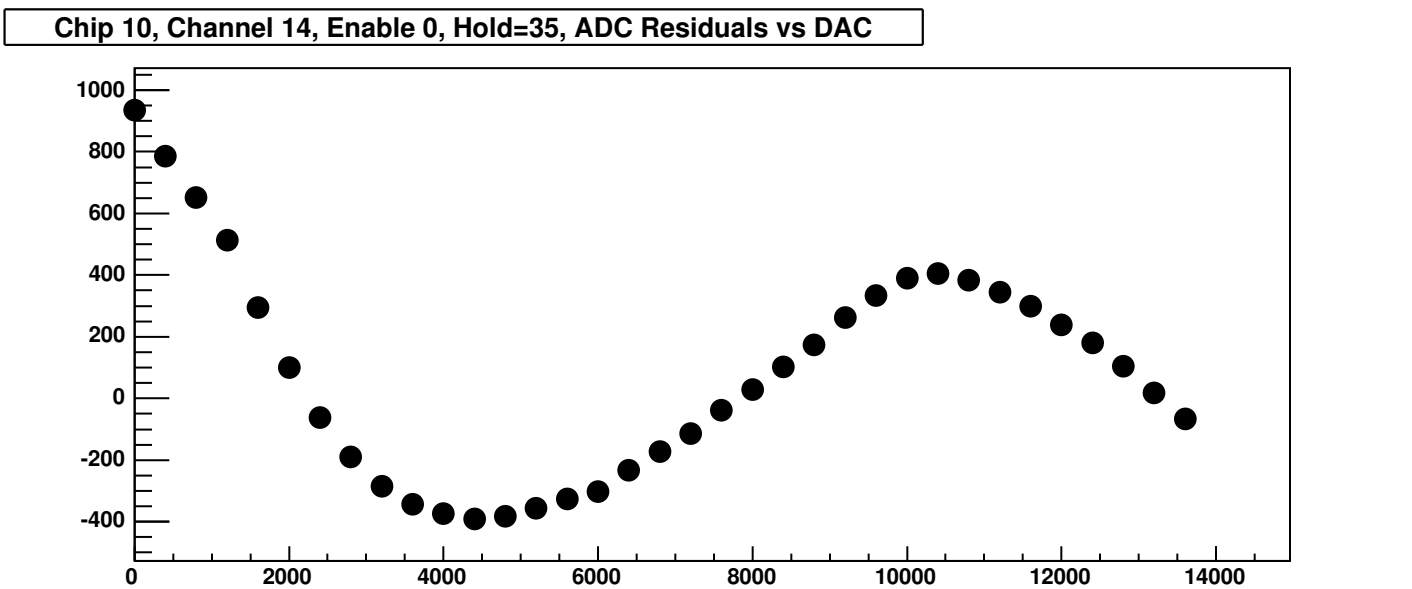


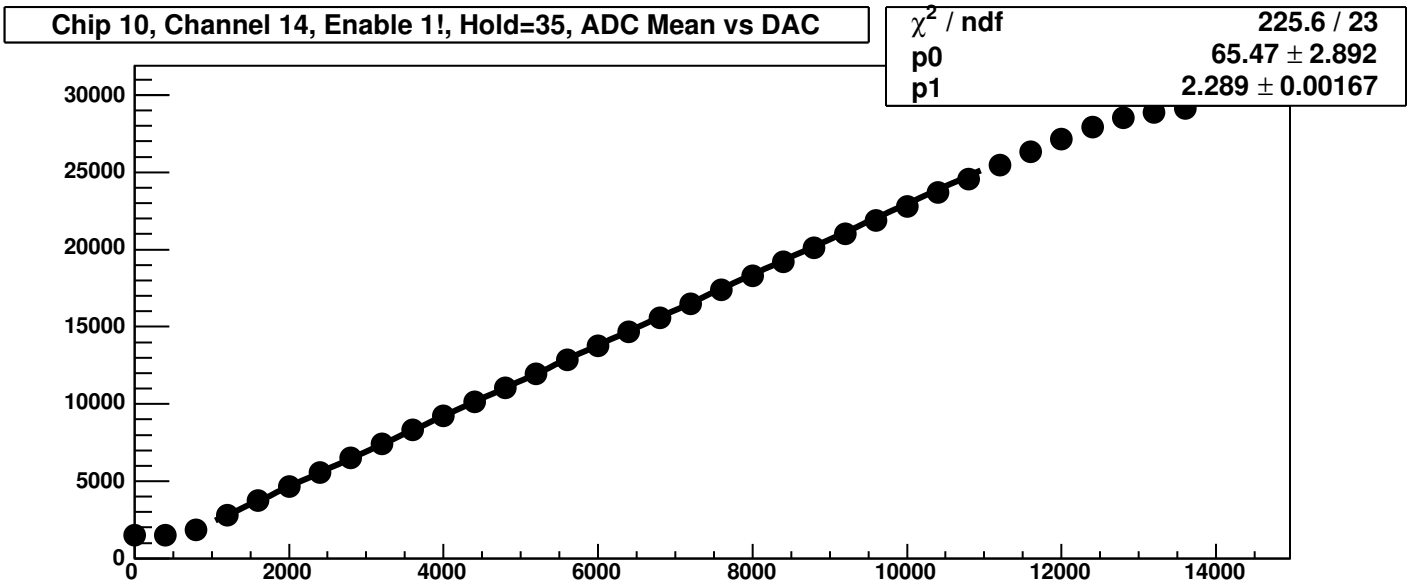
Chip 10, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

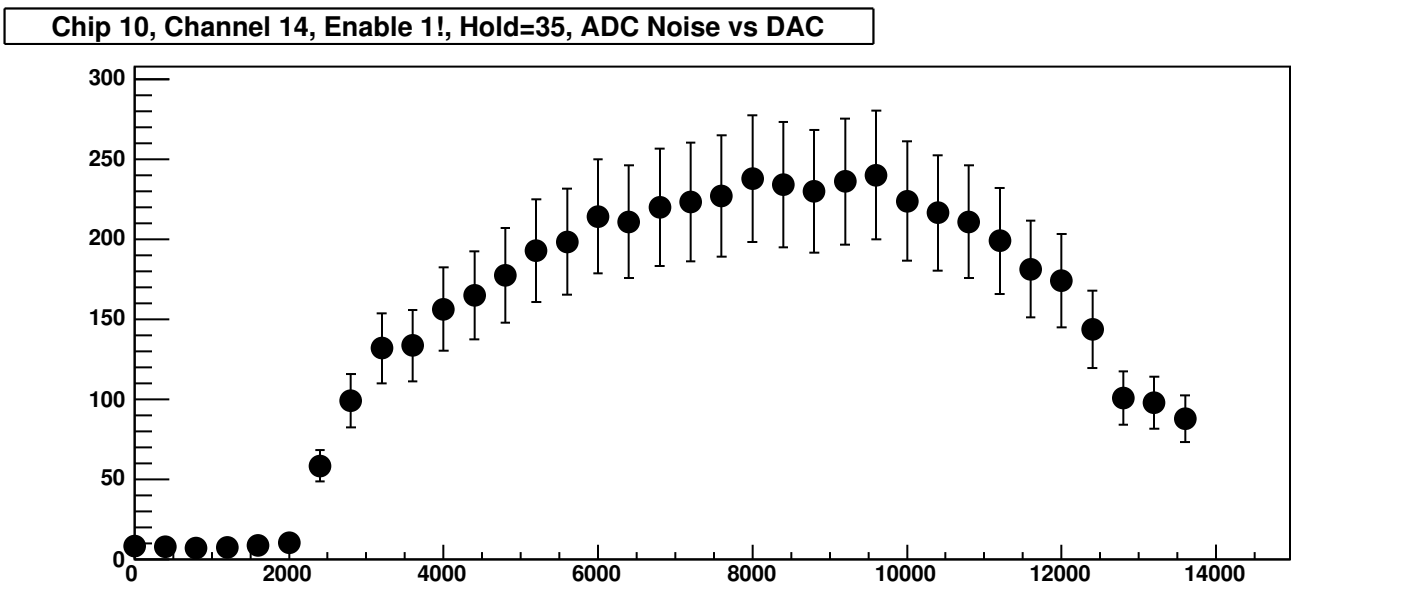


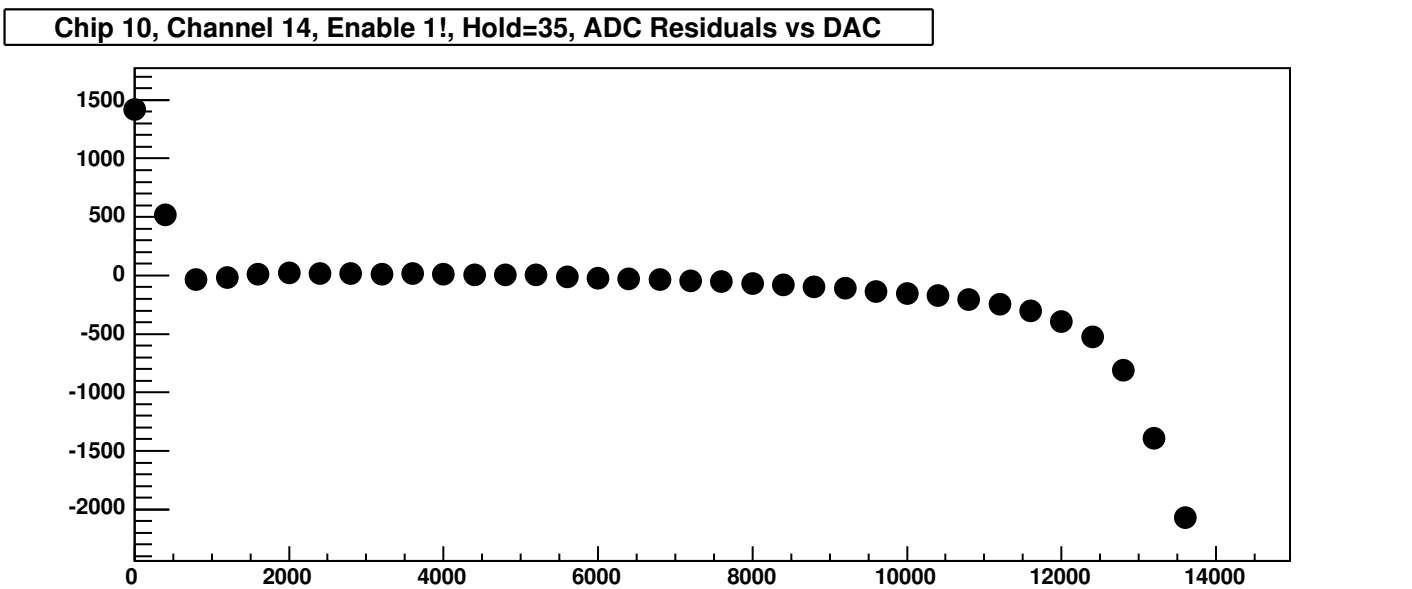

Chip 10, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.789e+05 / 23
p0 -852.9 ± 0.9742
p1 0.3822 ± 0.0002196
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

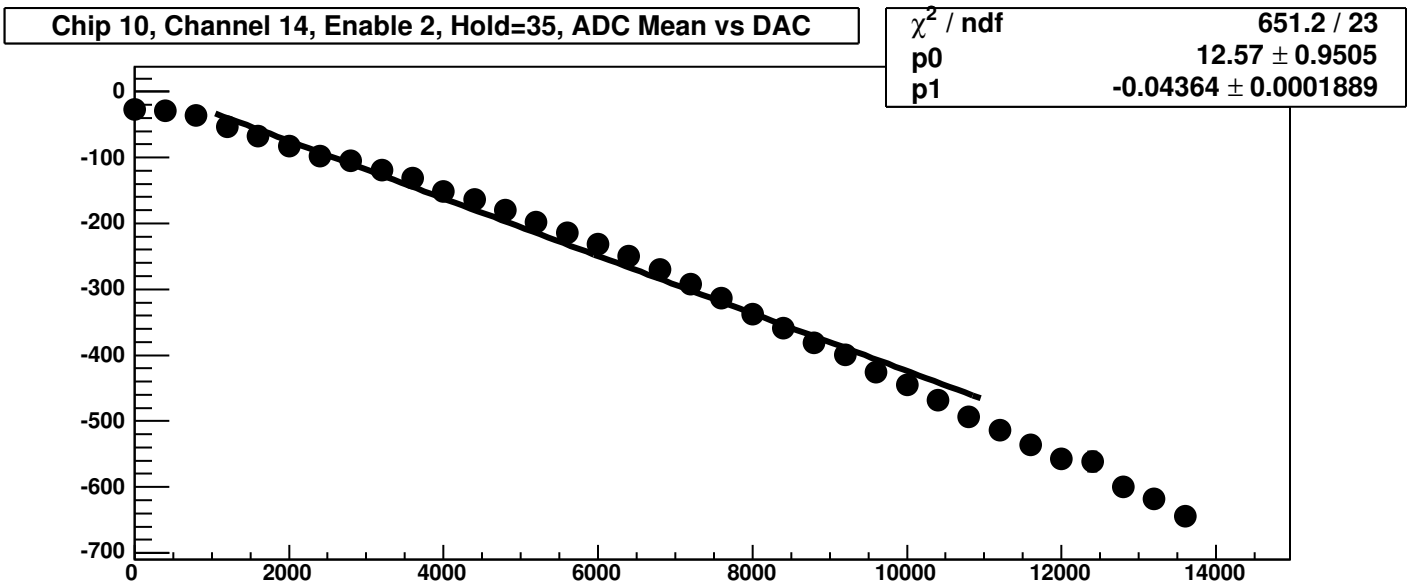

Chip 10, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

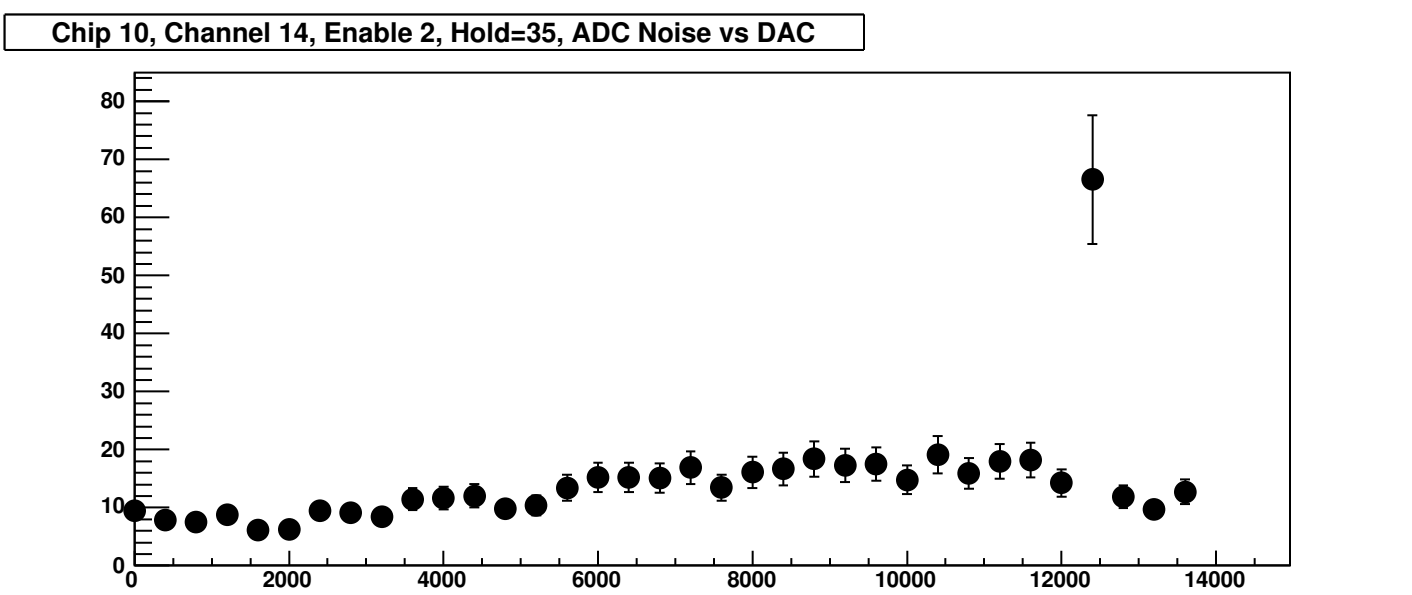

Chip 10, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

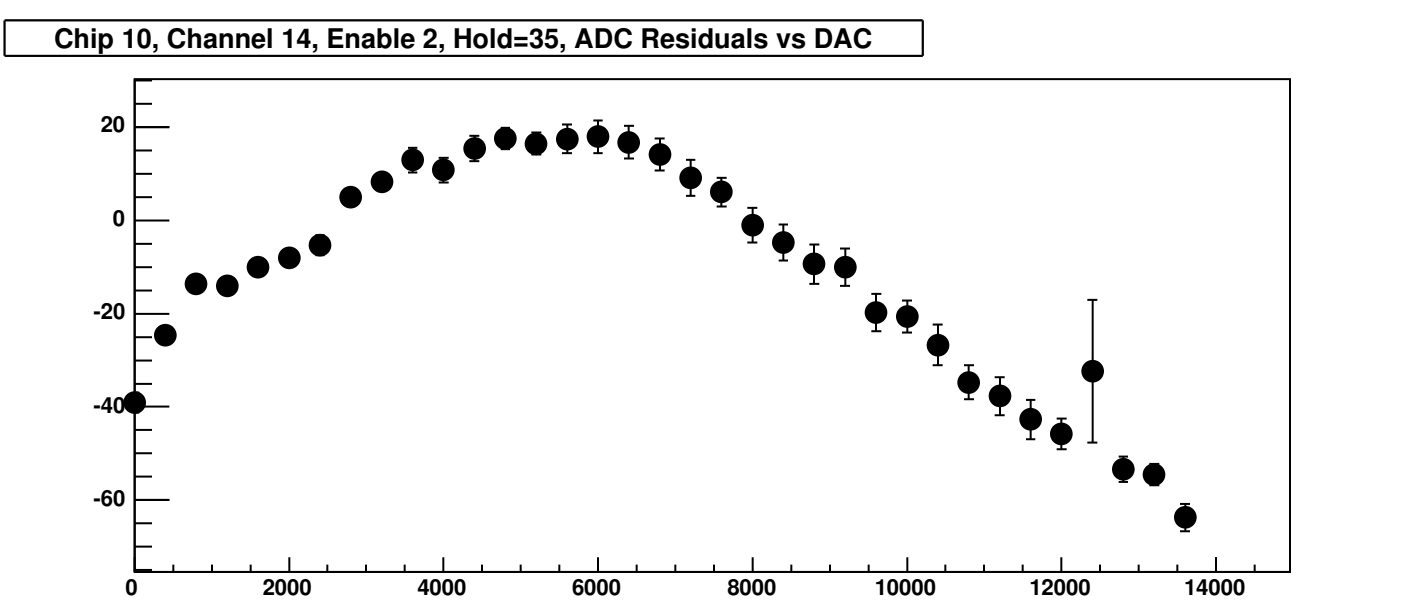

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 225.6 / 23
p0 65.47 ± 2.892
p1 2.289 ± 0.00167
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

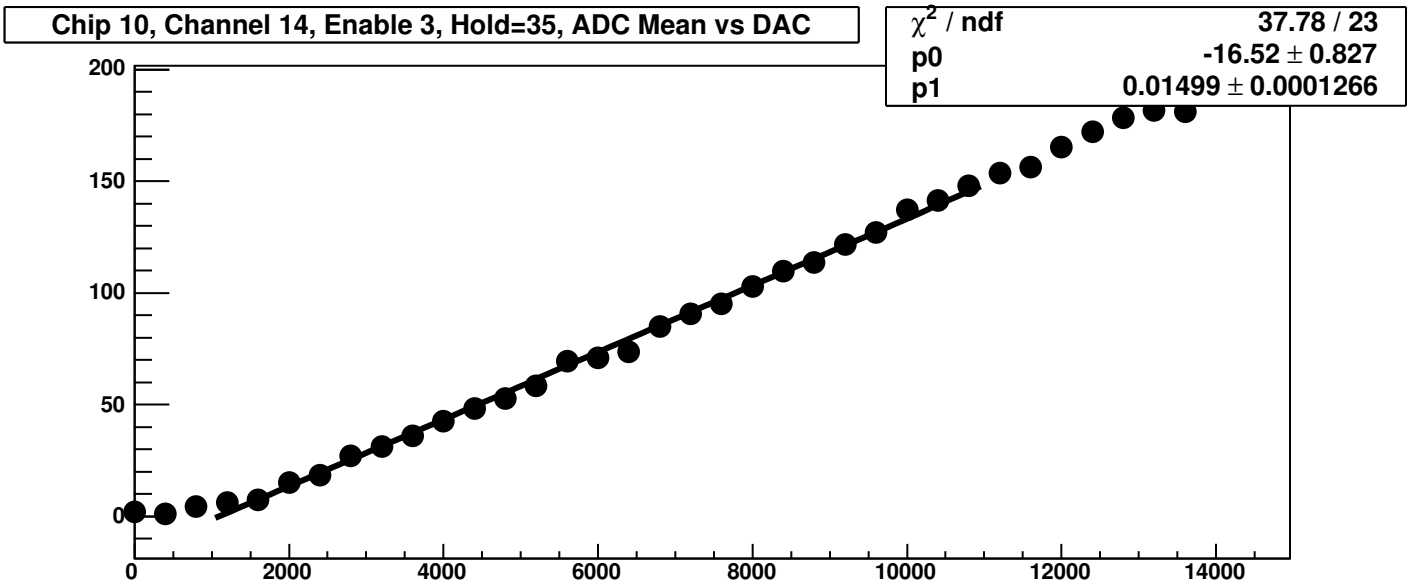

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

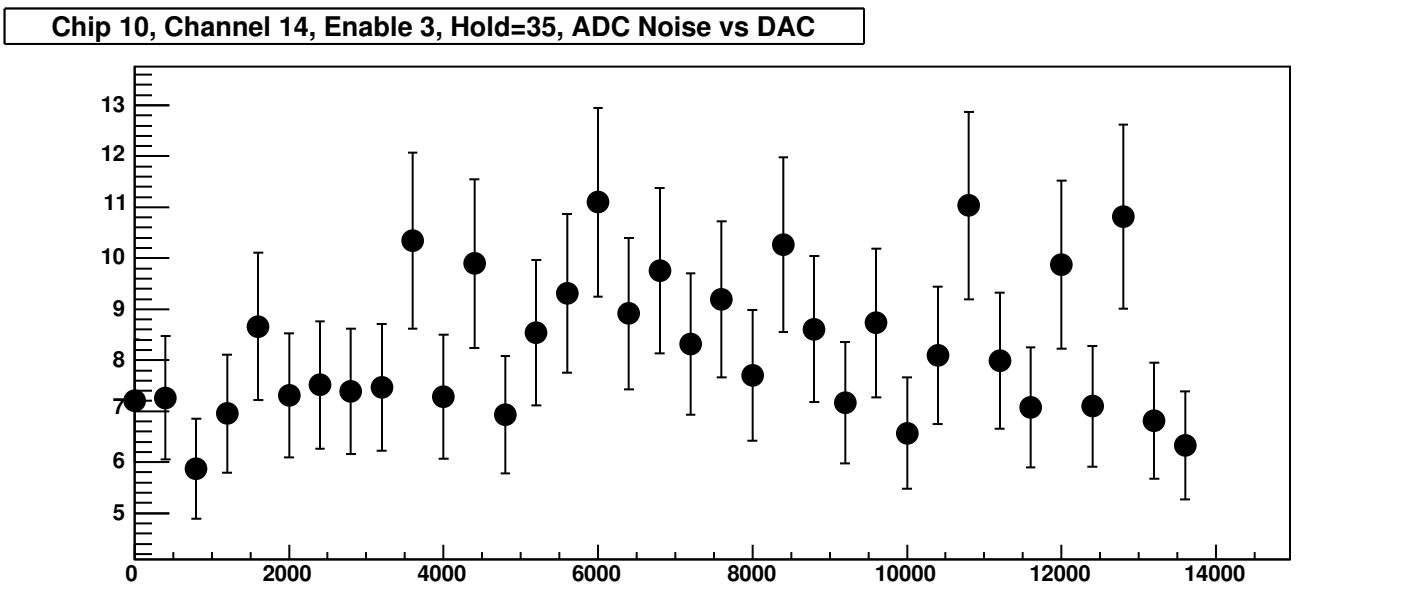

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

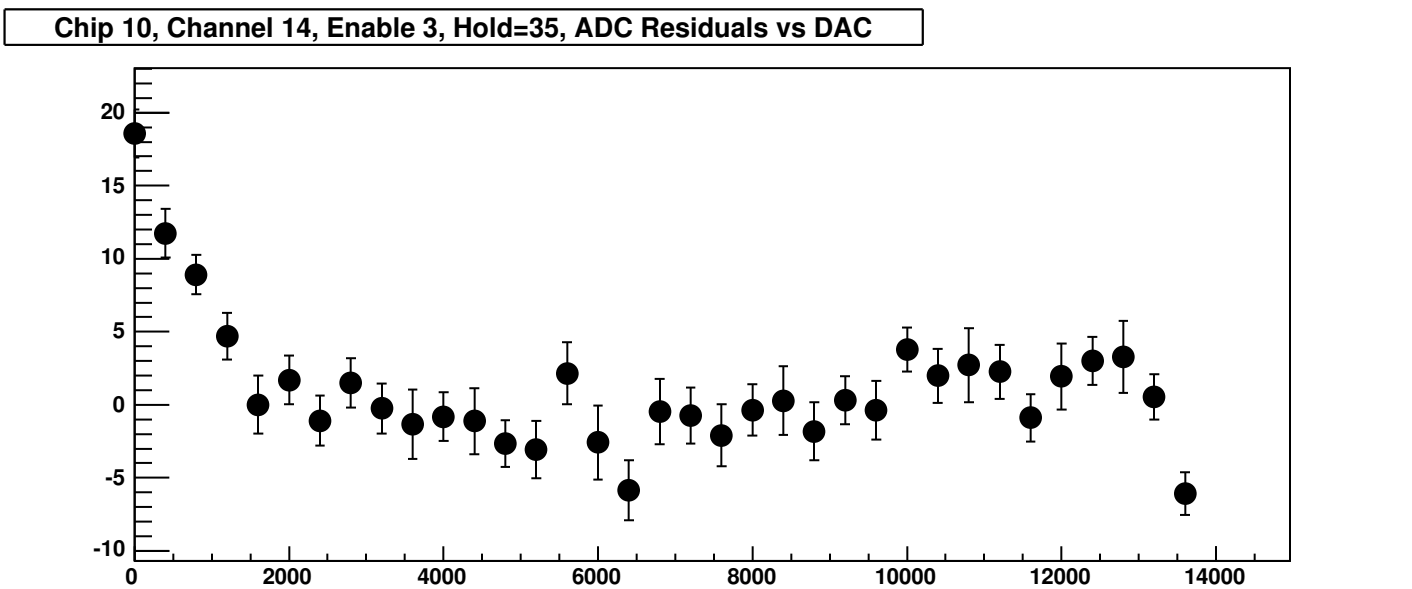

Chip 10, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 651.2 / 23
p0 12.57 ± 0.9505
p1 -0.04364 ± 0.0001889
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

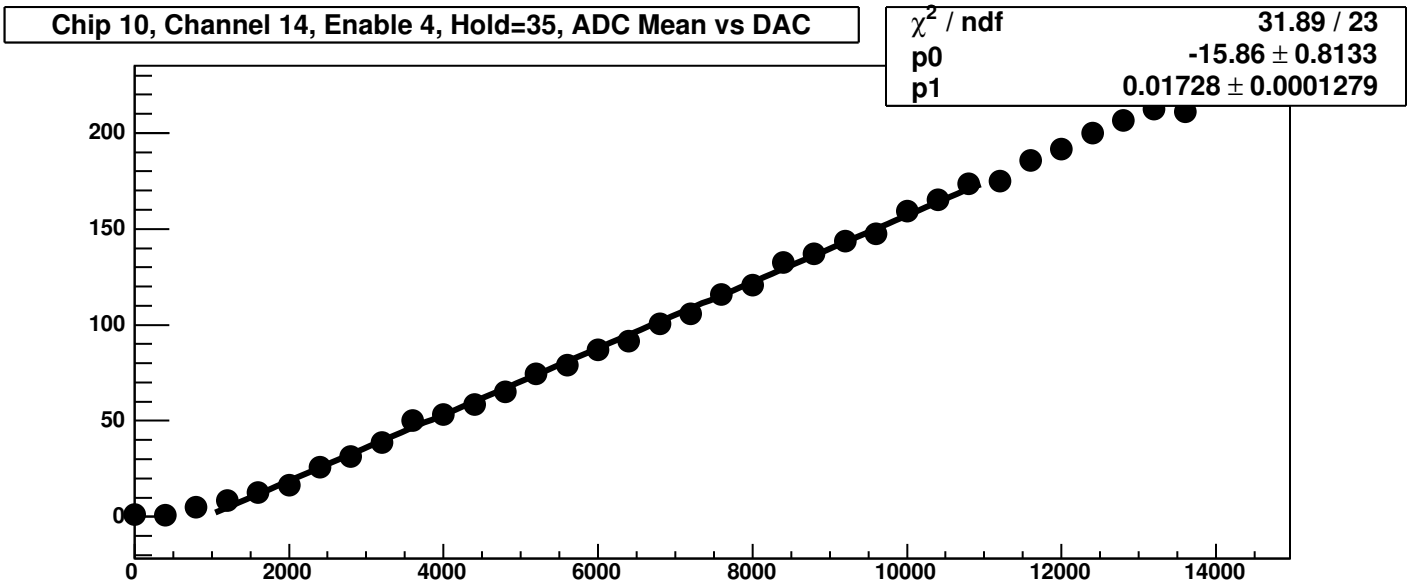

Chip 10, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

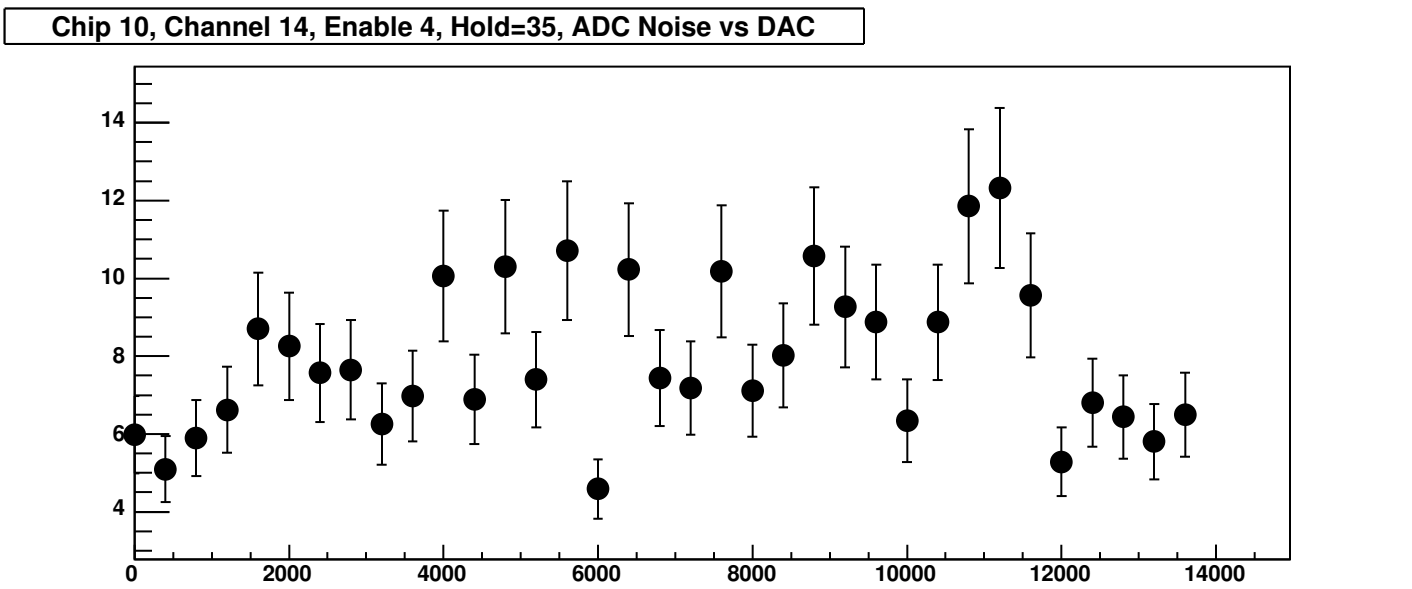

Chip 10, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

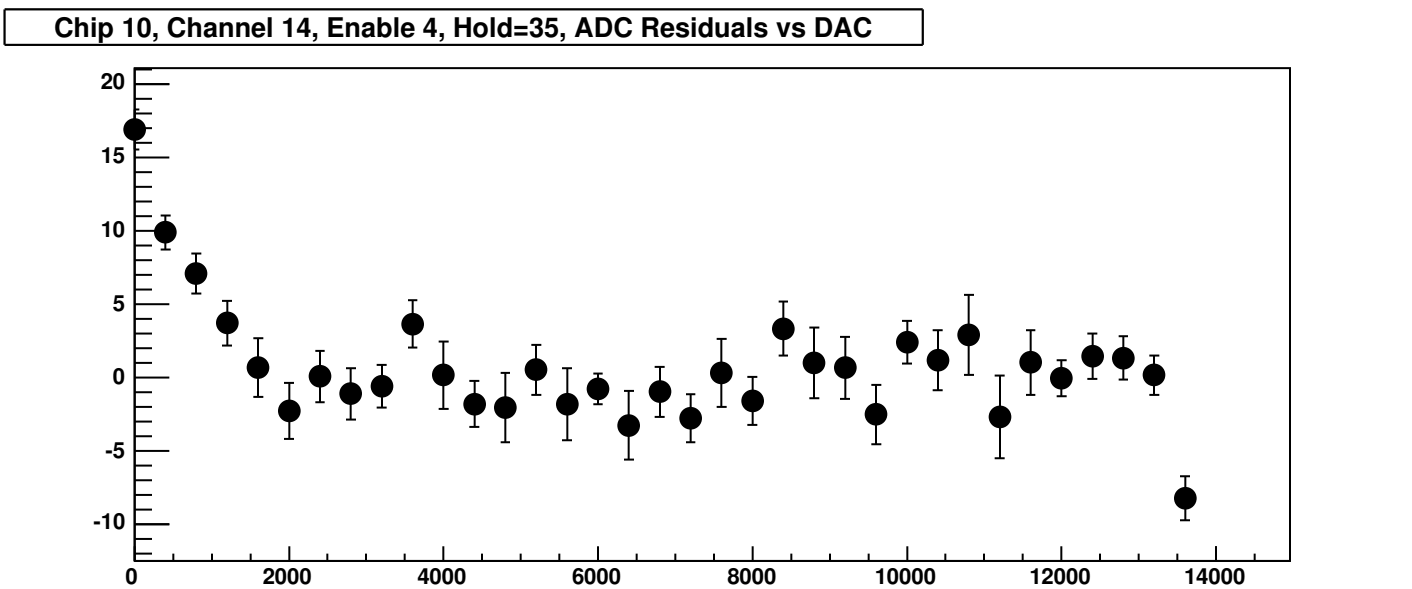

Chip 10, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.78 / 23
p0 -16.52 ± 0.827
p1 0.01499 ± 0.0001266
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

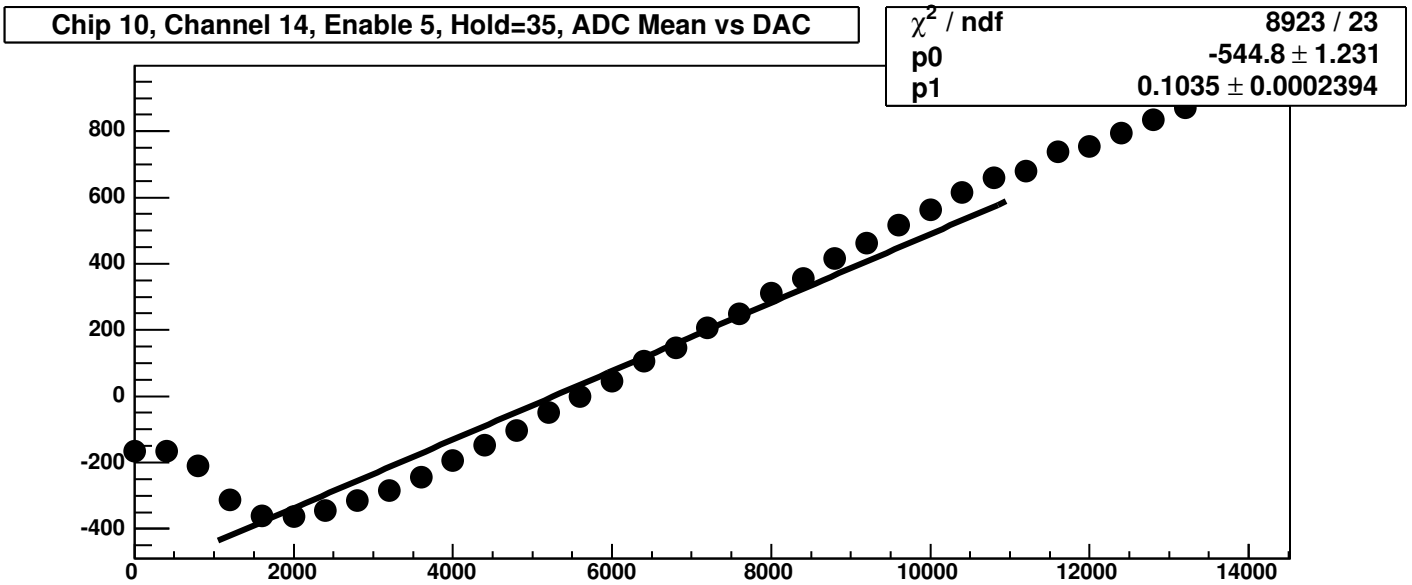

Chip 10, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

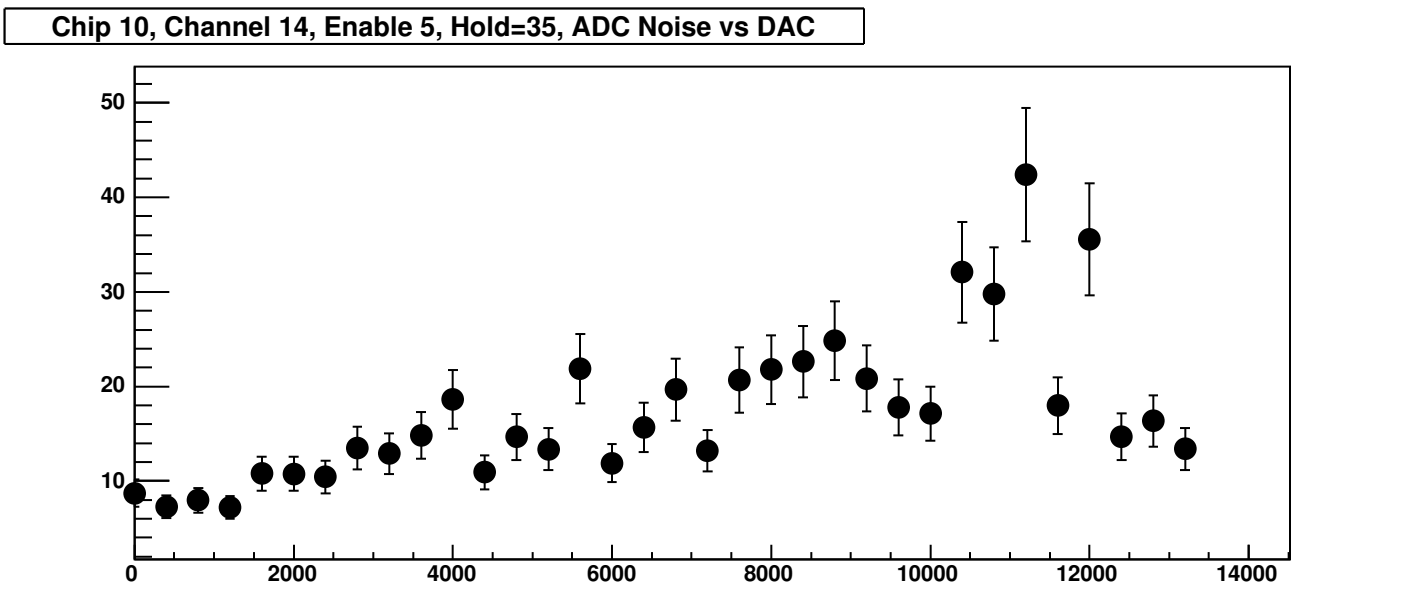

Chip 10, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

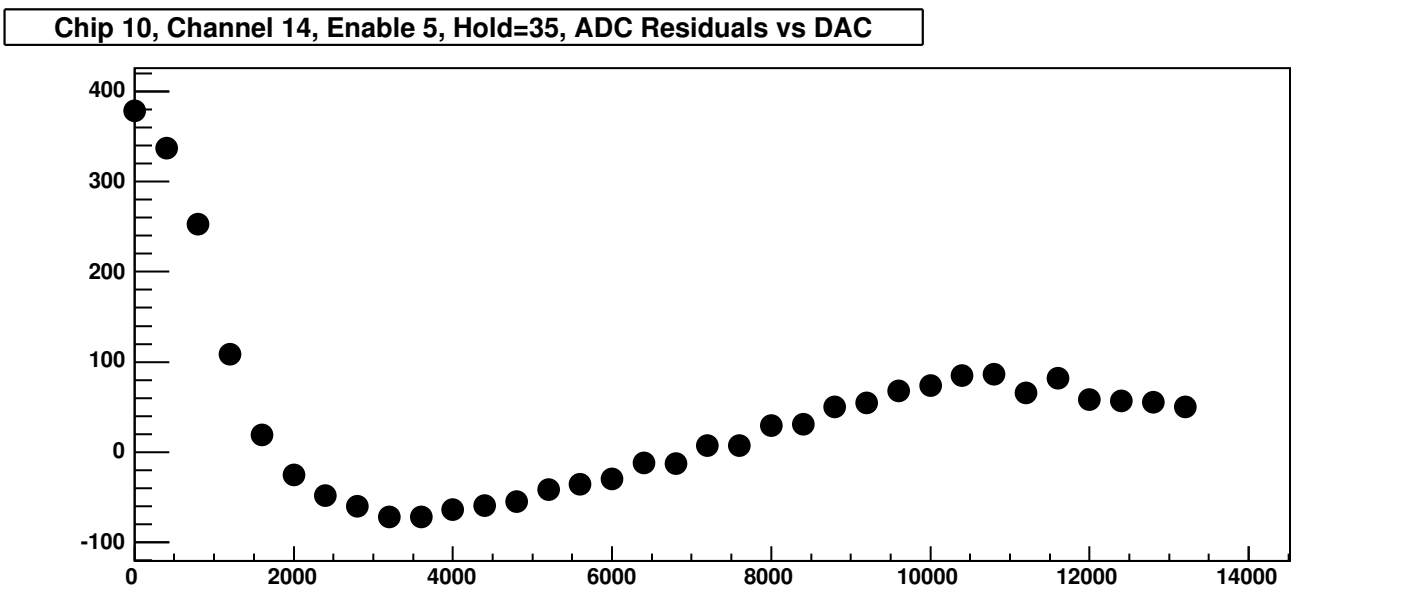

Chip 10, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.89 / 23
p0 -15.86 ± 0.8133
p1 0.01728 ± 0.0001279
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

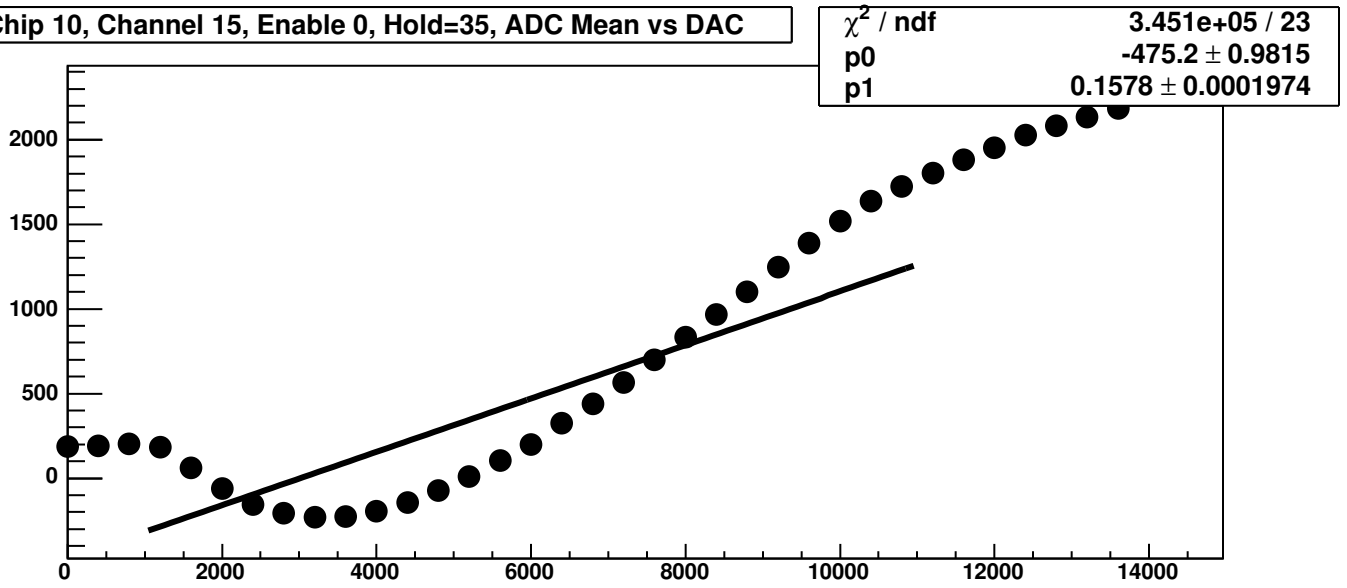

Chip 10, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8923 / 23
p0 -544.8 ± 1.231
p1 0.1035 ± 0.0002394
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

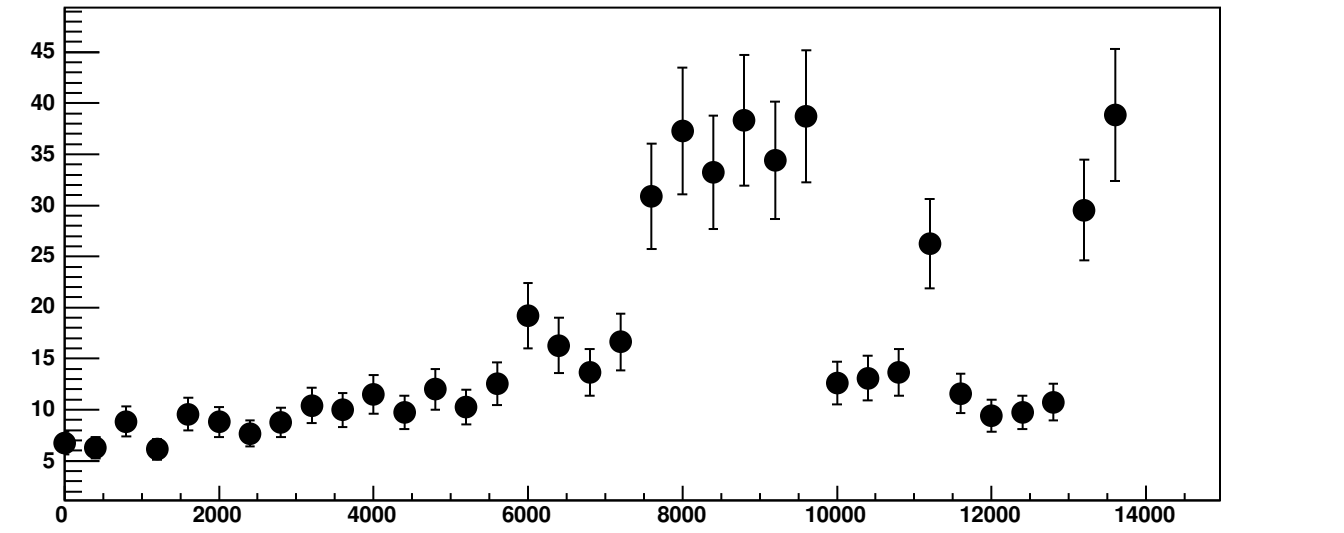

Chip 10, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

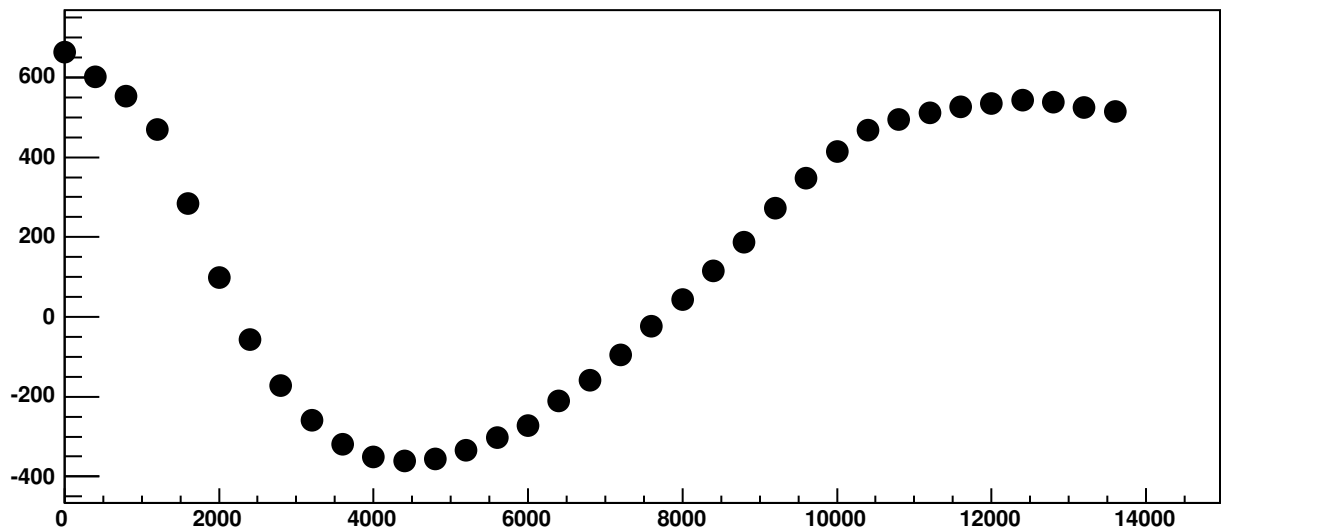
Chip 10, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC



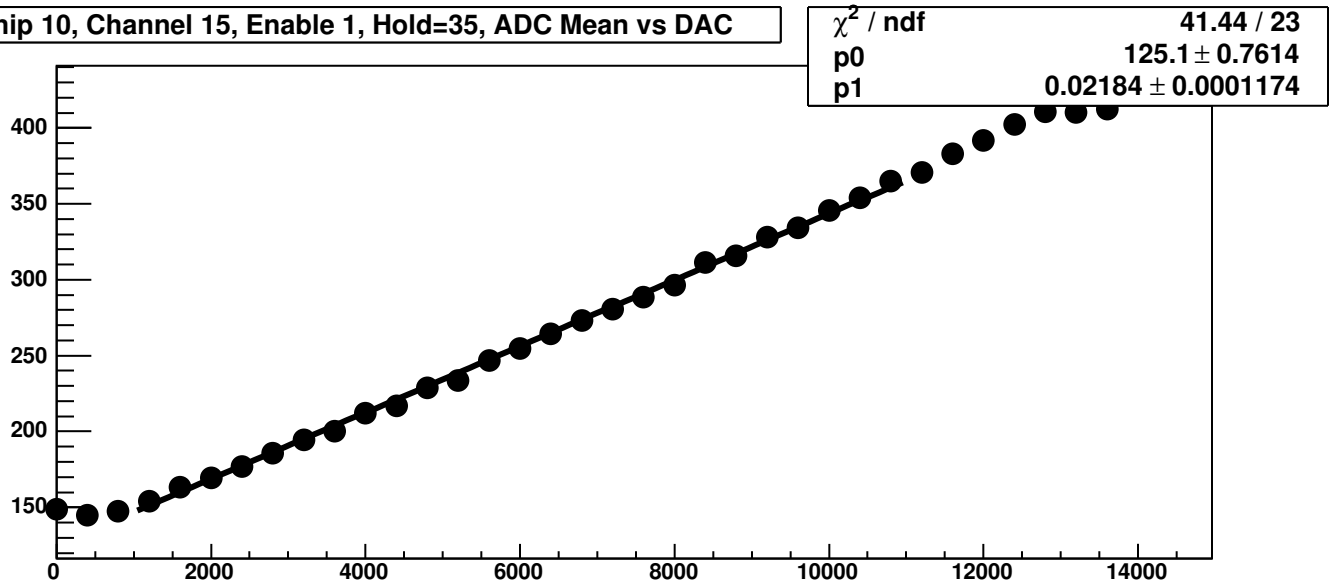
Chip 10, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC



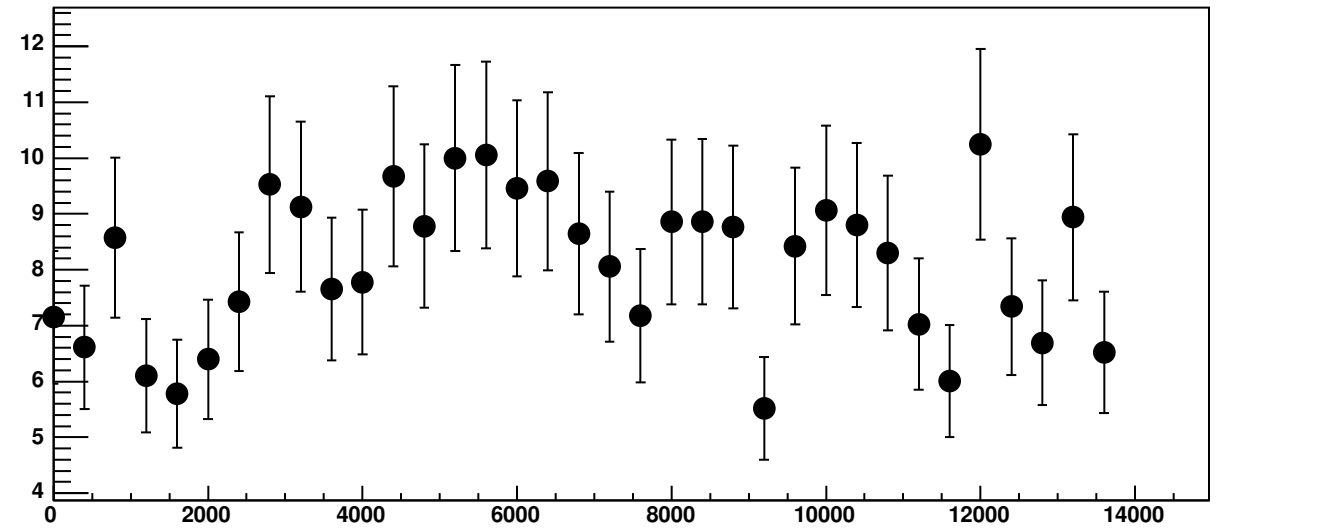
Chip 10, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC



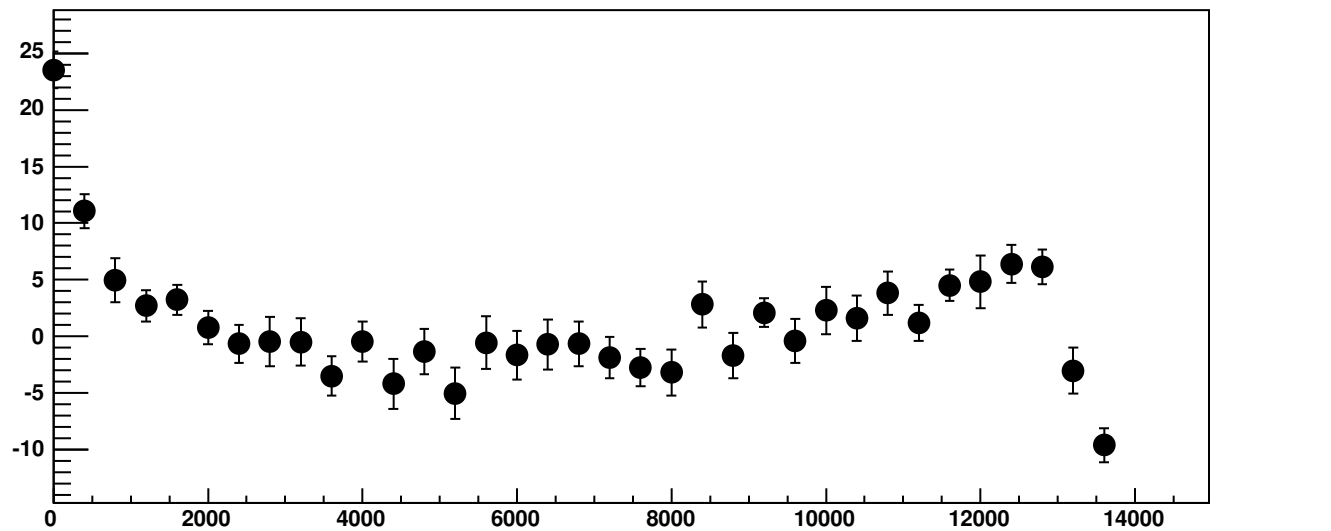
Chip 10, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

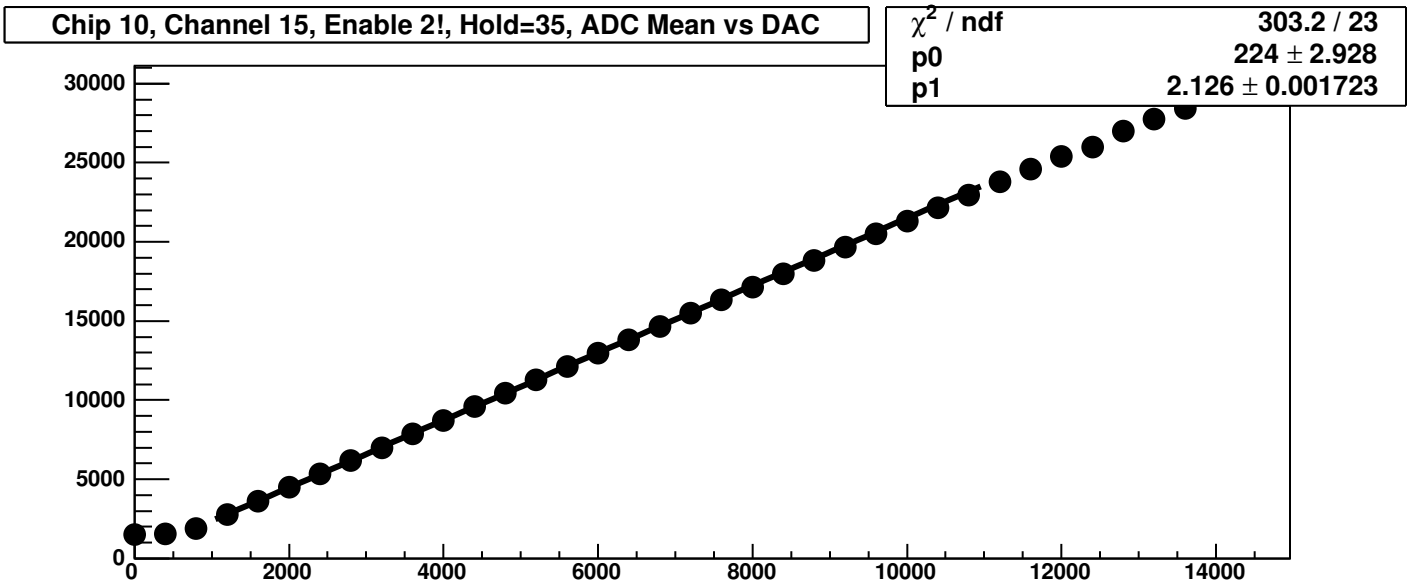


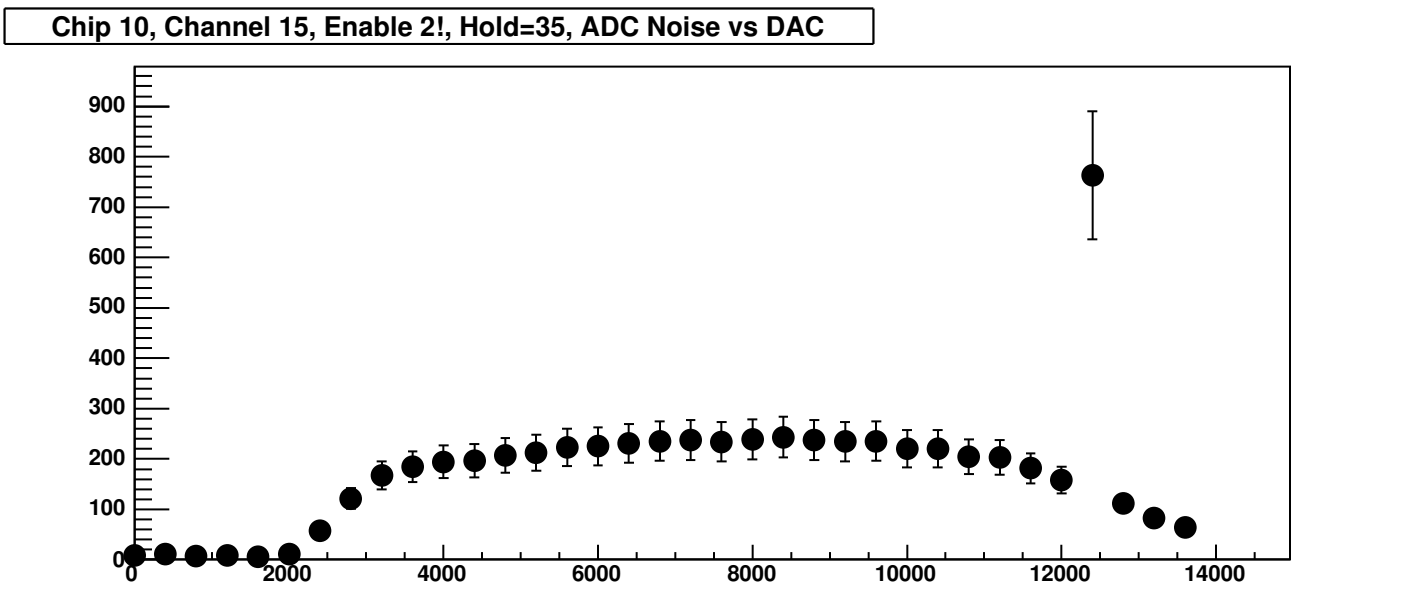
Chip 10, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

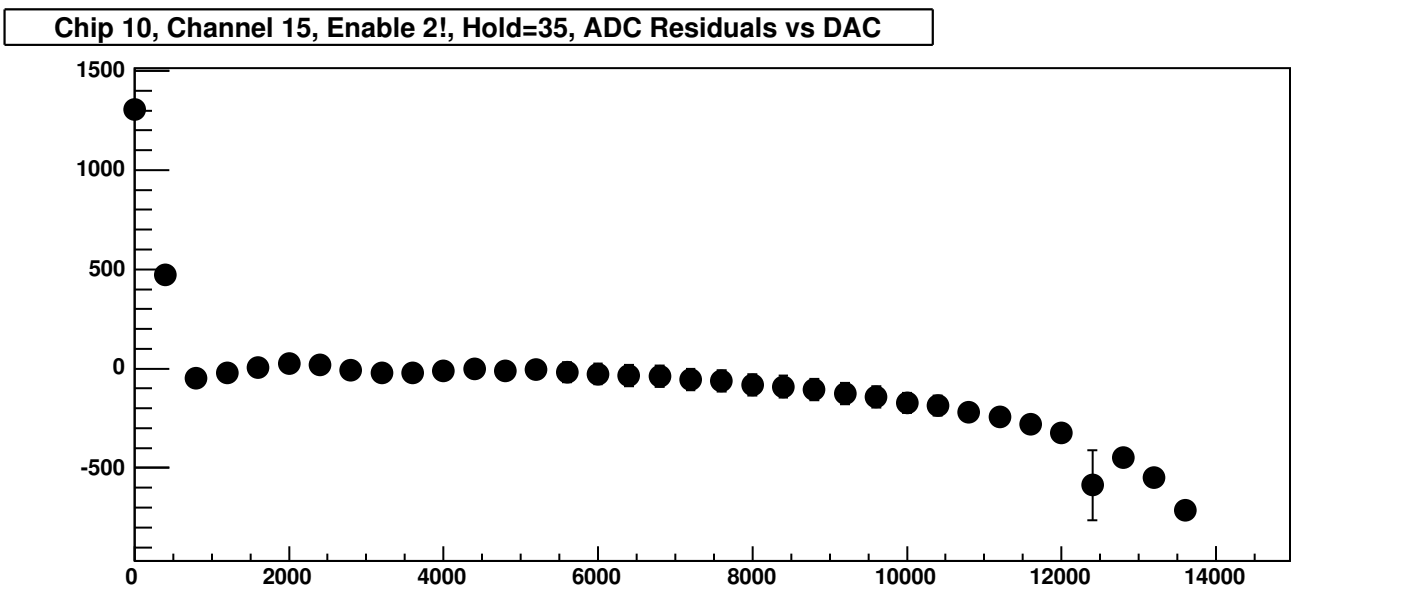


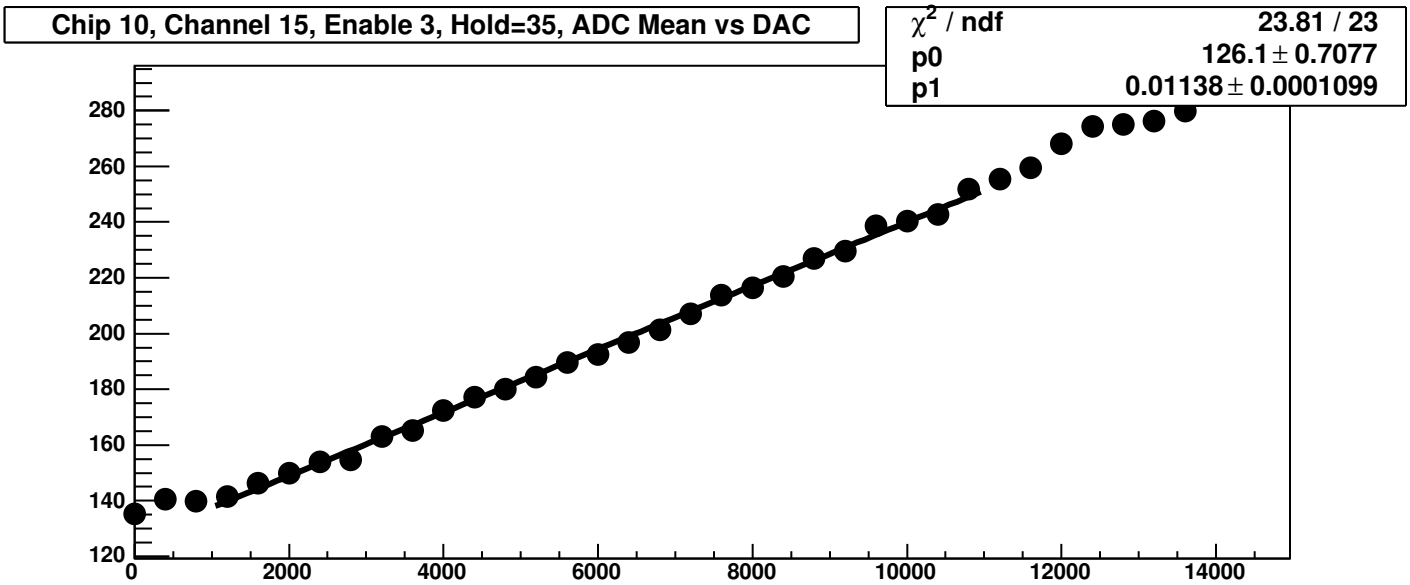
Chip 10, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

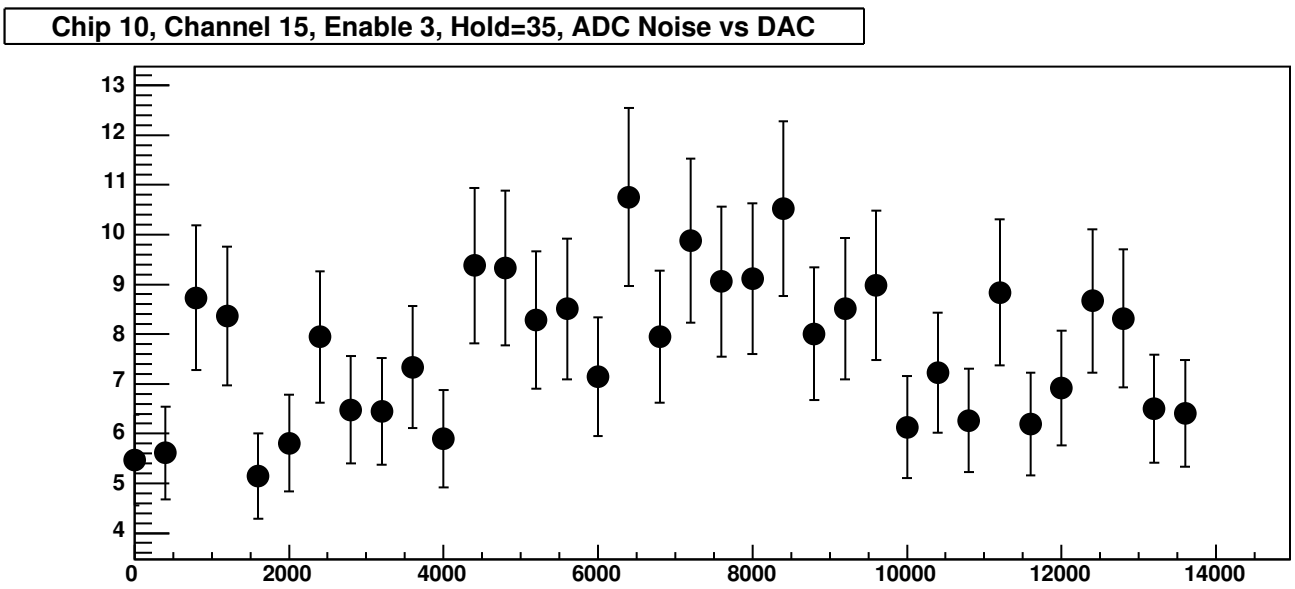


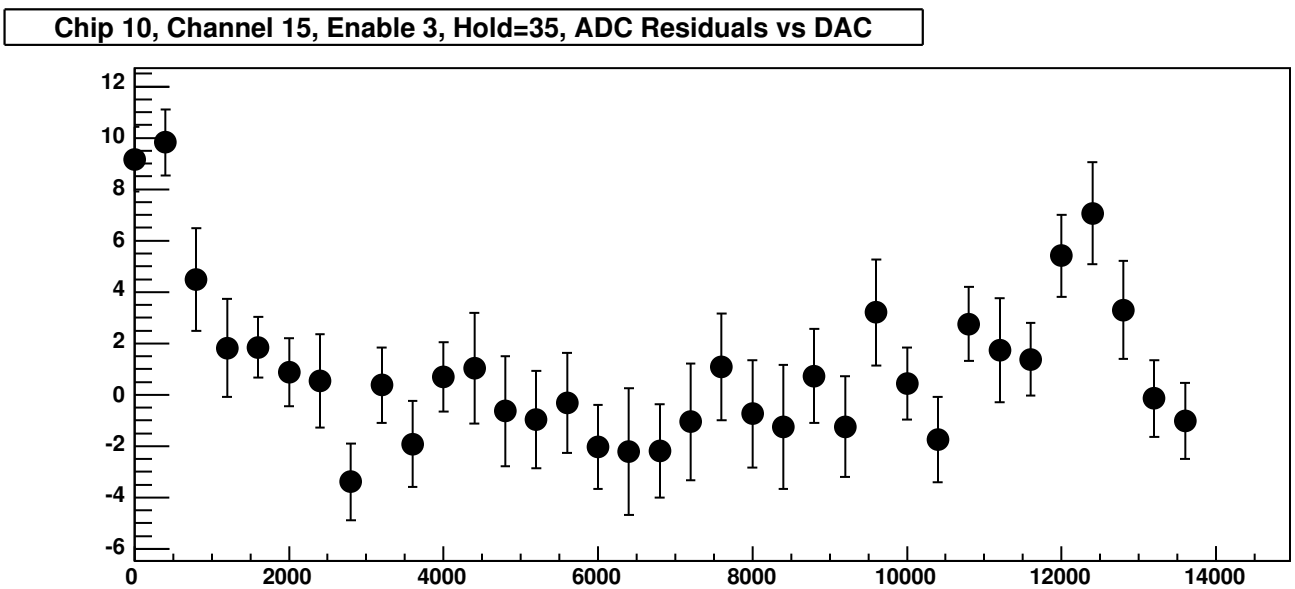

Chip 10, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 303.2 / 23
p0 224 ± 2.928
p1 2.126 ± 0.001723
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

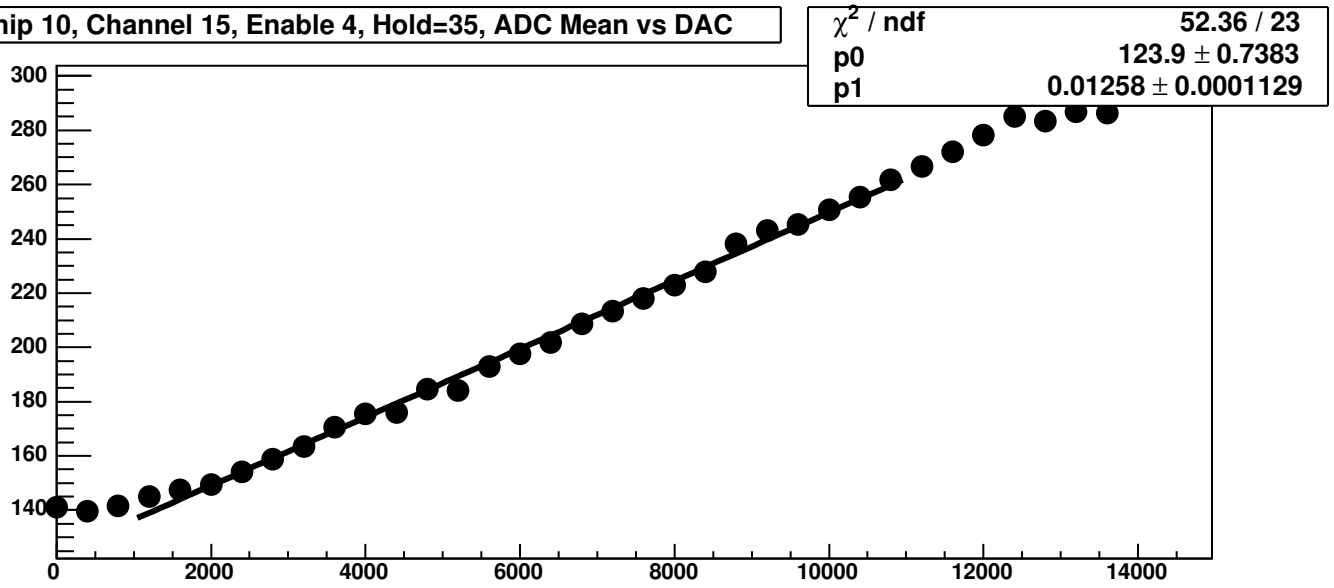

Chip 10, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.81 / 23
p0 126.1 ± 0.7077
p1 0.01138 ± 0.0001099
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

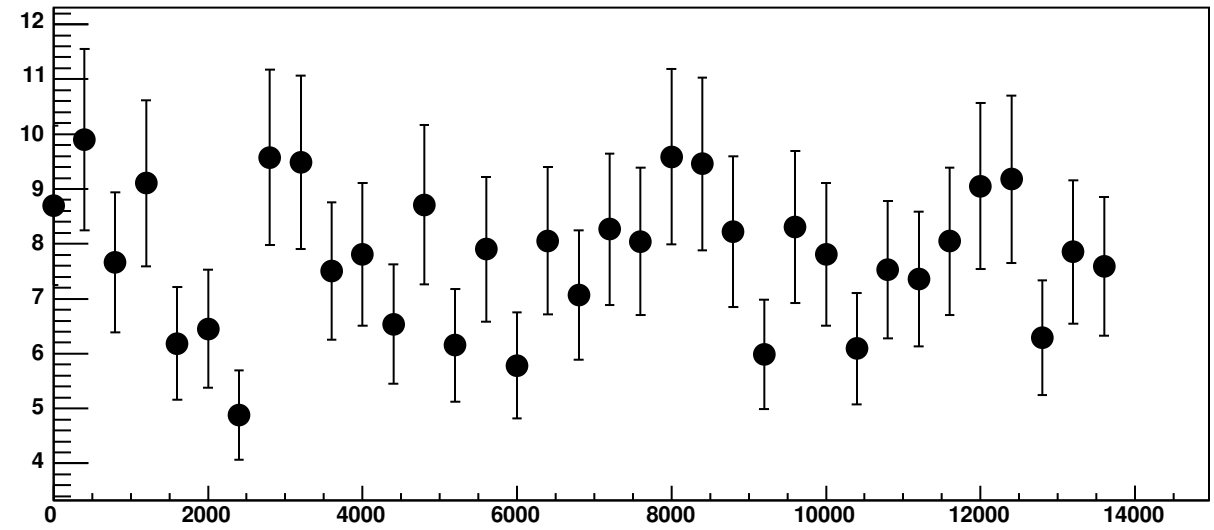

Chip 10, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

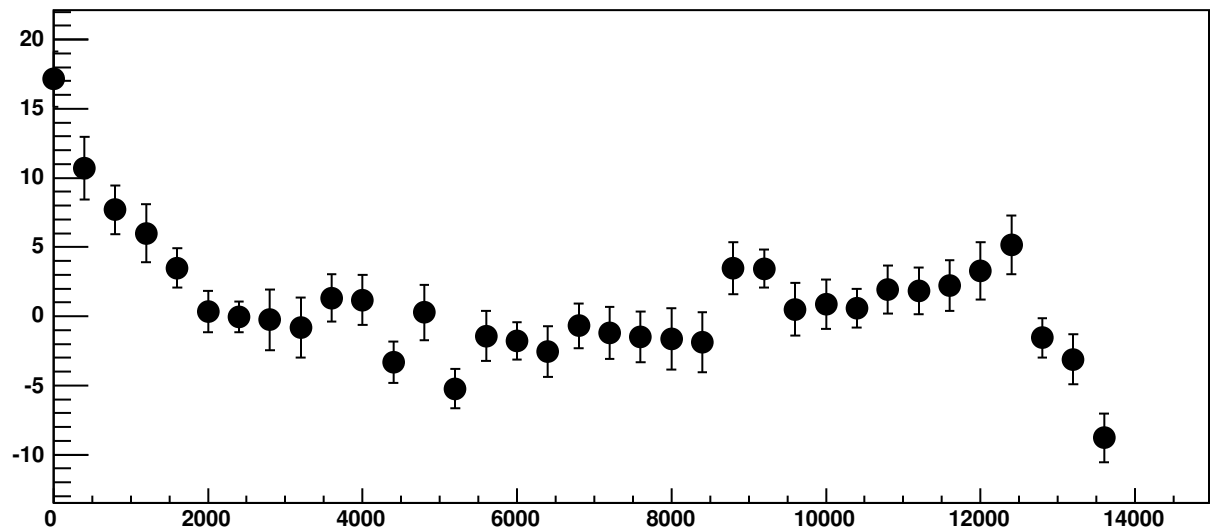
Chip 10, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

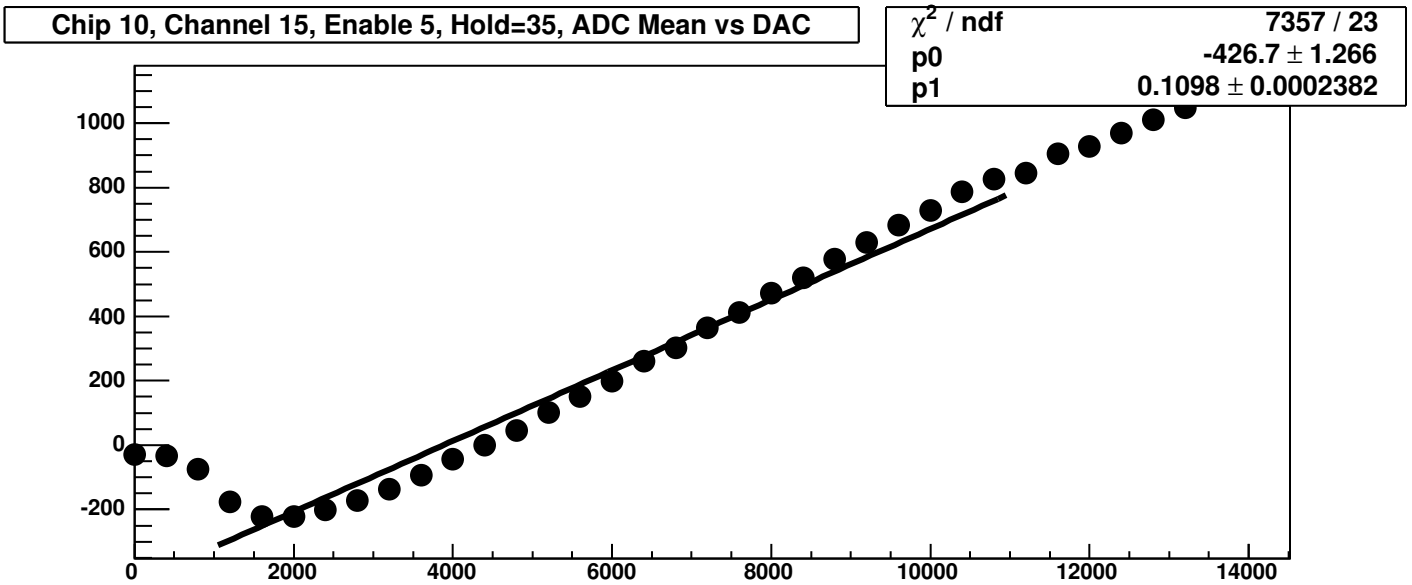


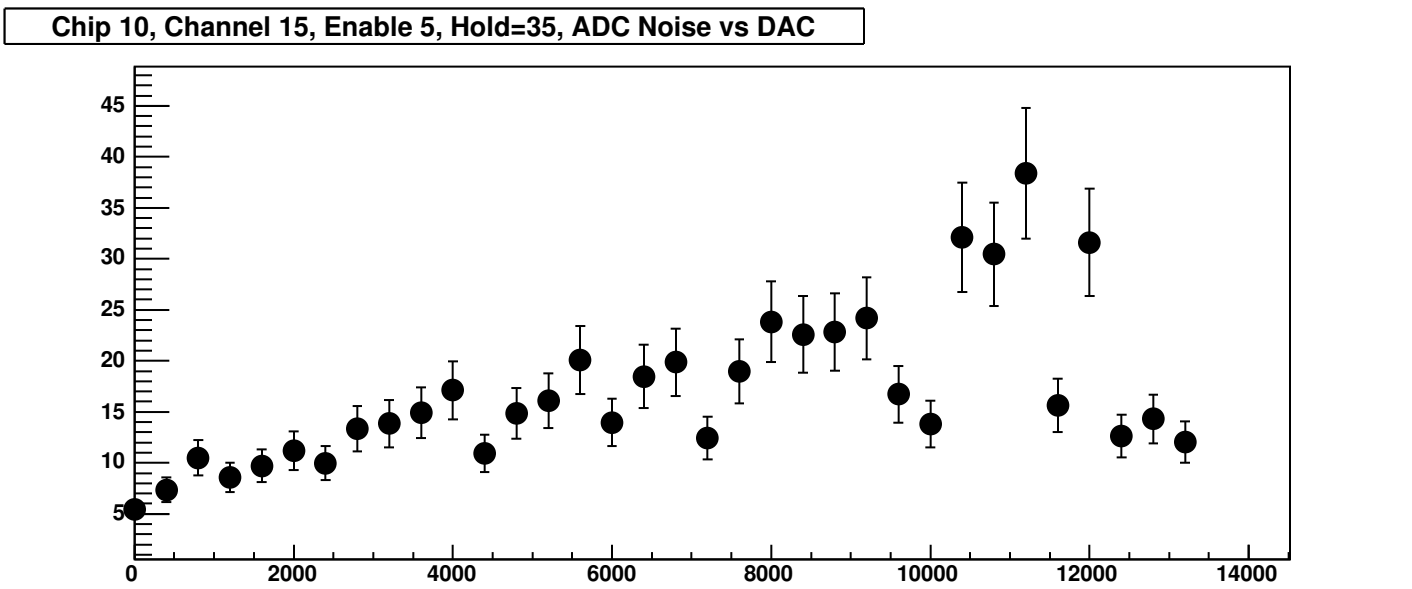
Chip 10, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

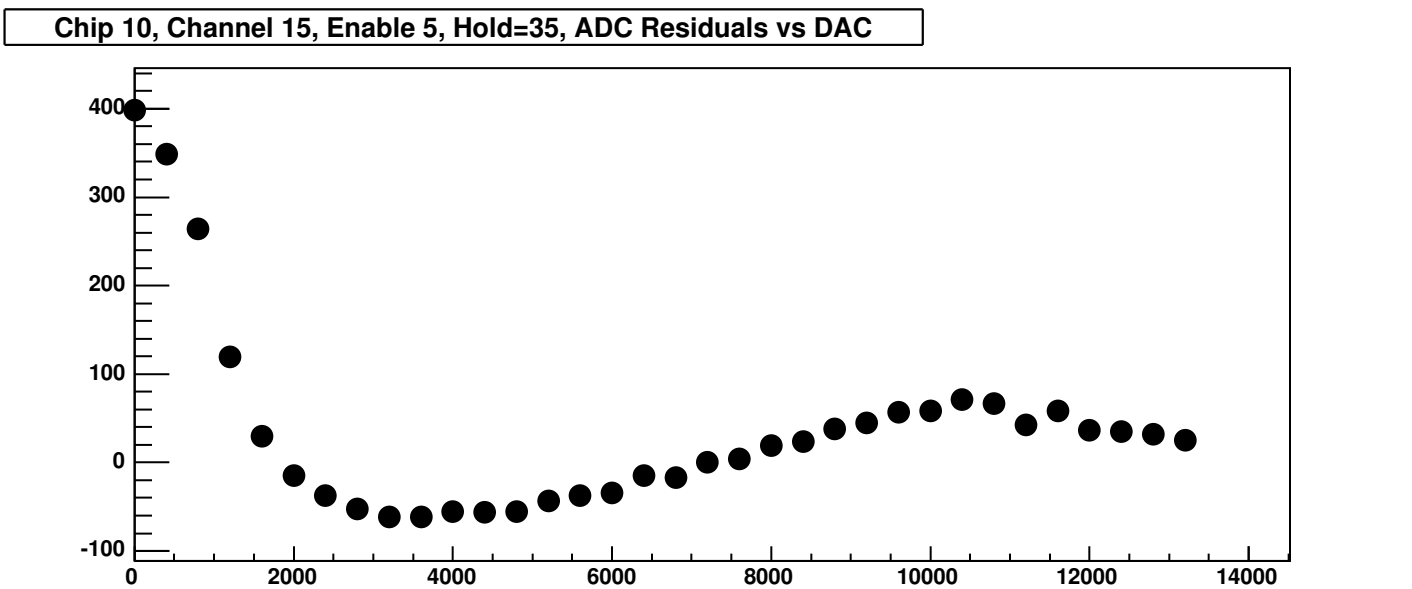


Chip 10, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

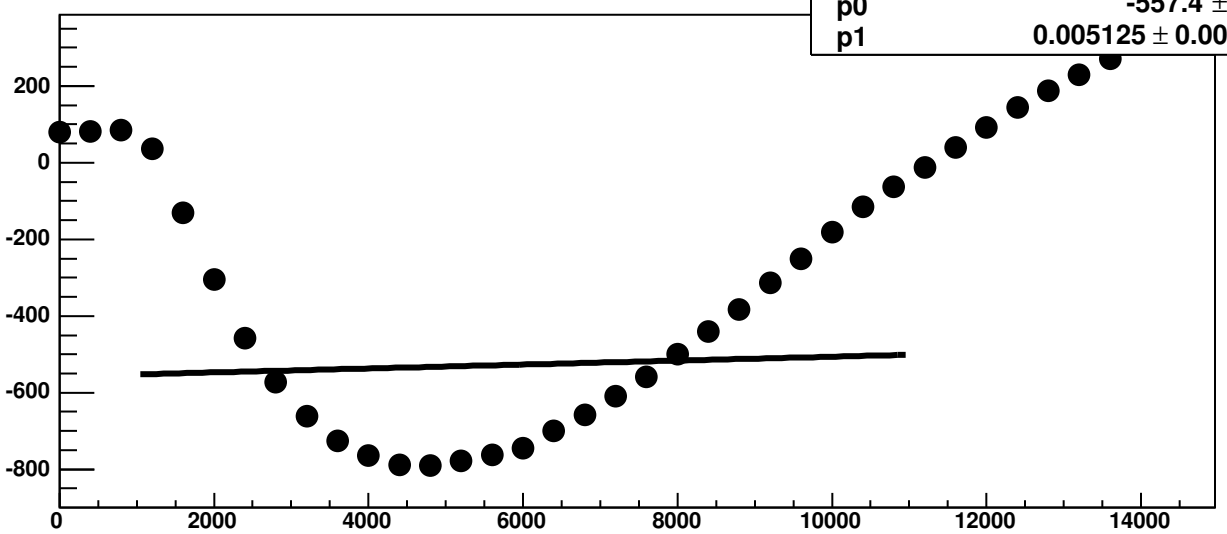



Chip 10, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7357 / 23
p0 -426.7 ± 1.266
p1 0.1098 ± 0.0002382
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

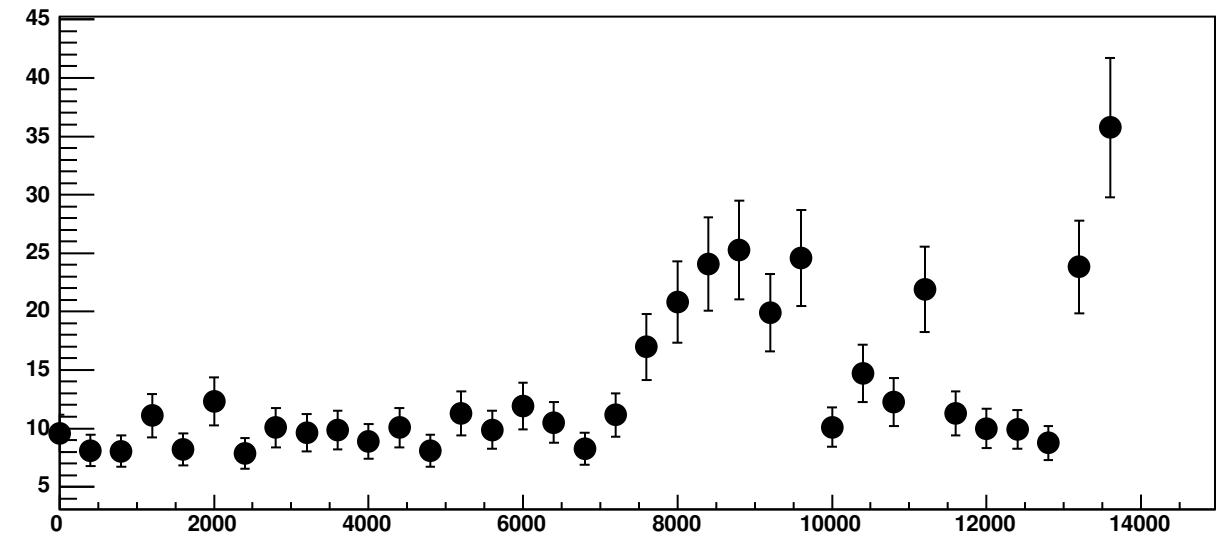

Chip 10, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

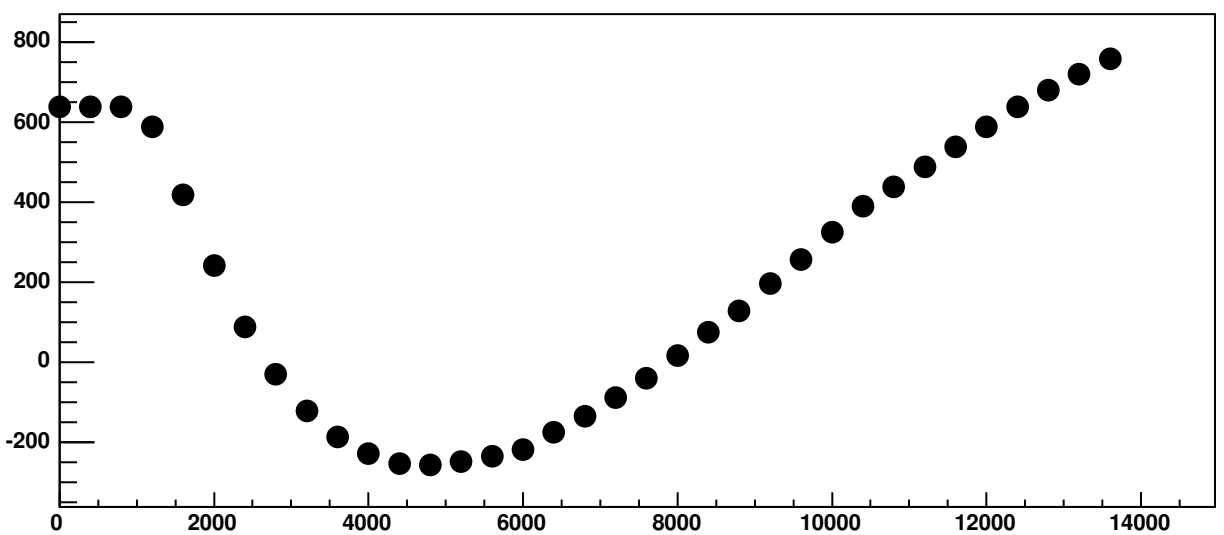
Chip 10, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

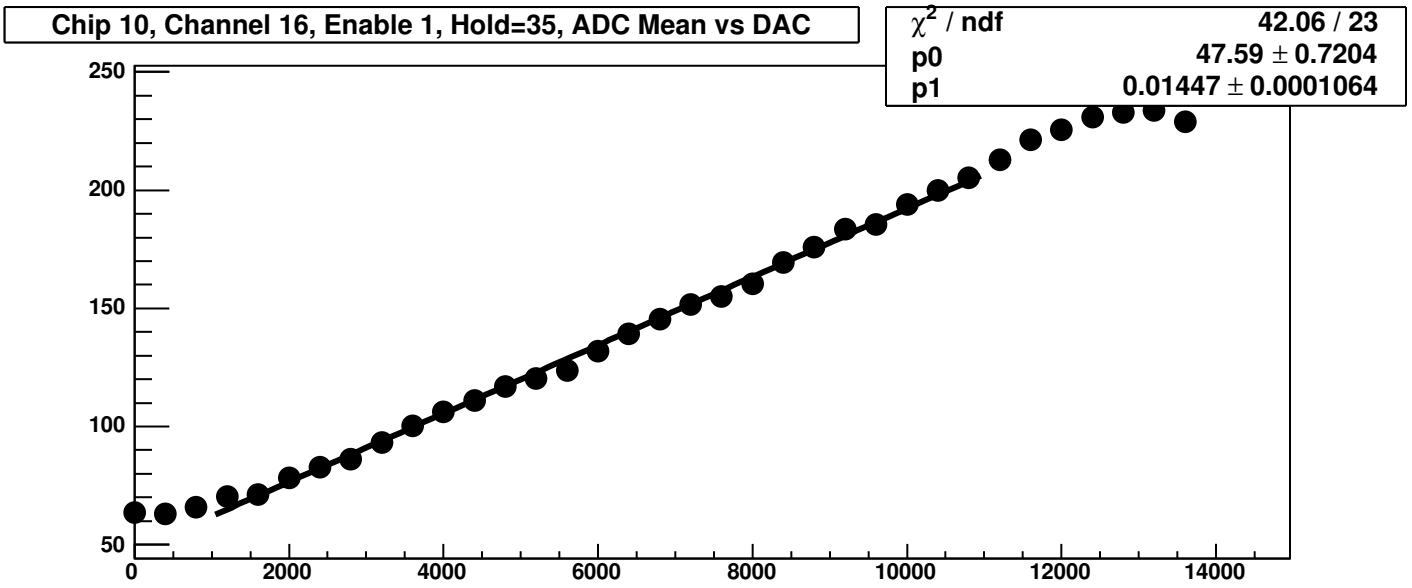


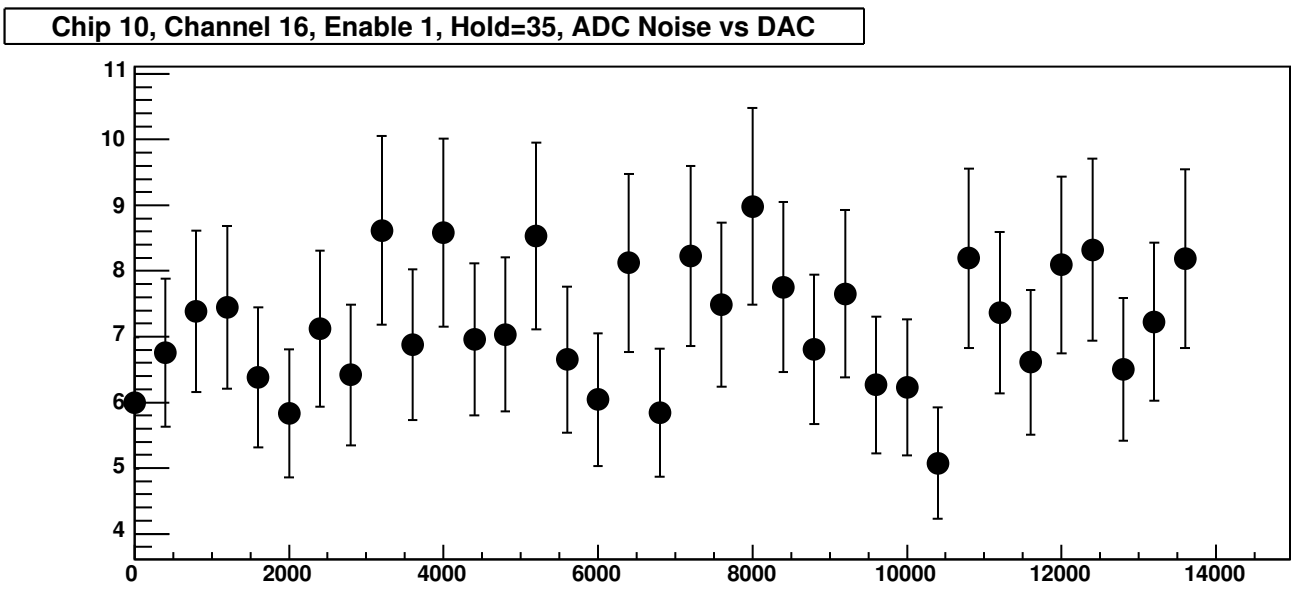
Chip 10, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

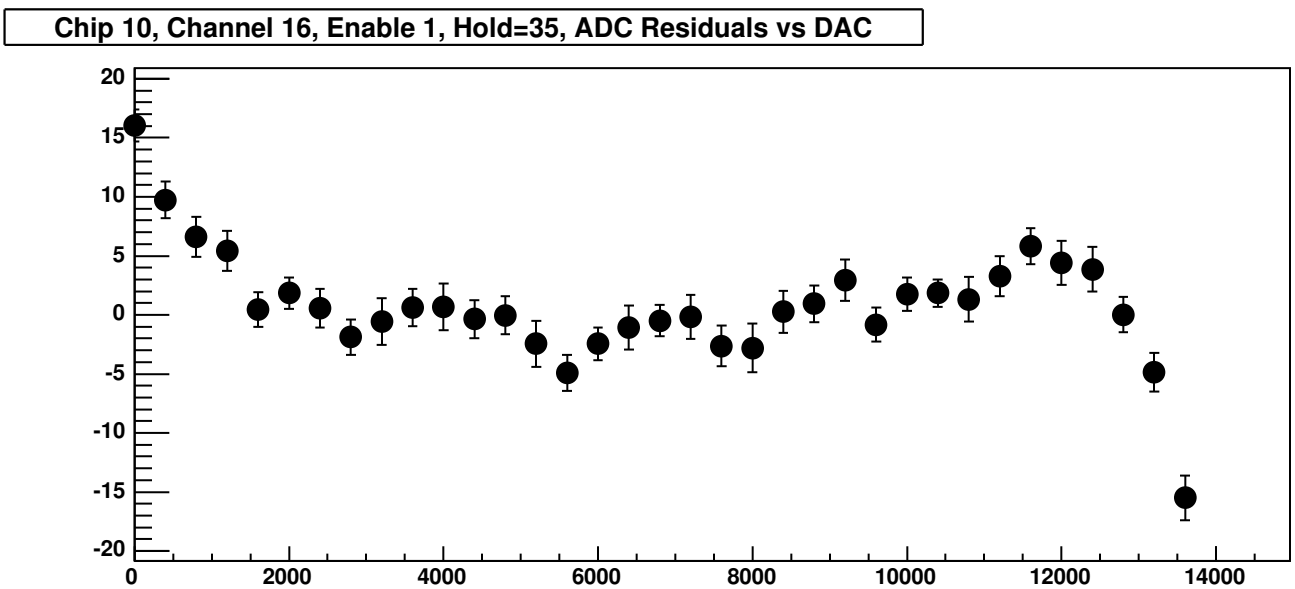


Chip 10, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



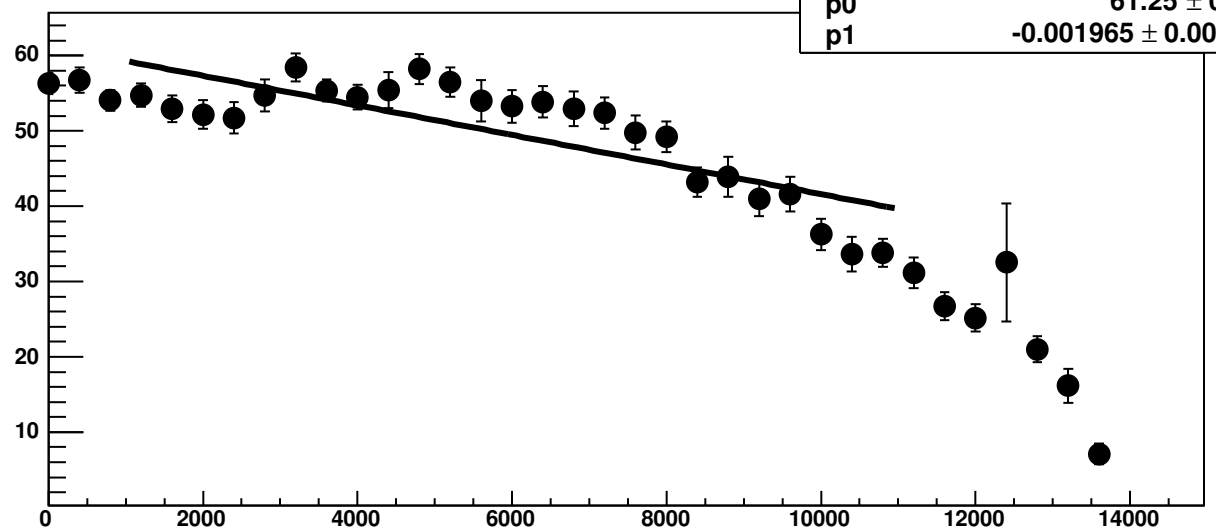

Chip 10, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.06 / 23
p0 47.59 ± 0.7204
p1 0.01447 ± 0.0001064
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

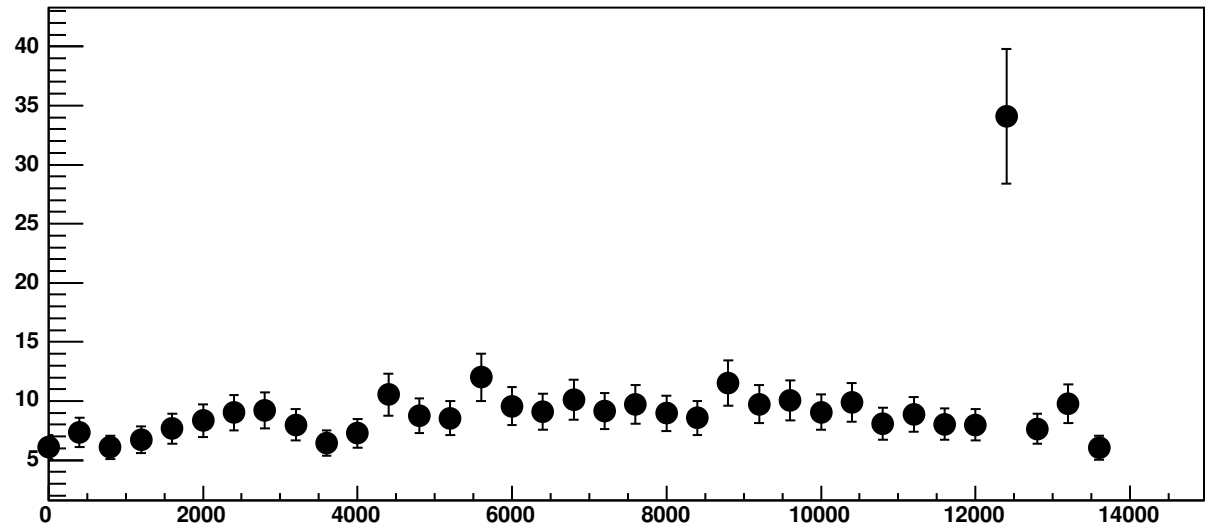

Chip 10, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

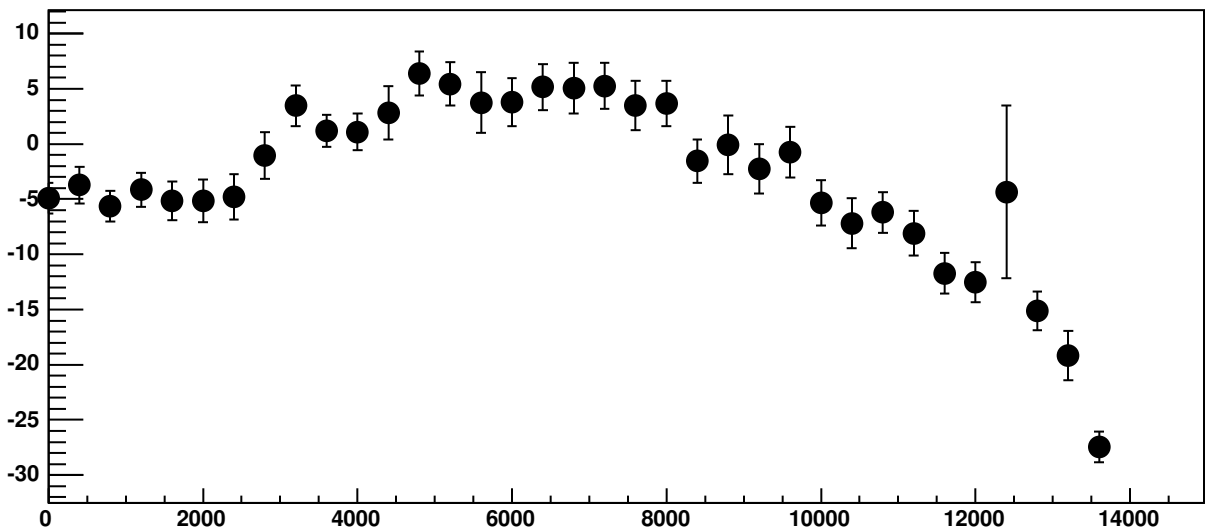
χ^2 / ndf	109.9 / 23
p0	61.25 ± 0.8493
p1	-0.001965 ± 0.0001347



Chip 10, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

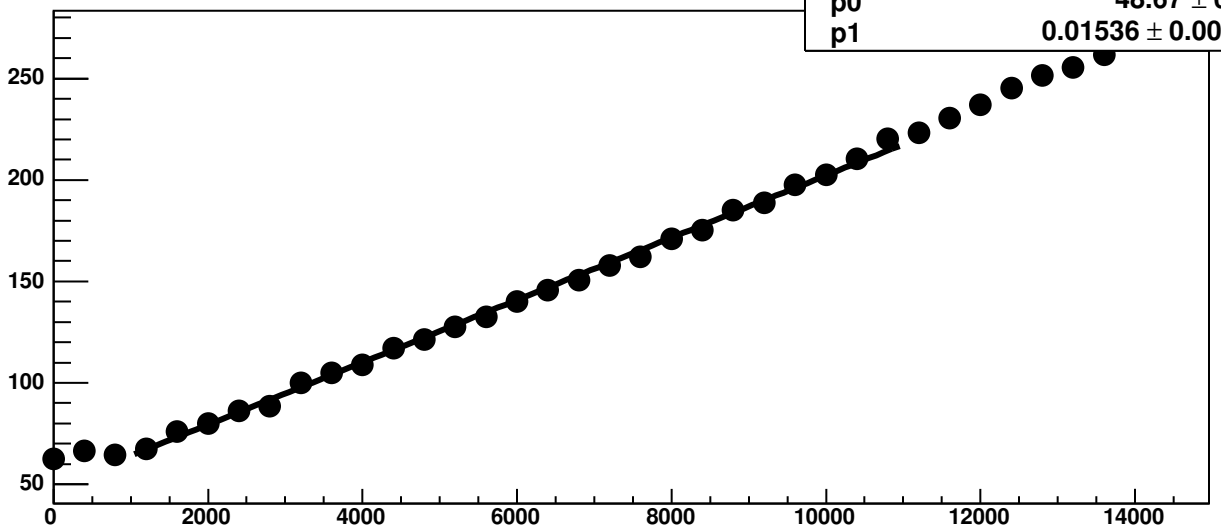


Chip 10, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

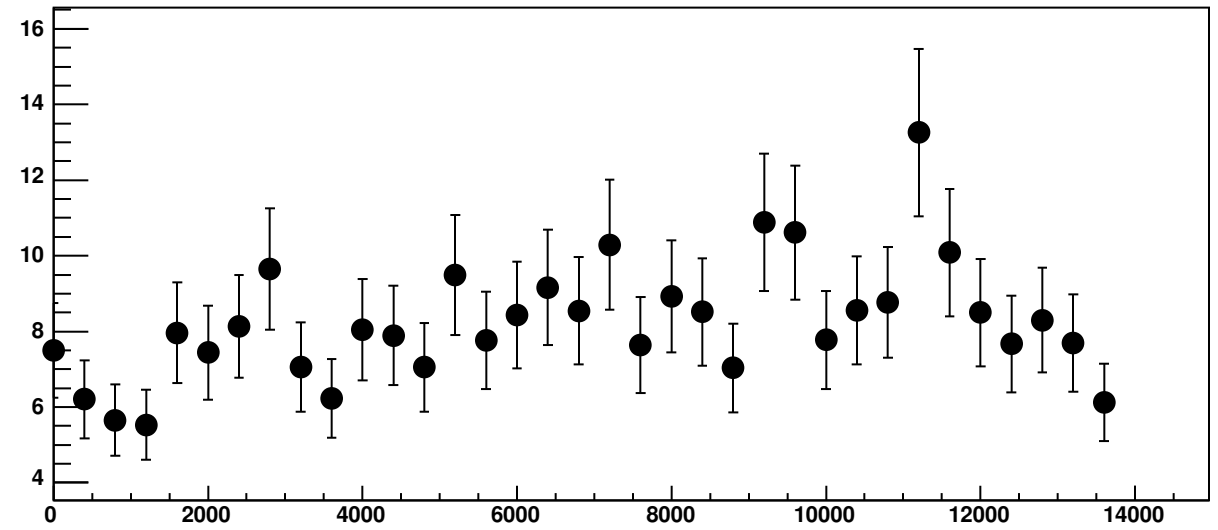


Chip 10, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

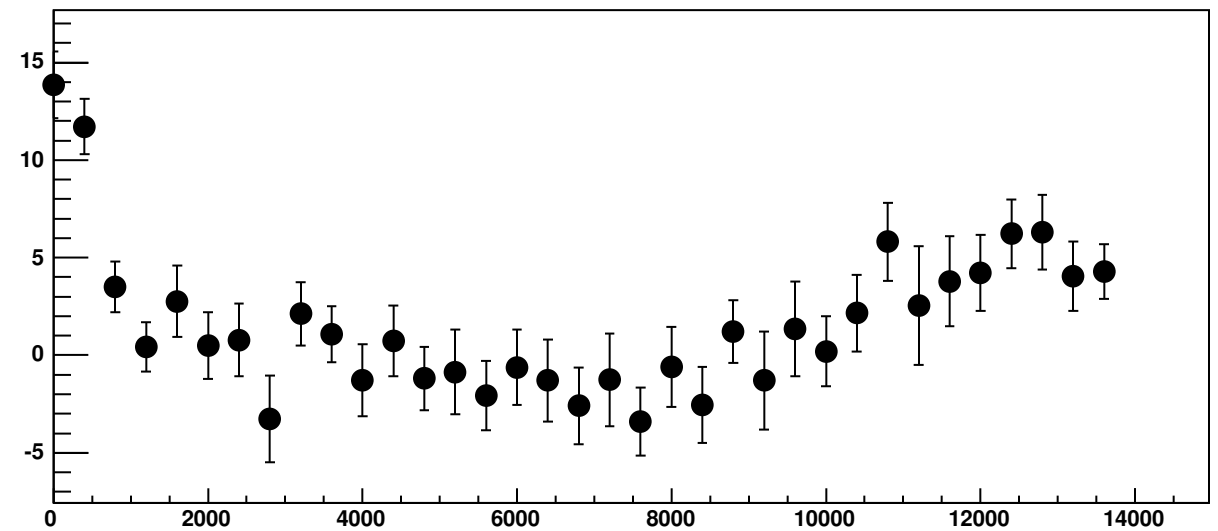
χ^2 / ndf	28.54 / 23
p0	48.67 ± 0.7796
p1	0.01536 ± 0.0001244

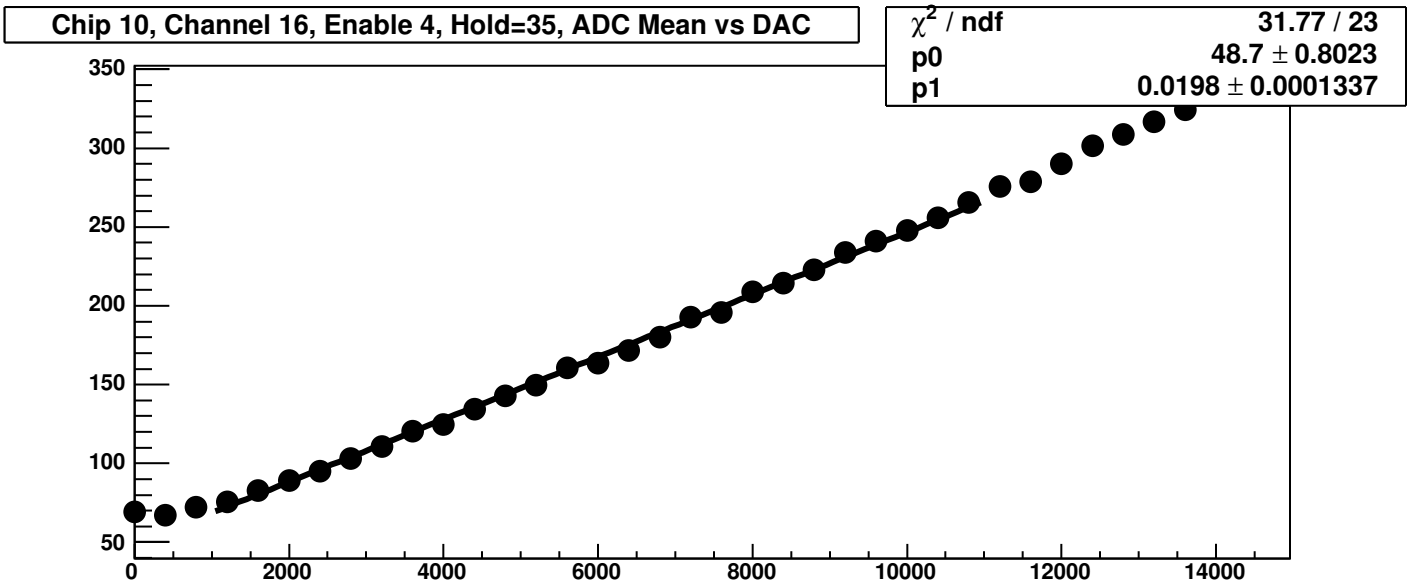


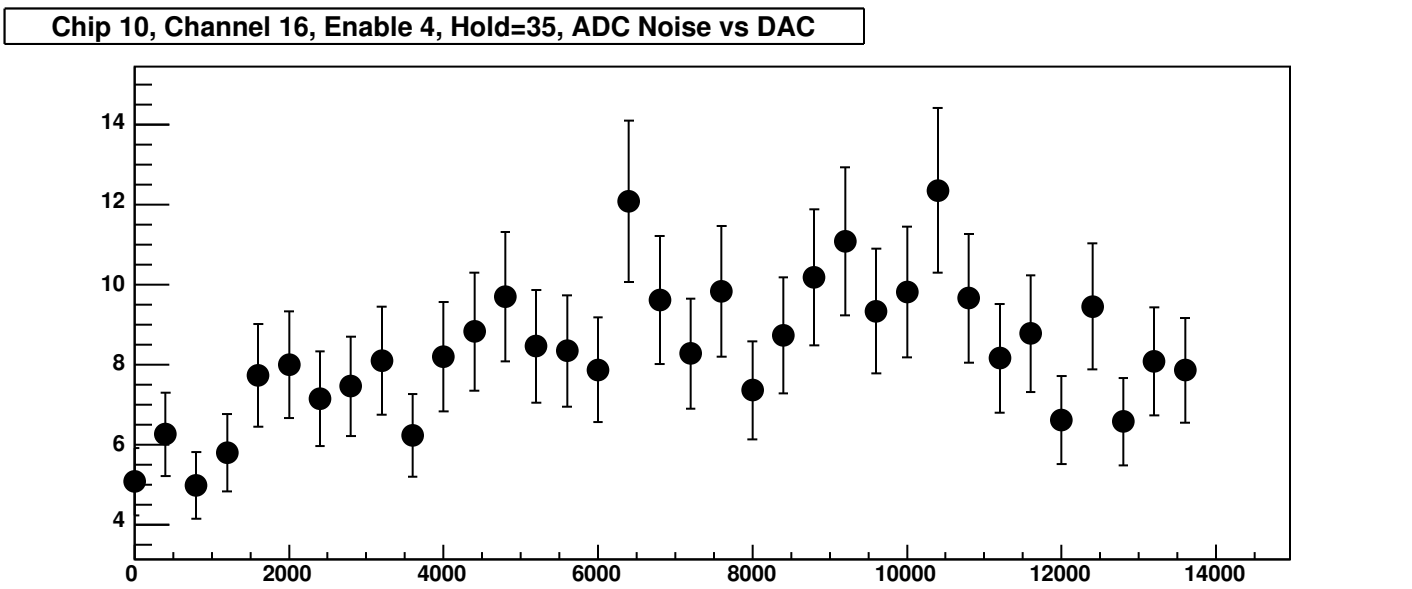
Chip 10, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

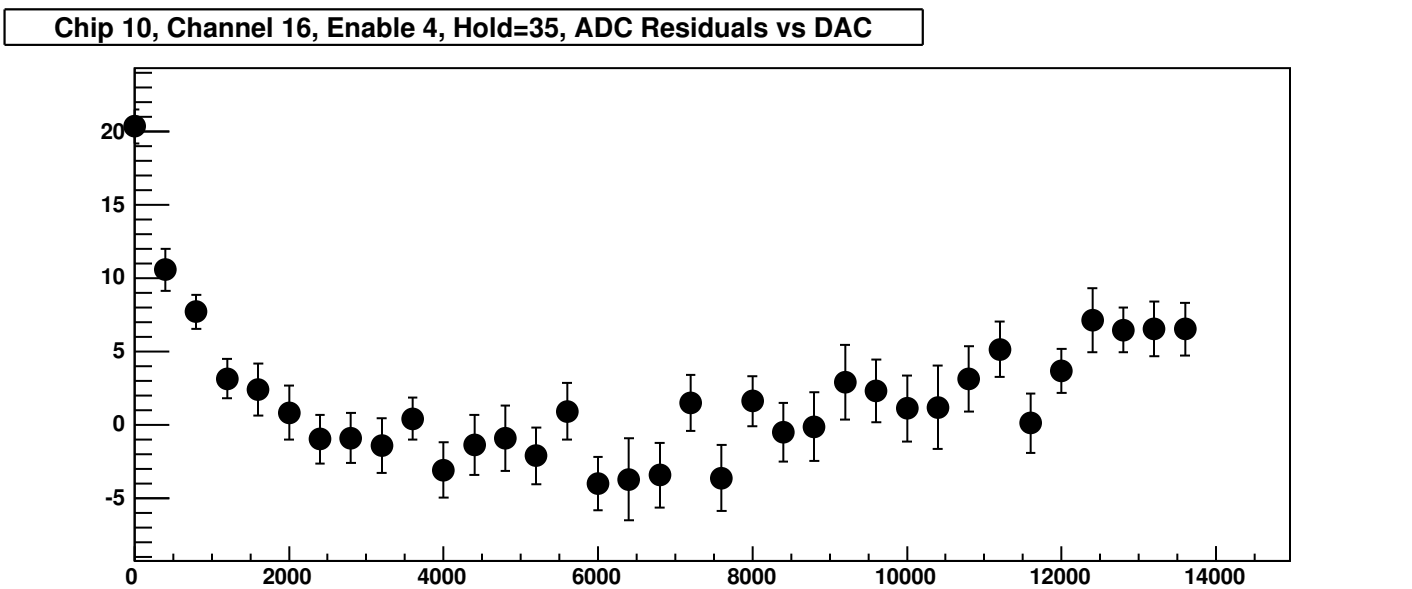


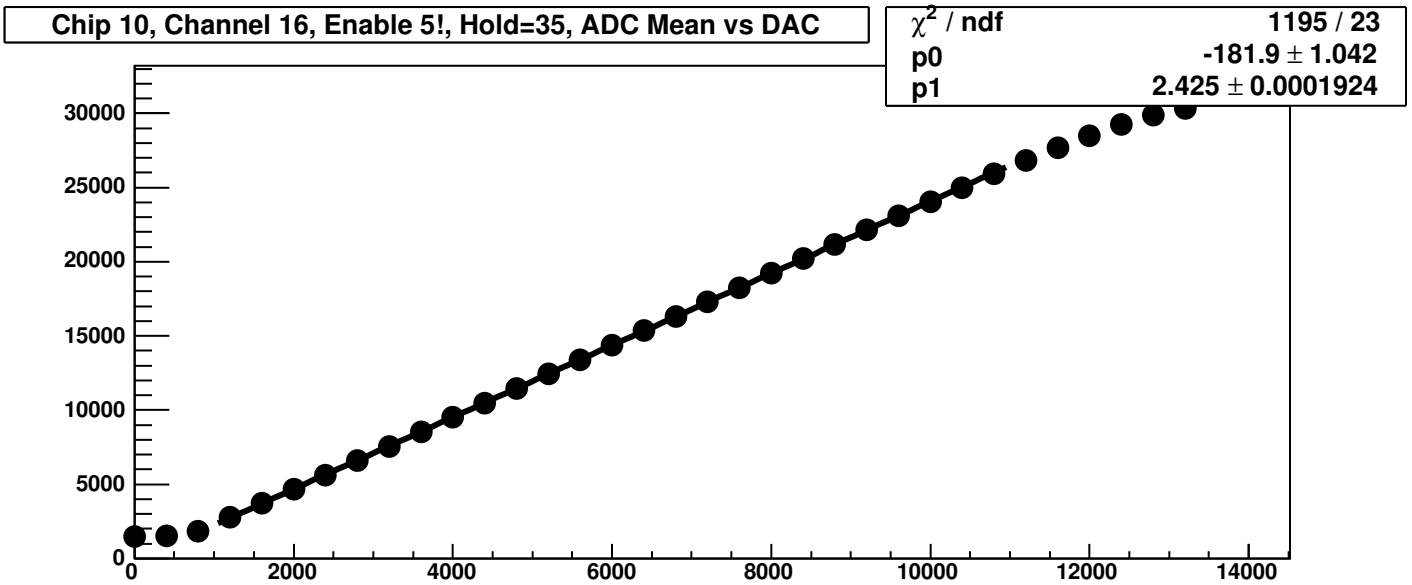
Chip 10, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

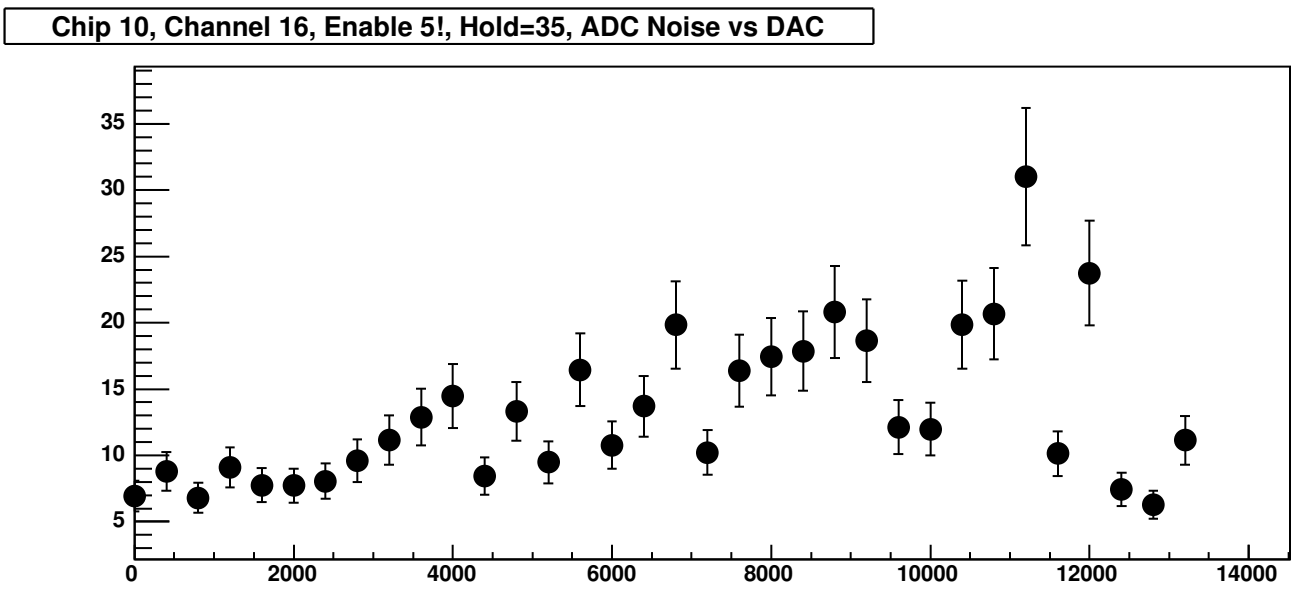


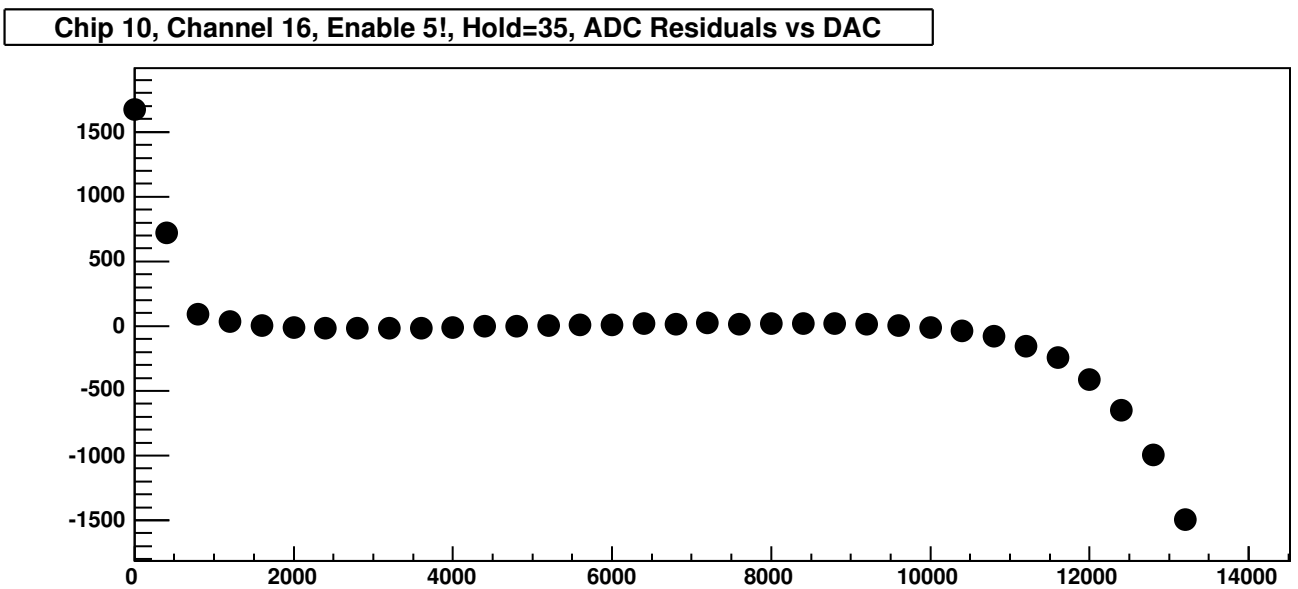

Chip 10, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.77 / 23
p0 48.7 ± 0.8023
p1 0.0198 ± 0.0001337
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

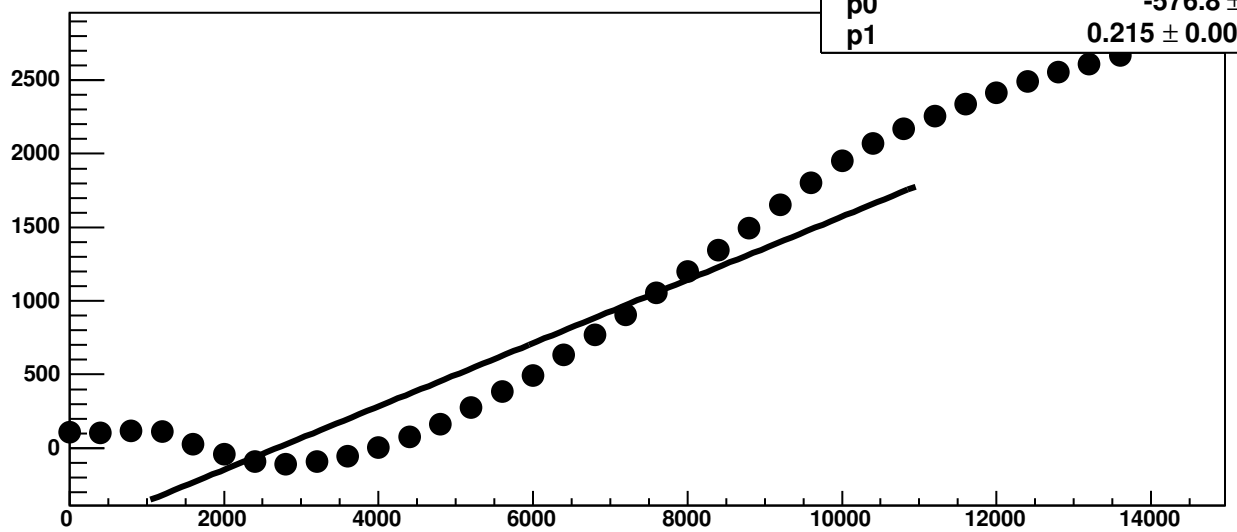

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1195 / 23
p0 -181.9 ± 1.042
p1 2.425 ± 0.0001924
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

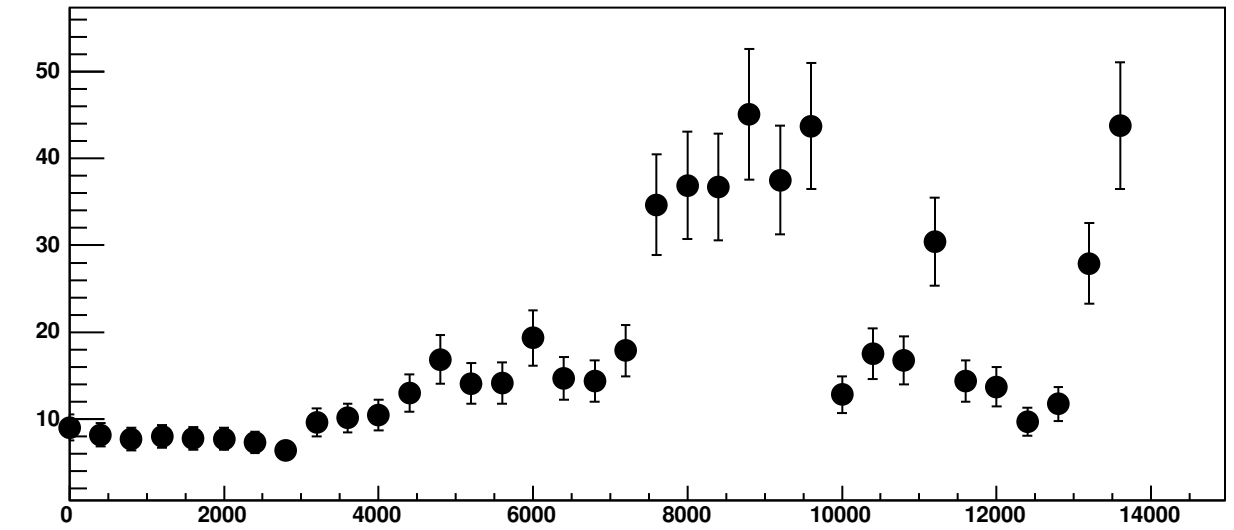

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

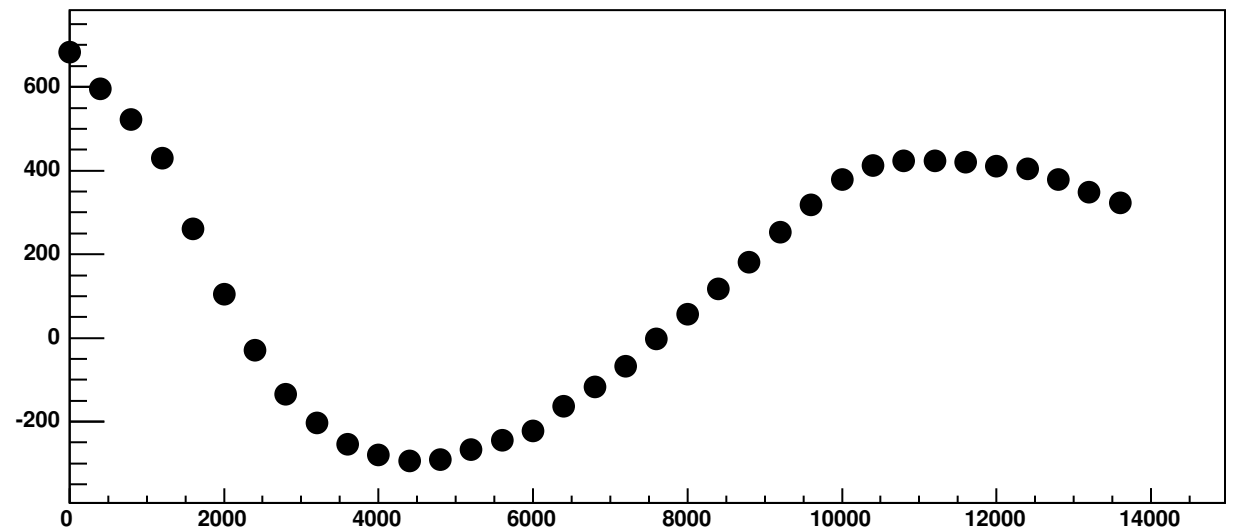
χ^2 / ndf	1.985e+05 / 23
p0	-576.8 ± 1.001
p1	0.215 ± 0.0002184

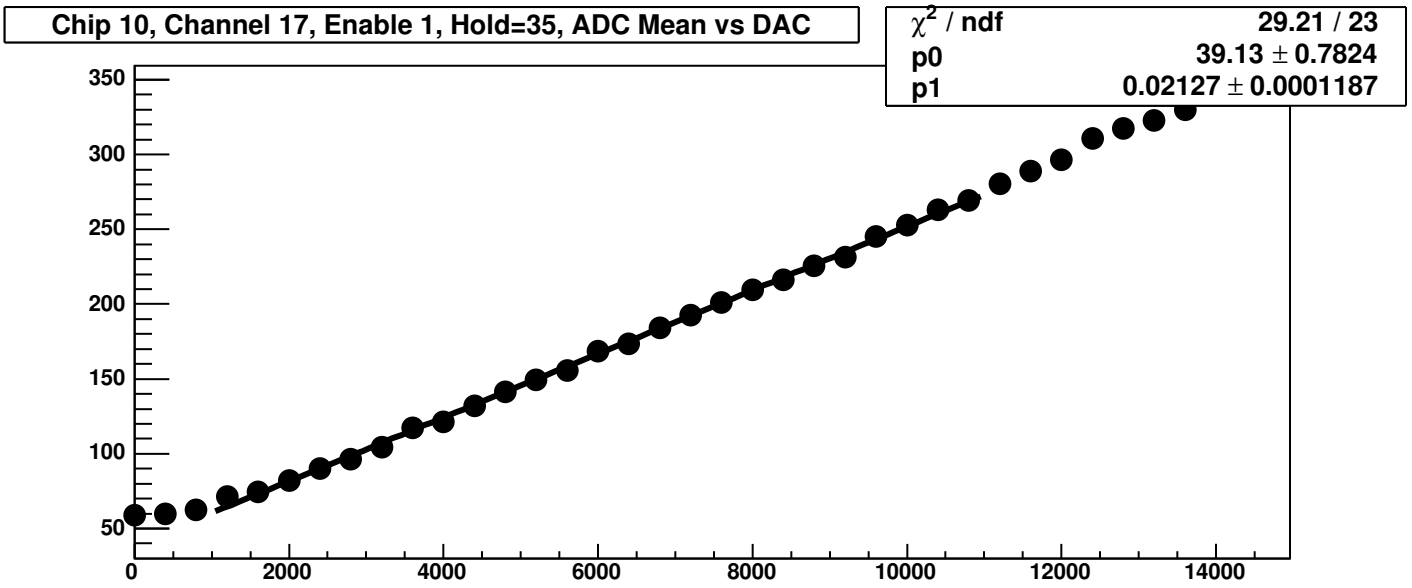


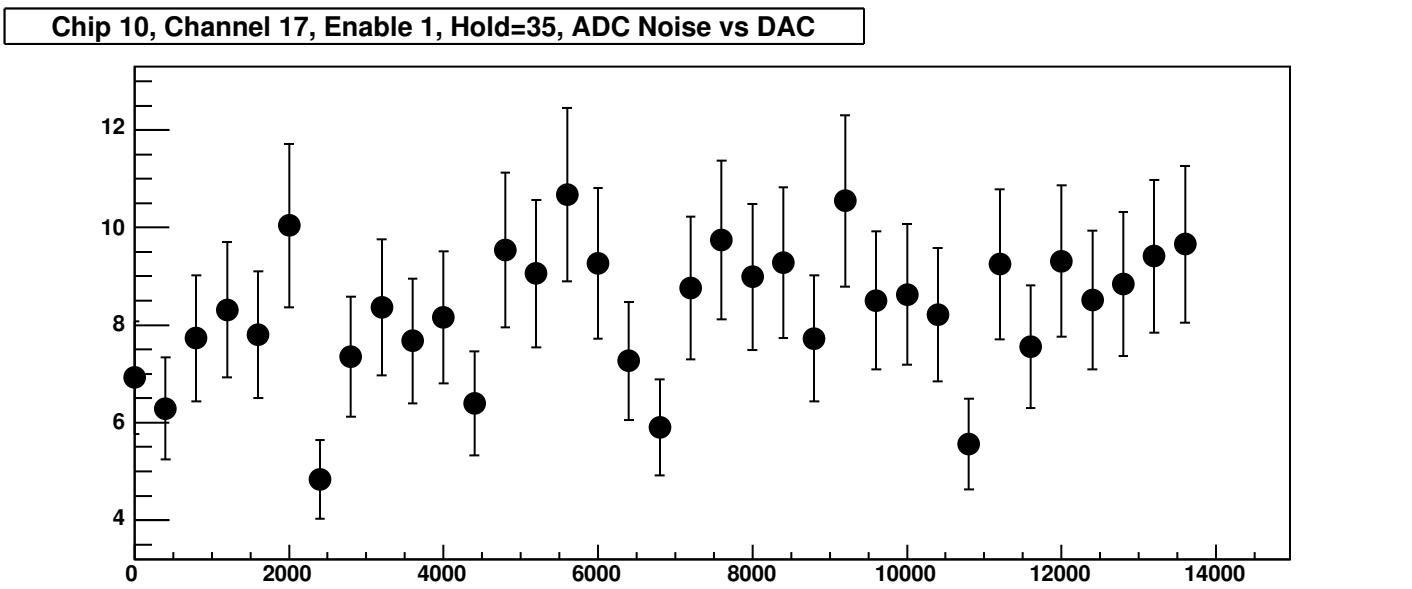
Chip 10, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

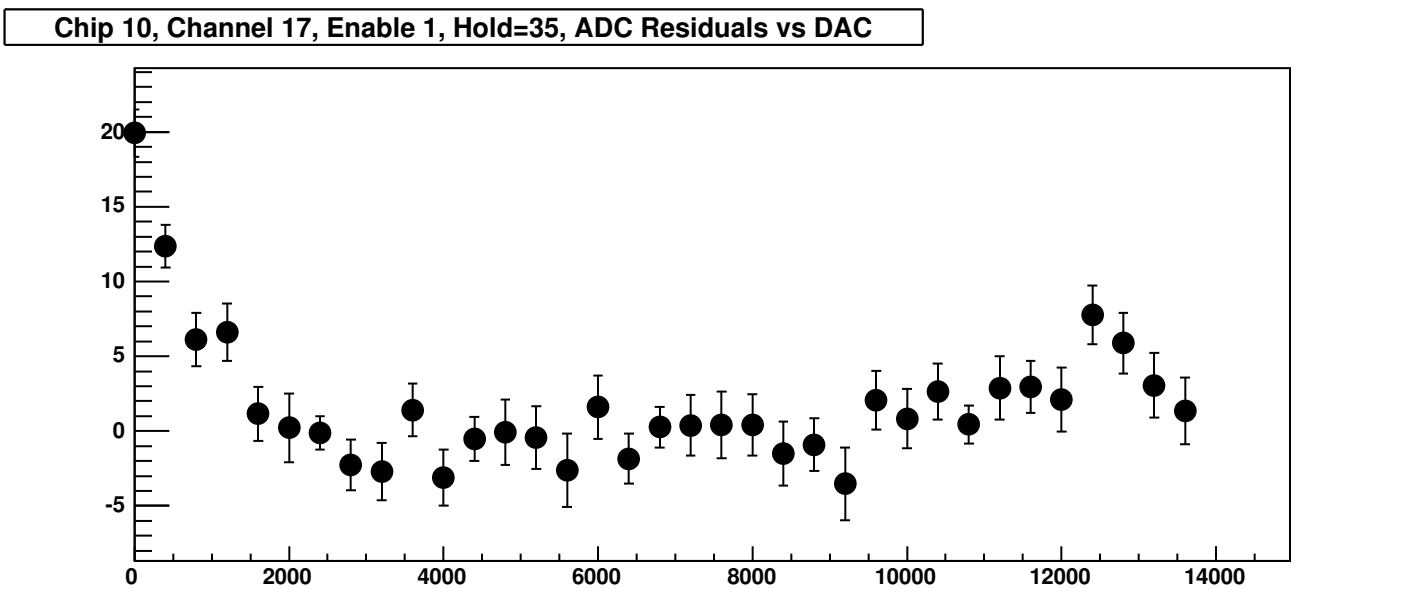


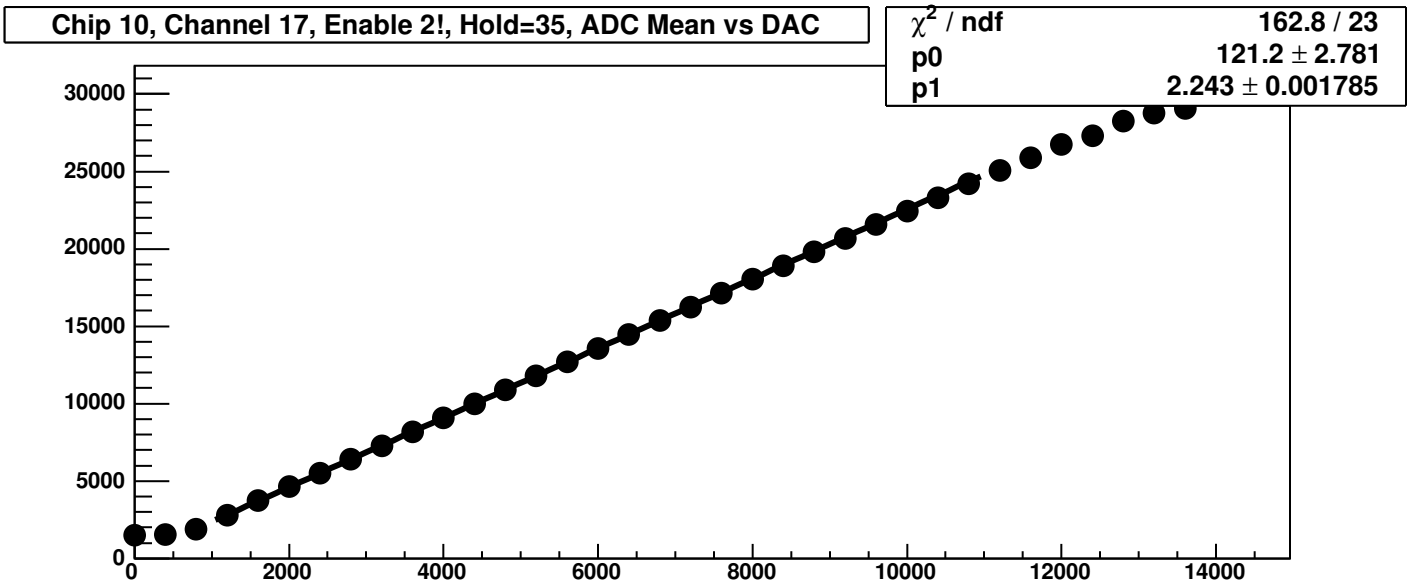
Chip 10, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

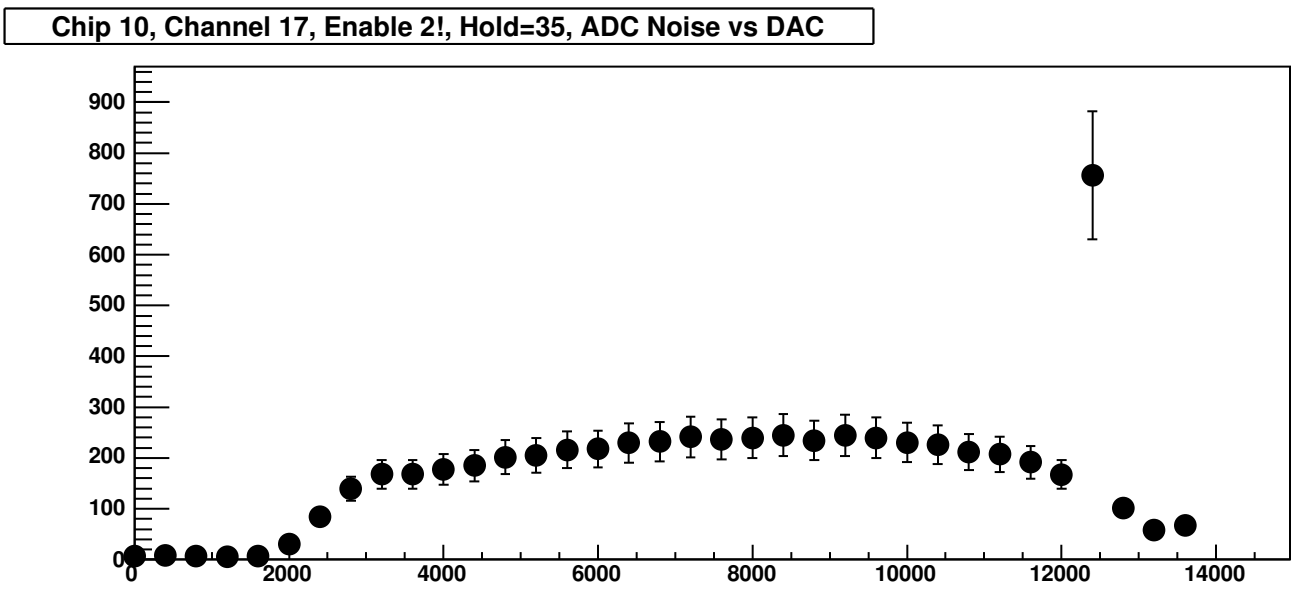


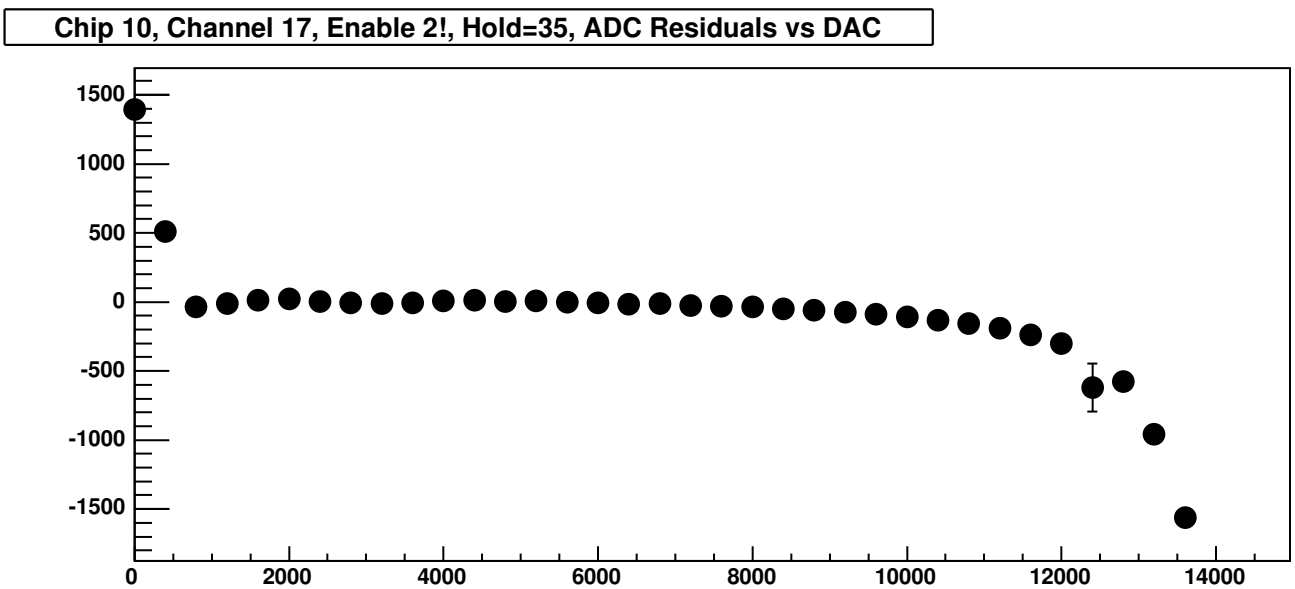

Chip 10, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.21 / 23
p0 39.13 ± 0.7824
p1 0.02127 ± 0.0001187
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

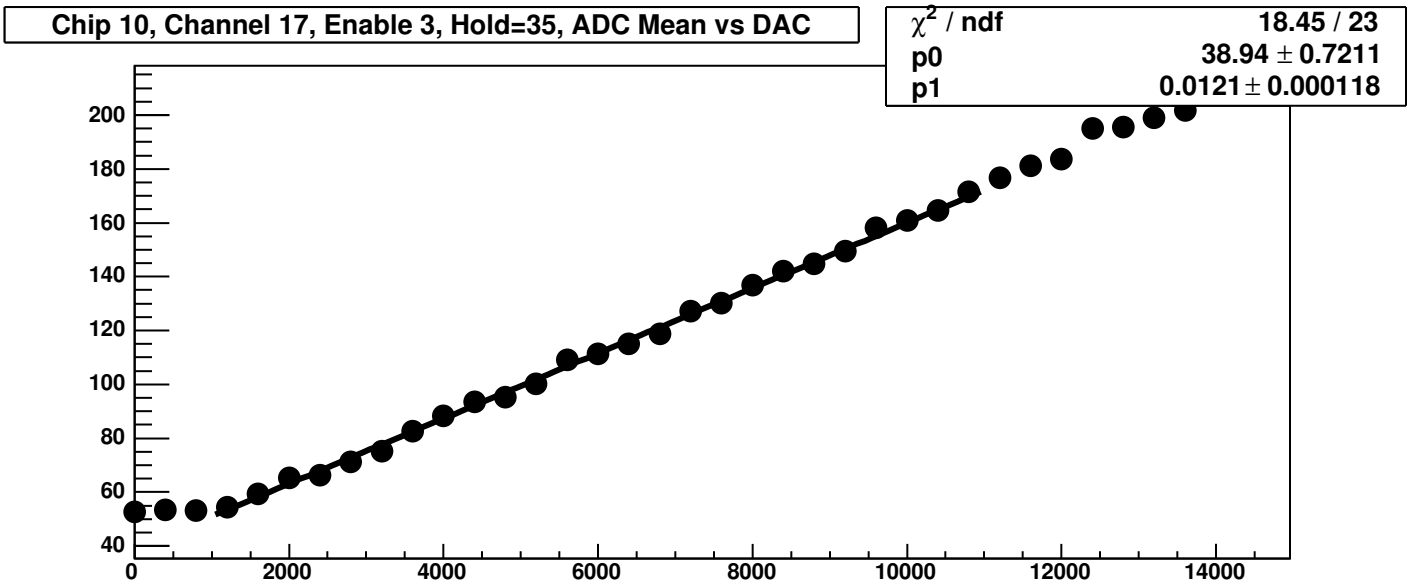

Chip 10, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

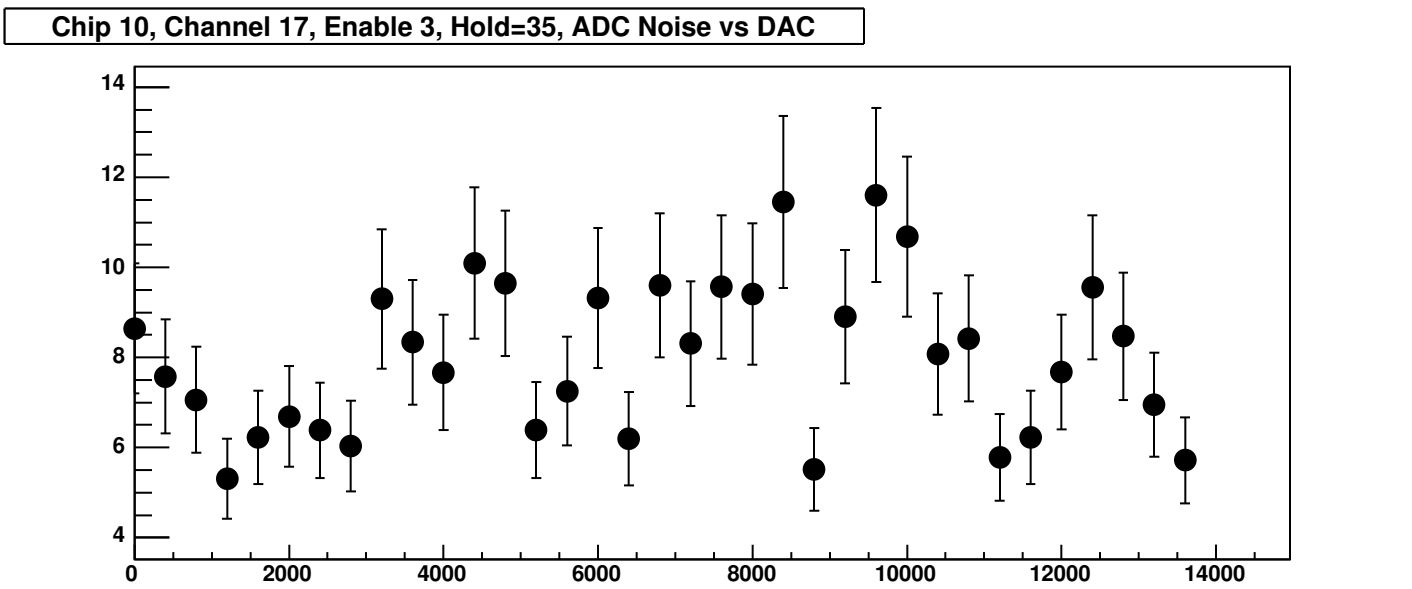

Chip 10, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

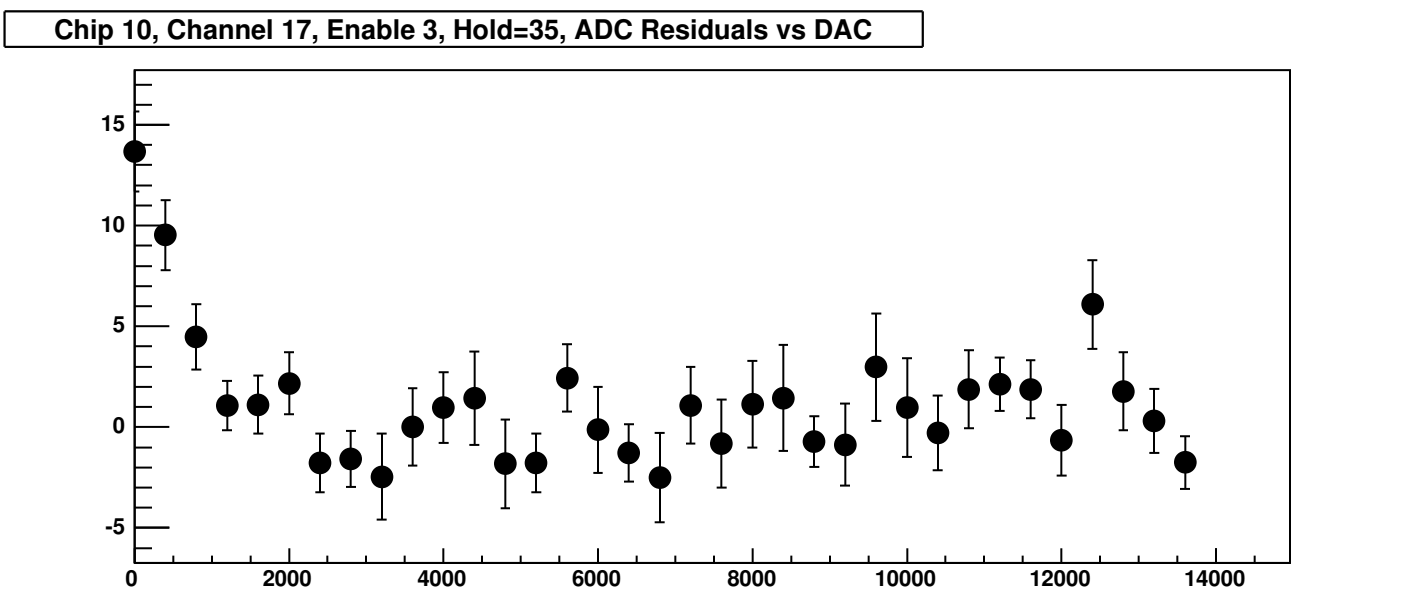

Chip 10, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 162.8 / 23
p0 121.2 ± 2.781
p1 2.243 ± 0.001785
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

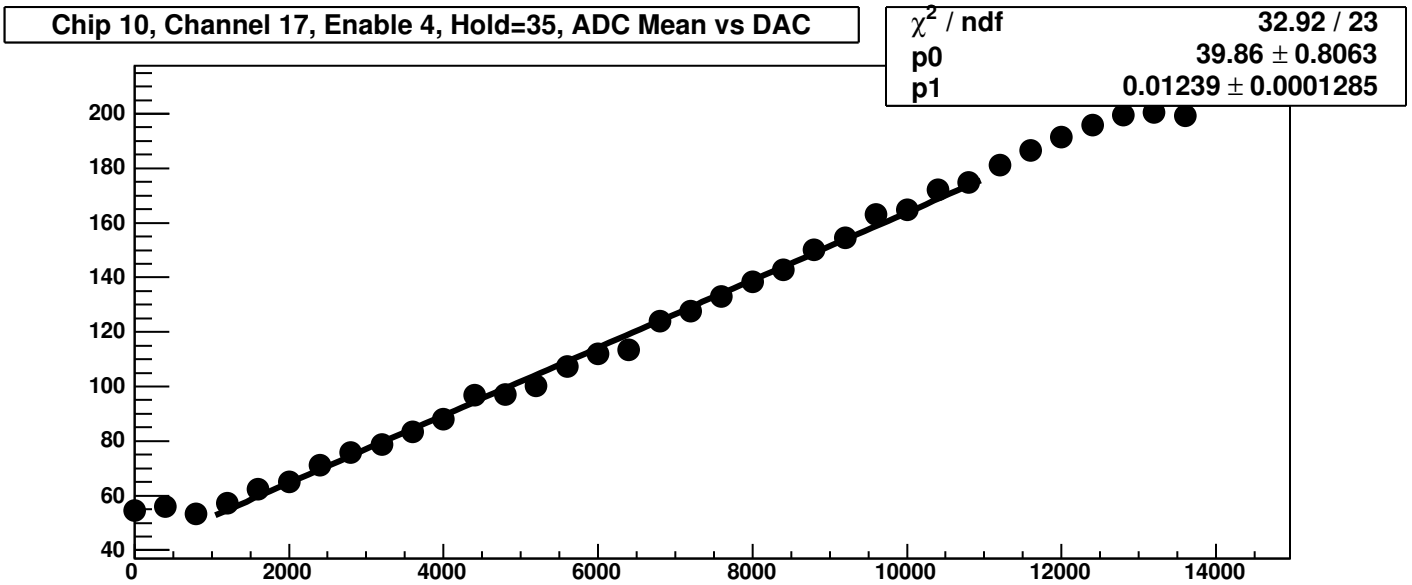

Chip 10, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

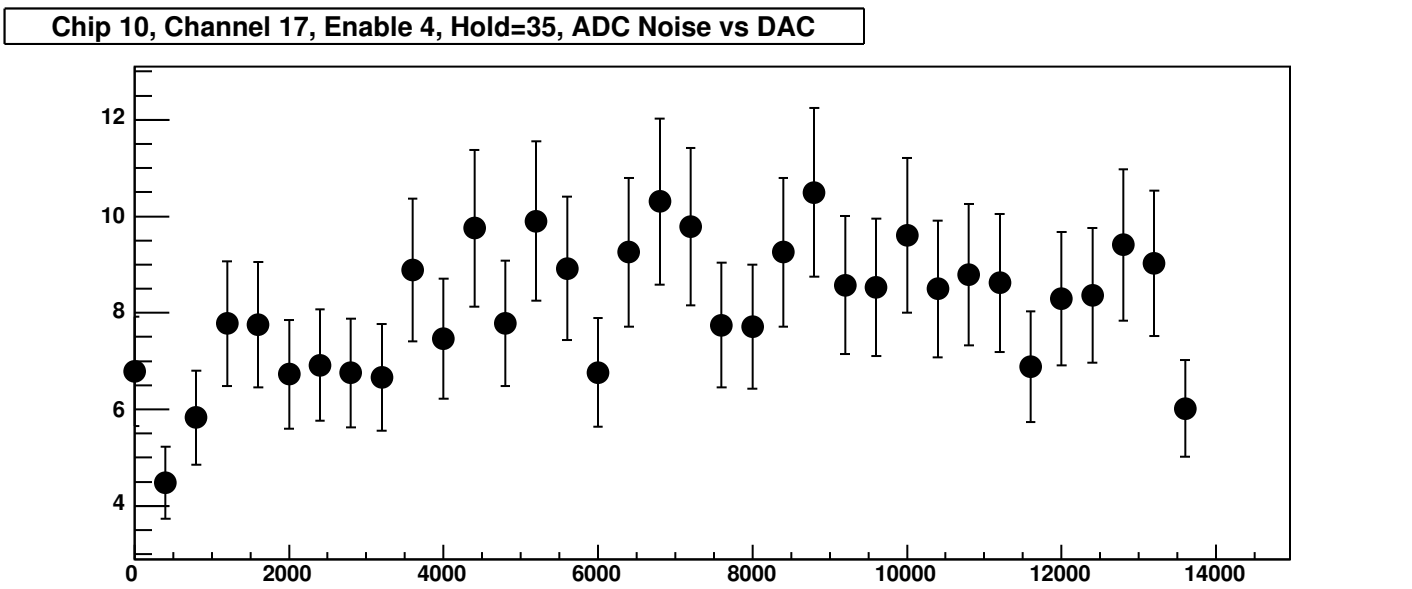

Chip 10, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

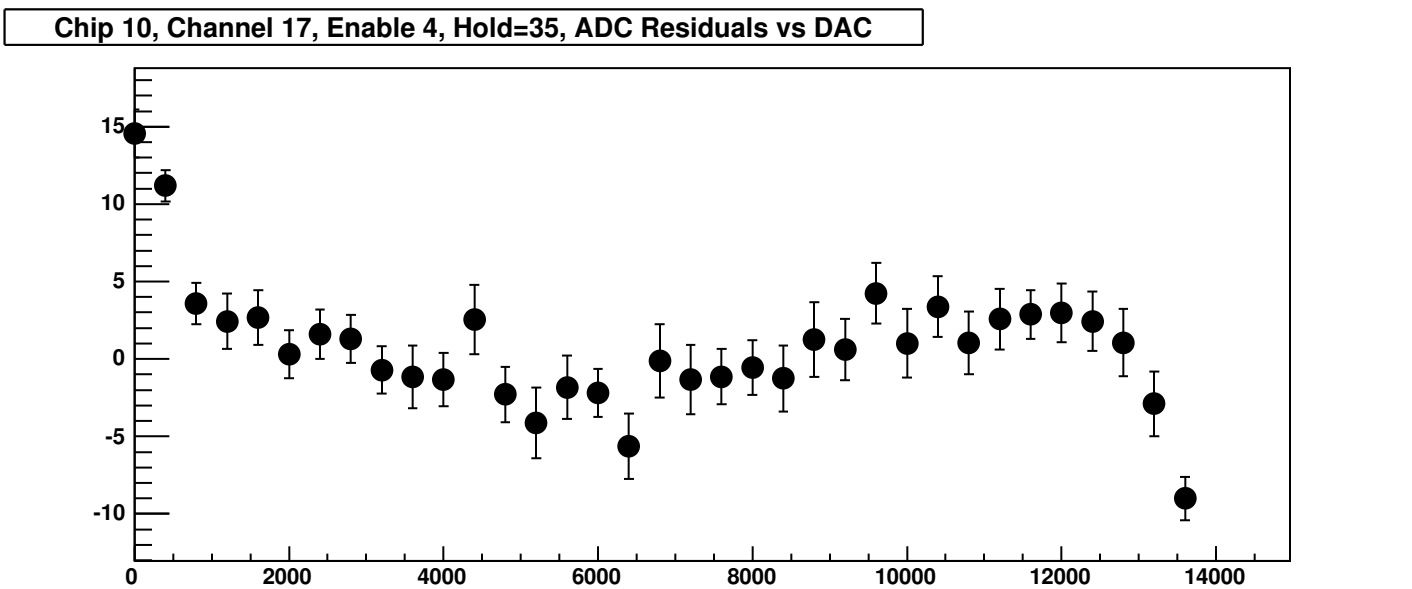

Chip 10, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.45 / 23
p0 38.94 ± 0.7211
p1 0.0121 ± 0.000118
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

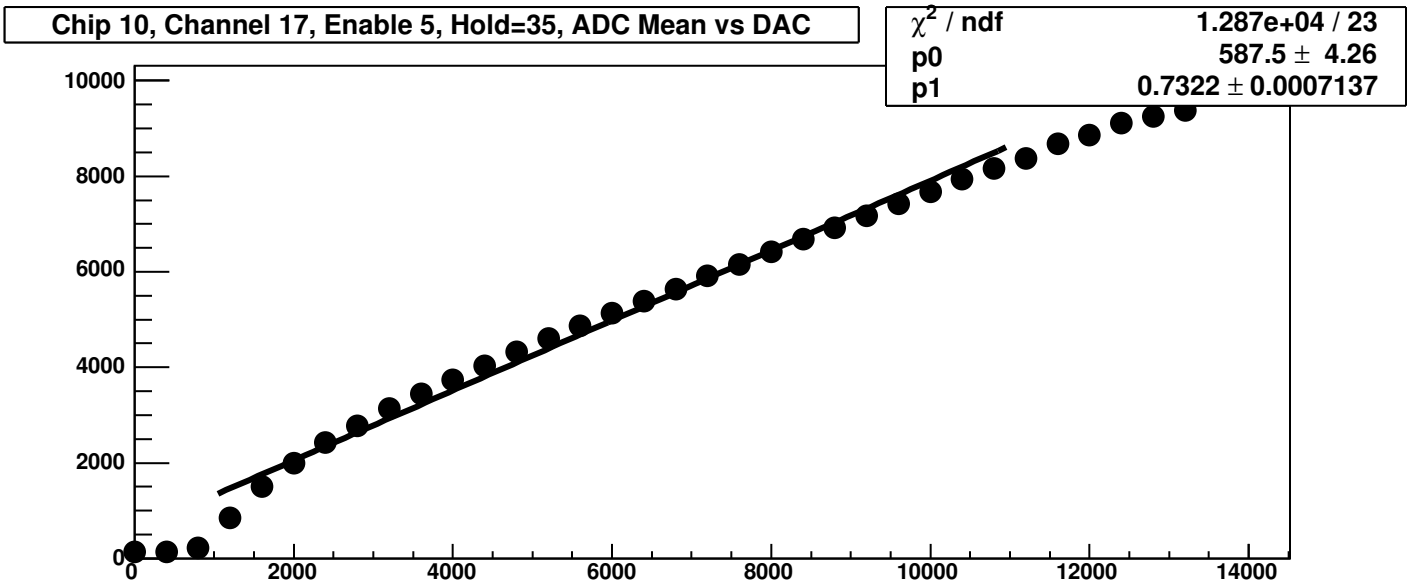

Chip 10, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

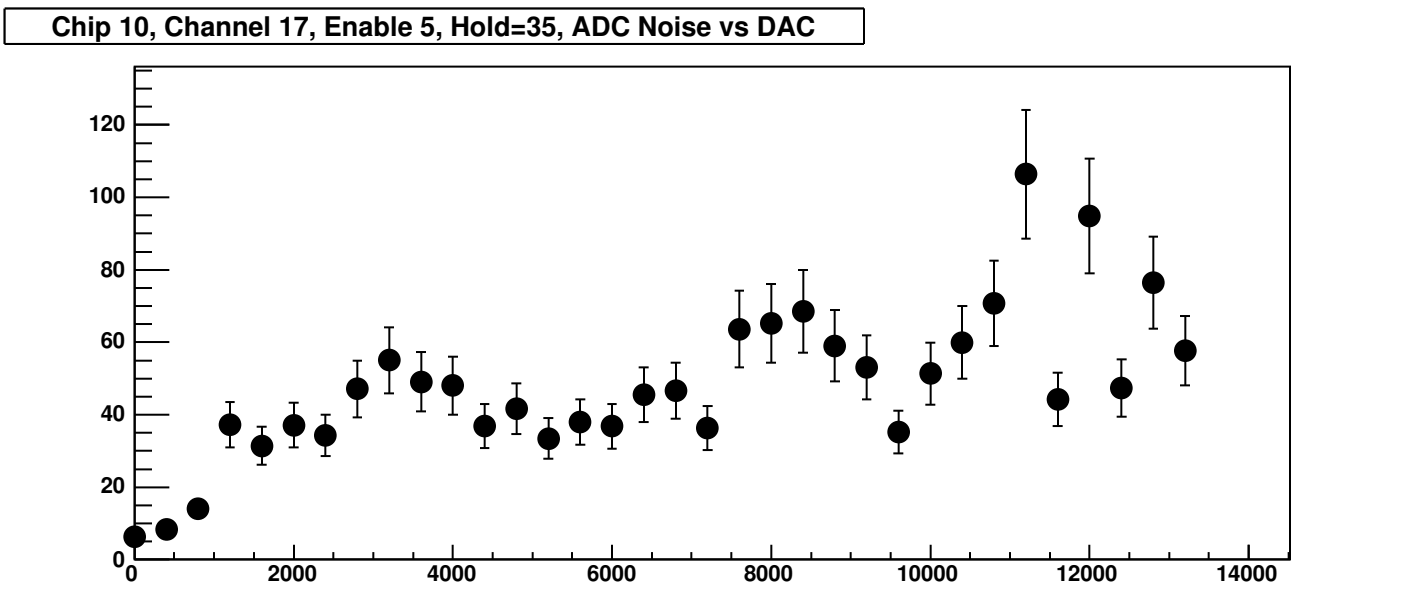

Chip 10, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

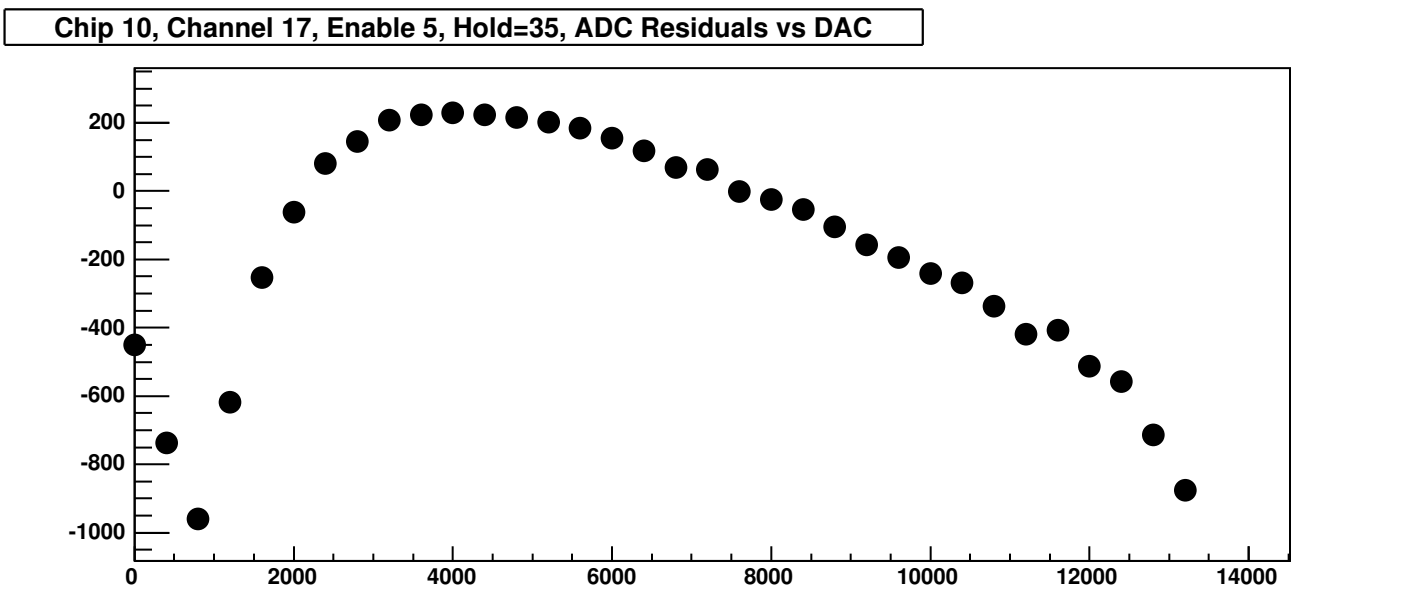

Chip 10, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.92 / 23
p0 39.86 ± 0.8063
p1 0.01239 ± 0.0001285
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

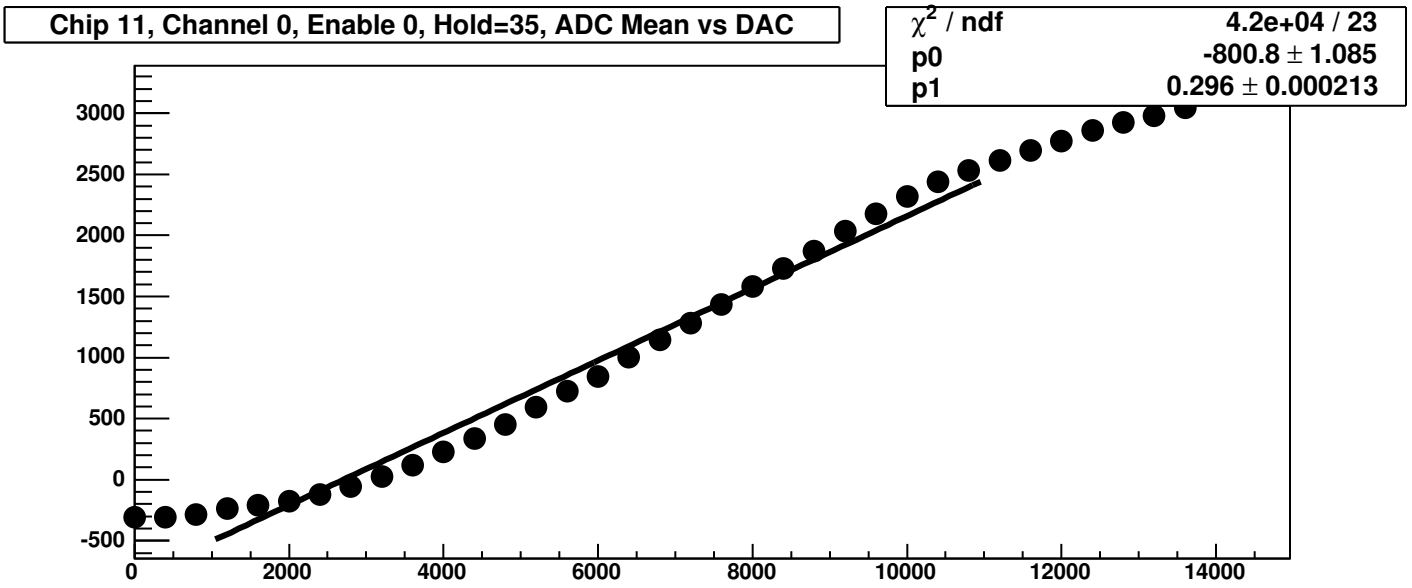

Chip 10, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

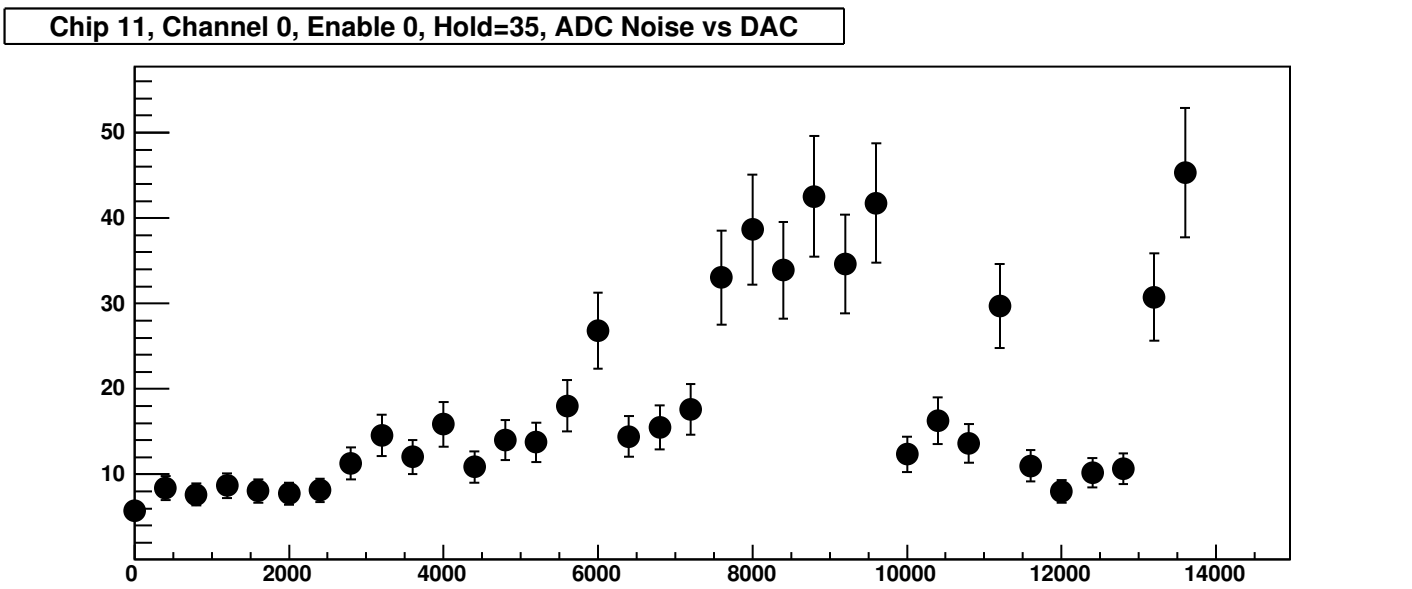

Chip 10, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

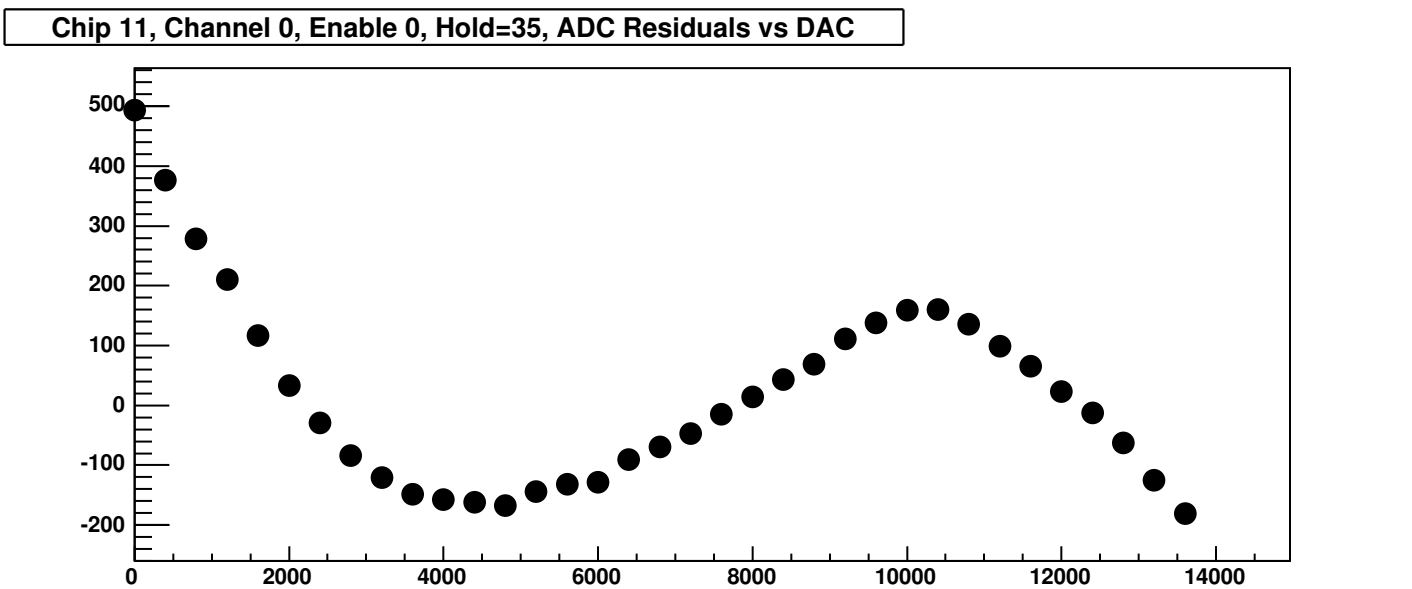

Chip 10, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.287e+04 / 23
p0 587.5 ± 4.26
p1 0.7322 ± 0.0007137
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

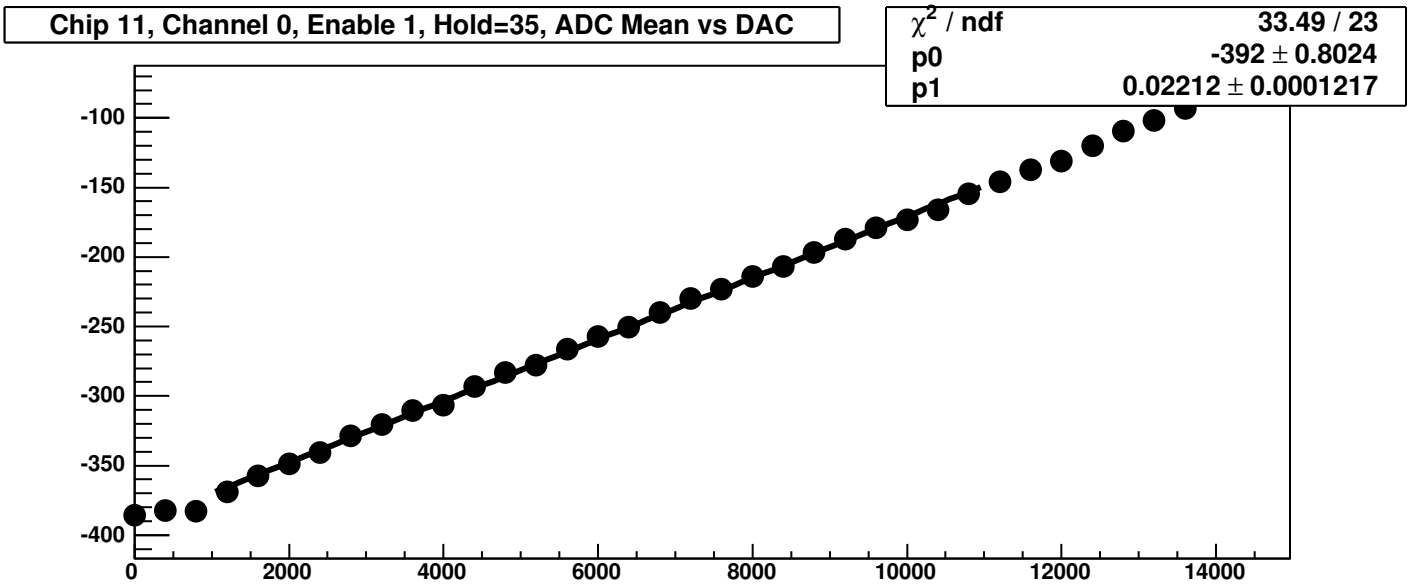

Chip 10, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

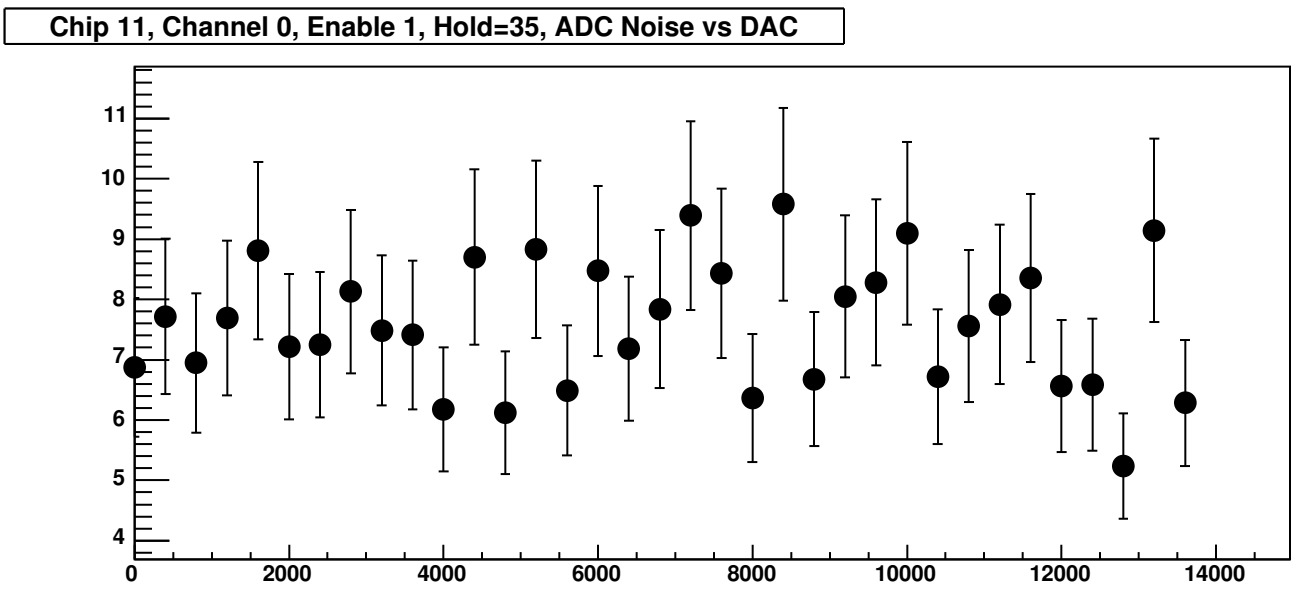

Chip 10, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

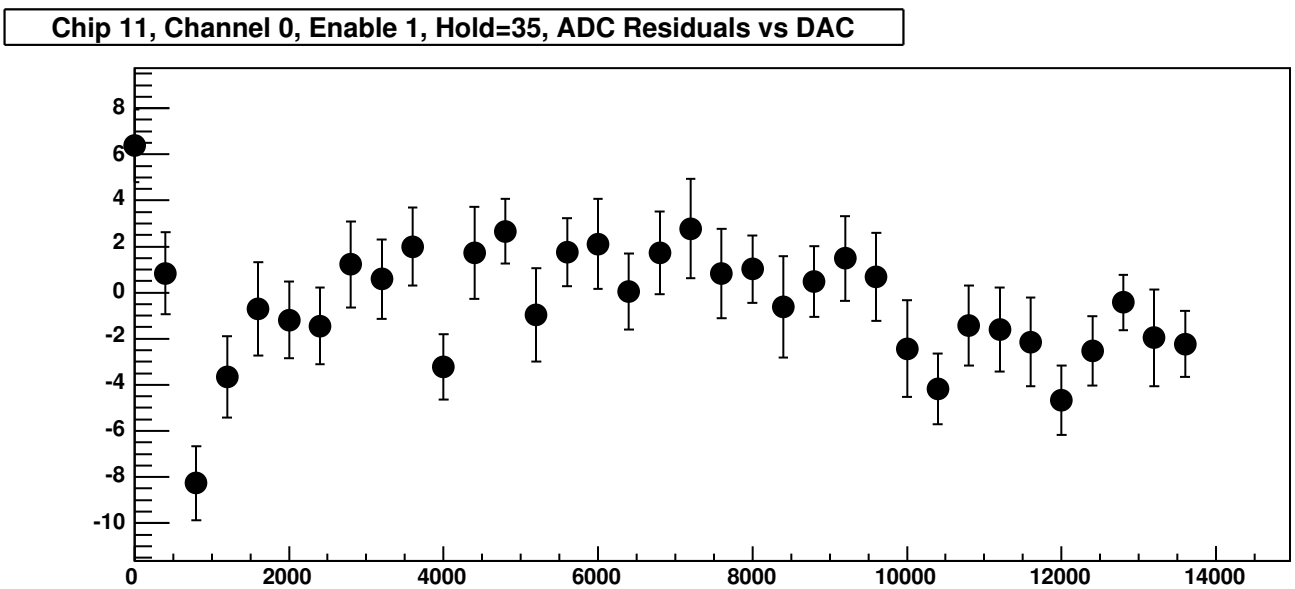

Chip 11, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.2e+04 / 23
p0 -800.8 ± 1.085
p1 0.296 ± 0.000213
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

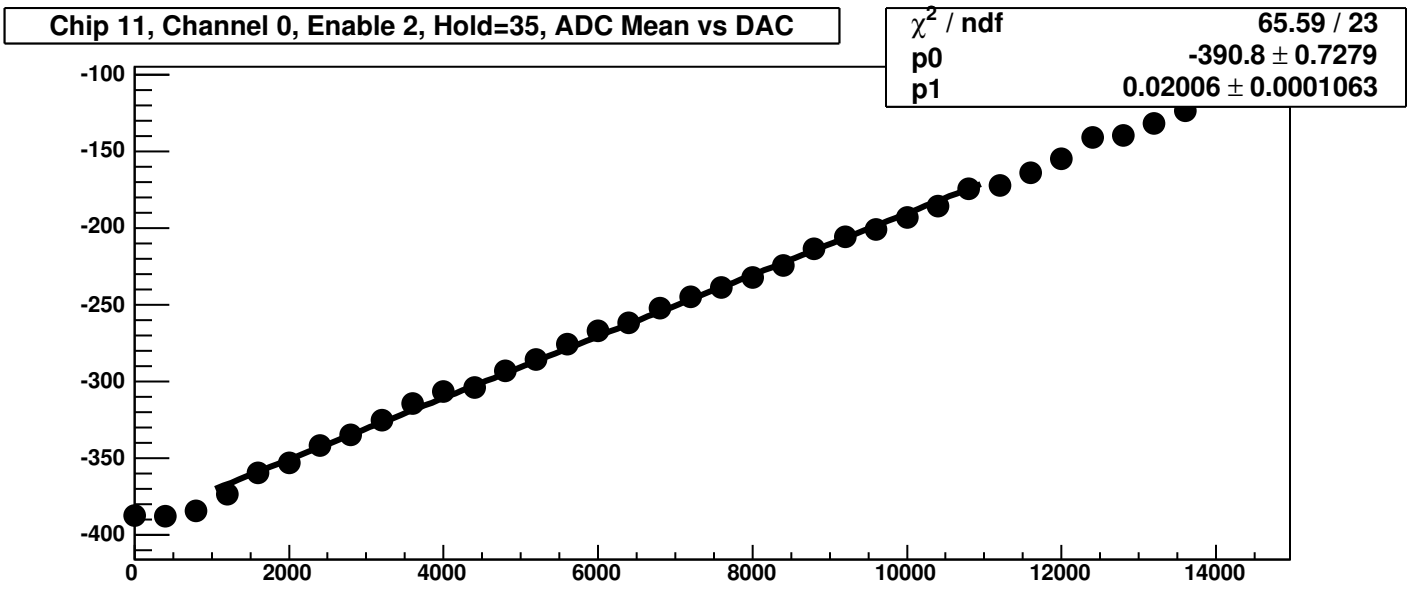

Chip 11, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

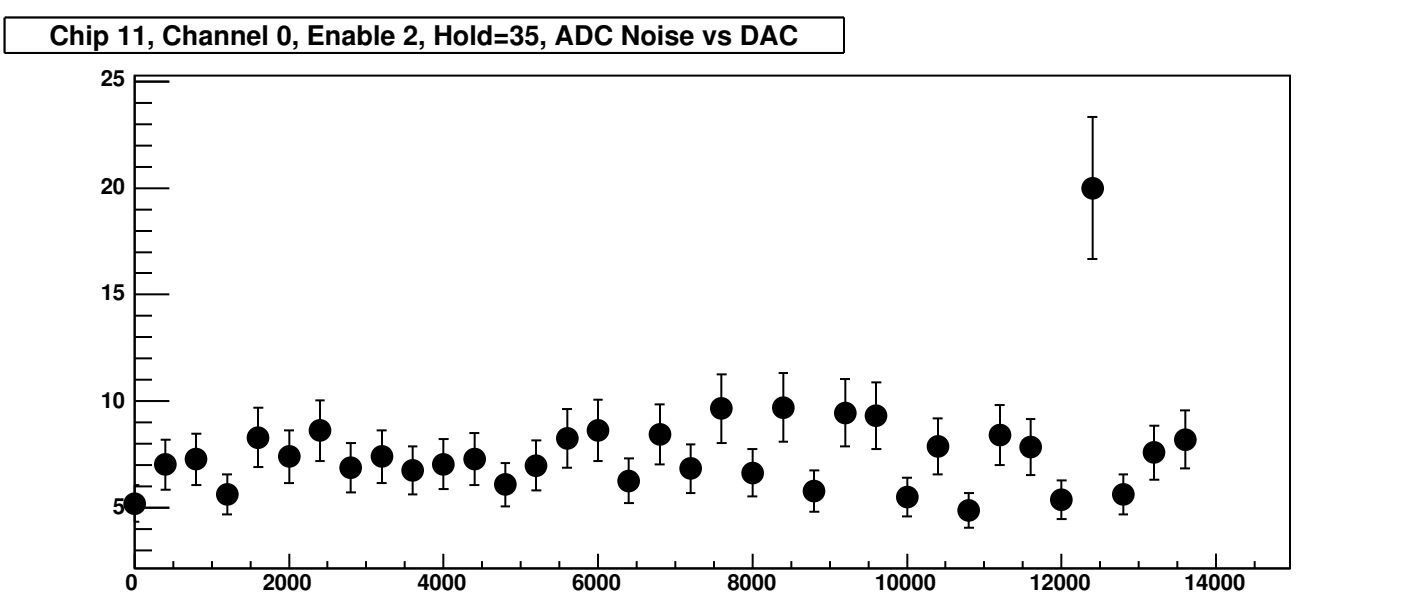

Chip 11, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

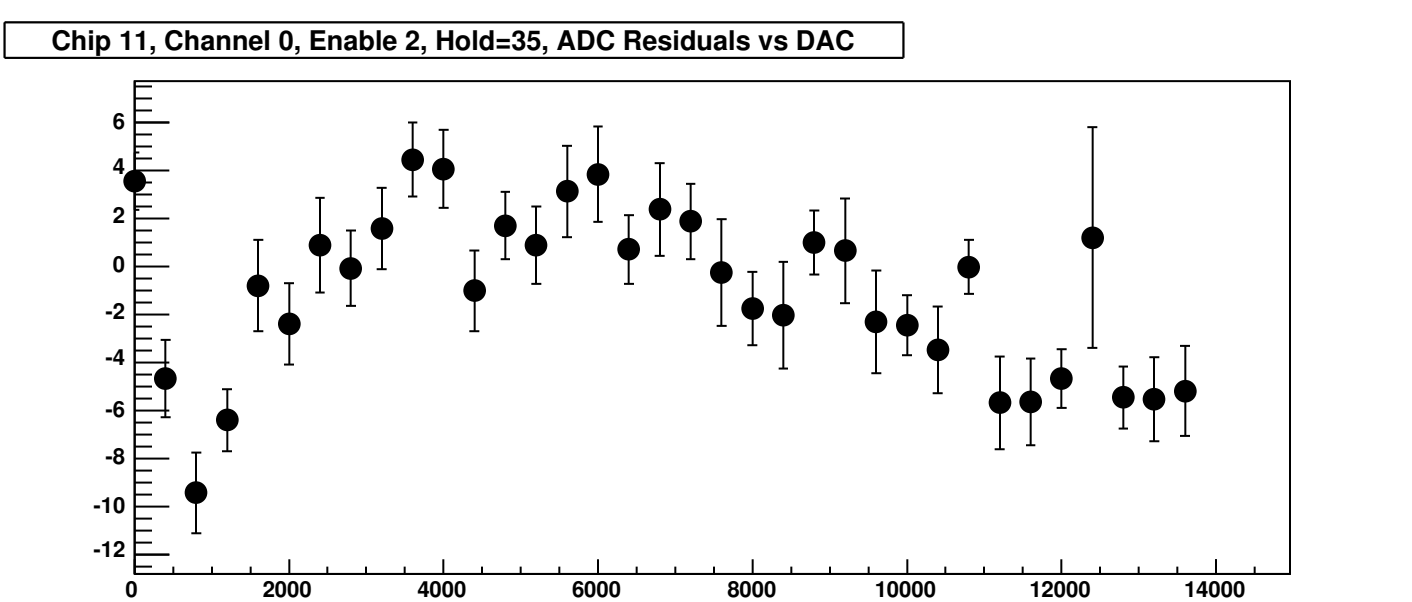

Chip 11, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.49 / 23
p0 -392 ± 0.8024
p1 0.02212 ± 0.0001217
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

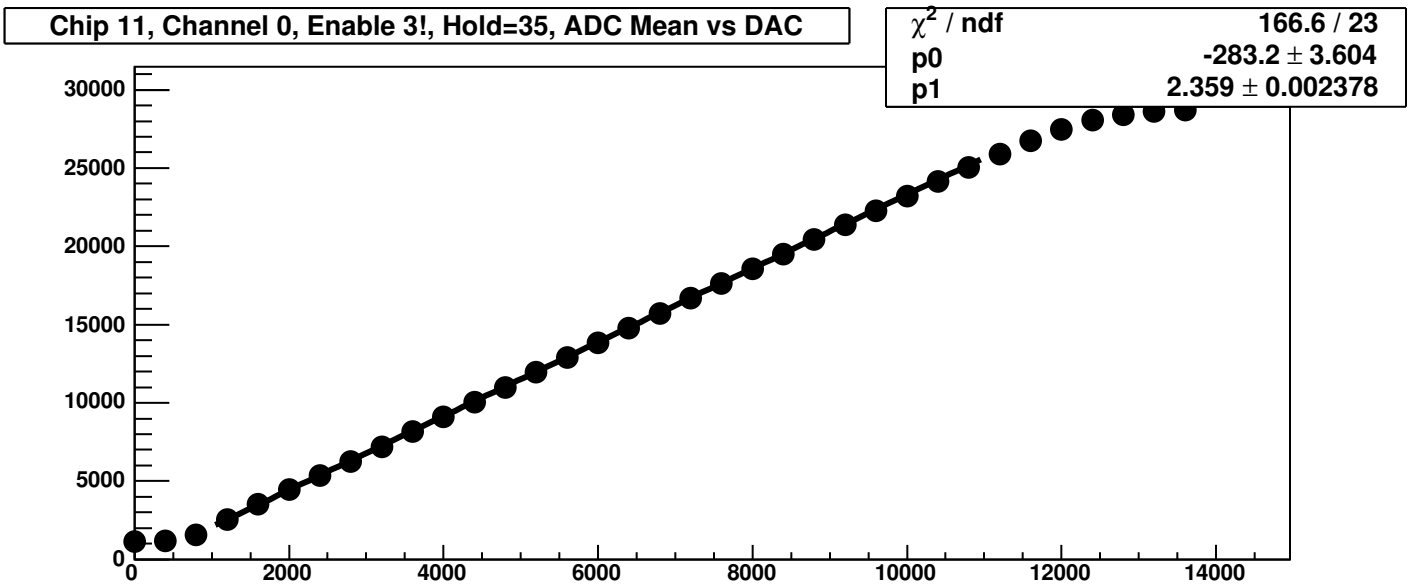

Chip 11, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

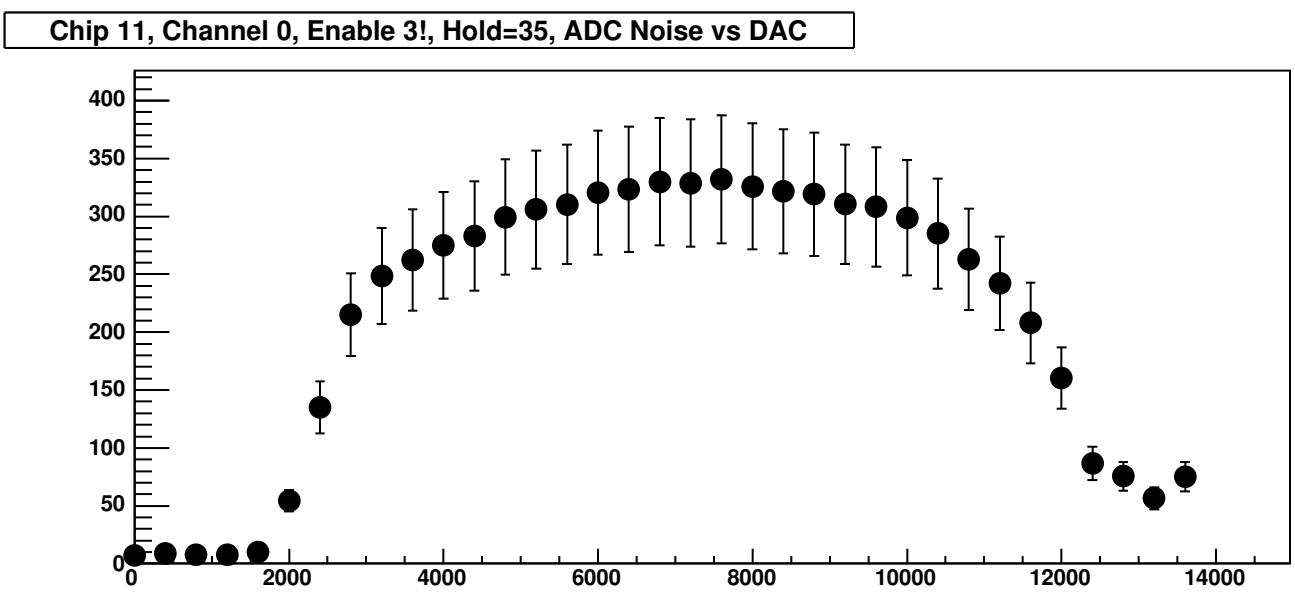

Chip 11, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

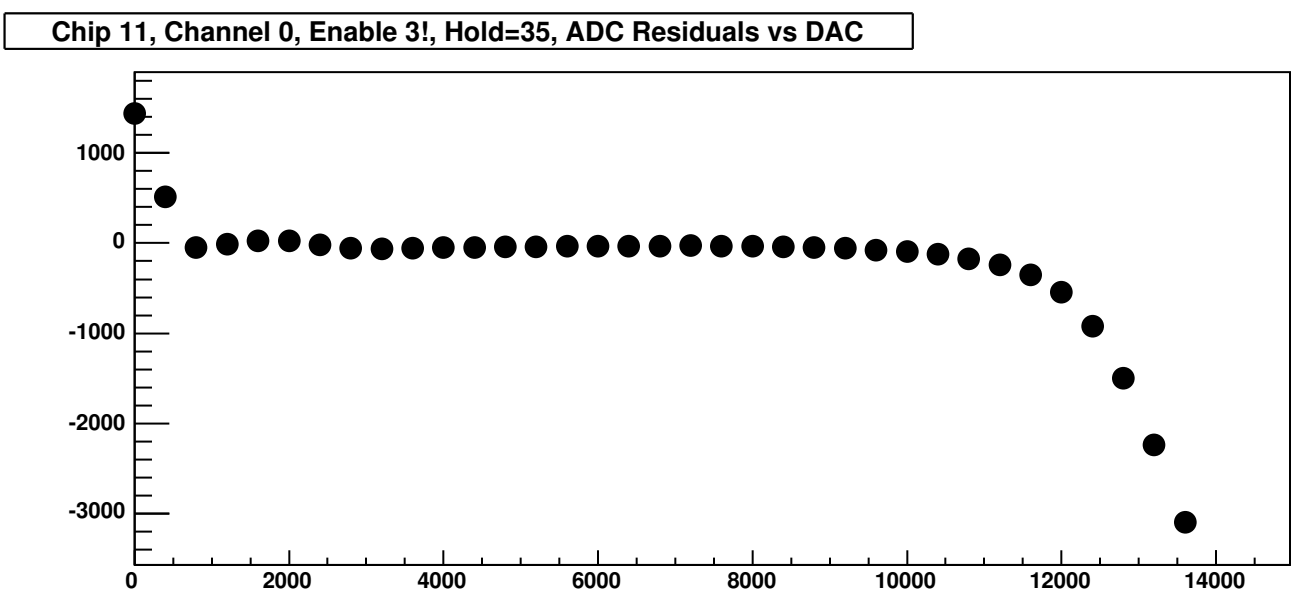

Chip 11, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 65.59 / 23
p0 -390.8 ± 0.7279
p1 0.02006 ± 0.0001063
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

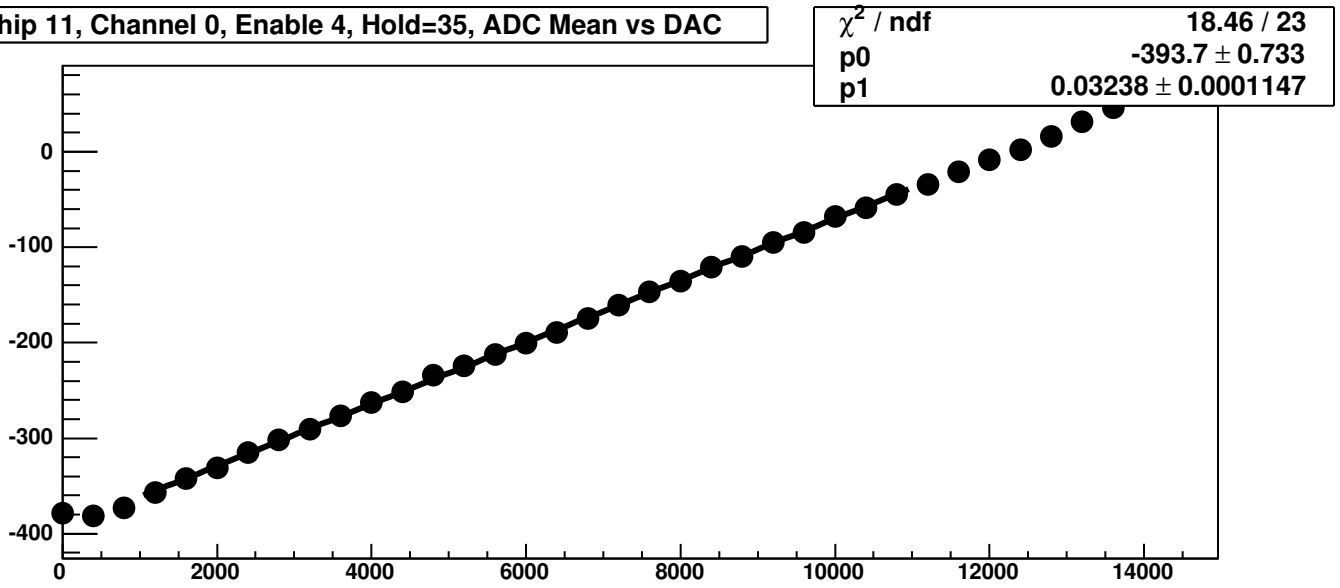

Chip 11, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 166.6 / 23
p0 -283.2 ± 3.604
p1 2.359 ± 0.002378
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

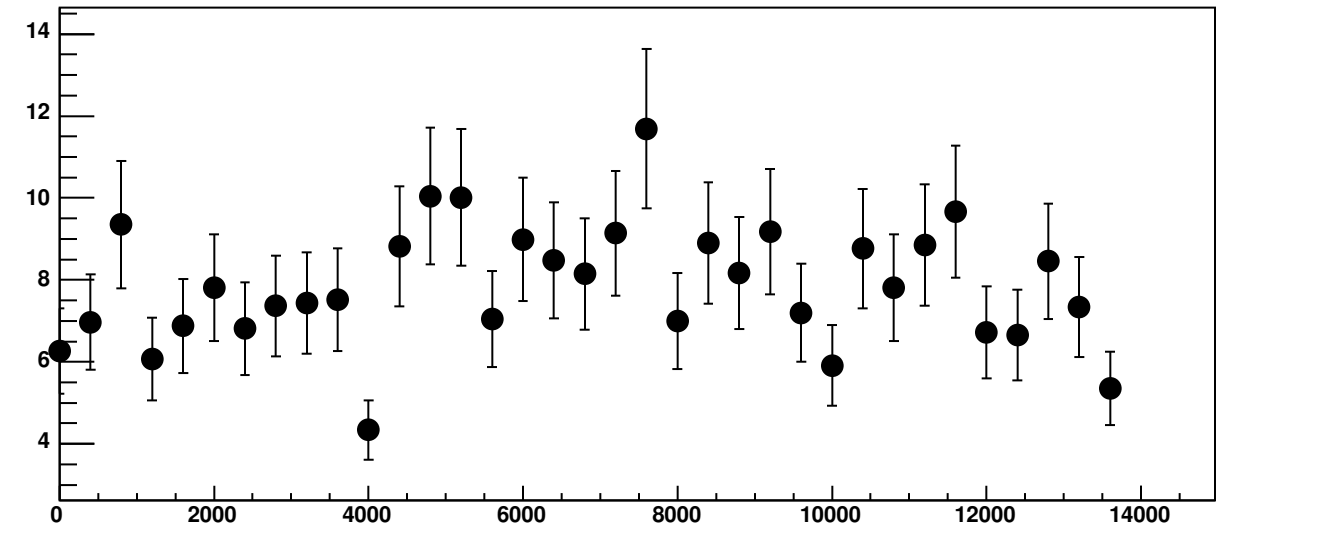

Chip 11, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

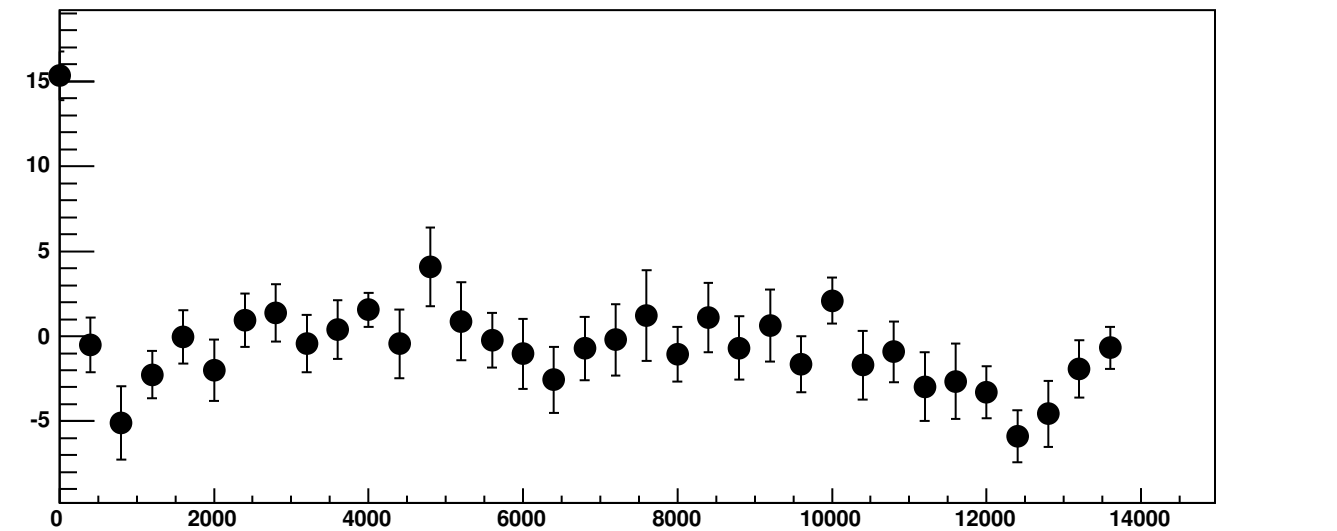
Chip 11, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

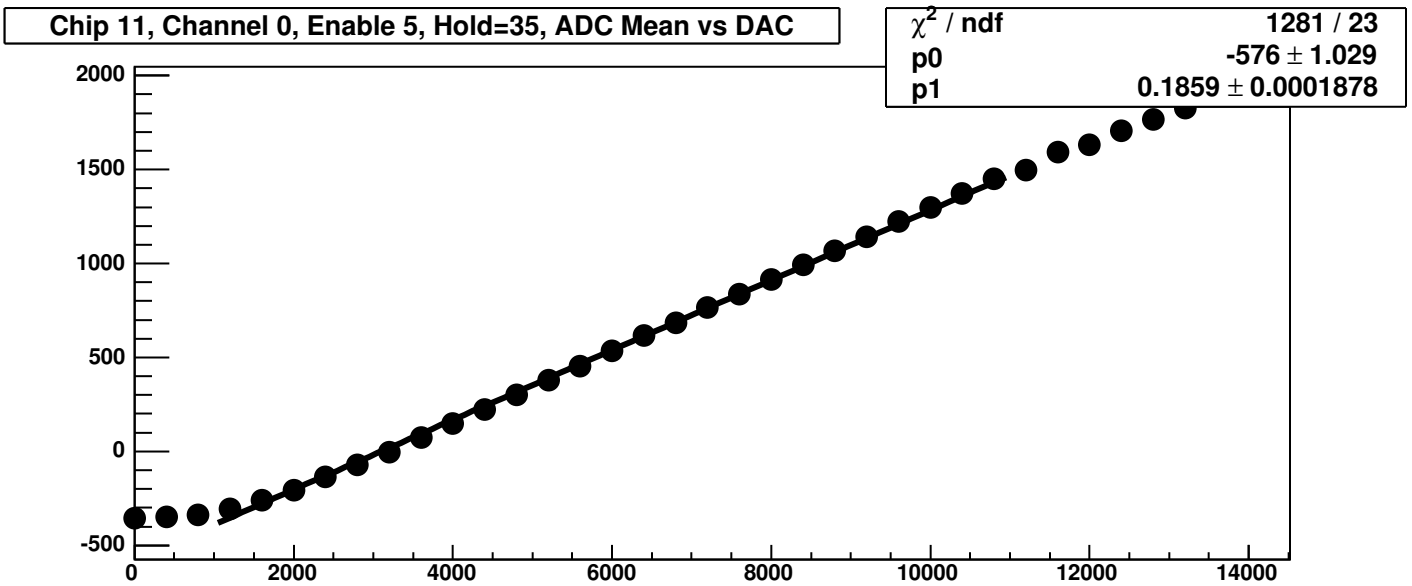


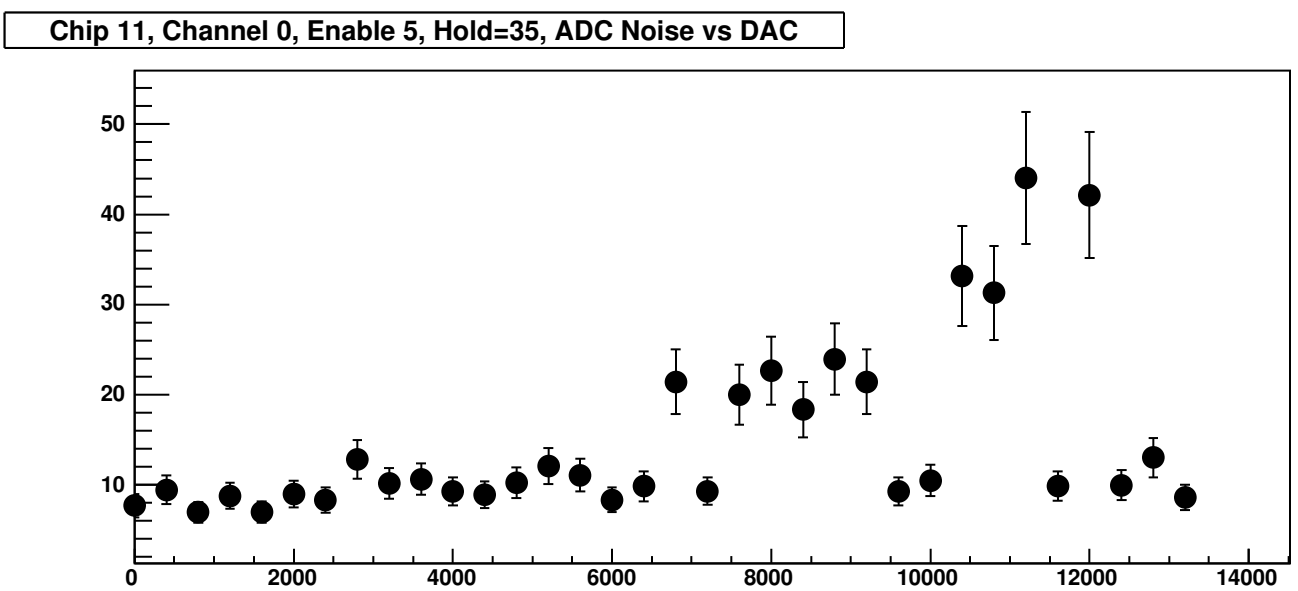
Chip 11, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

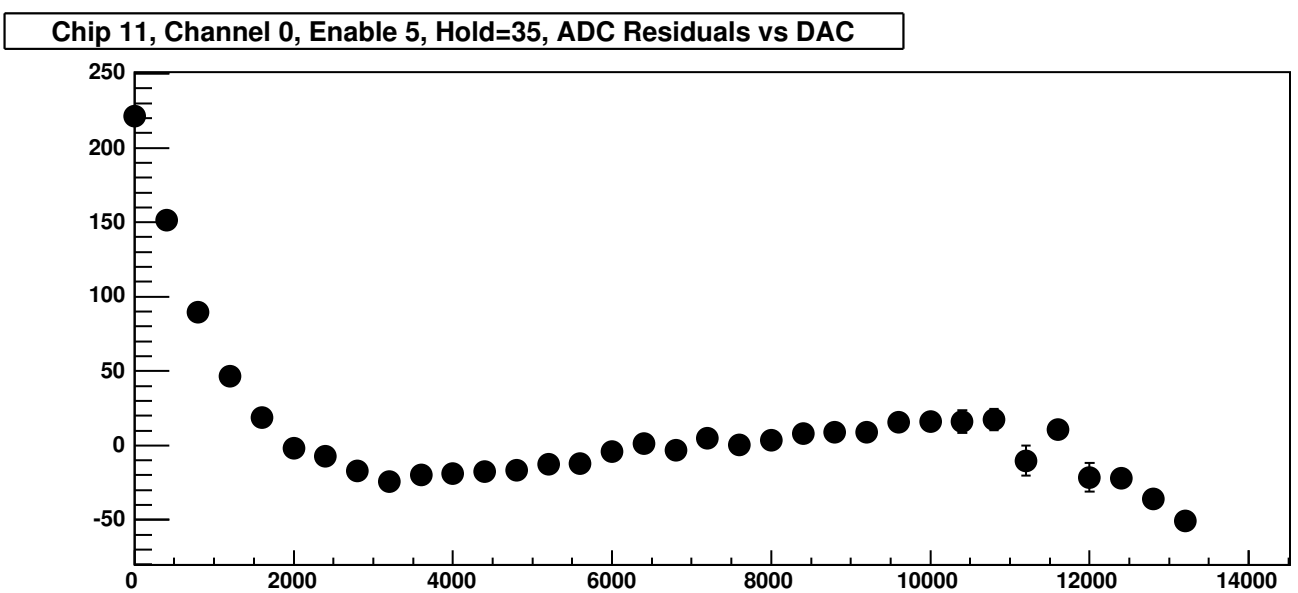


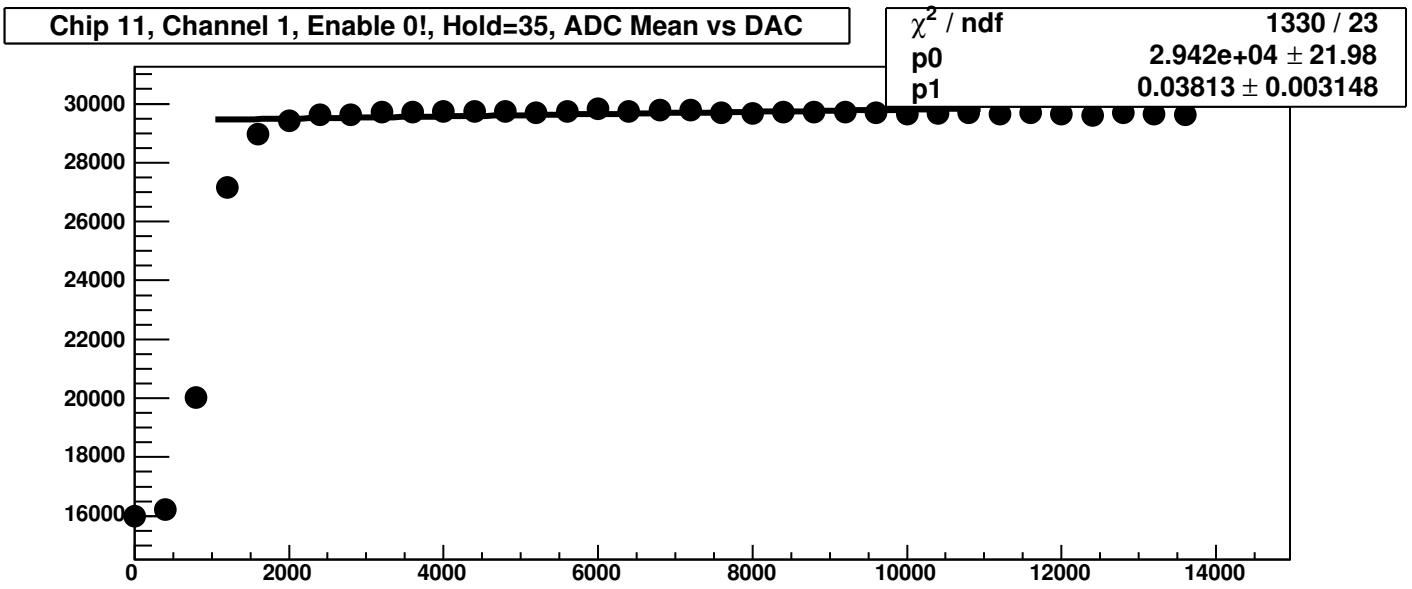
Chip 11, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

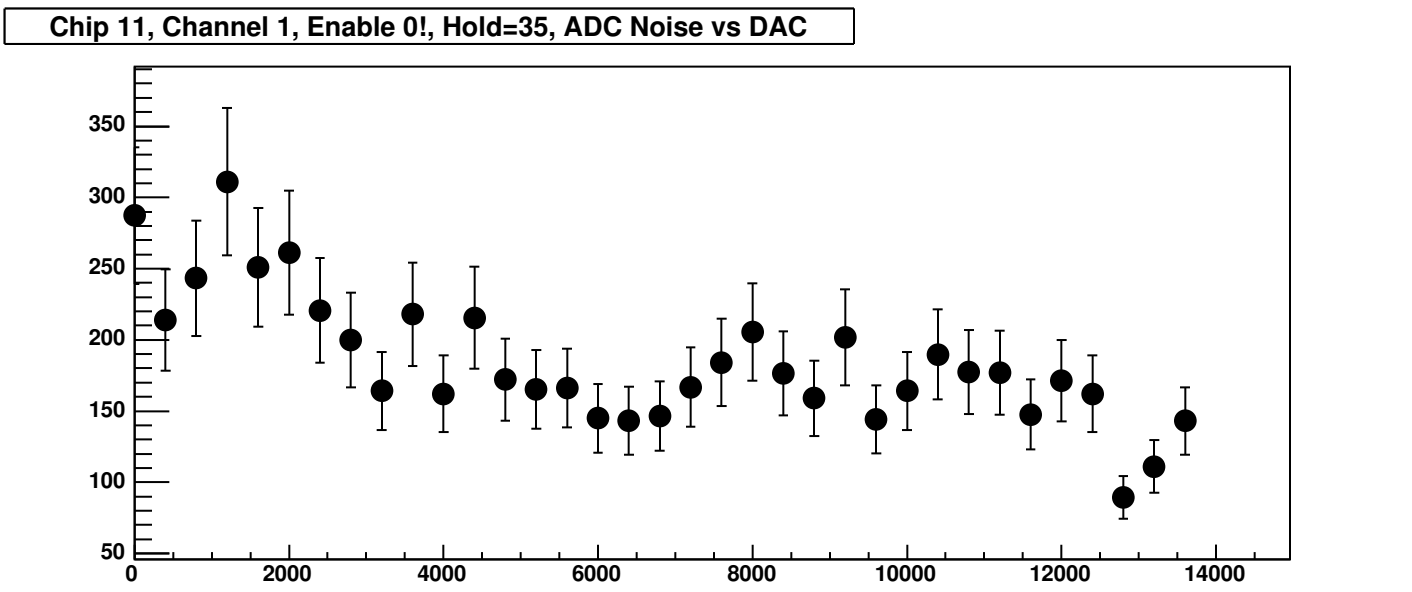


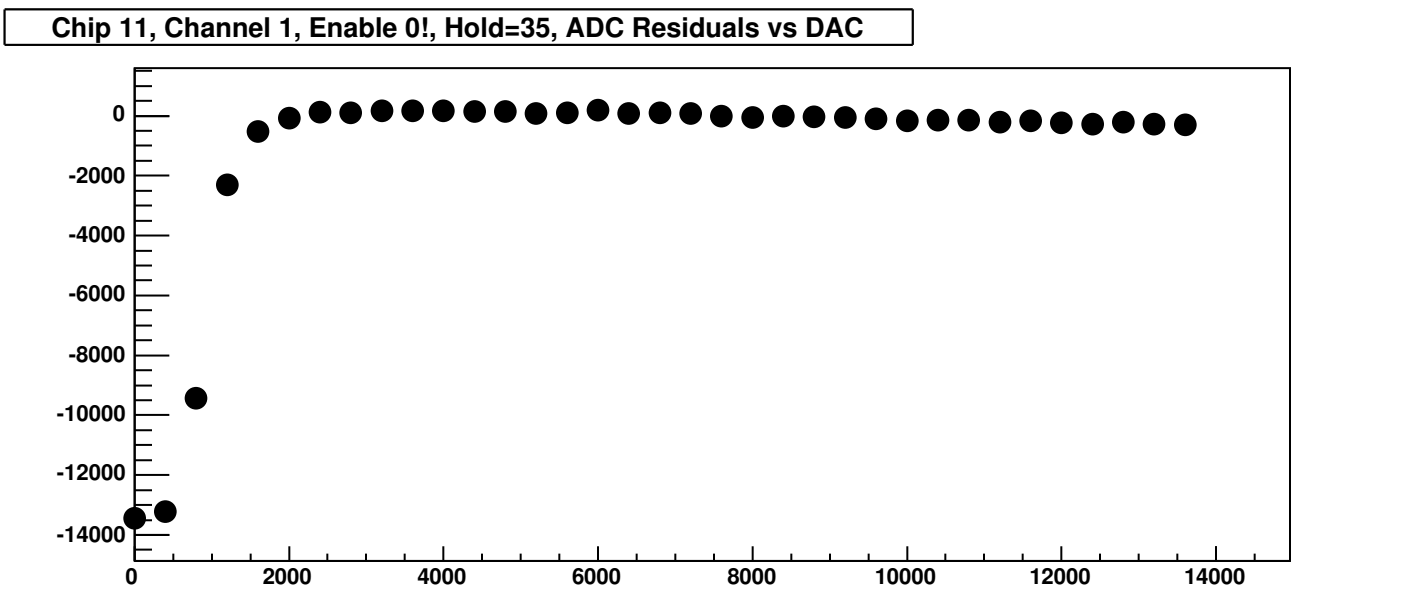

Chip 11, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1281 / 23
p0 -576 ± 1.029
p1 0.1859 ± 0.0001878
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

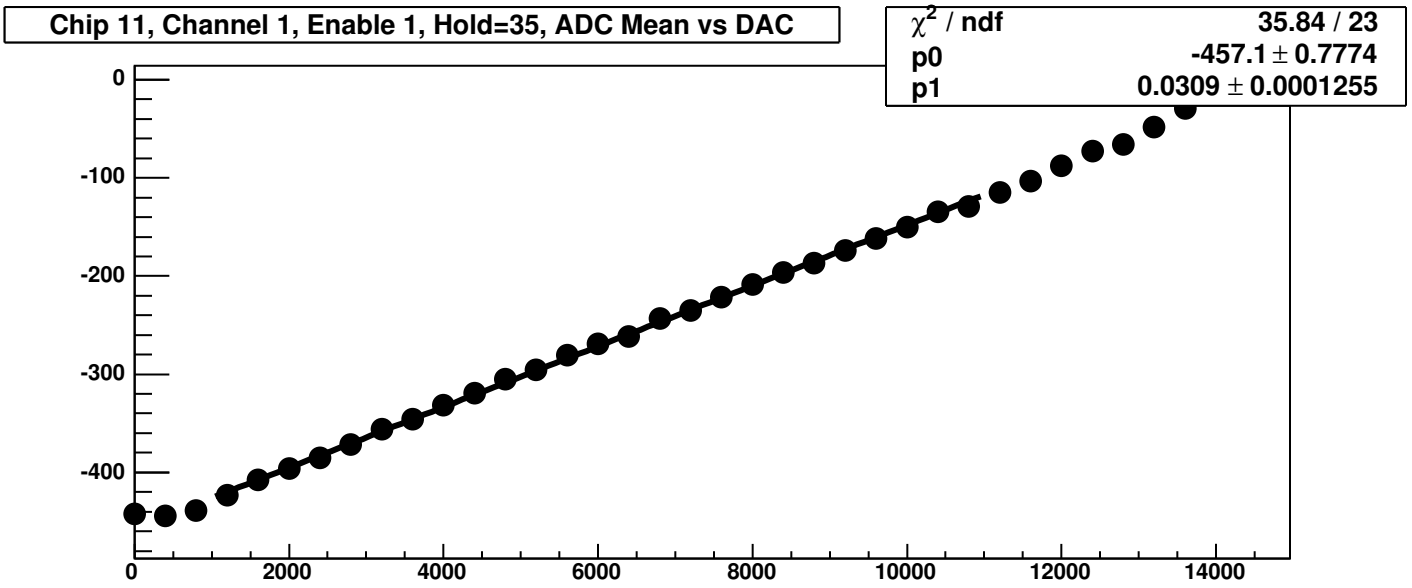

Chip 11, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

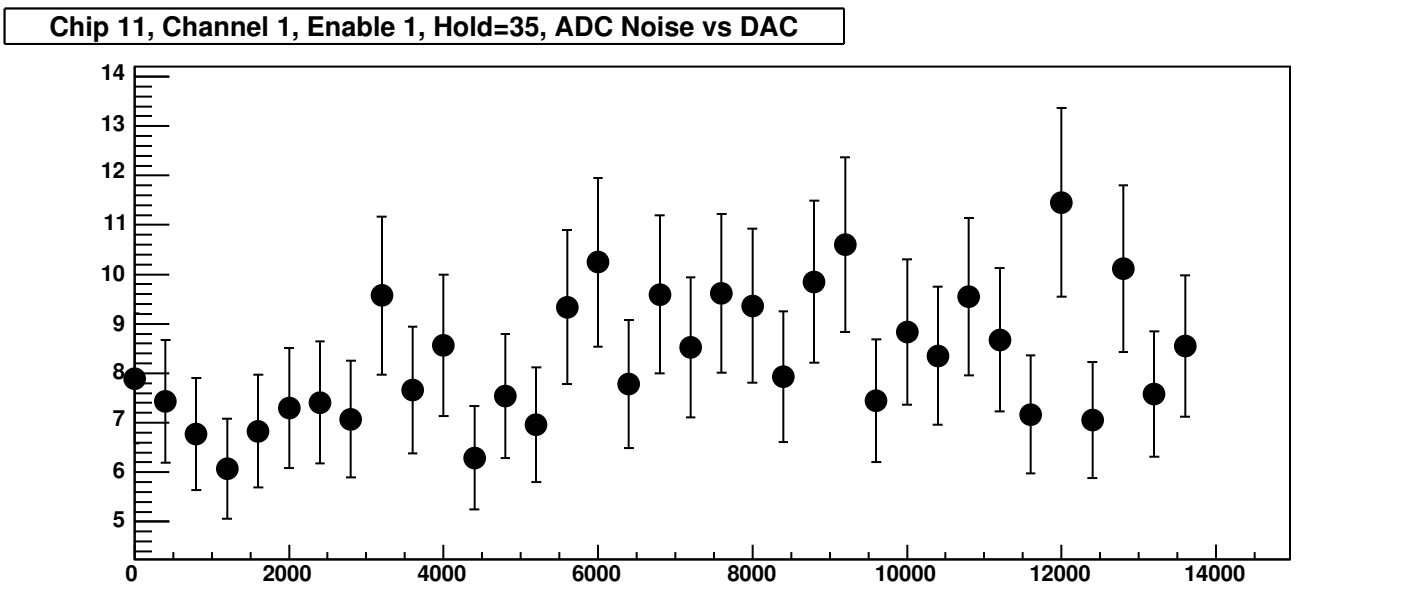

Chip 11, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

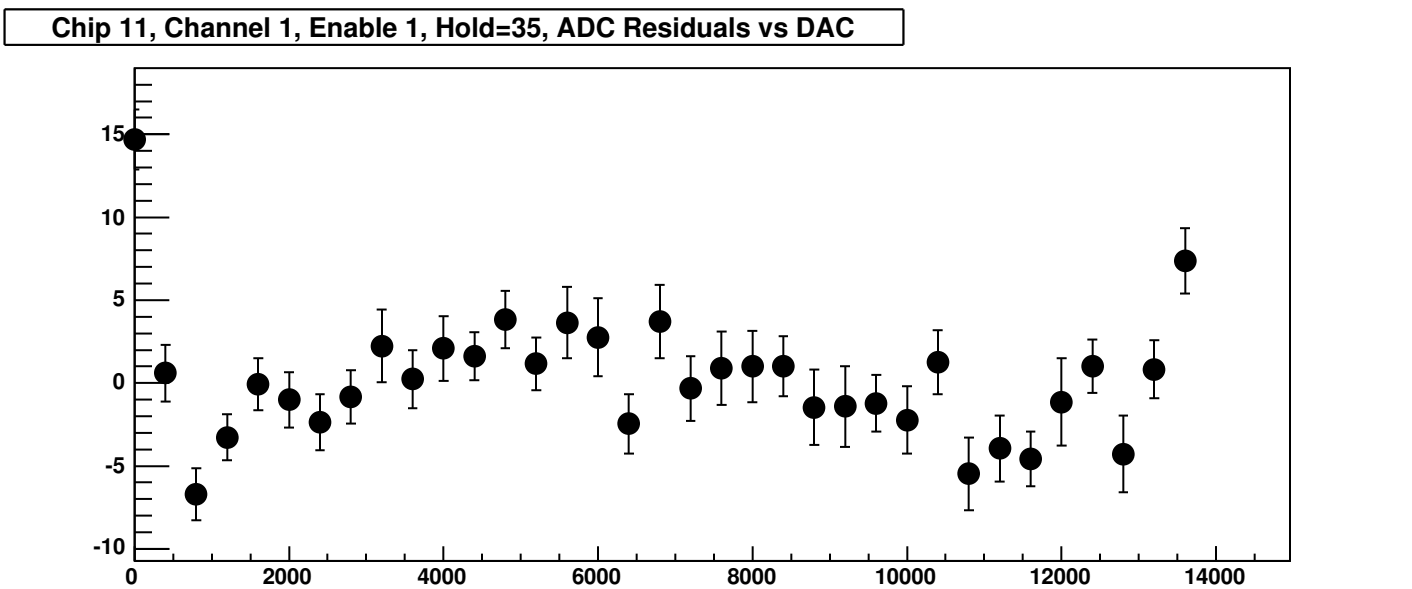

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1330 / 23
p0 2.942e+04 ± 21.98
p1 0.03813 ± 0.003148
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

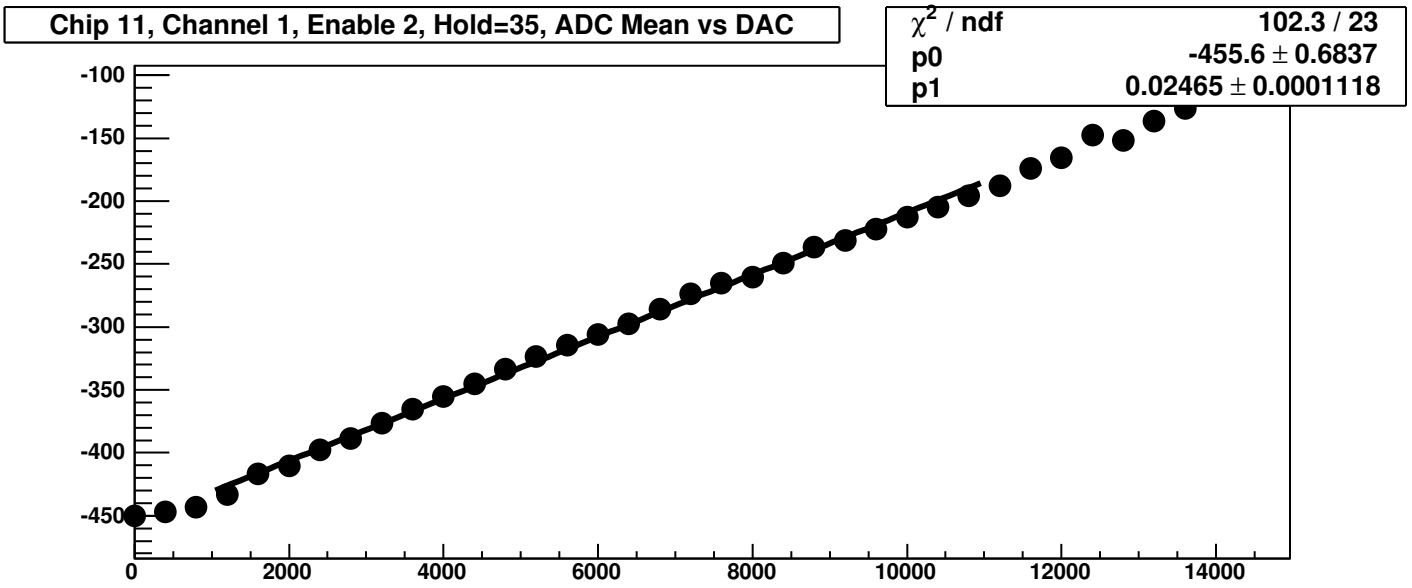

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

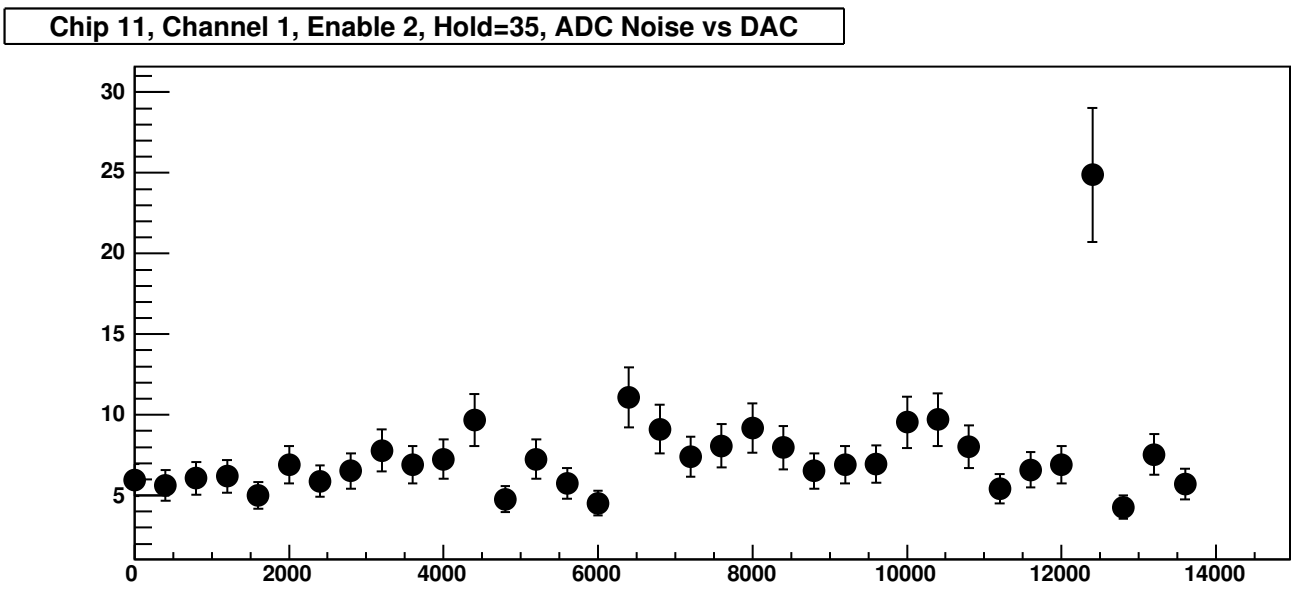

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

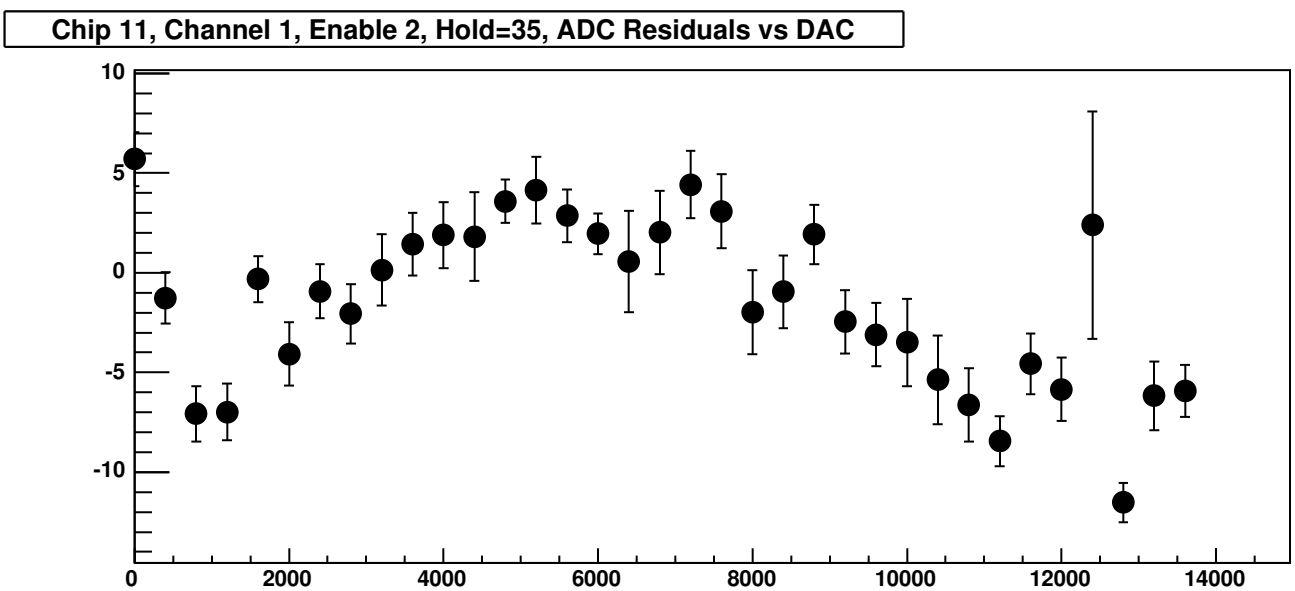

Chip 11, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.84 / 23
p0 -457.1 ± 0.7774
p1 0.0309 ± 0.0001255
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

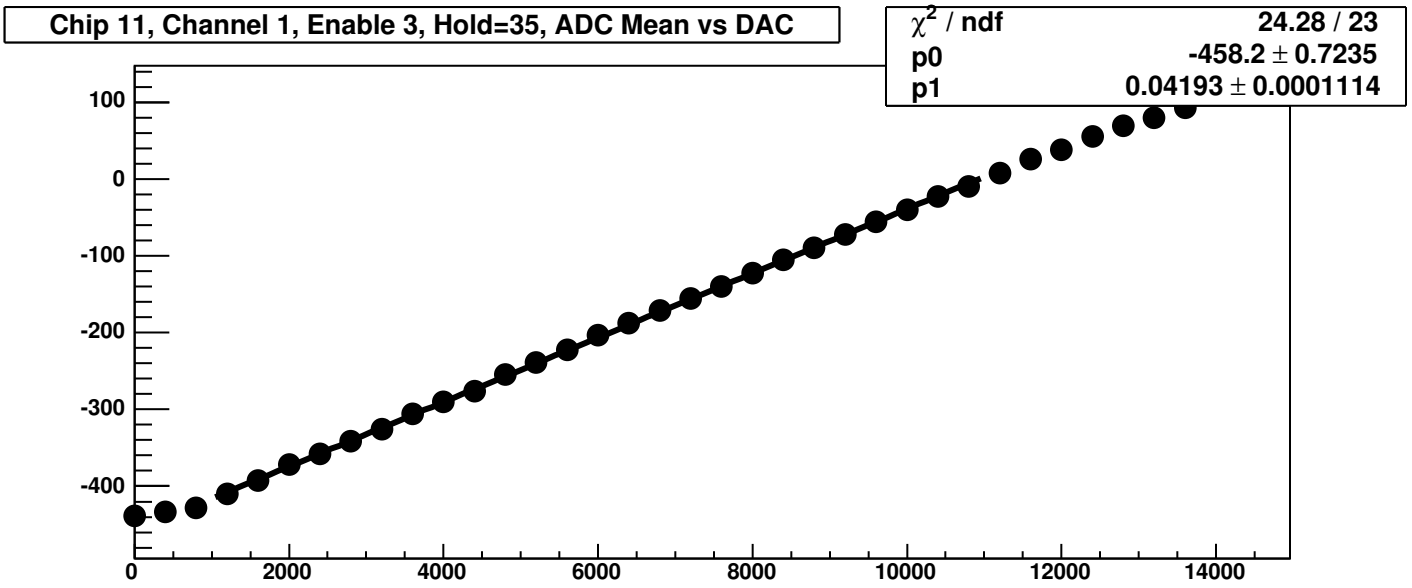

Chip 11, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

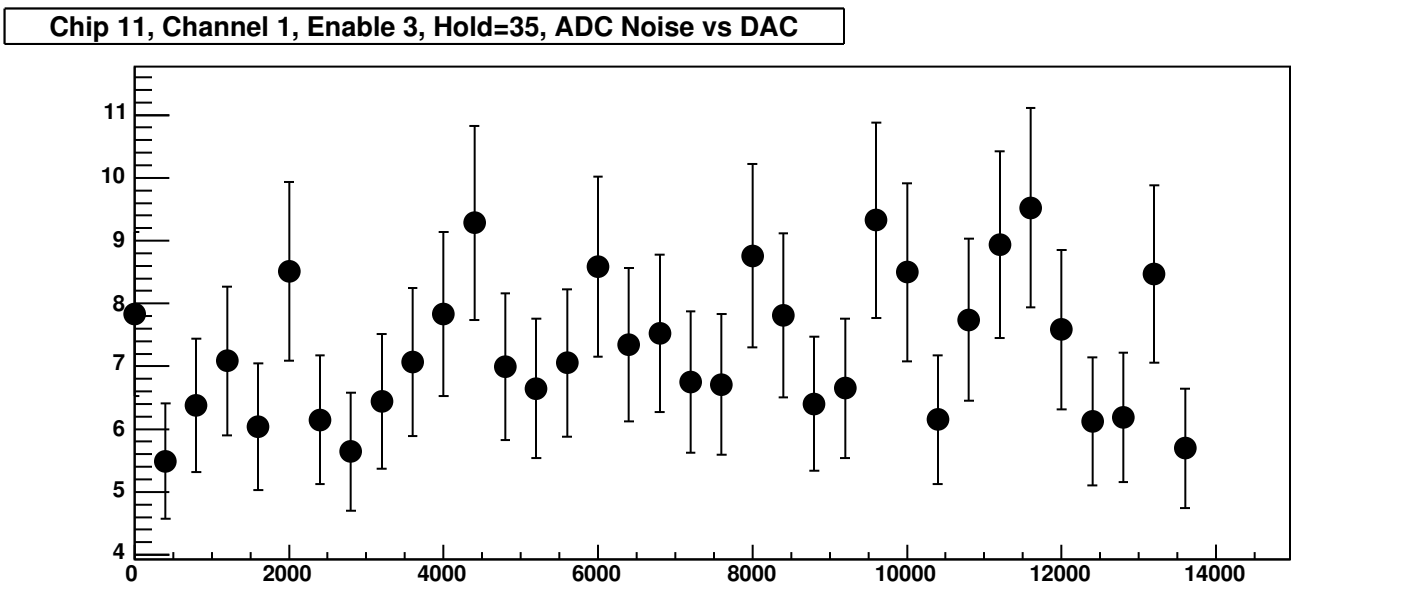

Chip 11, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

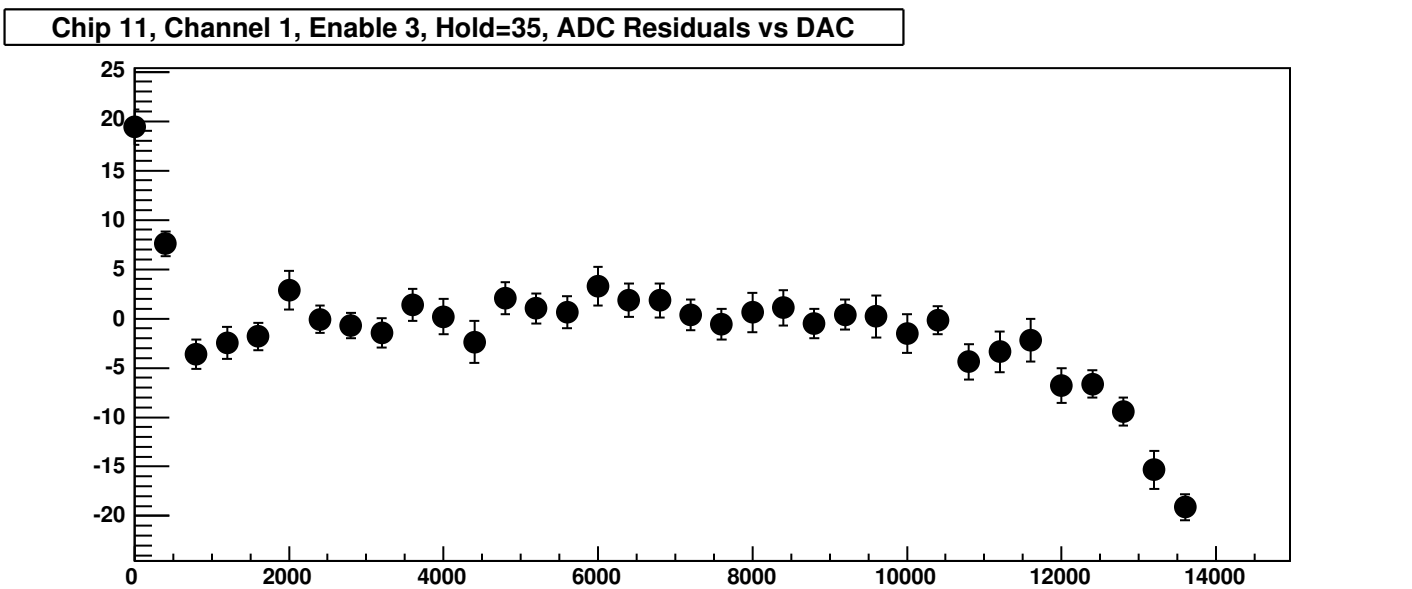

Chip 11, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 102.3 / 23
p0 -455.6 ± 0.6837
p1 0.02465 ± 0.0001118
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

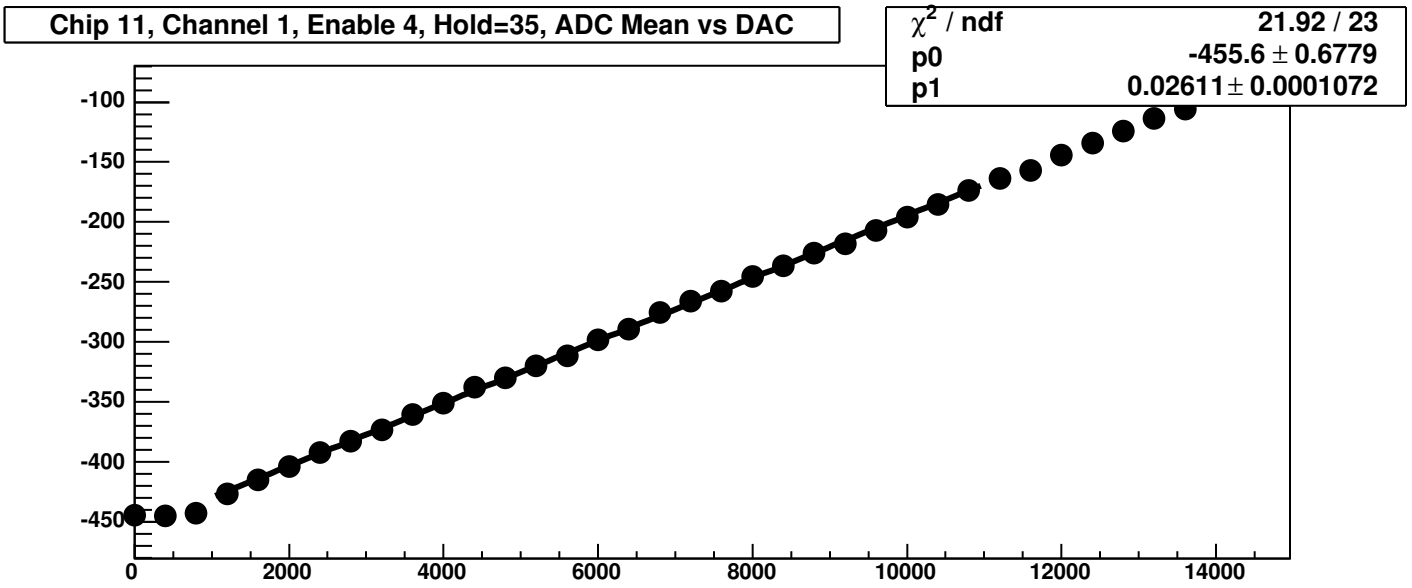

Chip 11, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

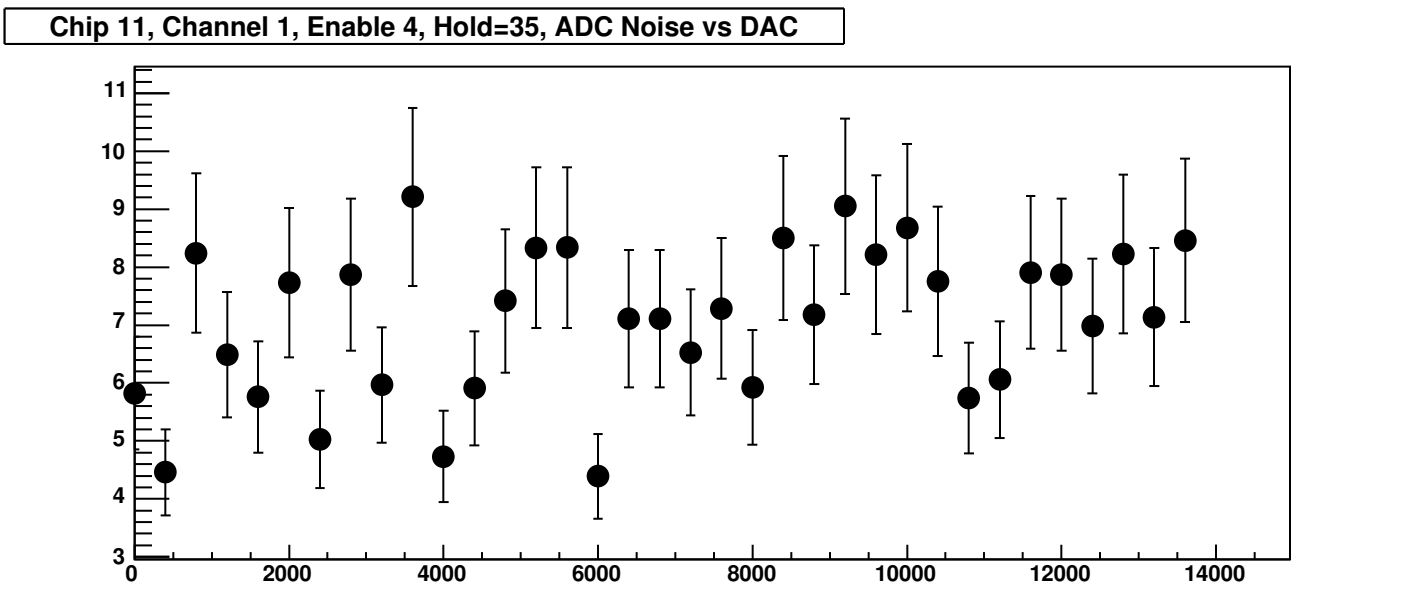

Chip 11, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

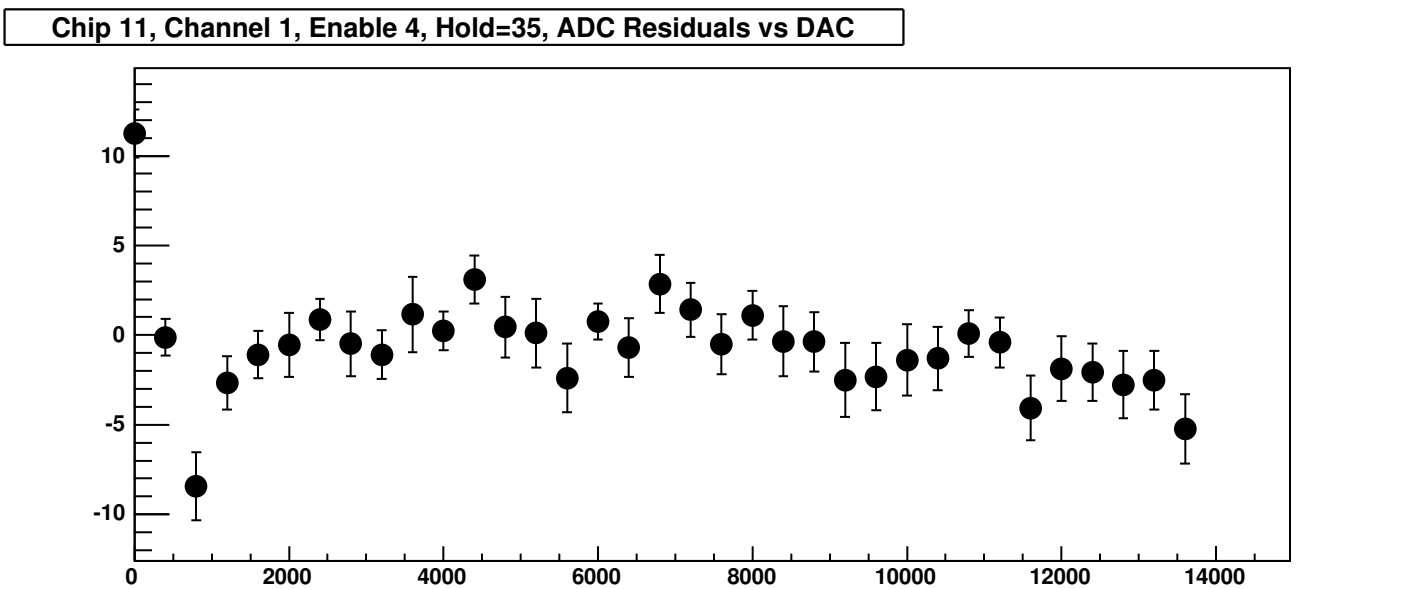

Chip 11, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.28 / 23
p0 -458.2 ± 0.7235
p1 0.04193 ± 0.0001114
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

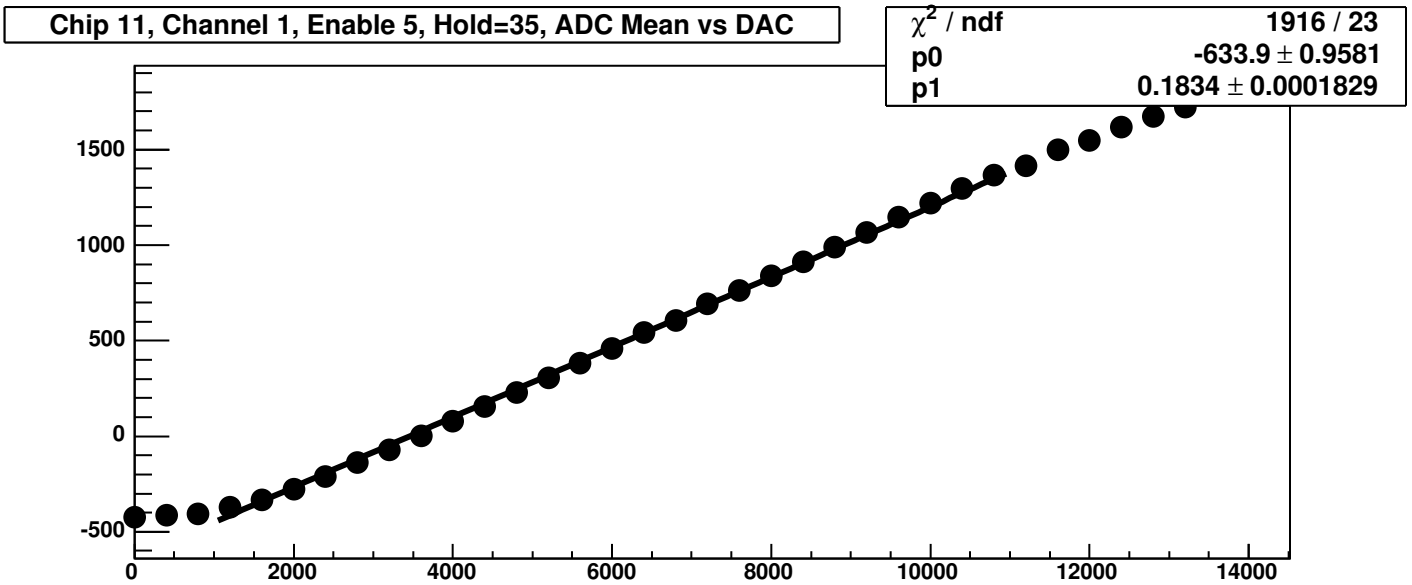

Chip 11, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

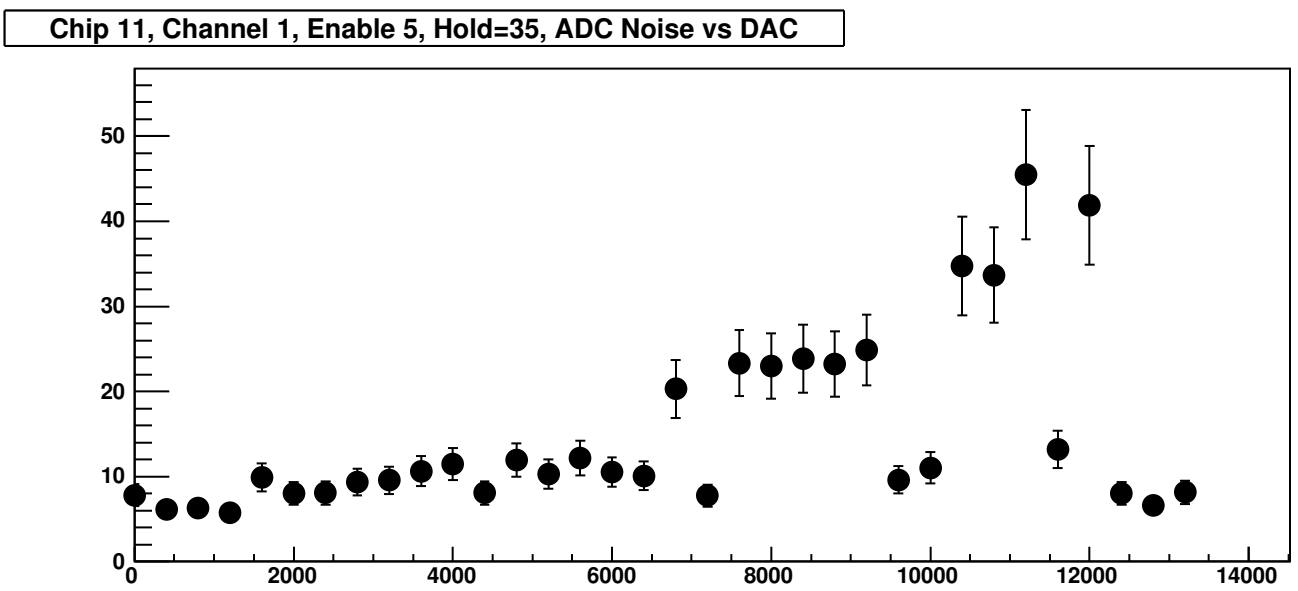

Chip 11, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

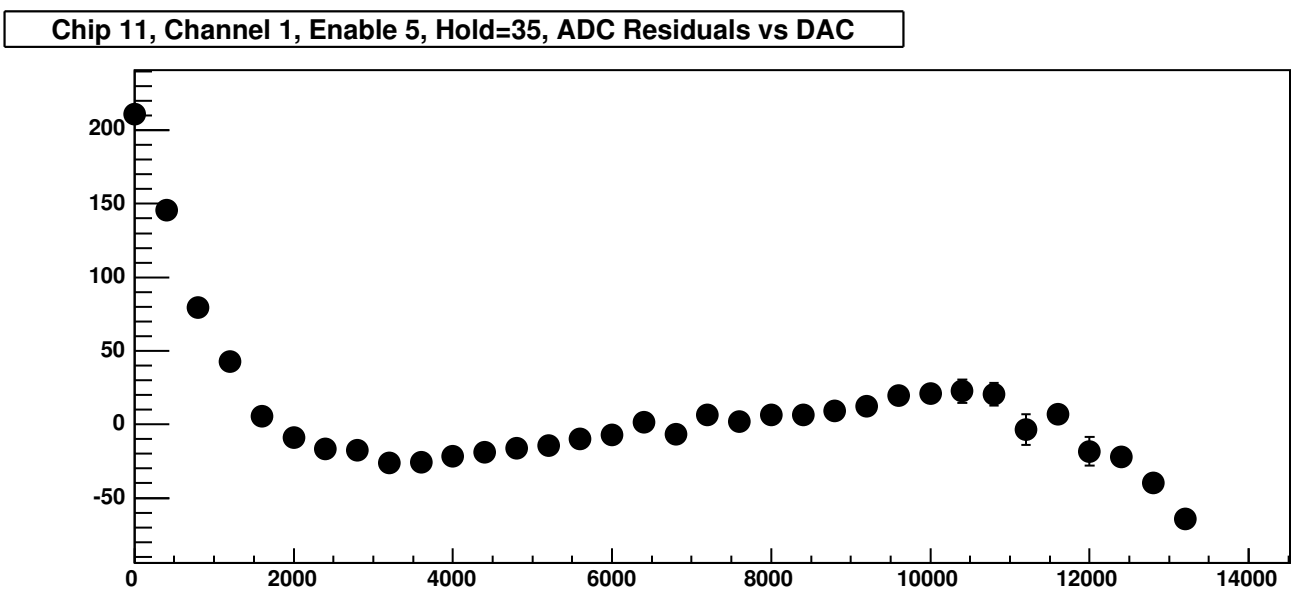

Chip 11, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.92 / 23
p0 -455.6 ± 0.6779
p1 0.02611 ± 0.0001072
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

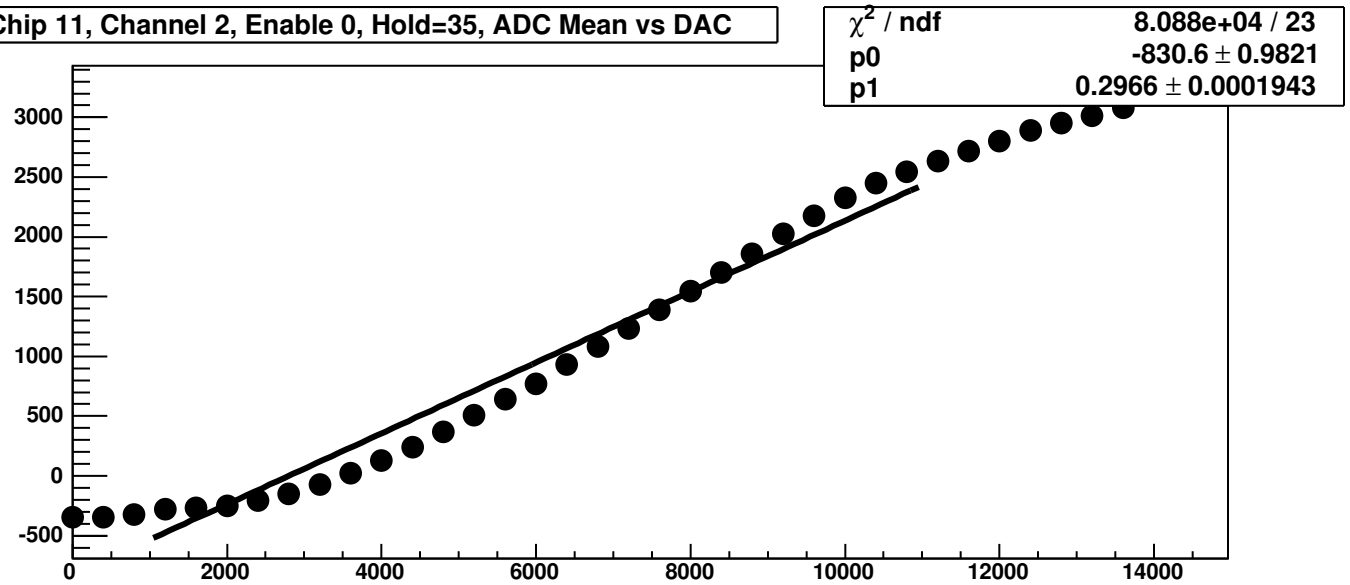

Chip 11, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1916 / 23
p0 -633.9 ± 0.9581
p1 0.1834 ± 0.0001829
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

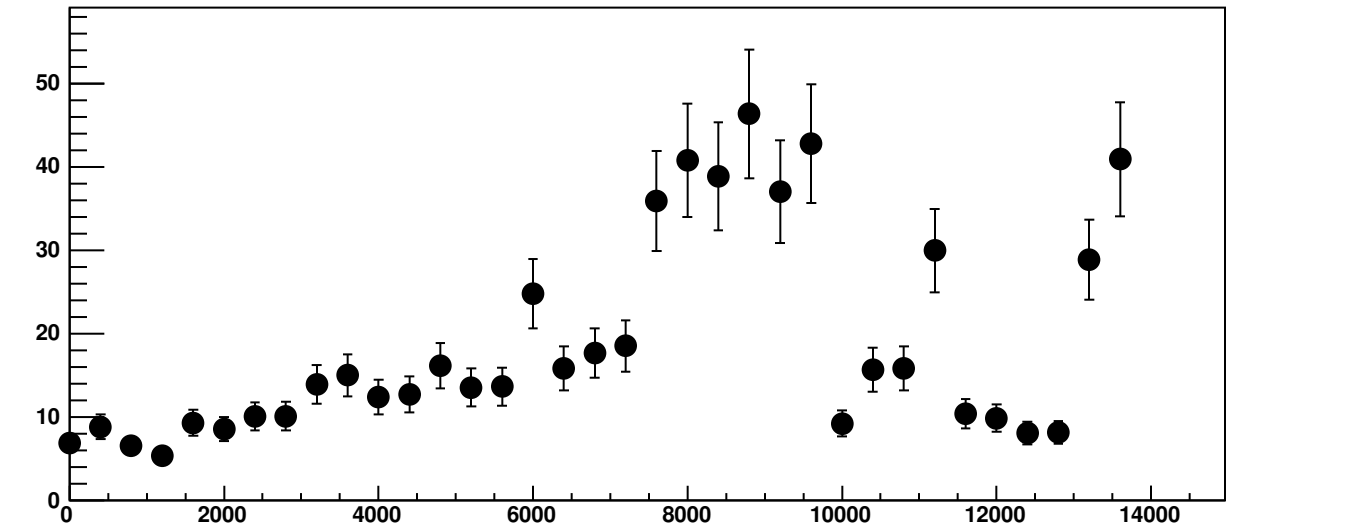

Chip 11, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

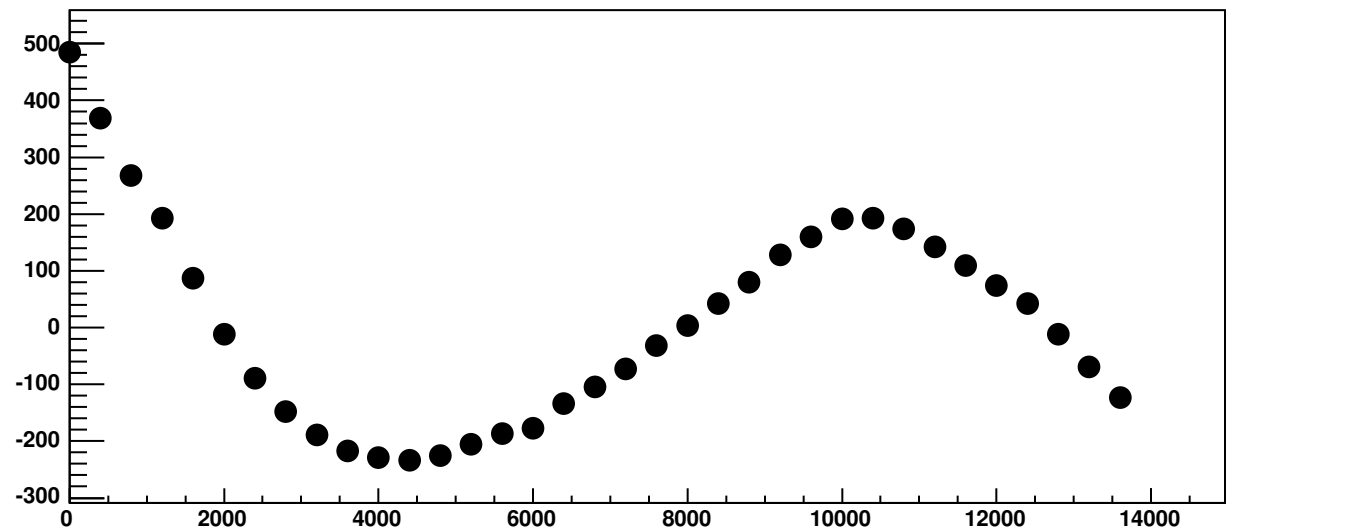
Chip 11, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

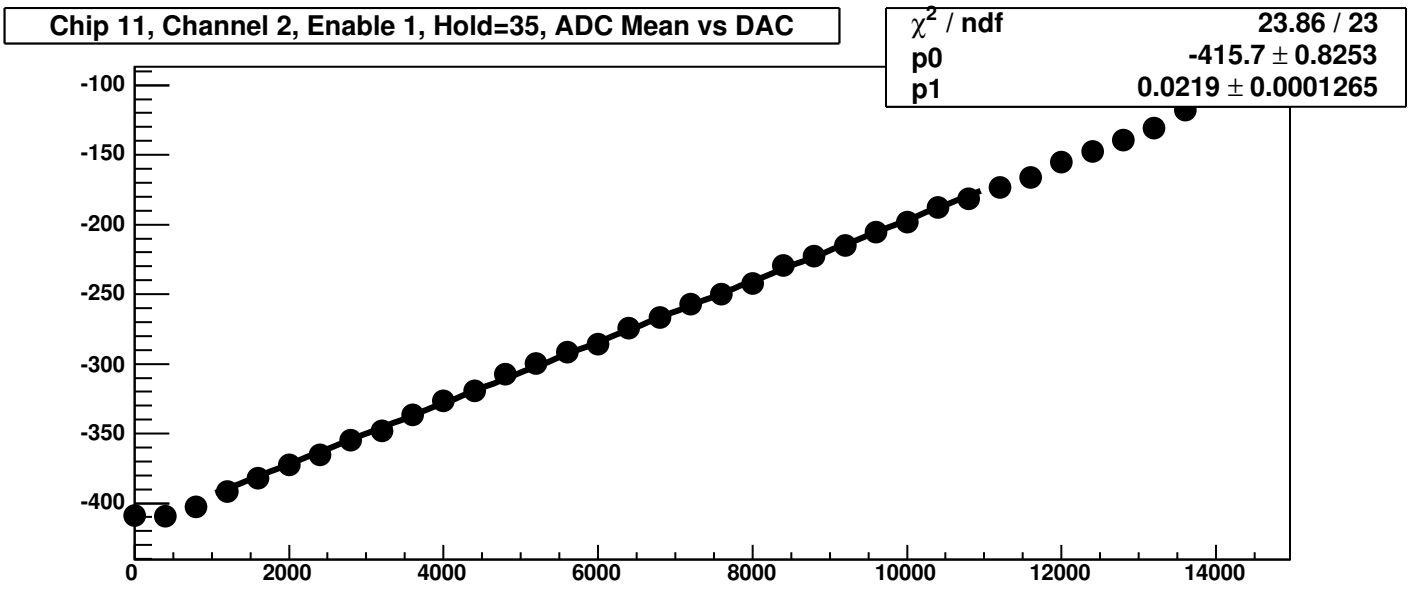


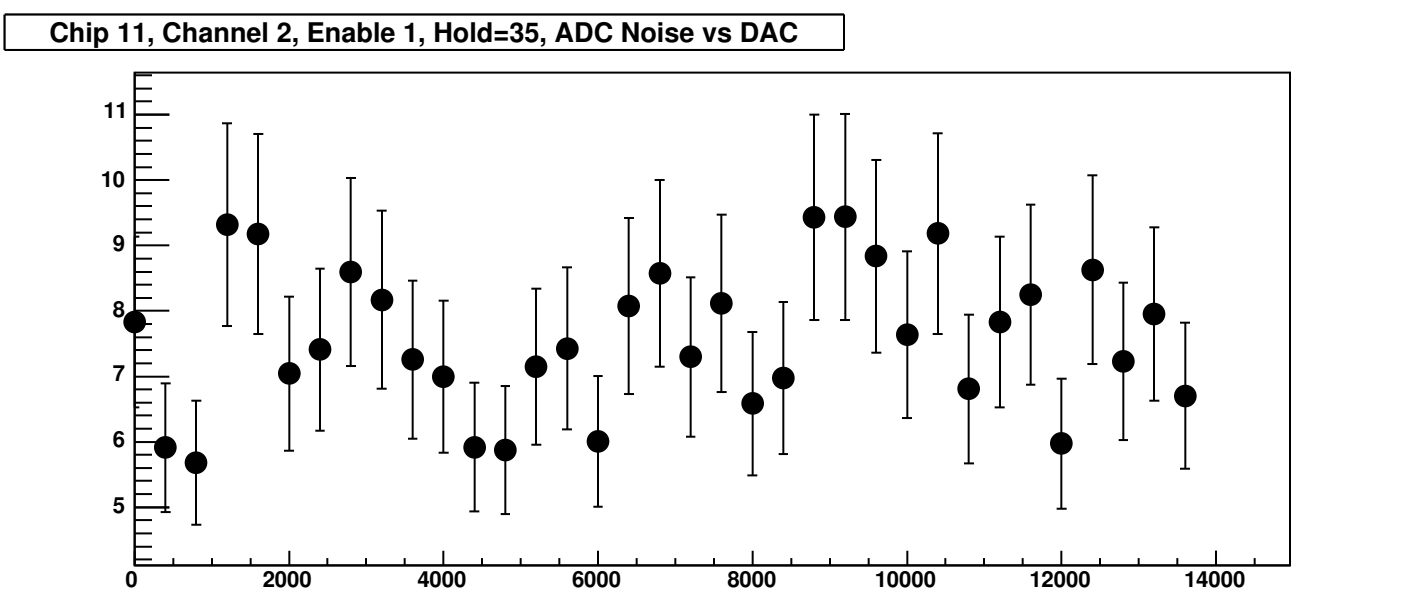
Chip 11, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC

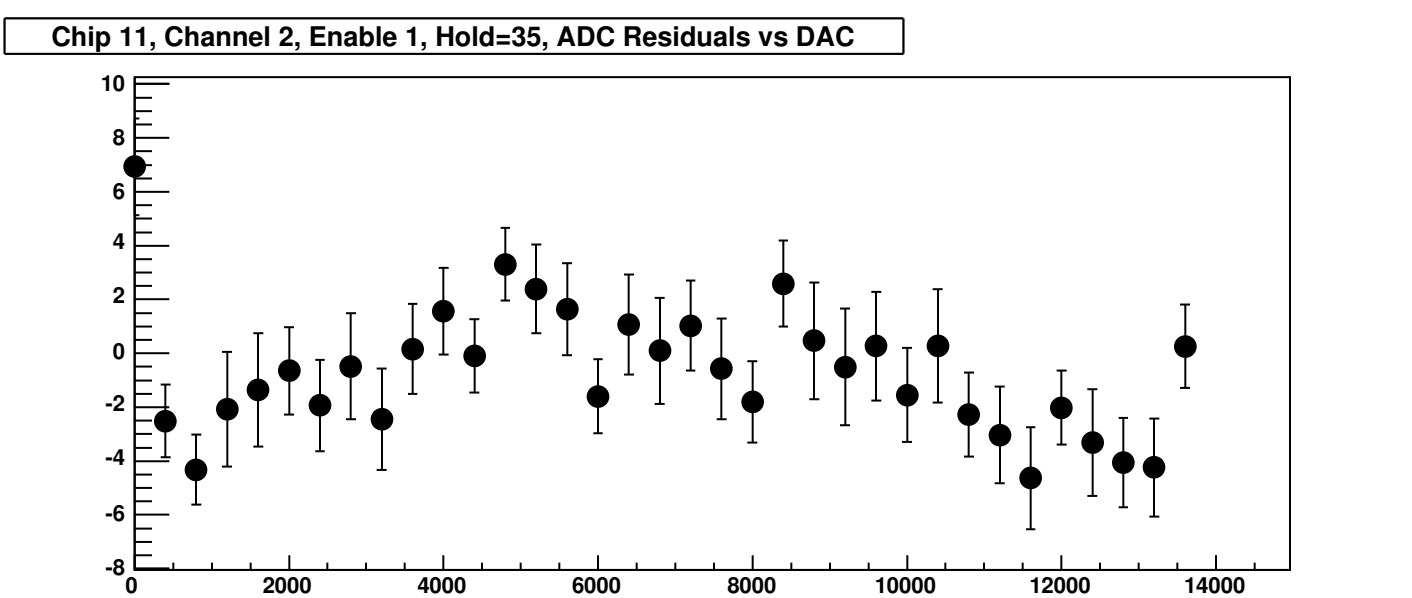


Chip 11, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

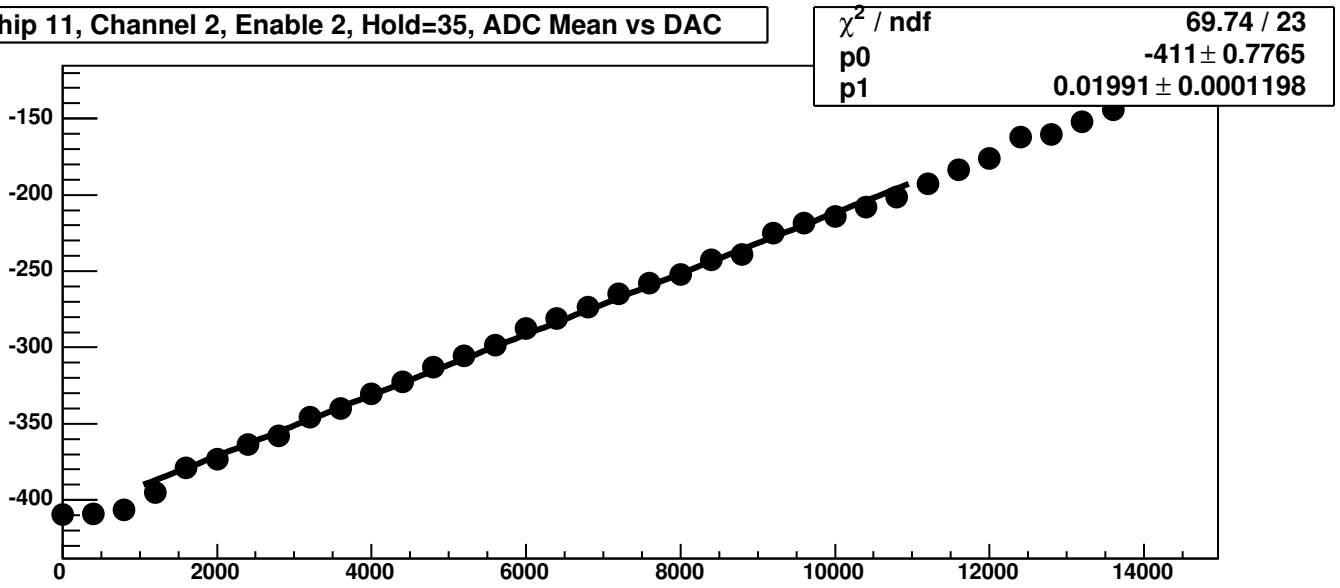



Chip 11, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.86 / 23
p0 -415.7 ± 0.8253
p1 0.0219 ± 0.0001265
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

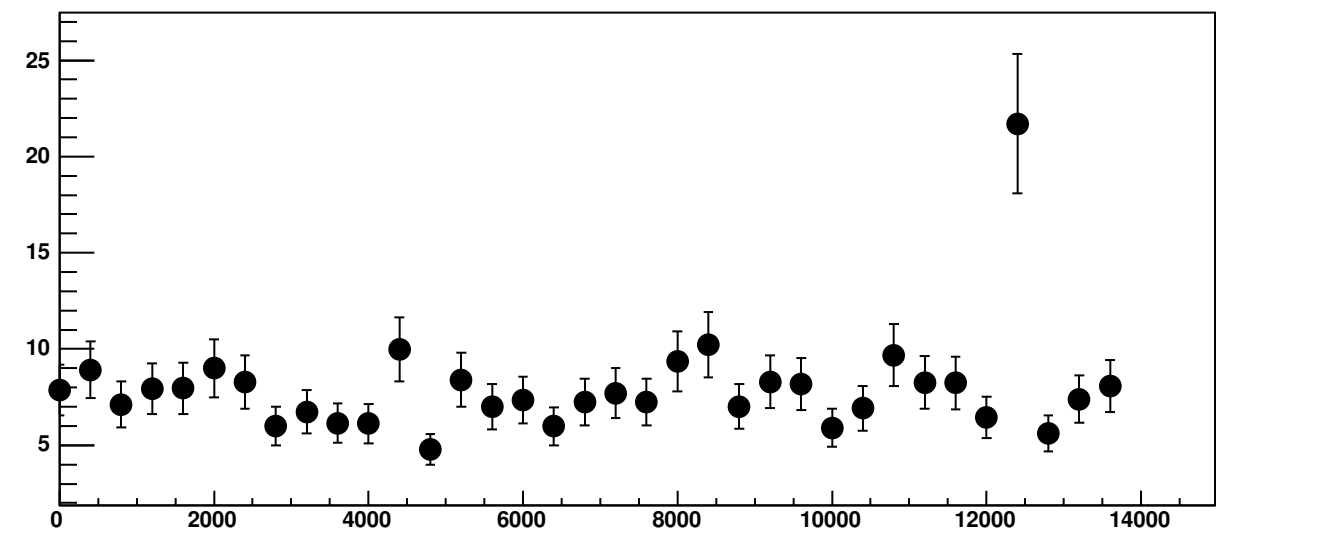

Chip 11, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

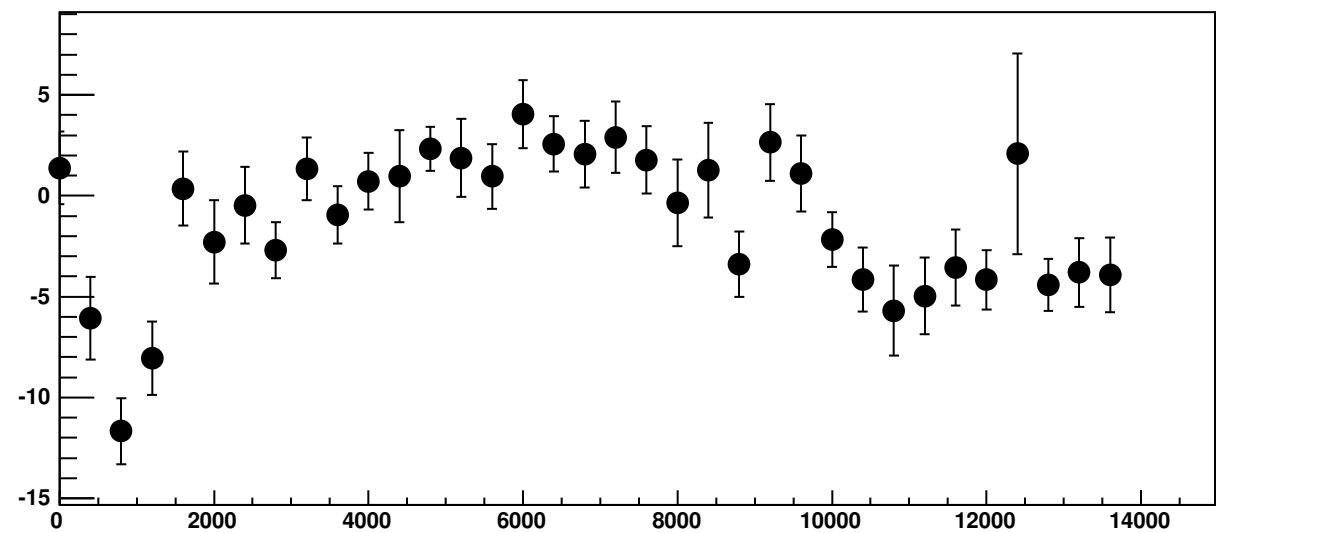
Chip 11, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

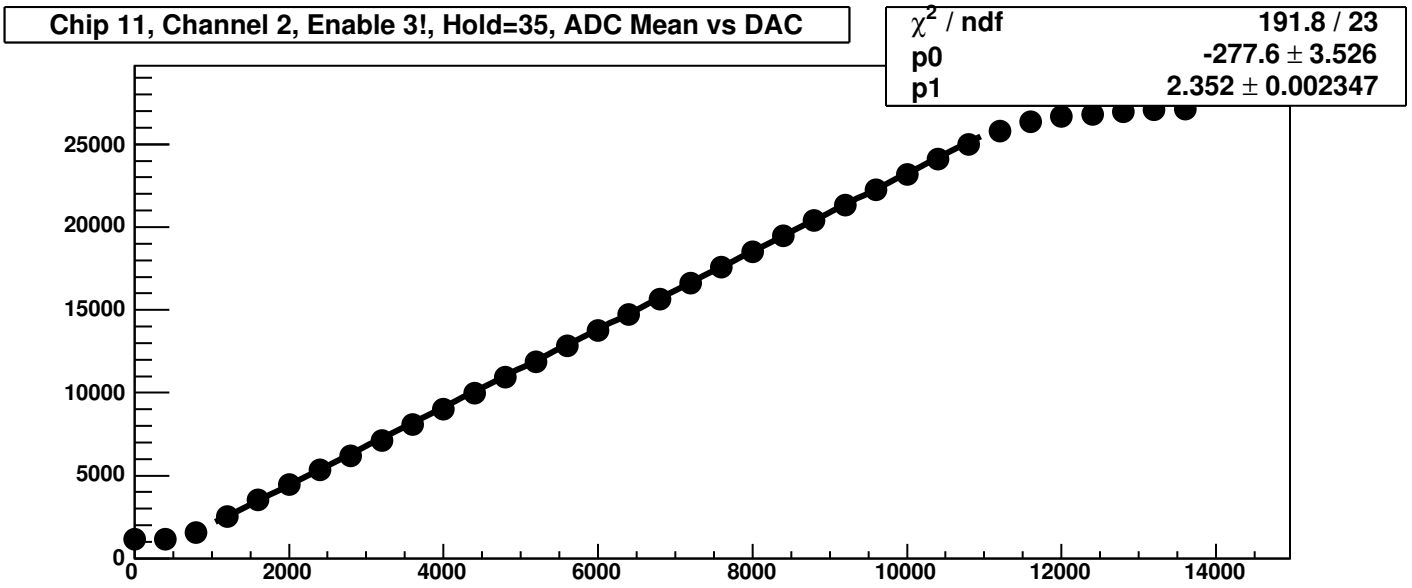


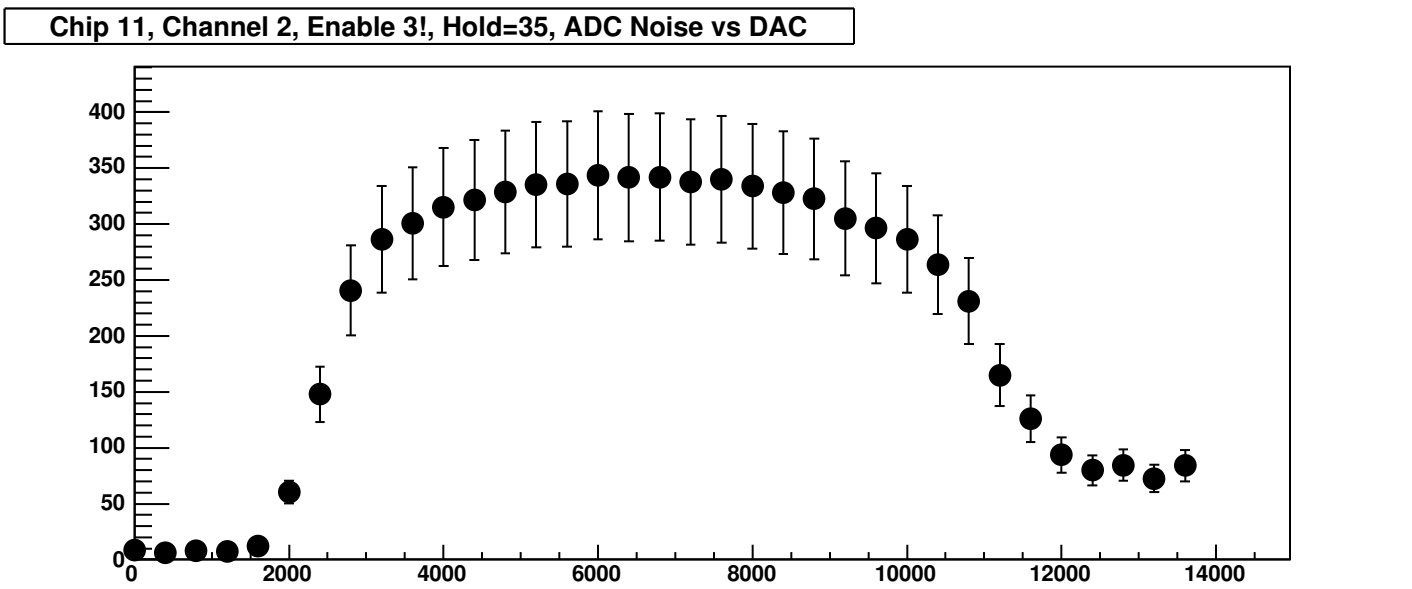
Chip 11, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

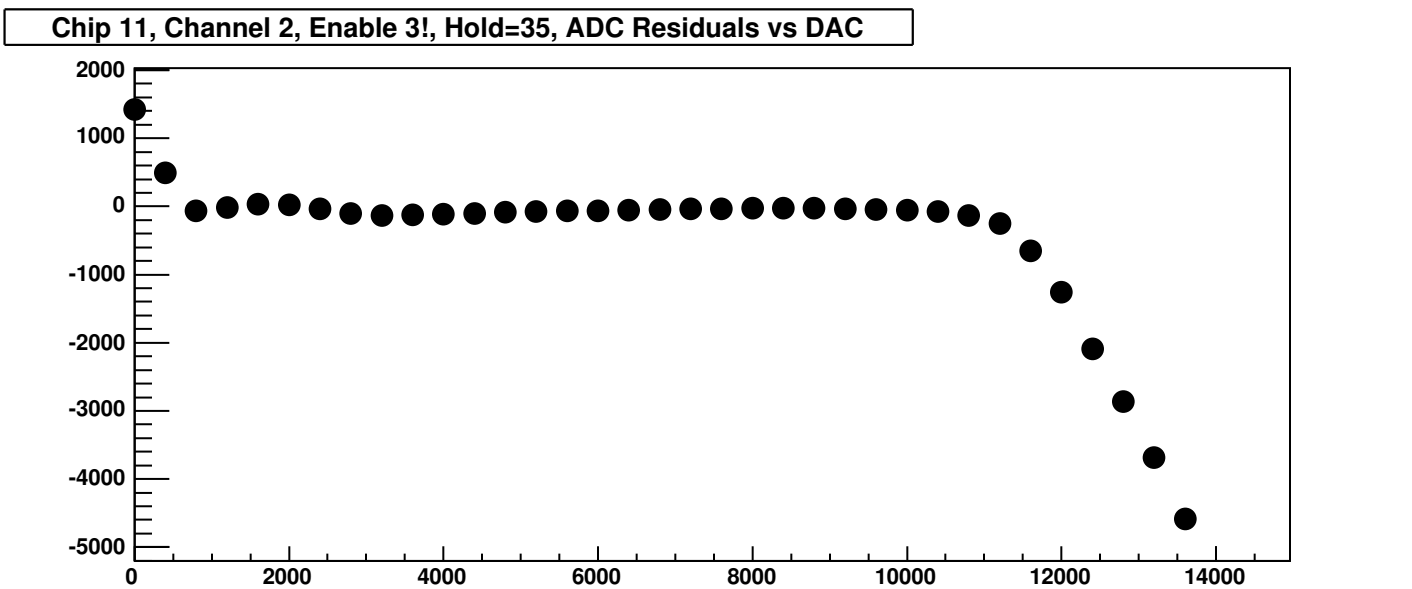


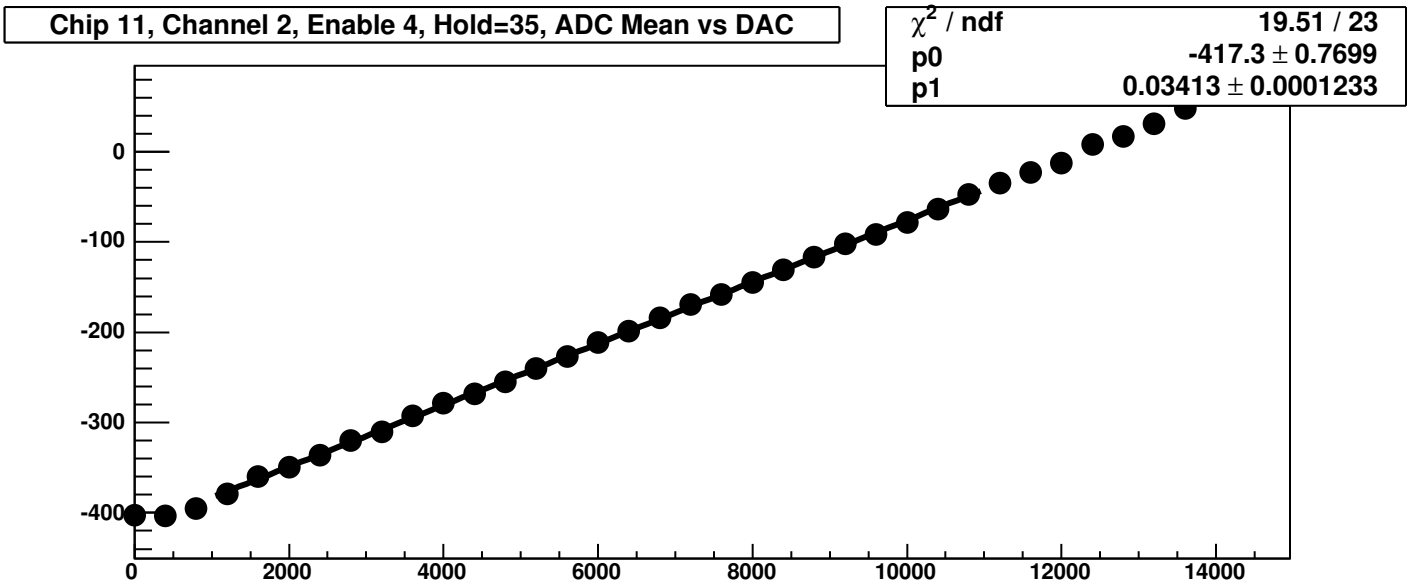
Chip 11, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

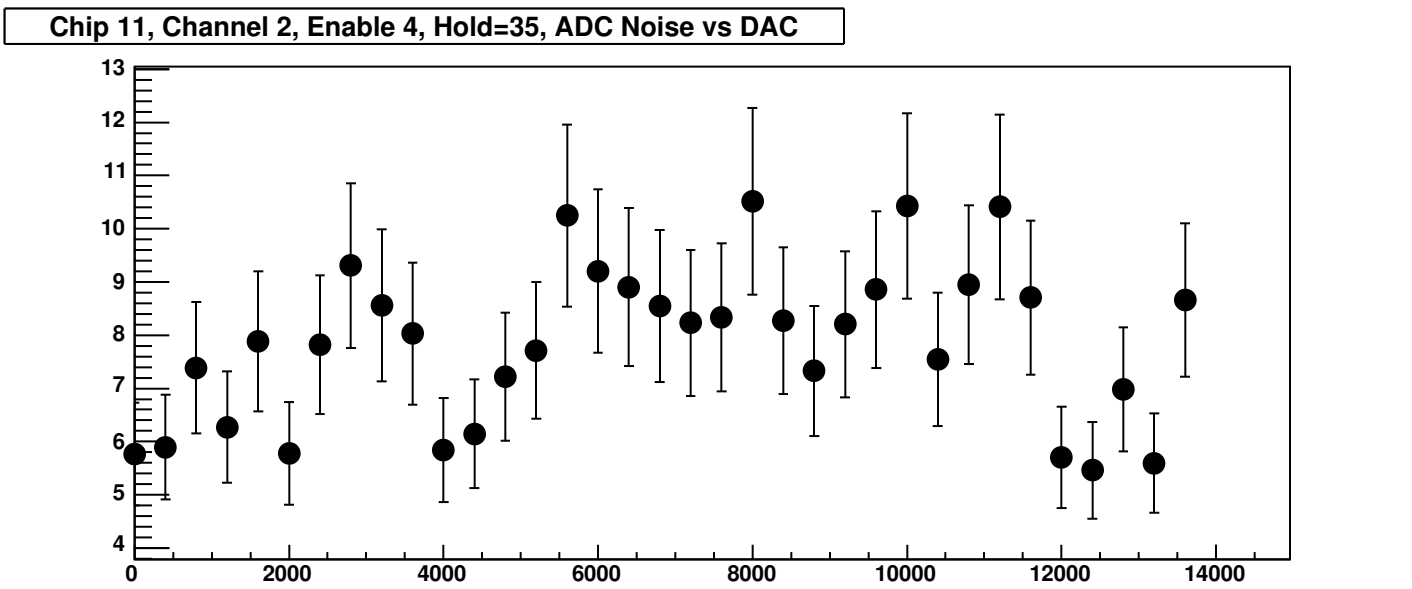


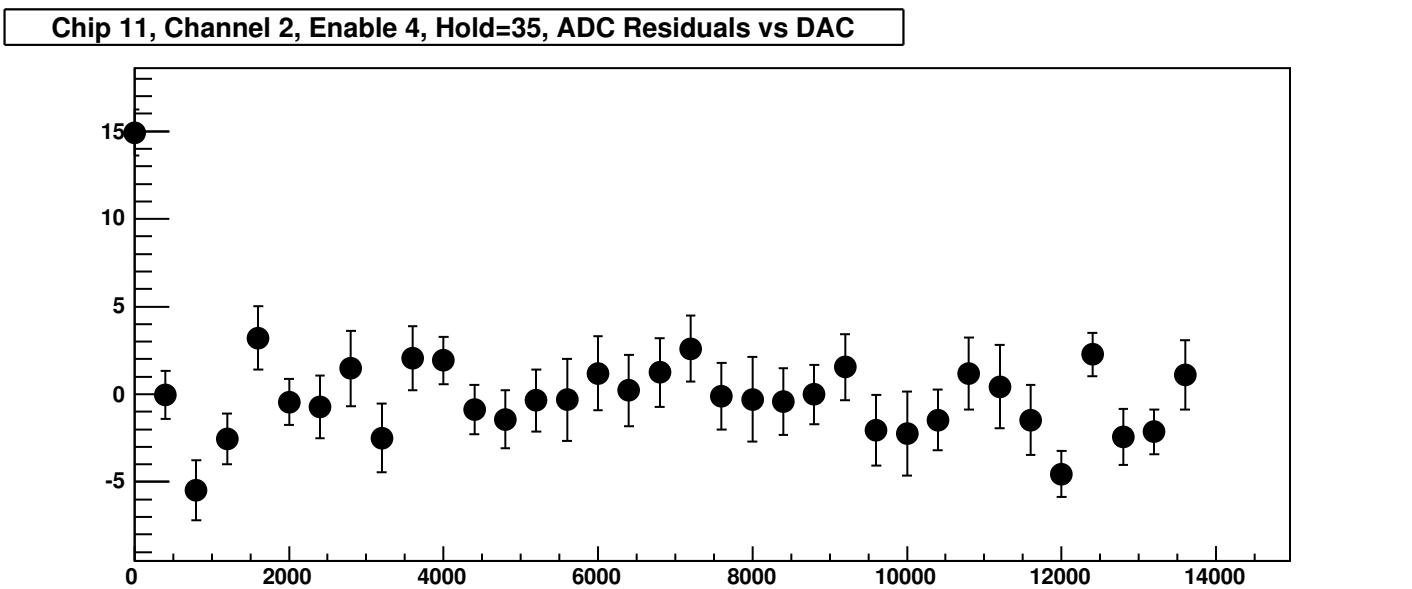

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 191.8 / 23
p0 -277.6 ± 3.526
p1 2.352 ± 0.002347
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

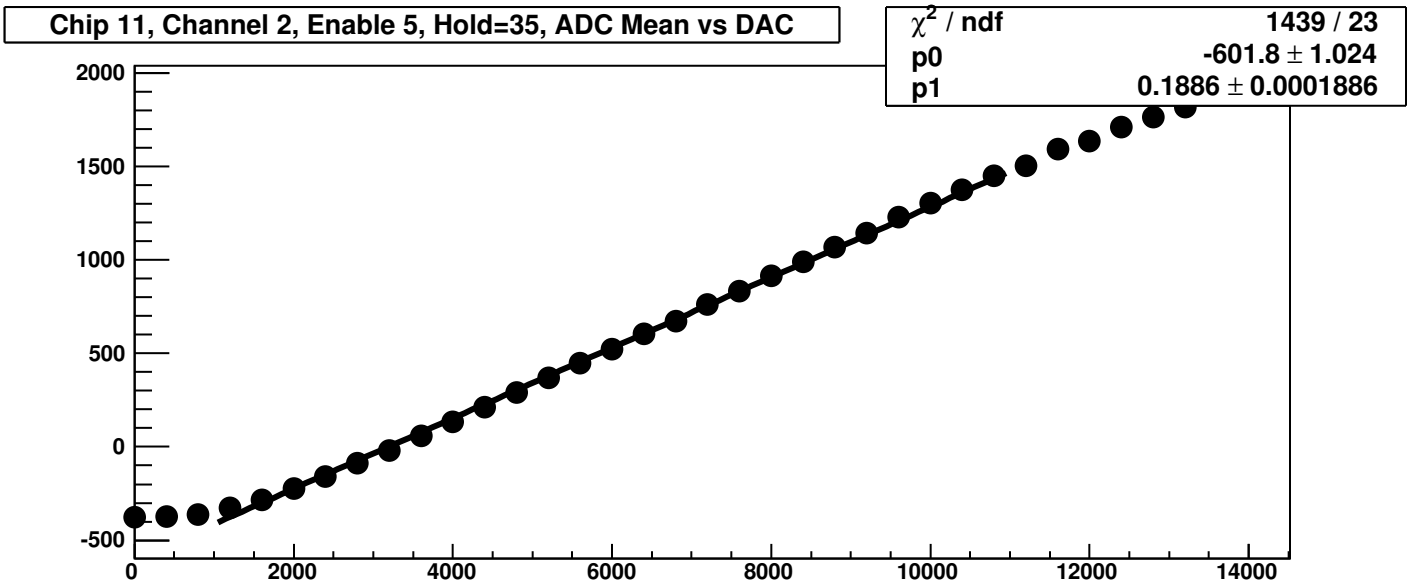

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

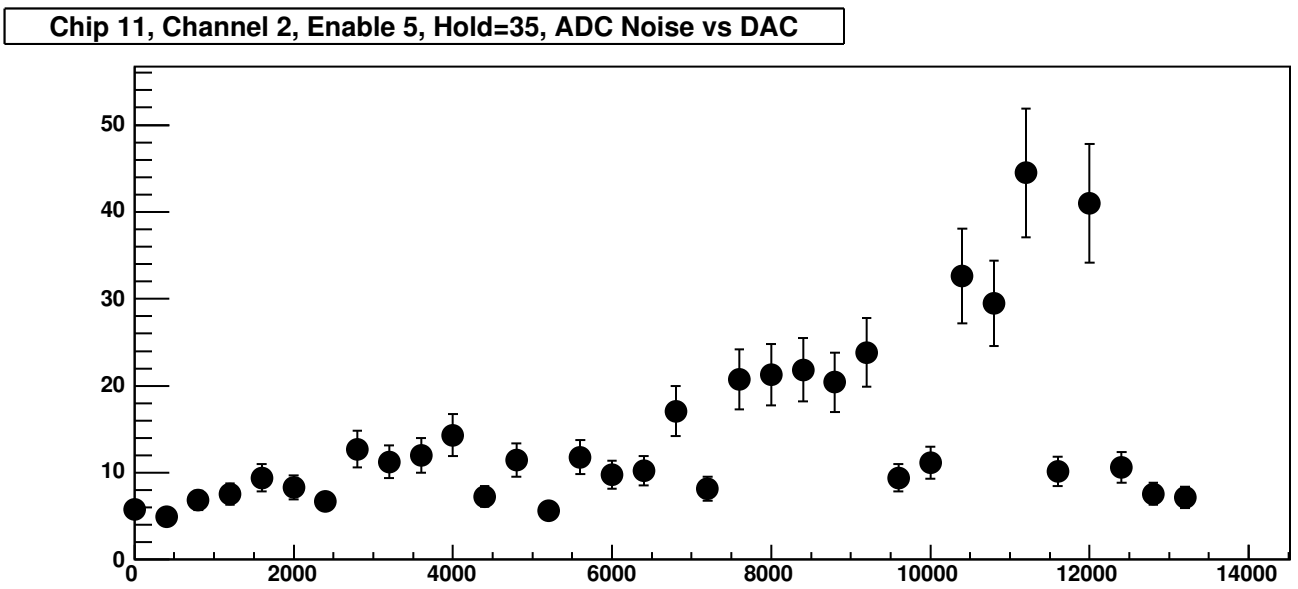

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

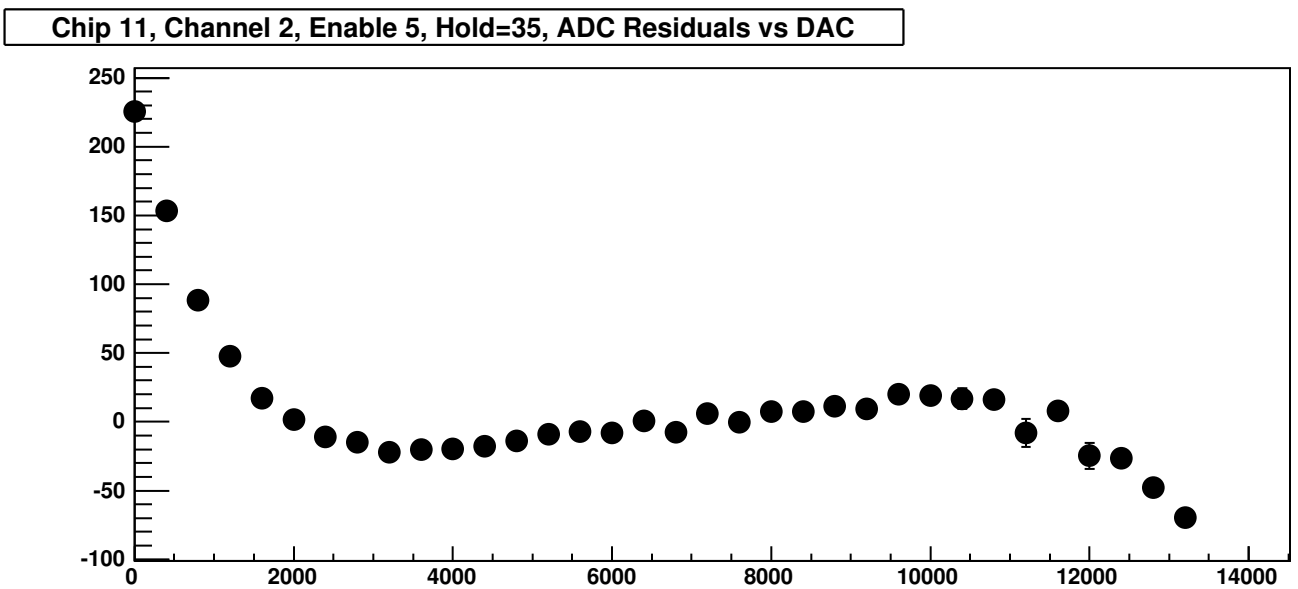

Chip 11, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.51 / 23
p0 -417.3 ± 0.7699
p1 0.03413 ± 0.0001233
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

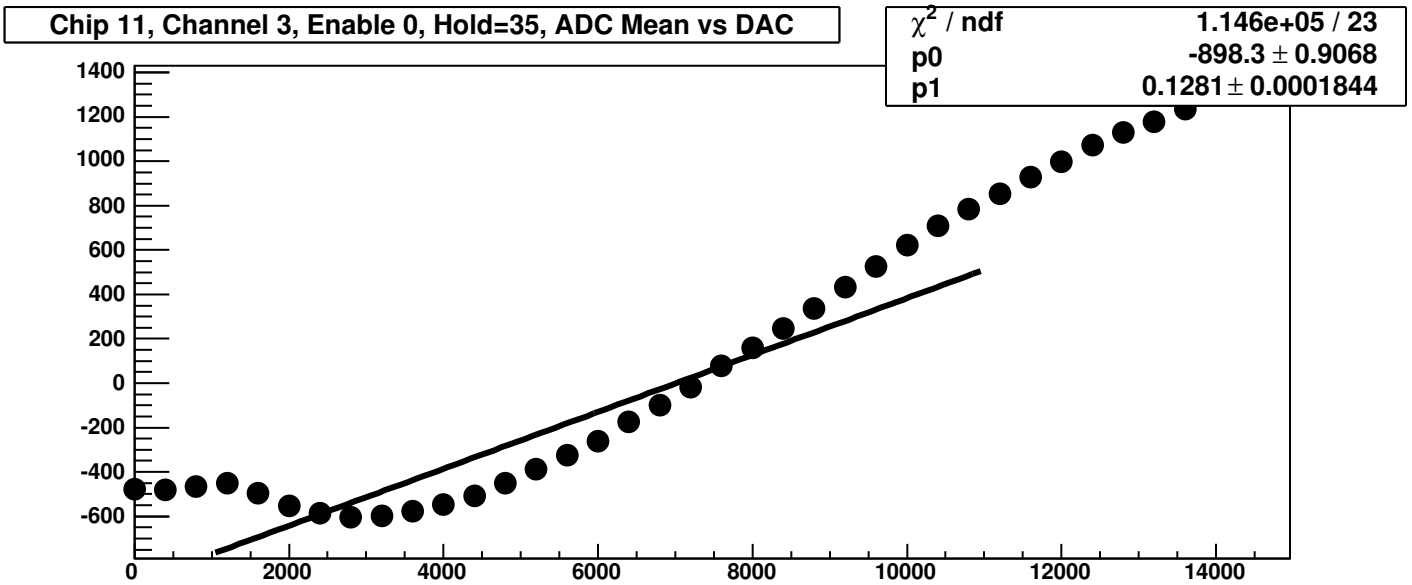

Chip 11, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

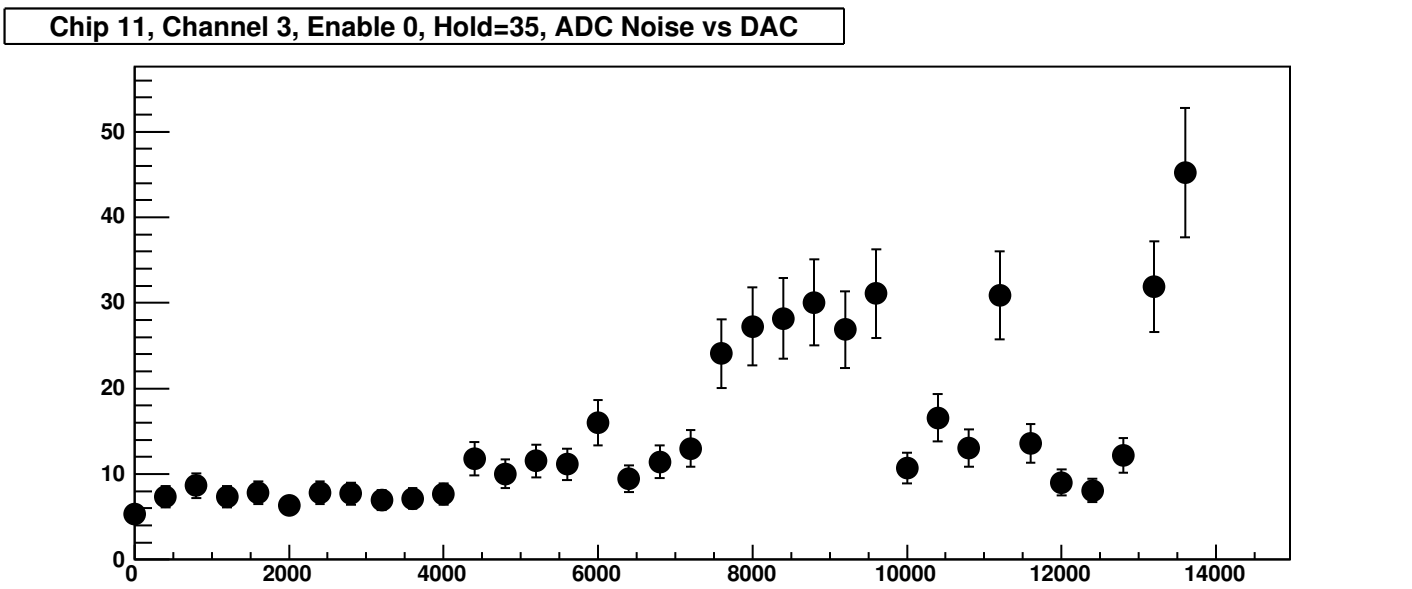

Chip 11, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

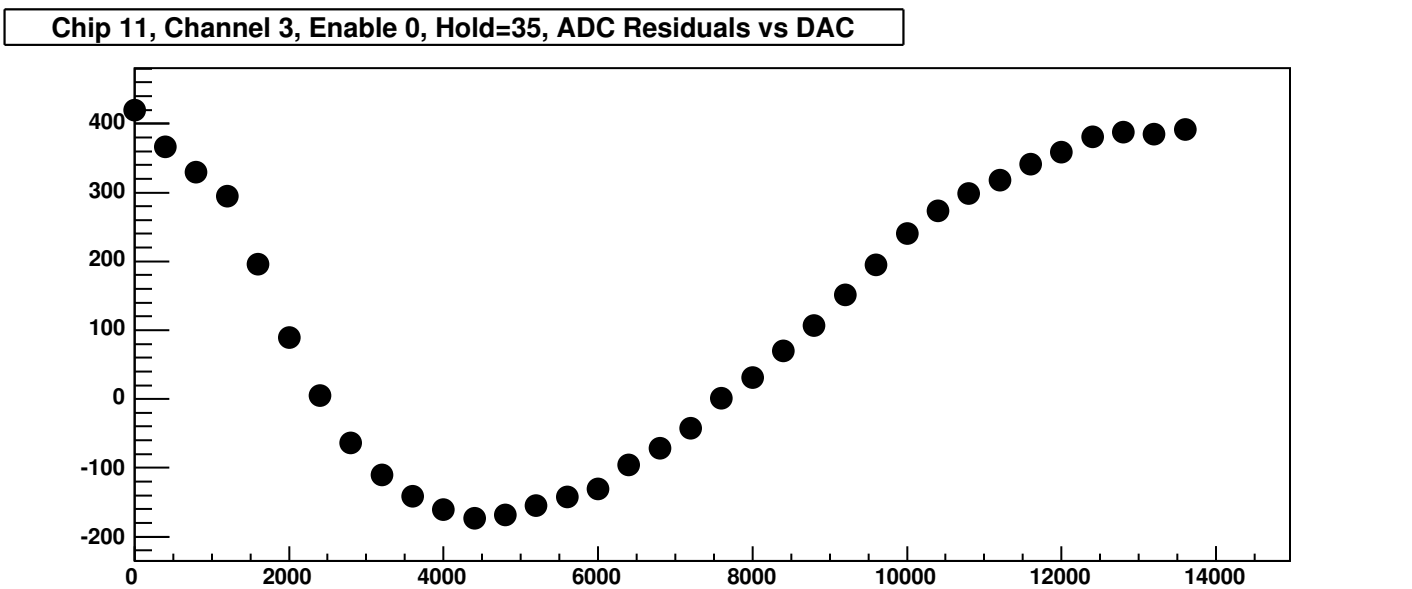

Chip 11, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1439 / 23
p0 -601.8 ± 1.024
p1 0.1886 ± 0.0001886
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

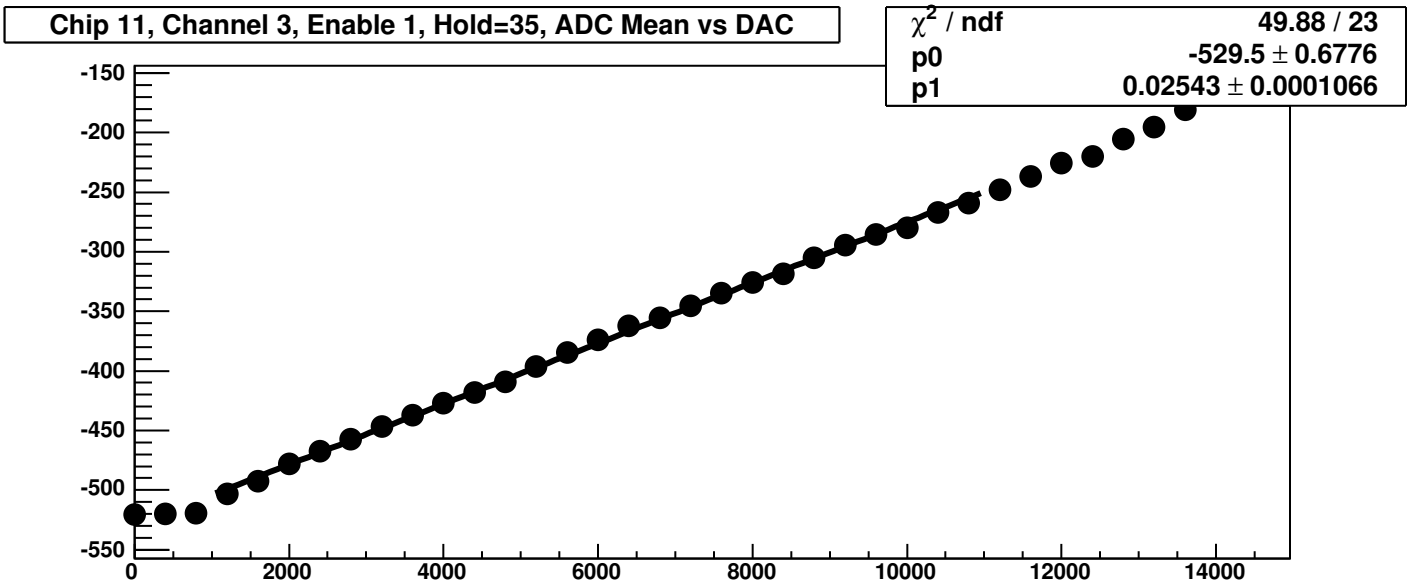

Chip 11, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

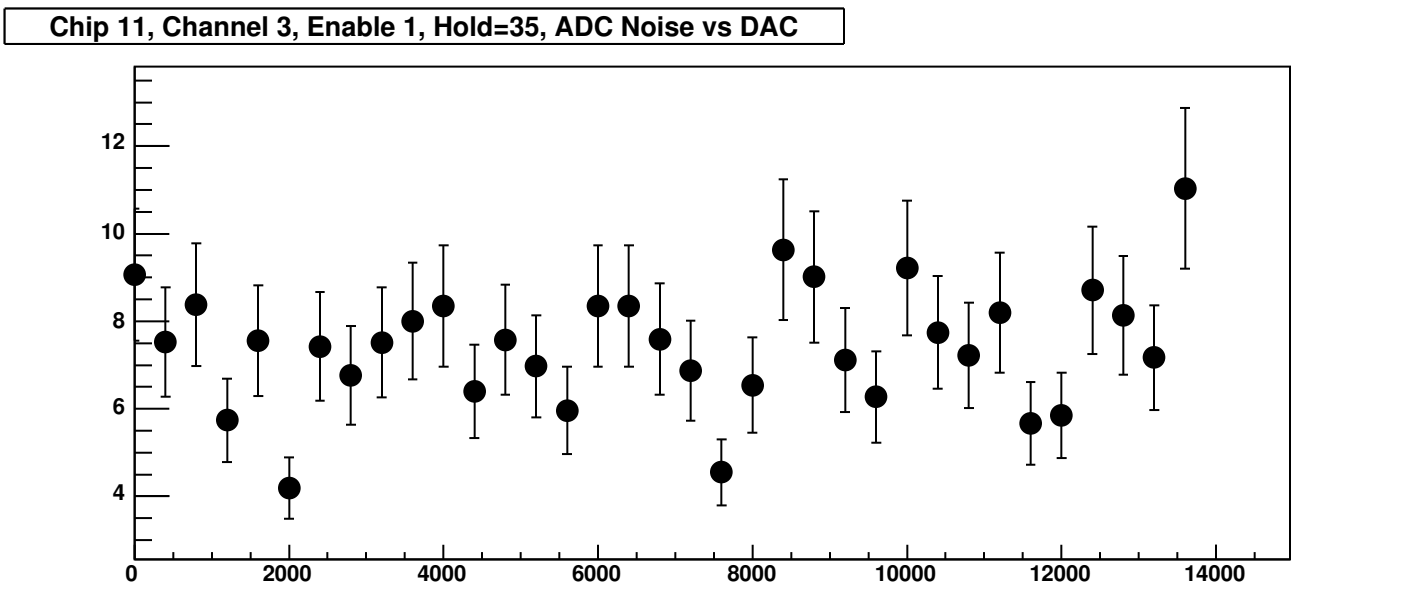

Chip 11, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

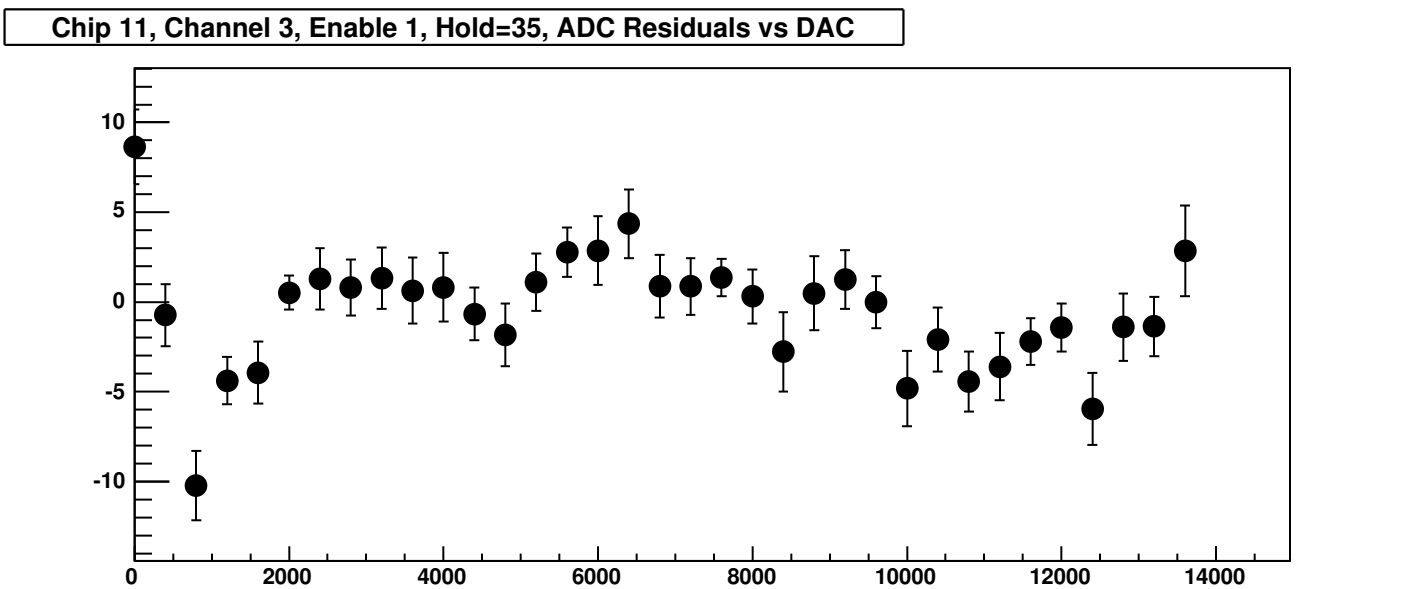

Chip 11, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.146e+05 / 23
p0 -898.3 ± 0.9068
p1 0.1281 ± 0.0001844
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

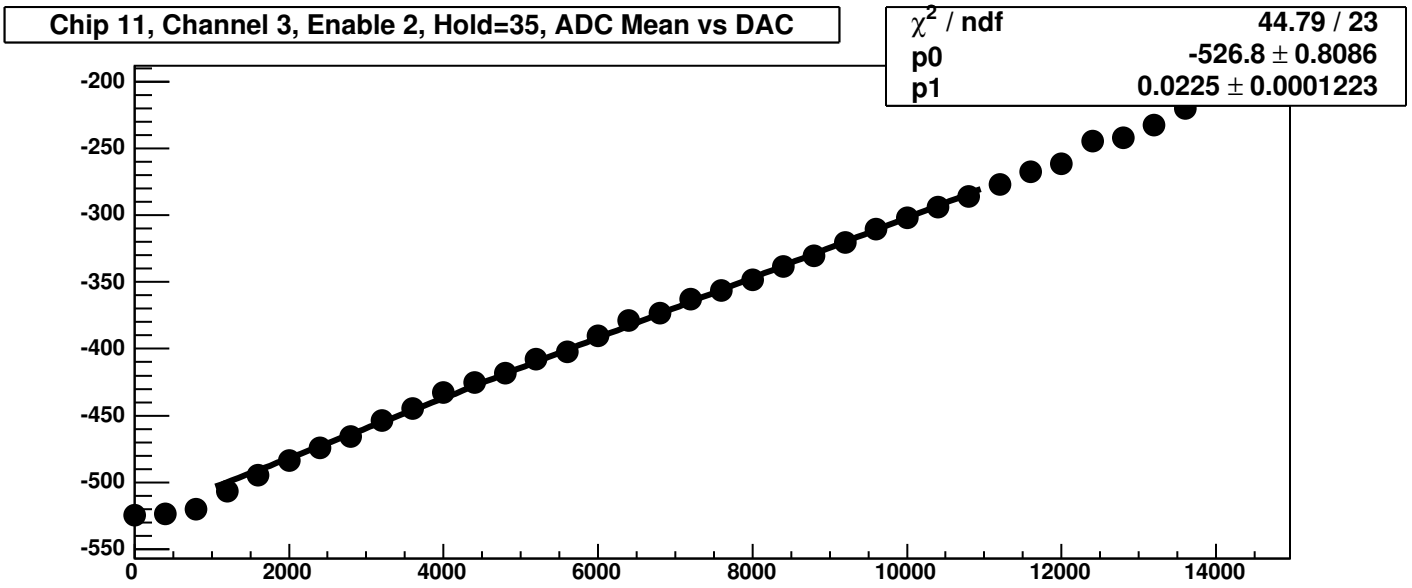

Chip 11, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

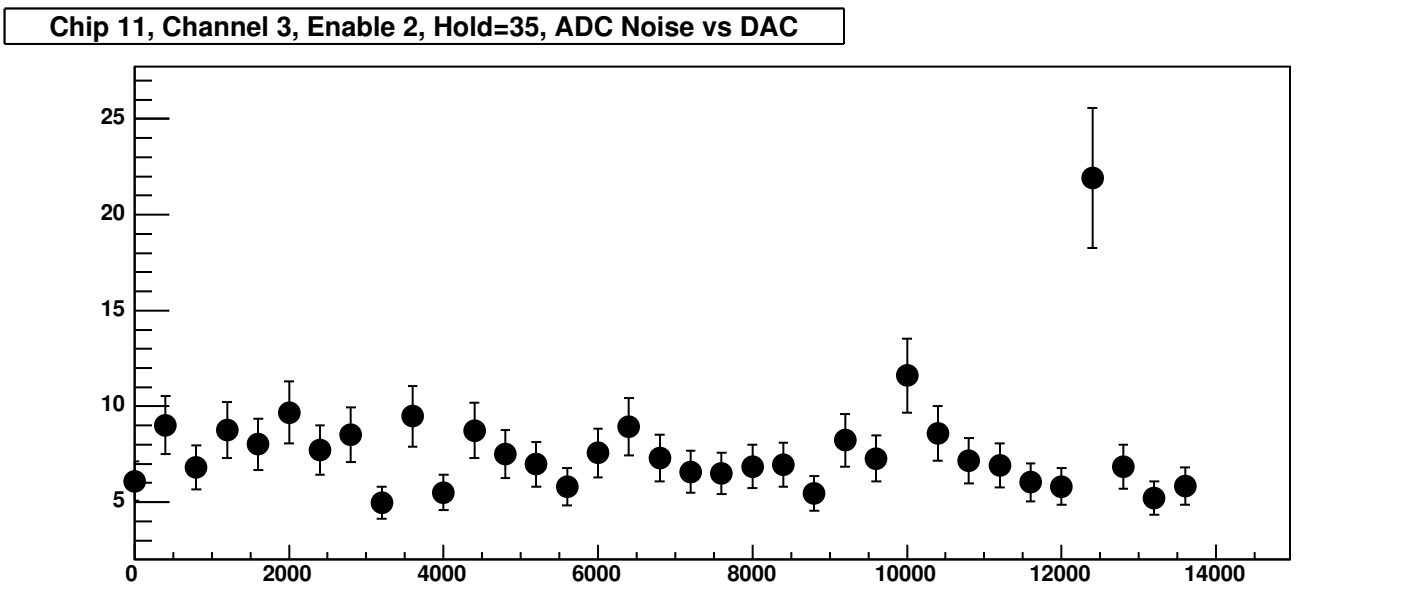

Chip 11, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

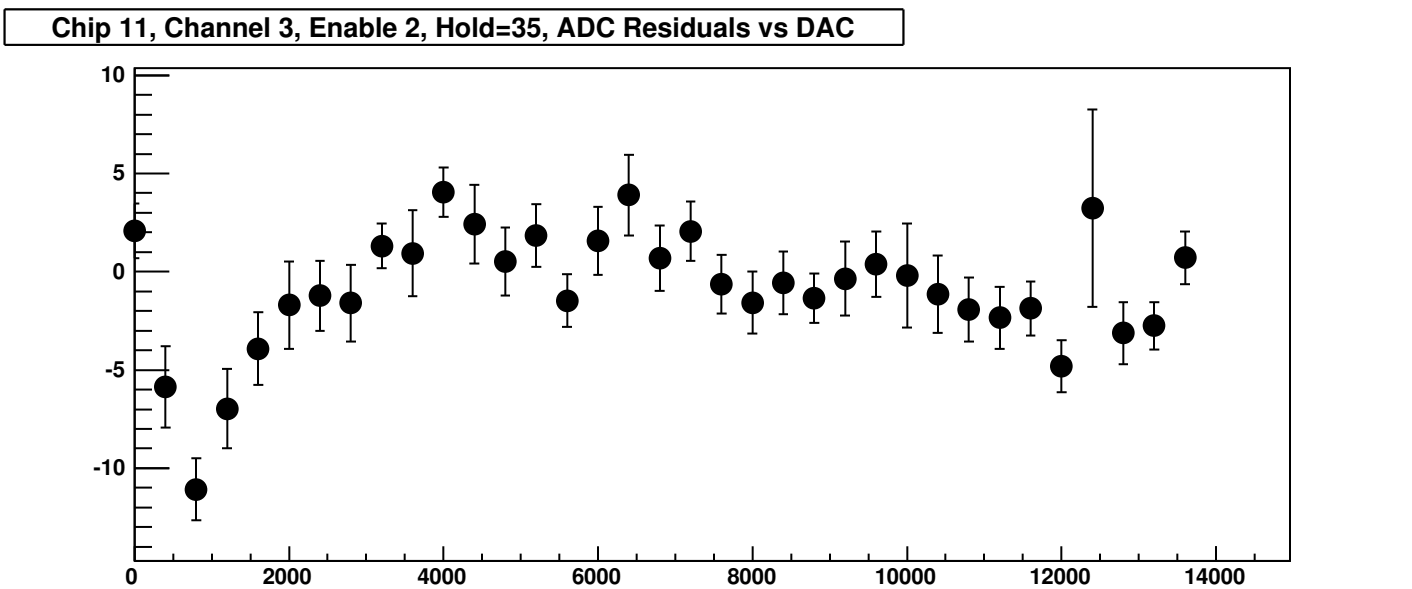

Chip 11, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.88 / 23
p0 -529.5 ± 0.6776
p1 0.02543 ± 0.0001066
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

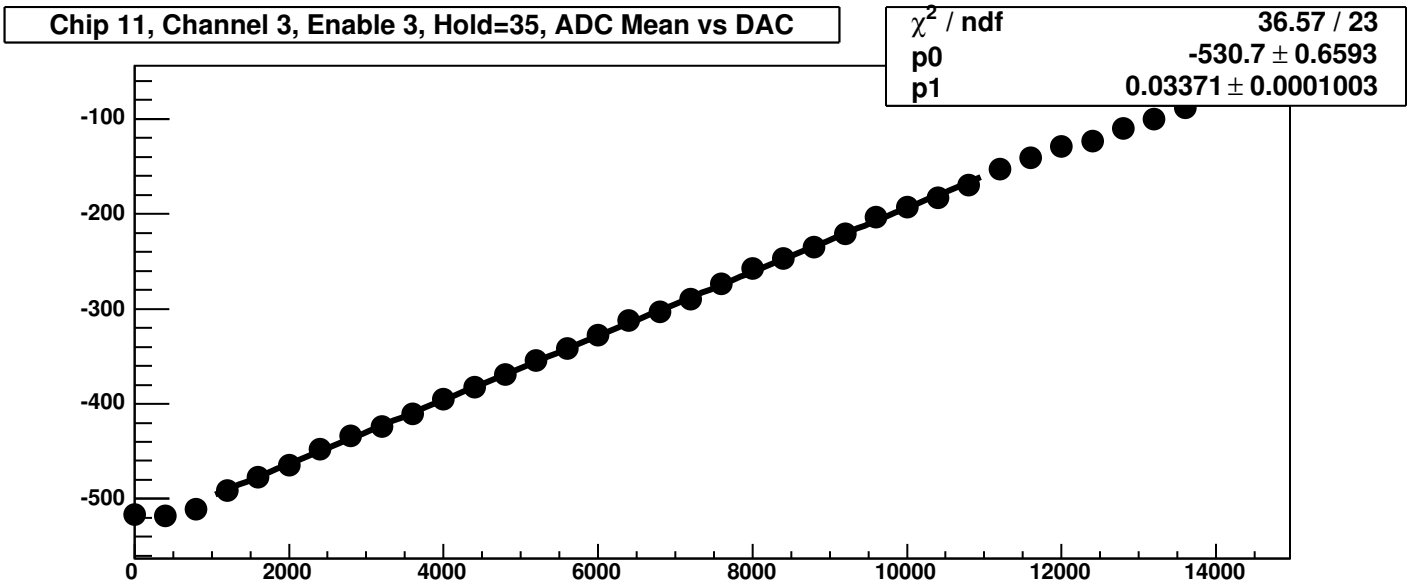

Chip 11, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

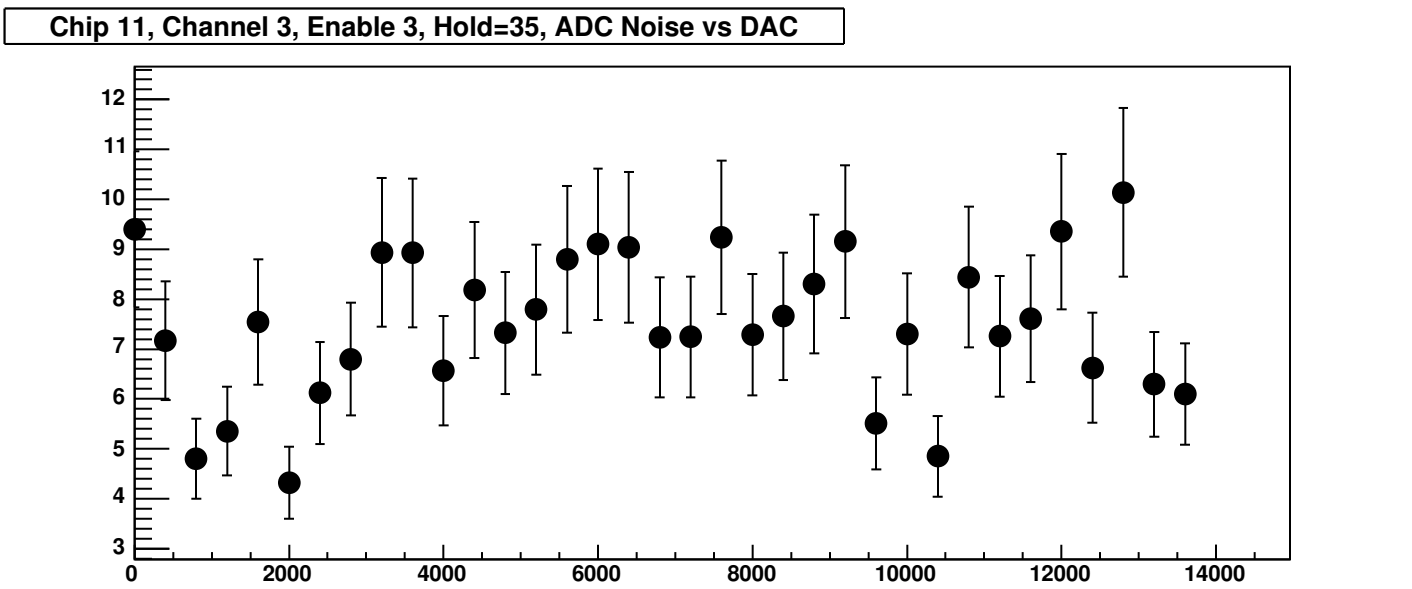

Chip 11, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

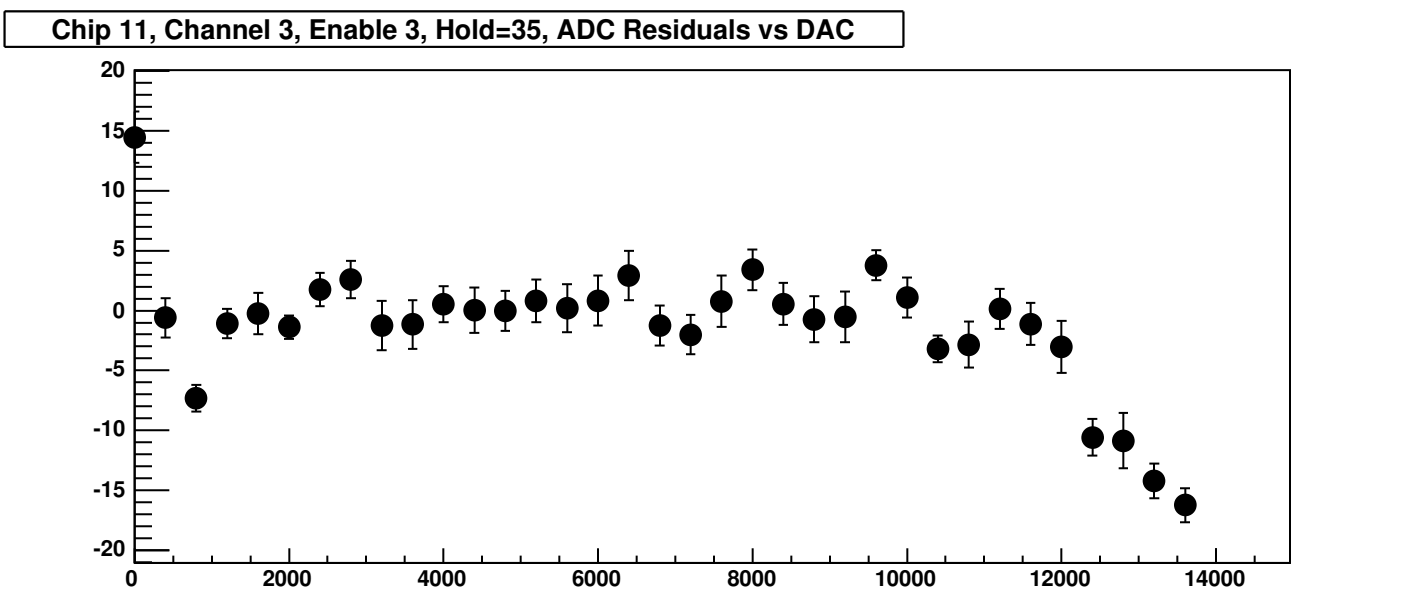

Chip 11, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 44.79 / 23
p0 -526.8 ± 0.8086
p1 0.0225 ± 0.0001223
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

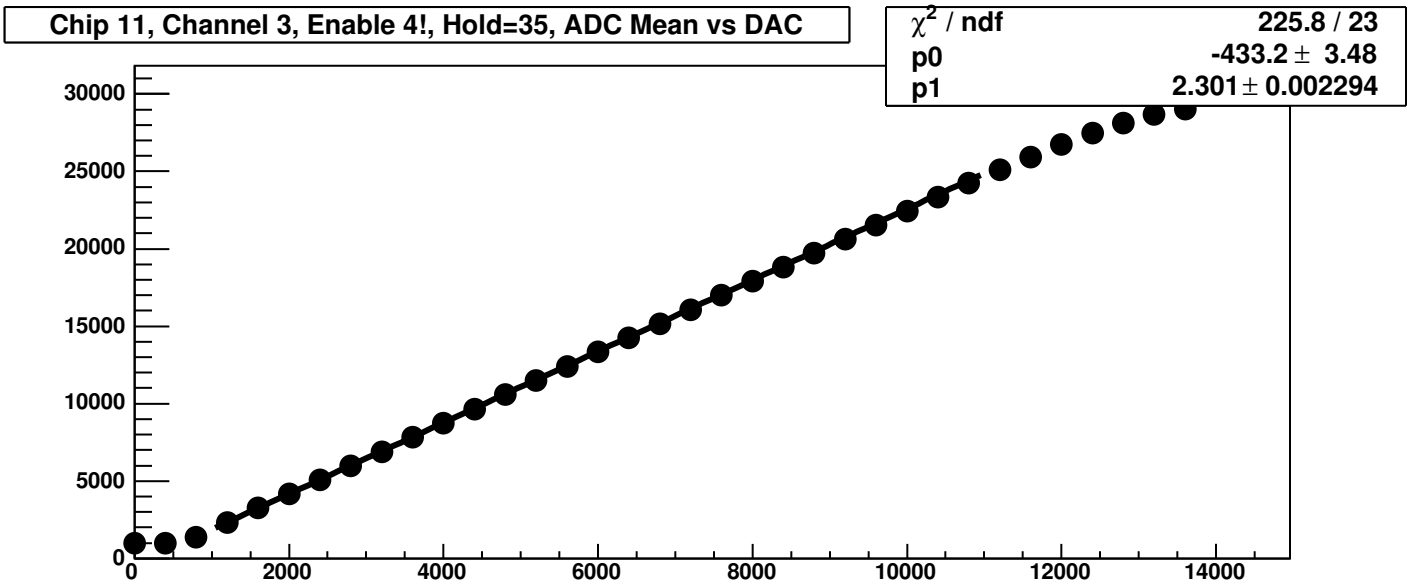

Chip 11, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

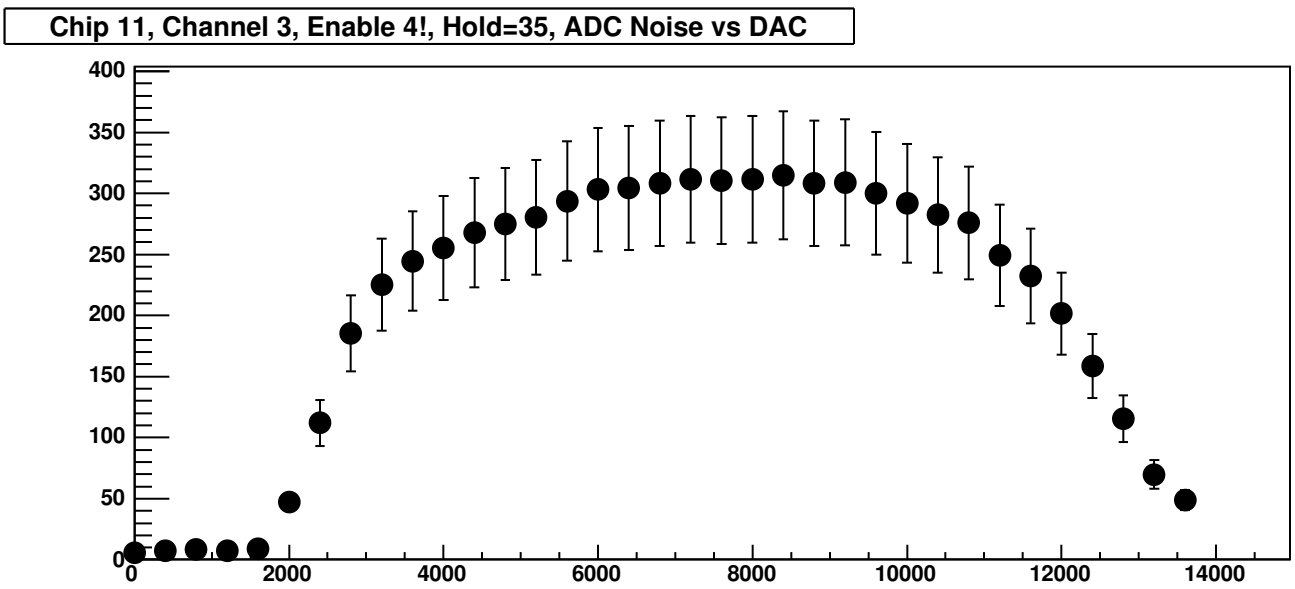

Chip 11, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

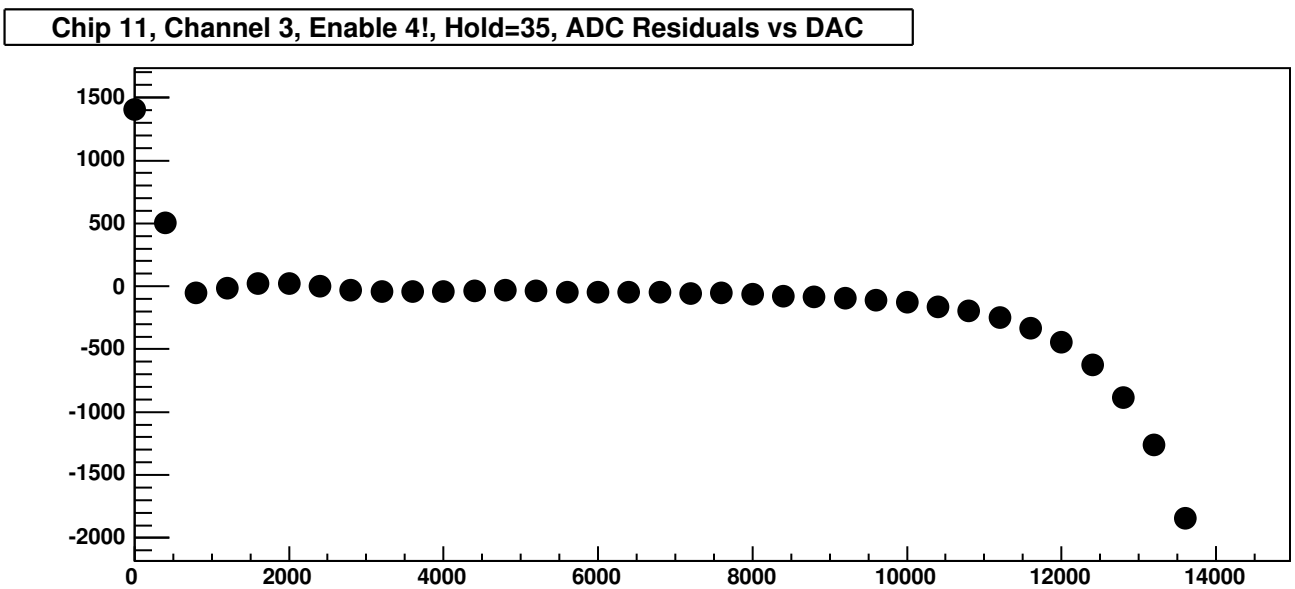

Chip 11, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.57 / 23
p0 -530.7 ± 0.6593
p1 0.03371 ± 0.0001003
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

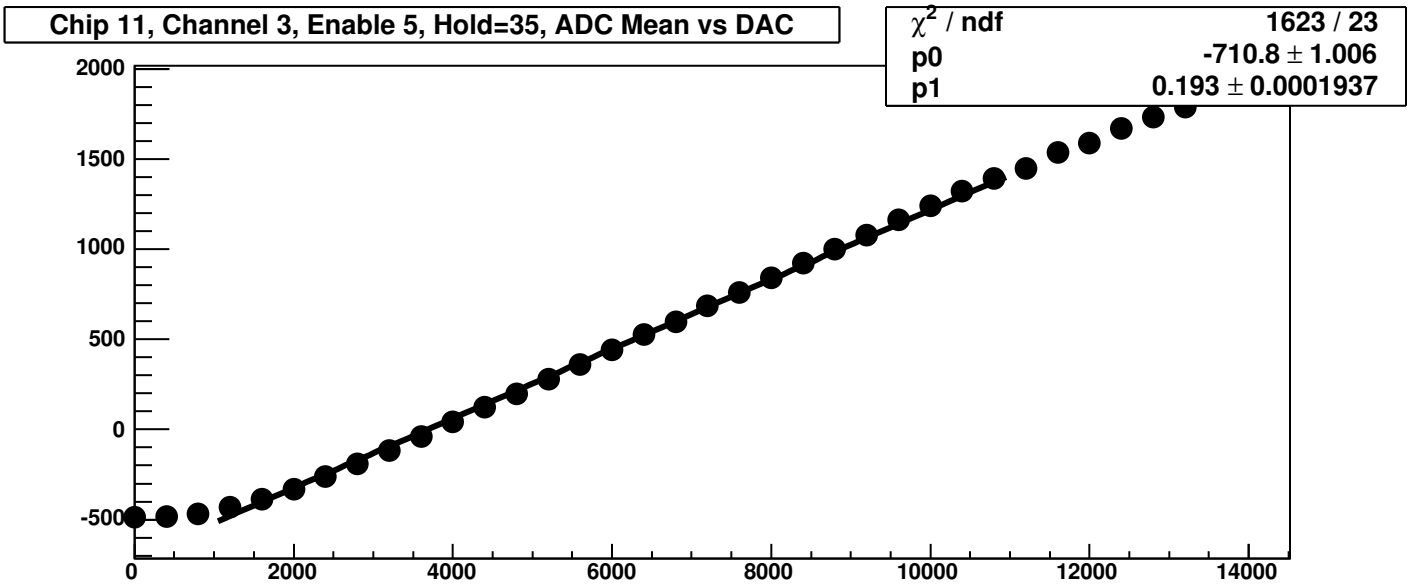

Chip 11, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

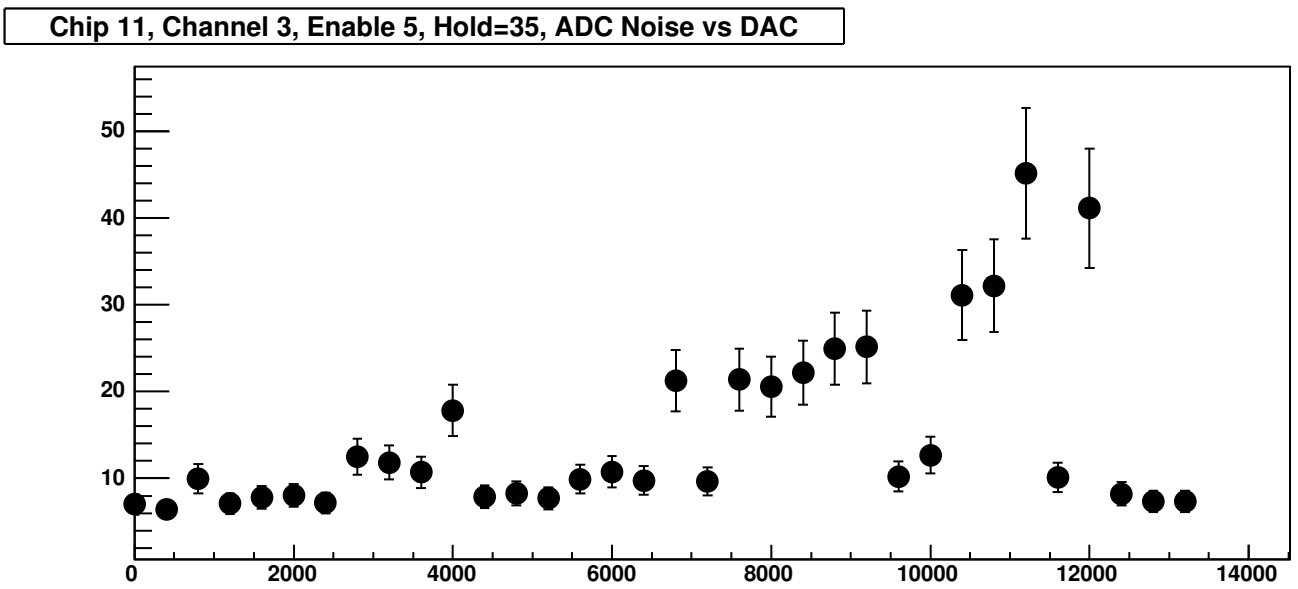

Chip 11, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

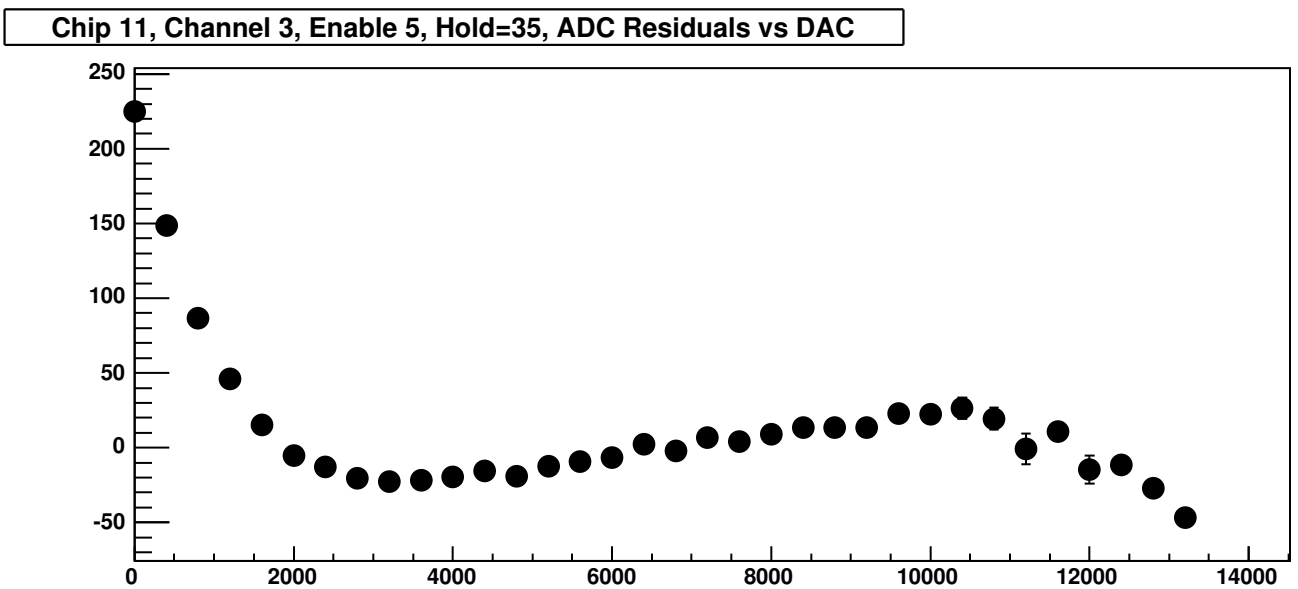

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 225.8 / 23
p0 -433.2 ± 3.48
p1 2.301 ± 0.002294
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

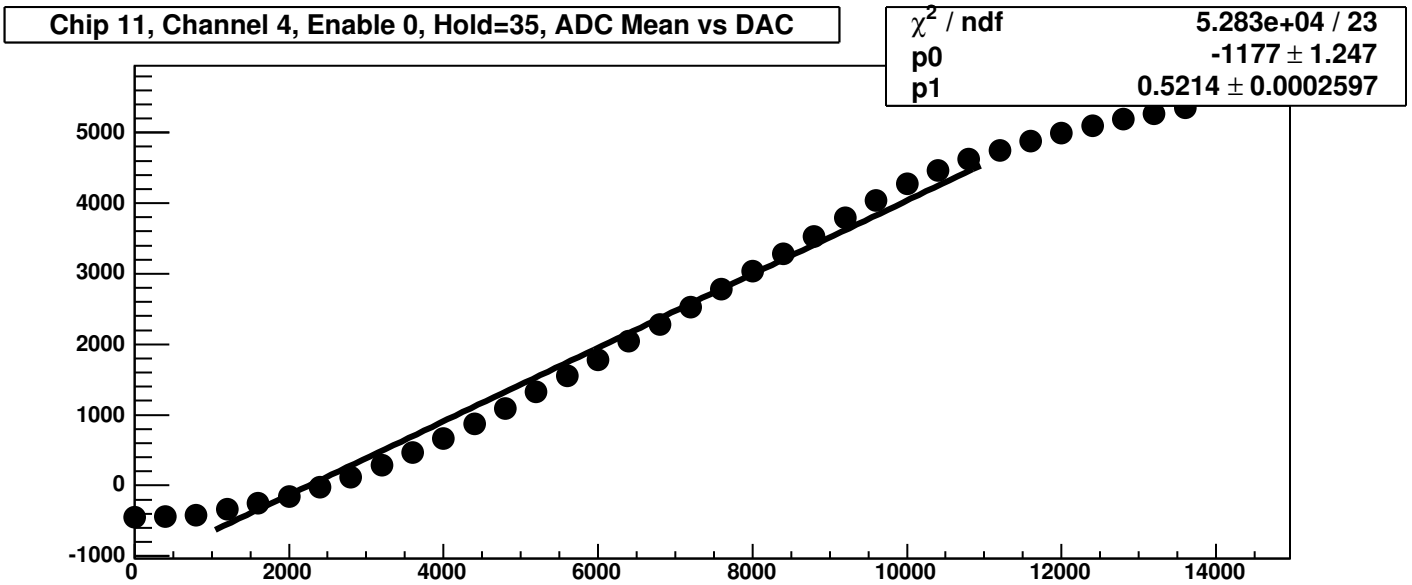

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

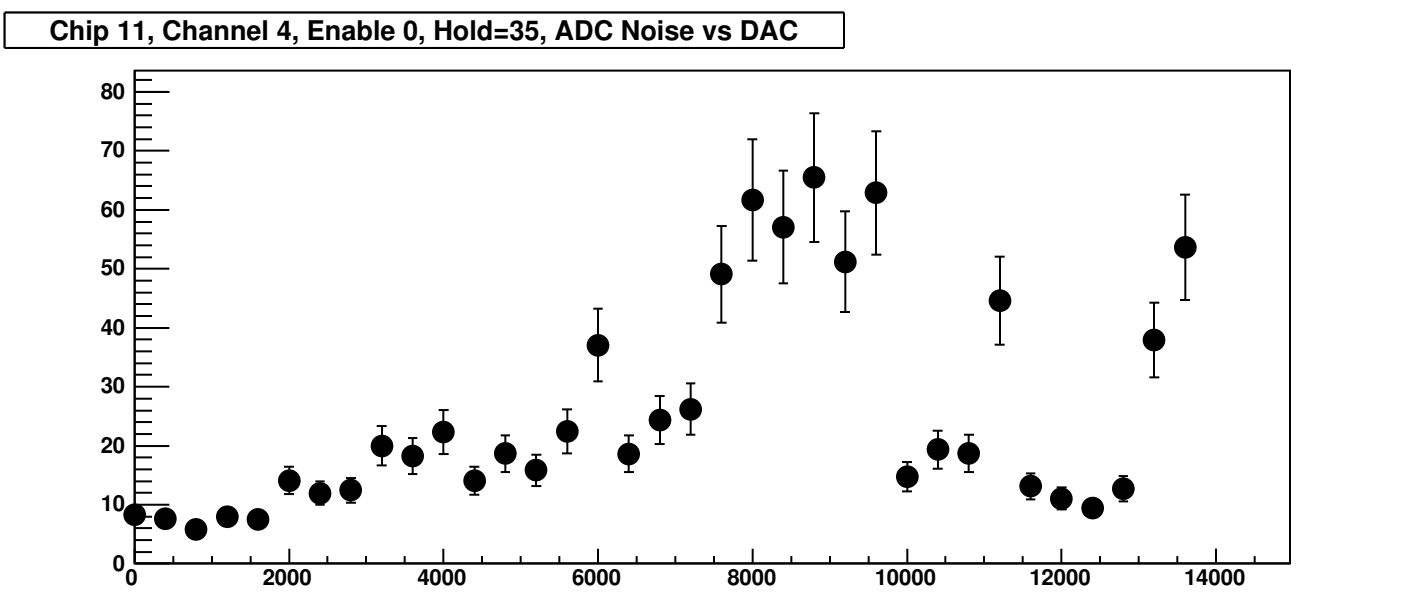

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

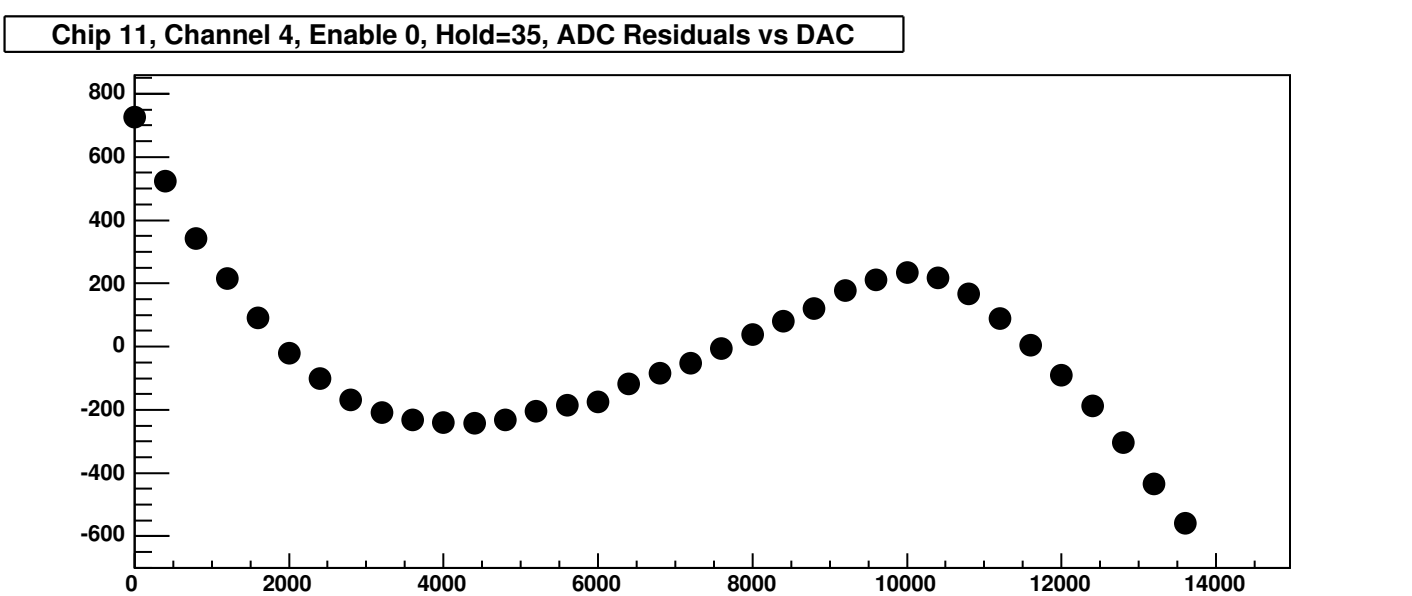

Chip 11, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1623 / 23
p0 -710.8 ± 1.006
p1 0.193 ± 0.0001937
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

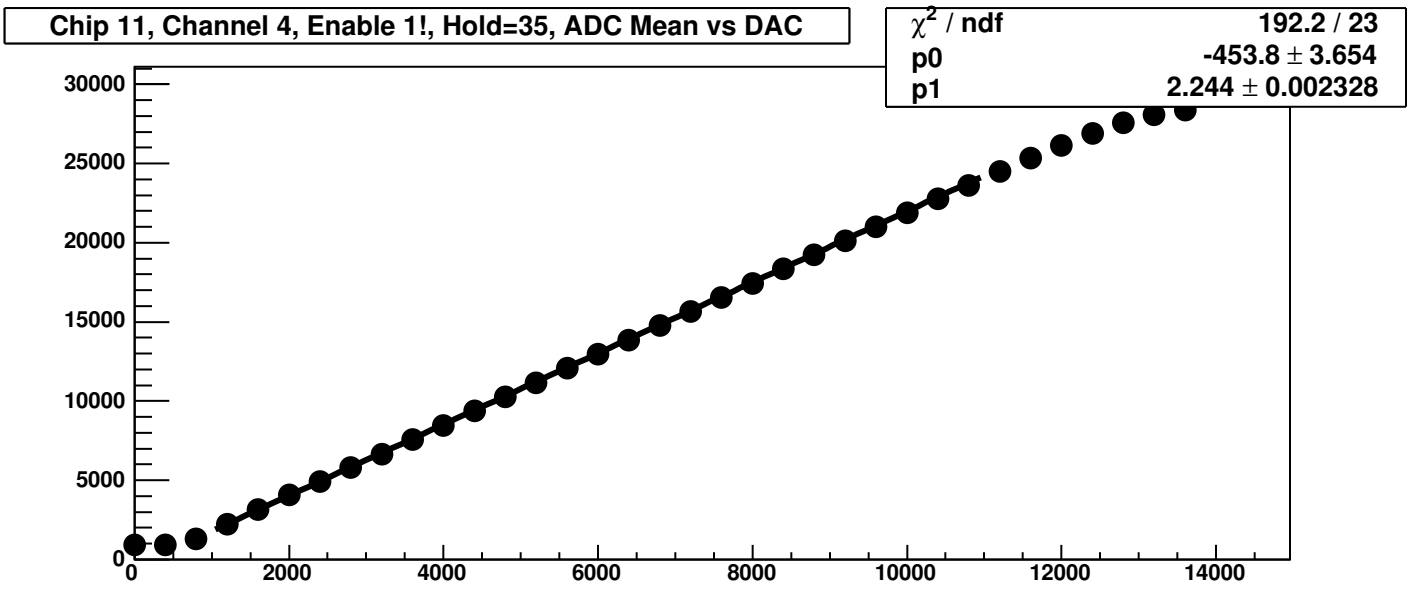

Chip 11, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

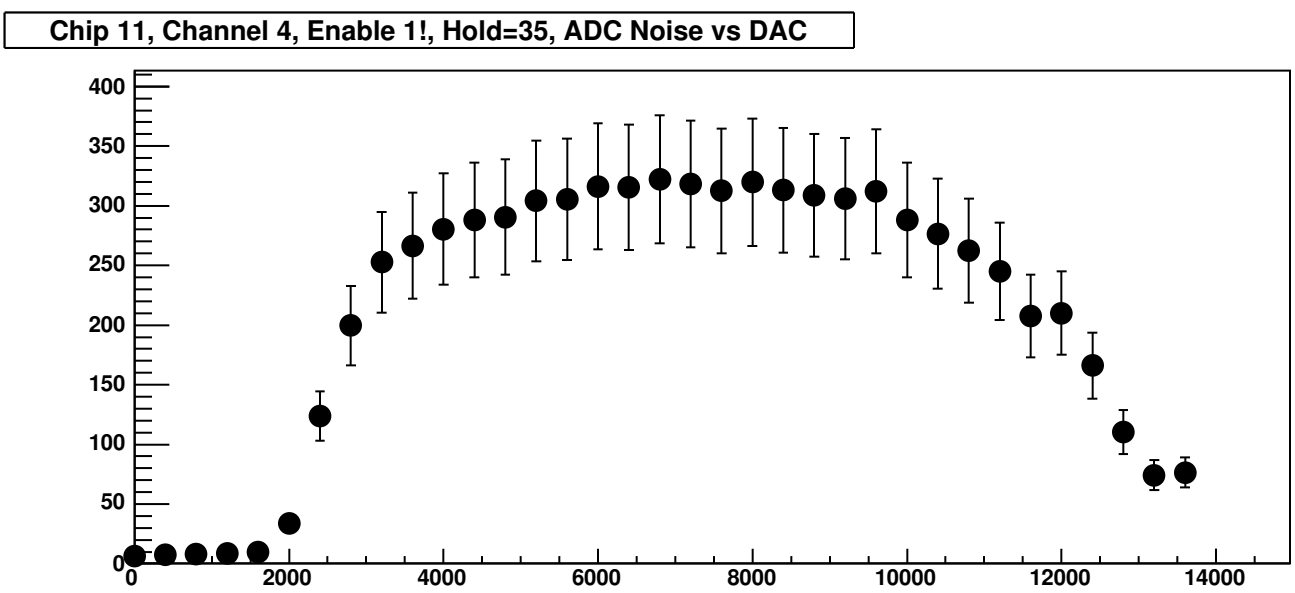

Chip 11, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

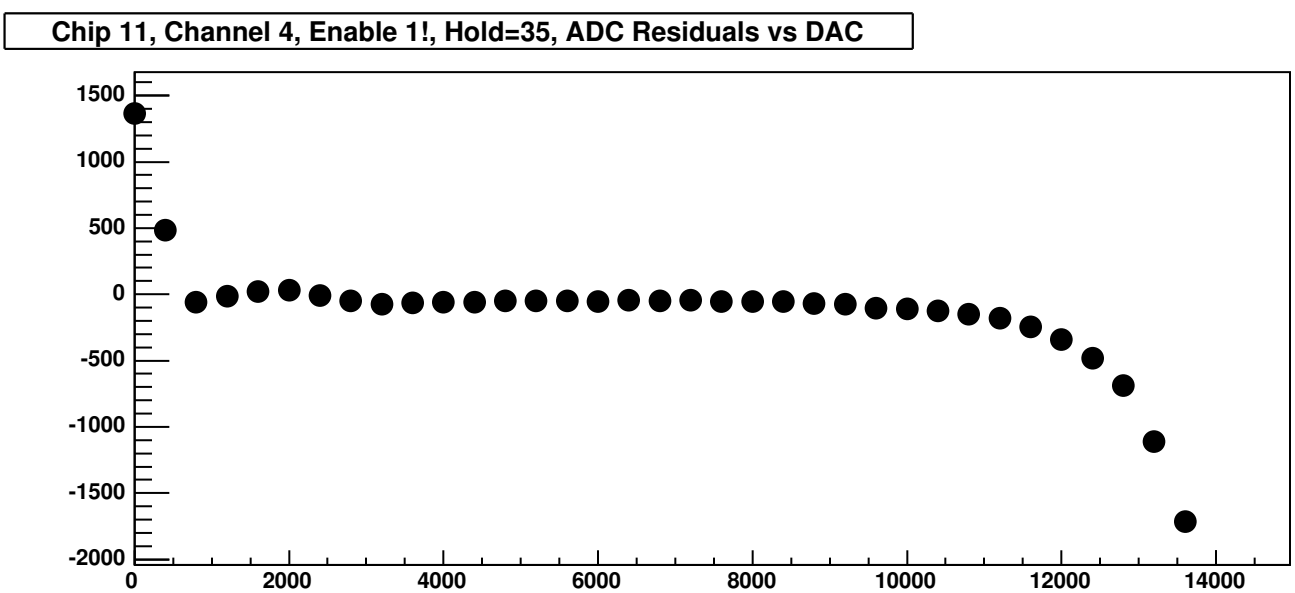

Chip 11, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.283e+04 / 23
p0 -1177 ± 1.247
p1 0.5214 ± 0.0002597
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

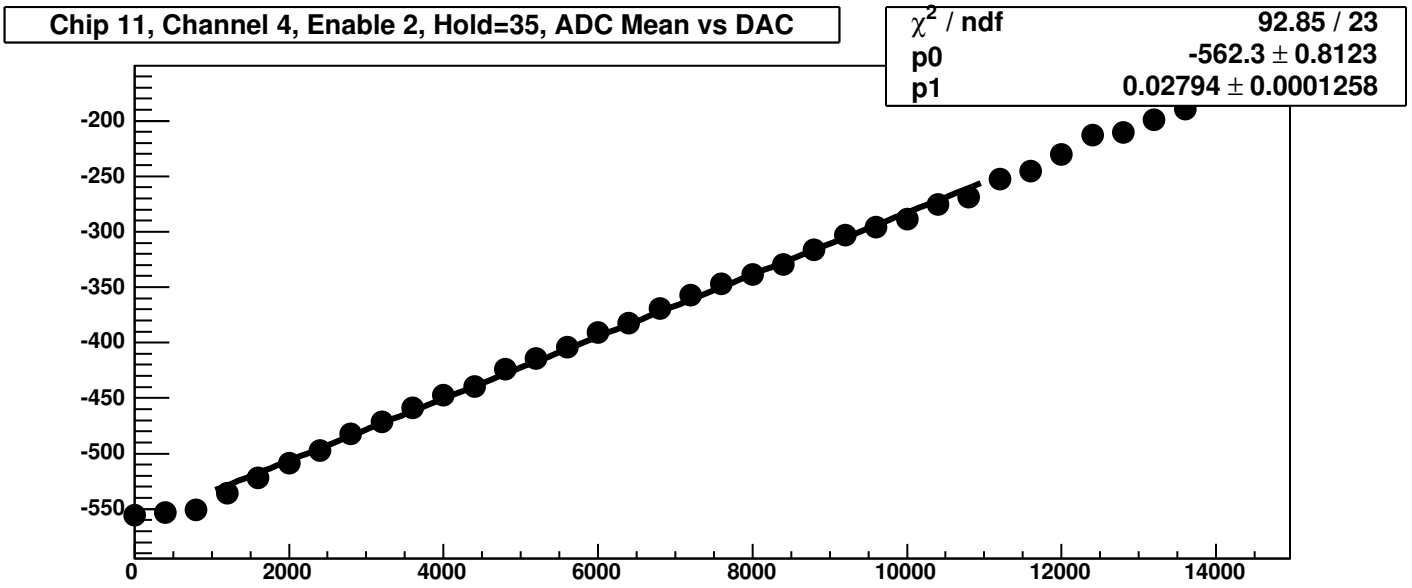

Chip 11, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

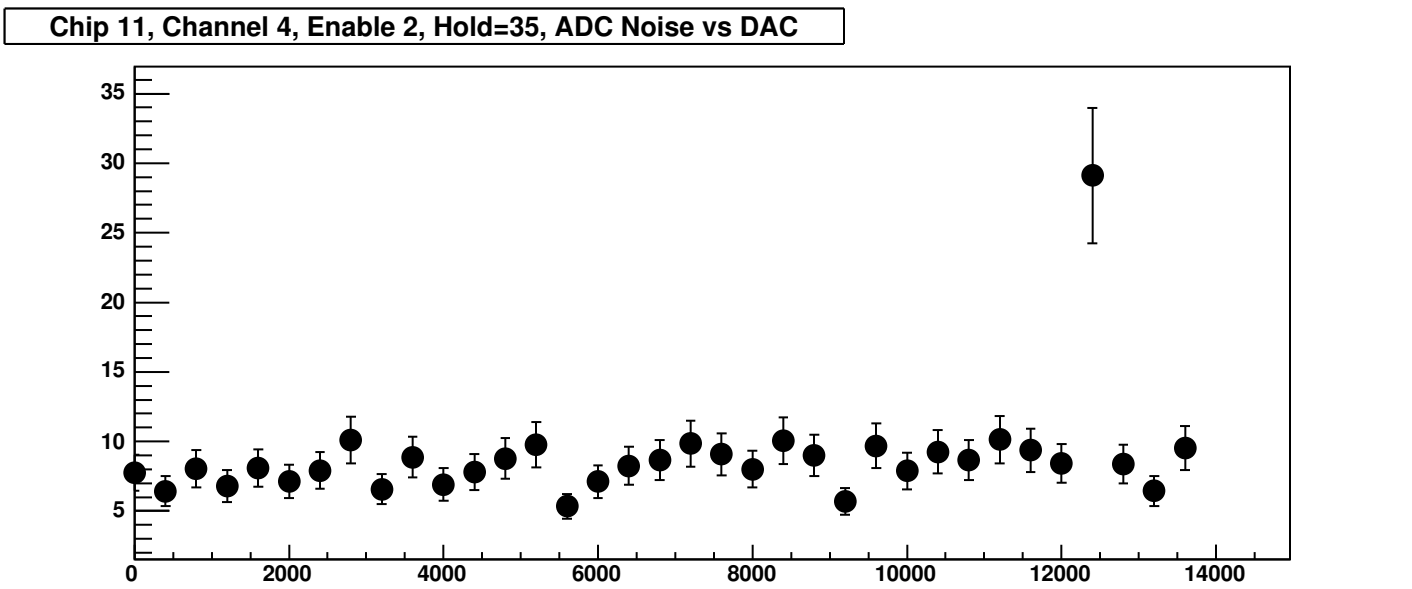

Chip 11, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

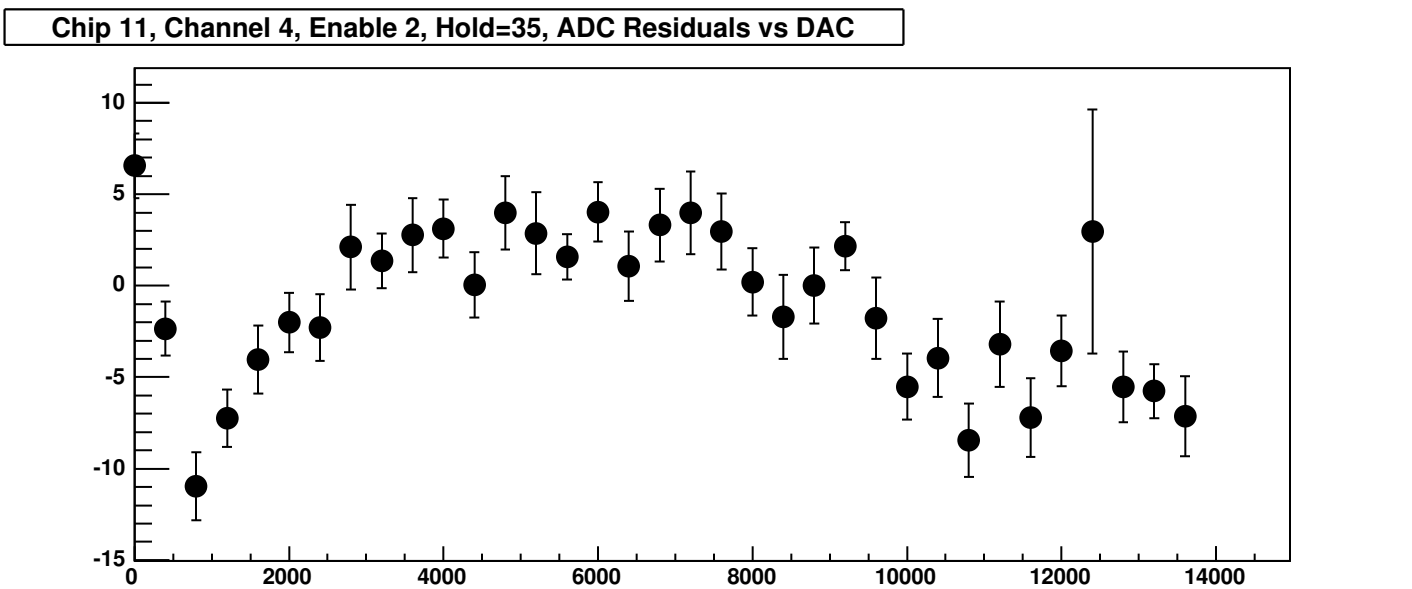

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 192.2 / 23
p0 -453.8 ± 3.654
p1 2.244 ± 0.002328
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

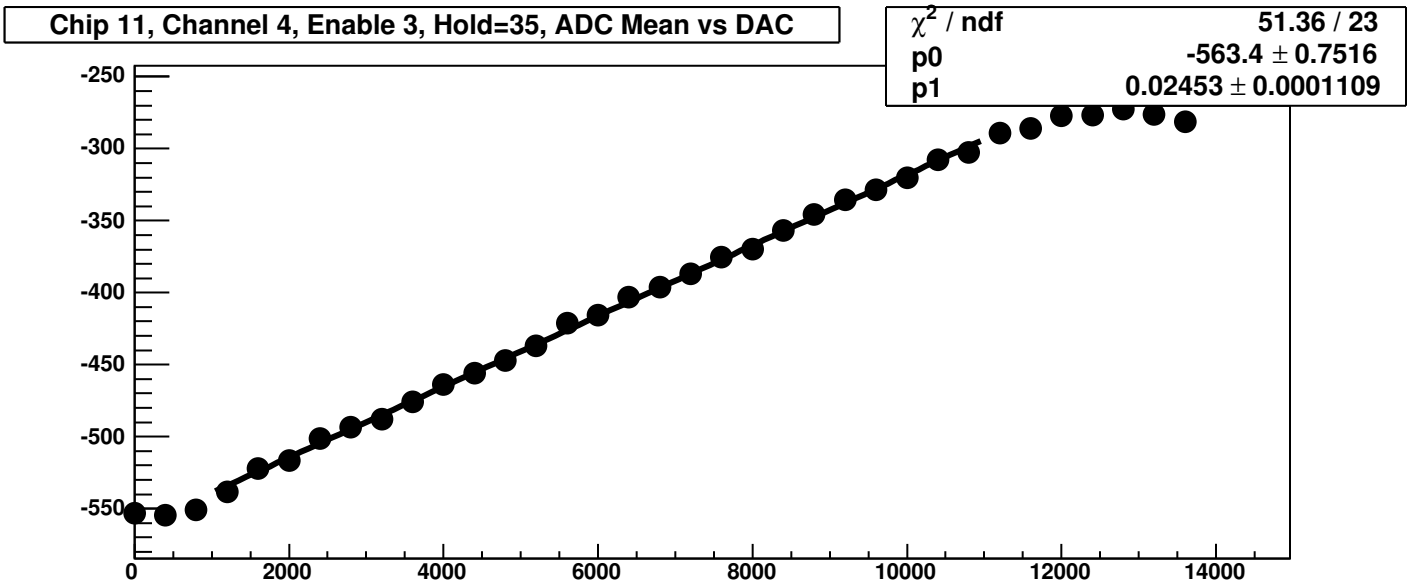

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

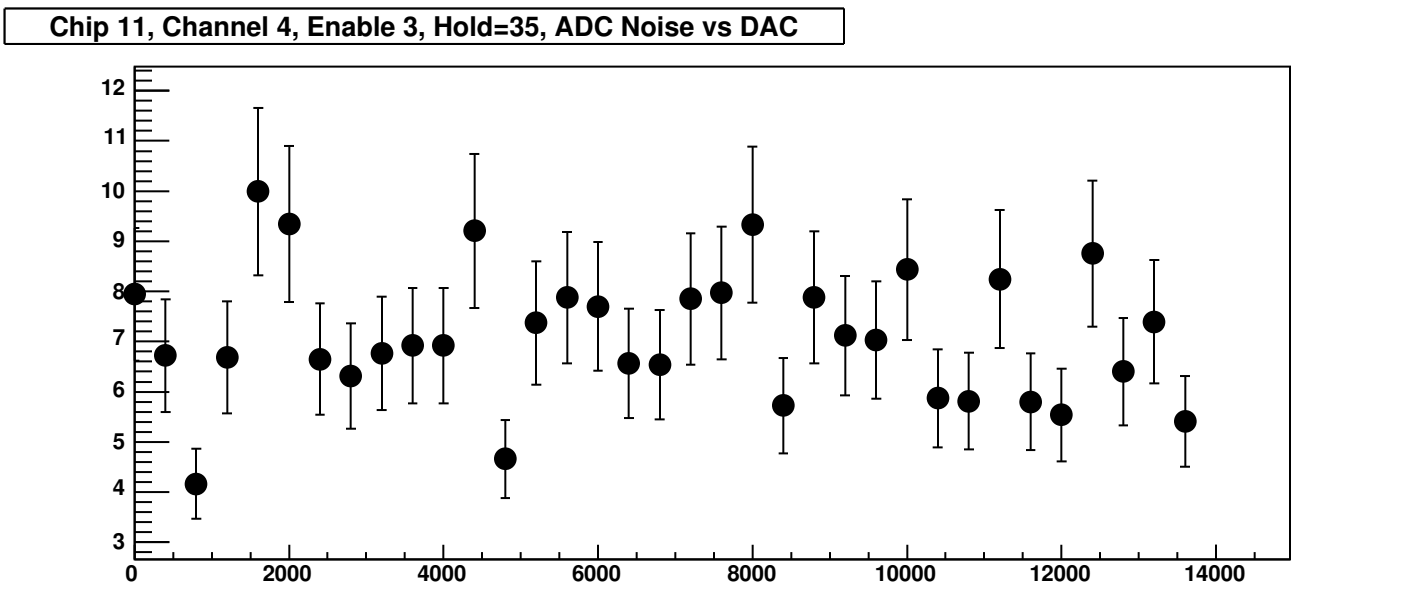

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

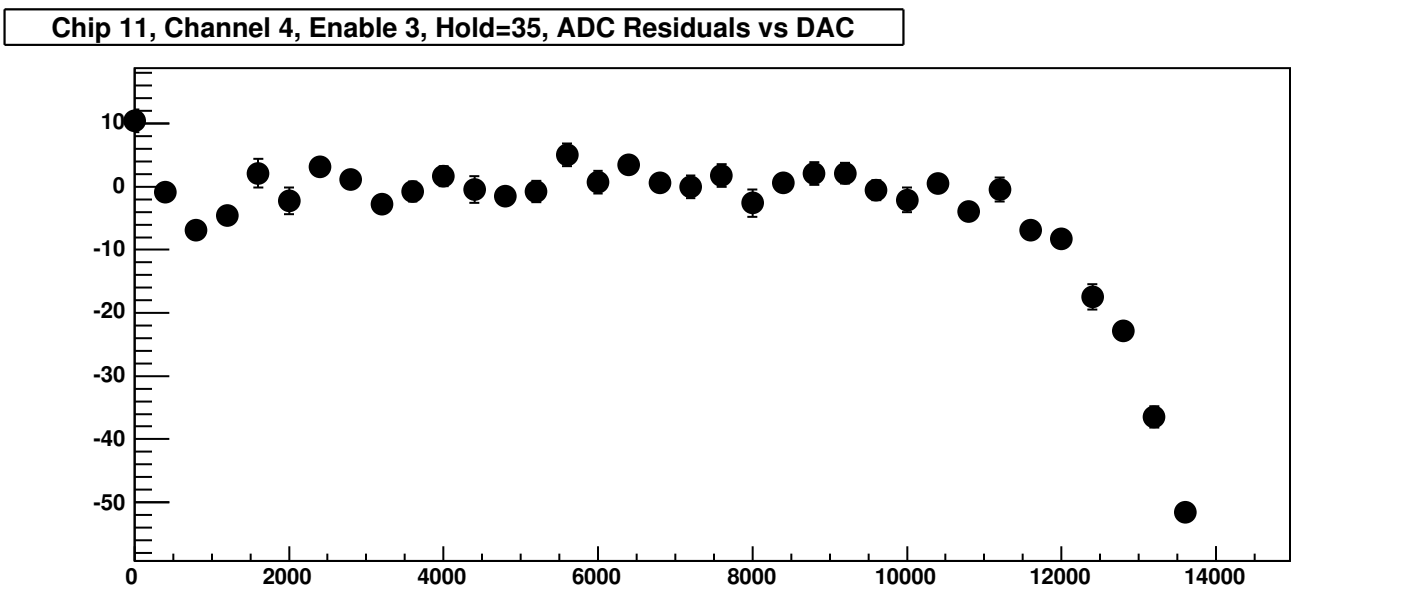

Chip 11, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 92.85 / 23
p0 -562.3 ± 0.8123
p1 0.02794 ± 0.0001258
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

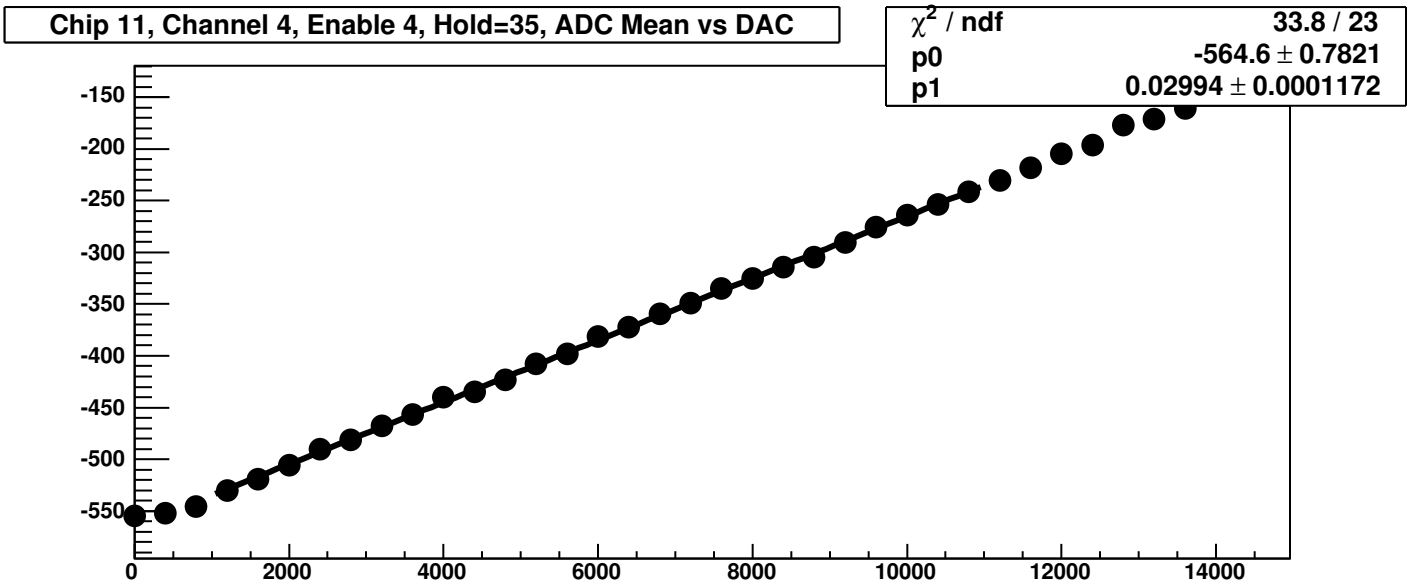

Chip 11, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

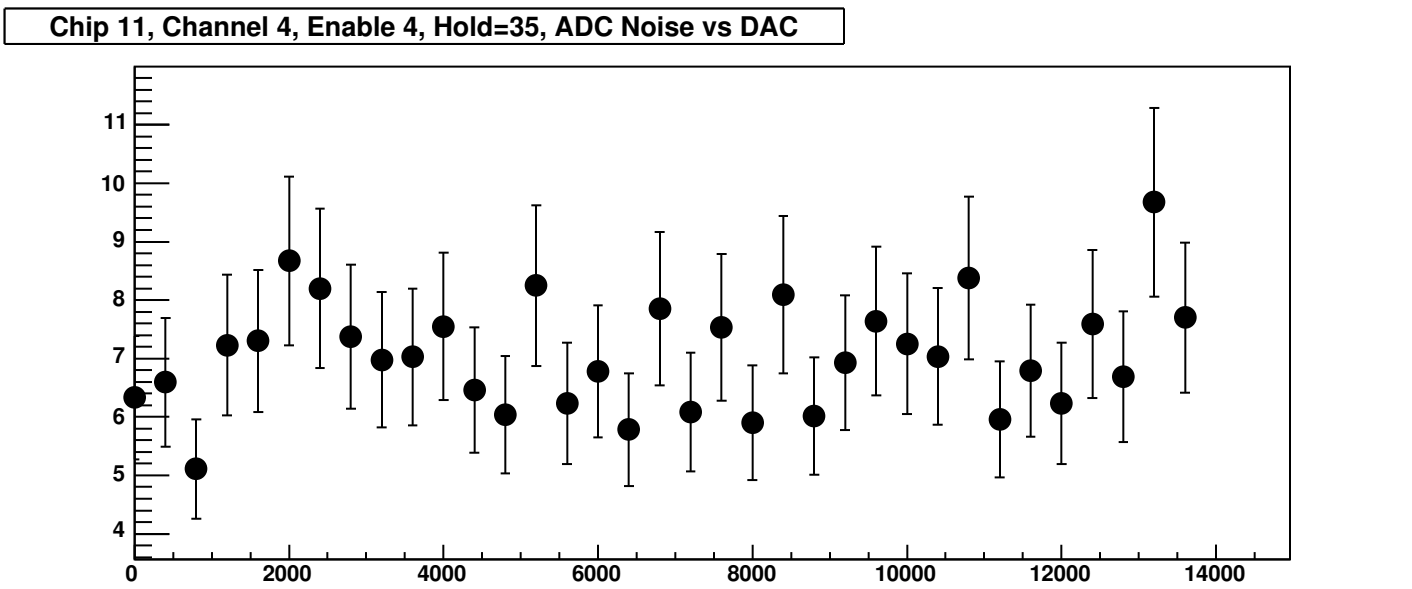

Chip 11, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

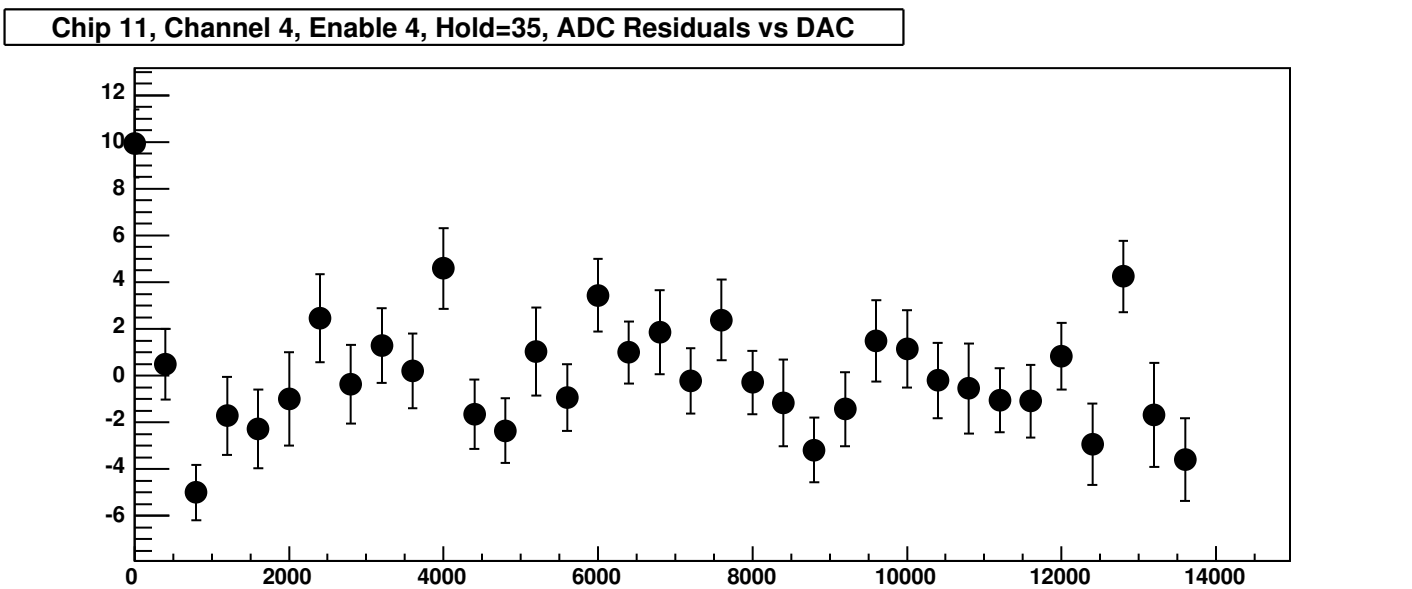

Chip 11, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.36 / 23
p0 -563.4 ± 0.7516
p1 0.02453 ± 0.0001109
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

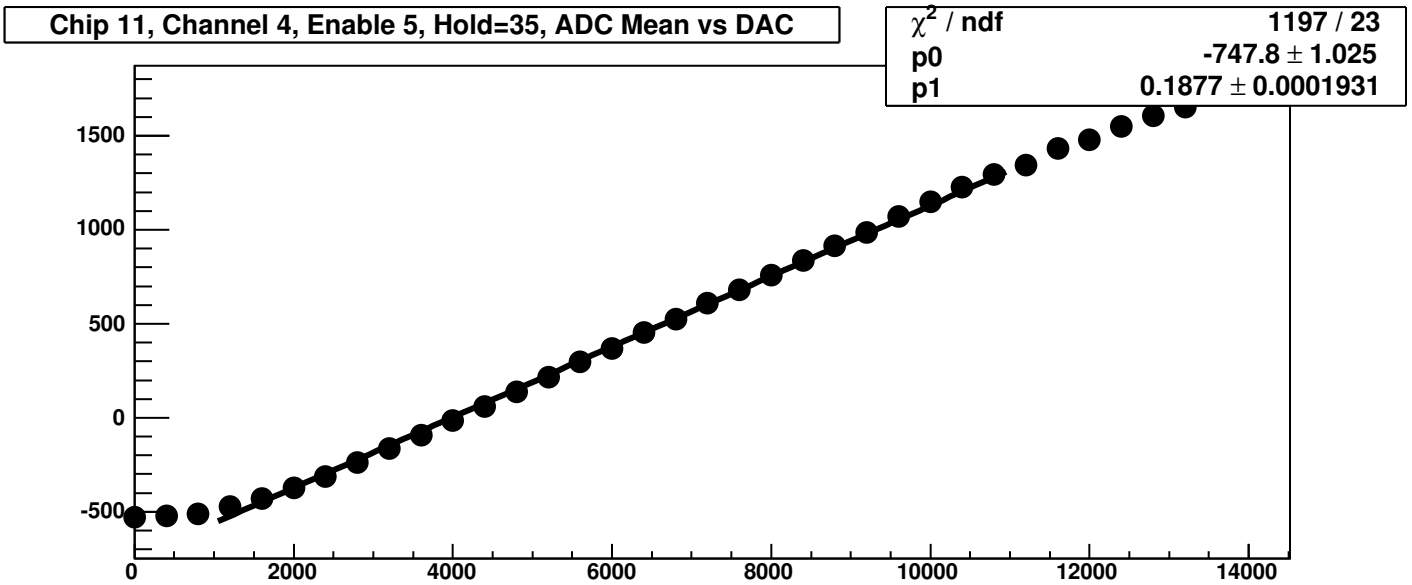

Chip 11, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

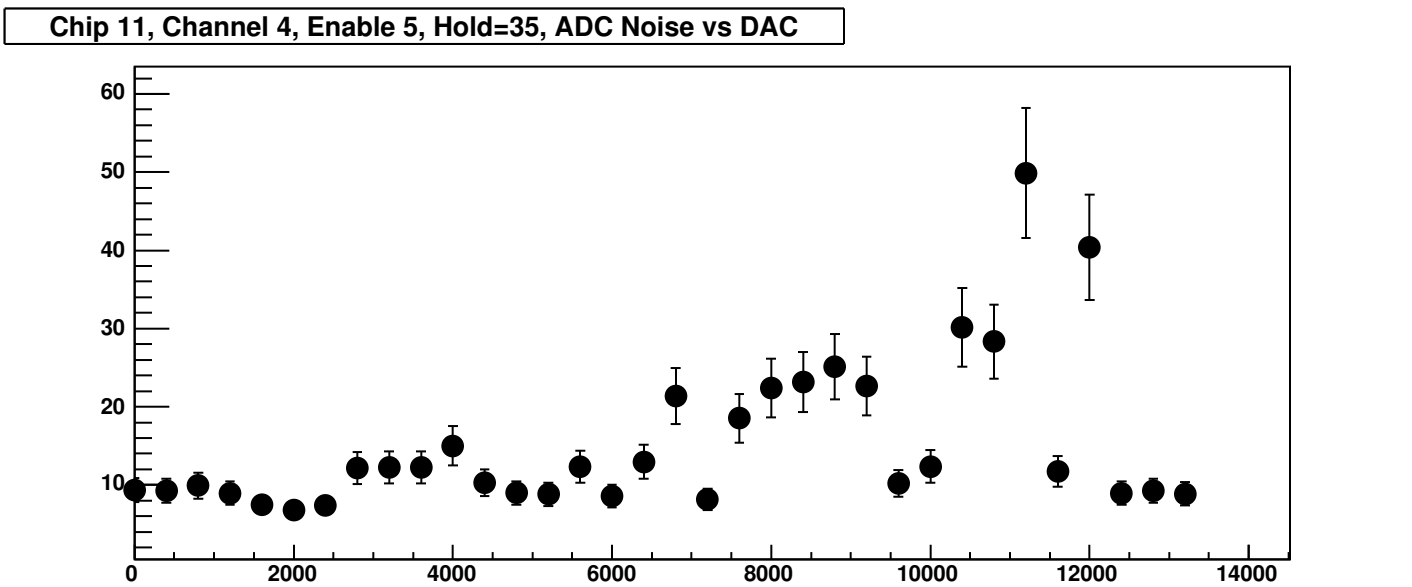

Chip 11, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

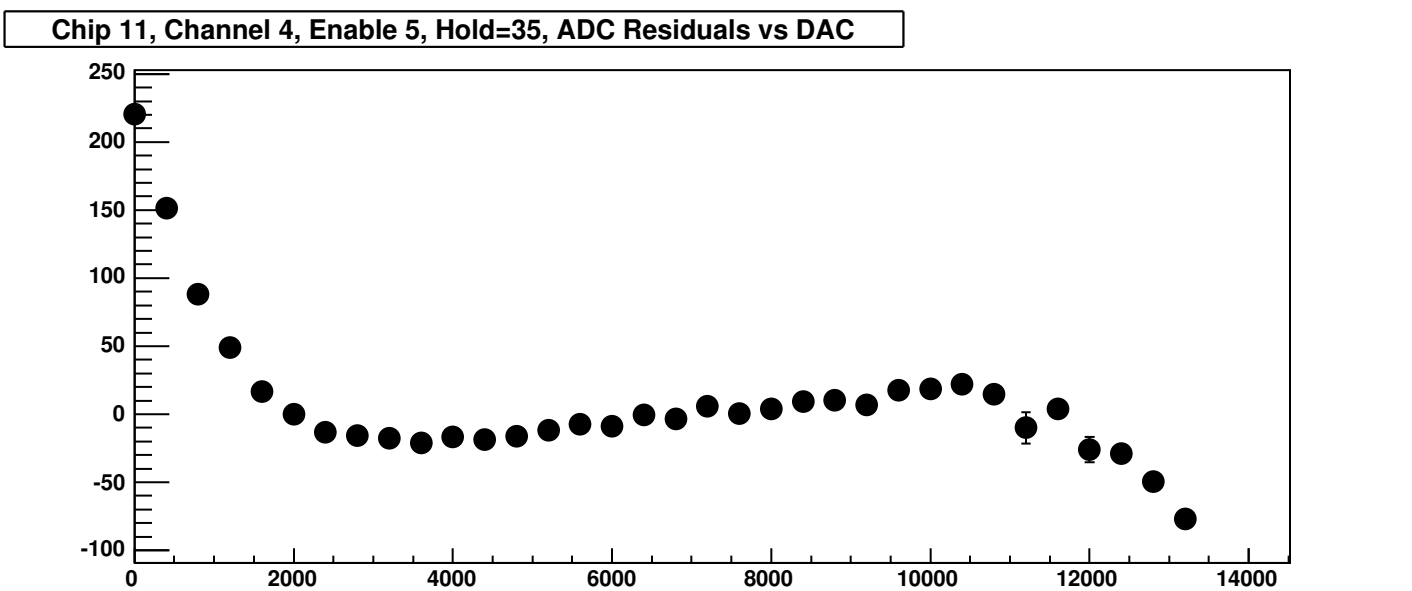

Chip 11, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.8 / 23
p0 -564.6 ± 0.7821
p1 0.02994 ± 0.0001172
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

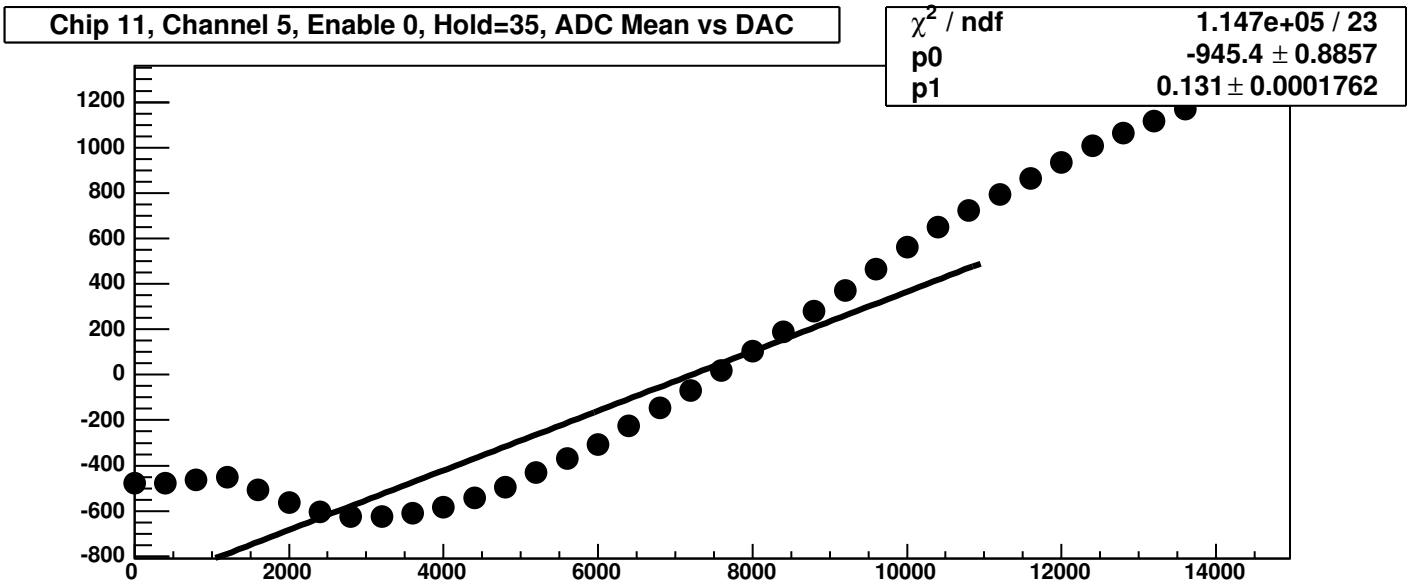

Chip 11, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

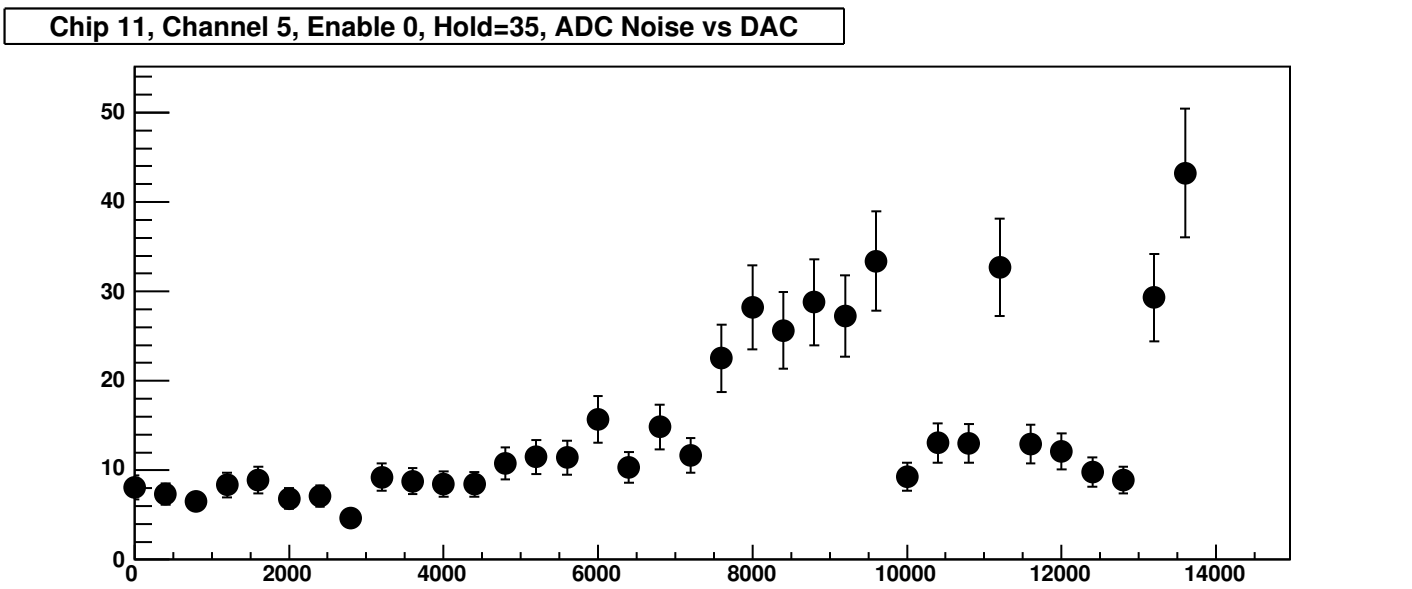

Chip 11, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

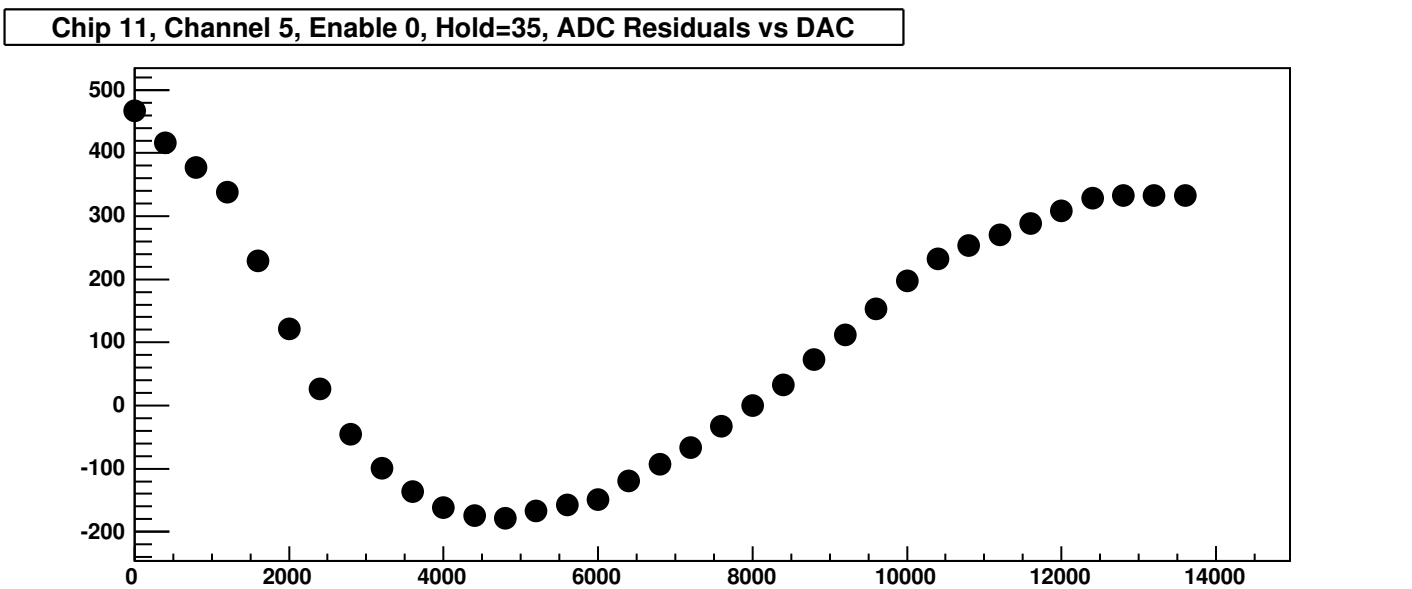

Chip 11, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1197 / 23
p0 -747.8 ± 1.025
p1 0.1877 ± 0.0001931
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

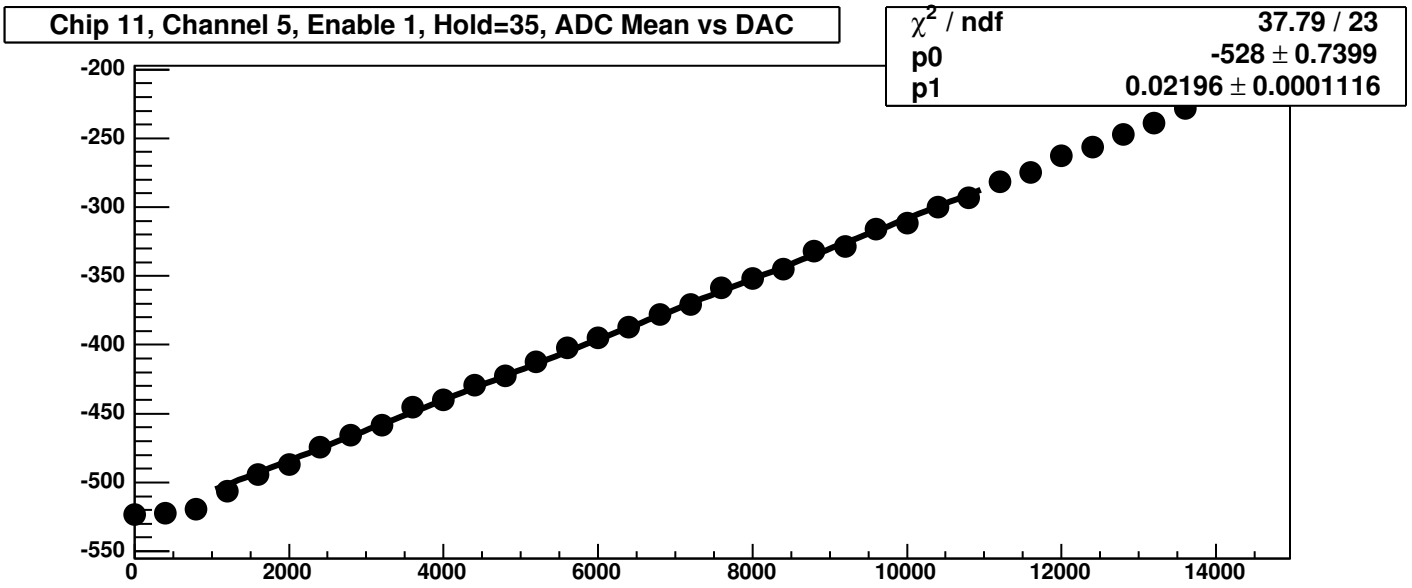

Chip 11, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

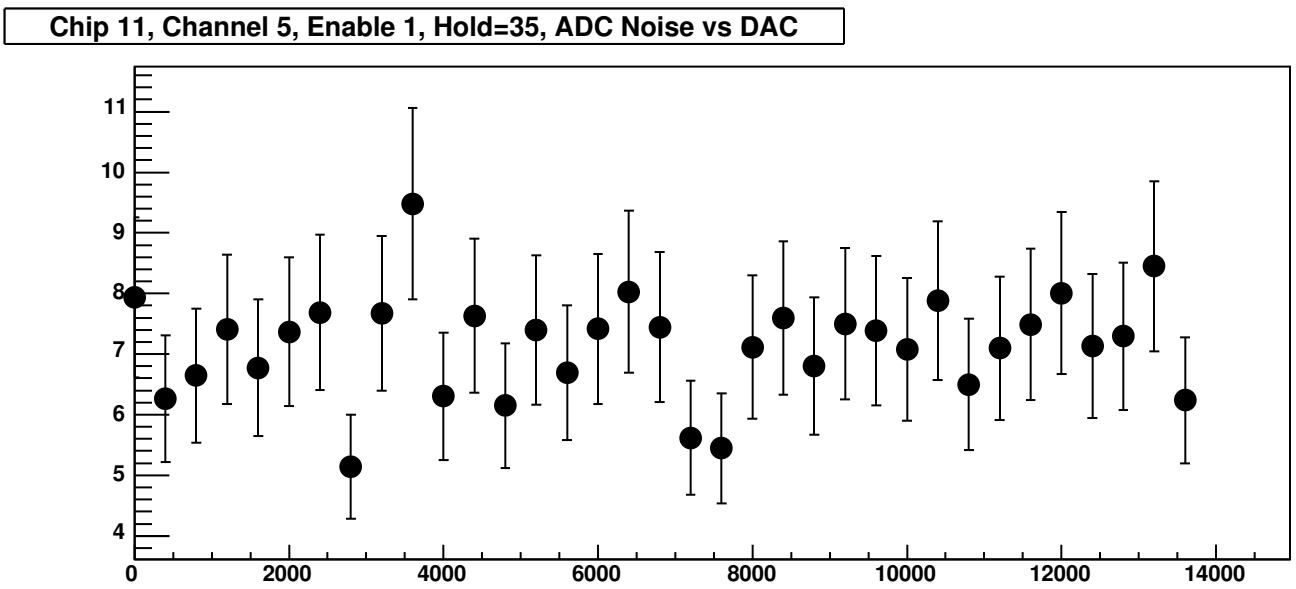

Chip 11, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

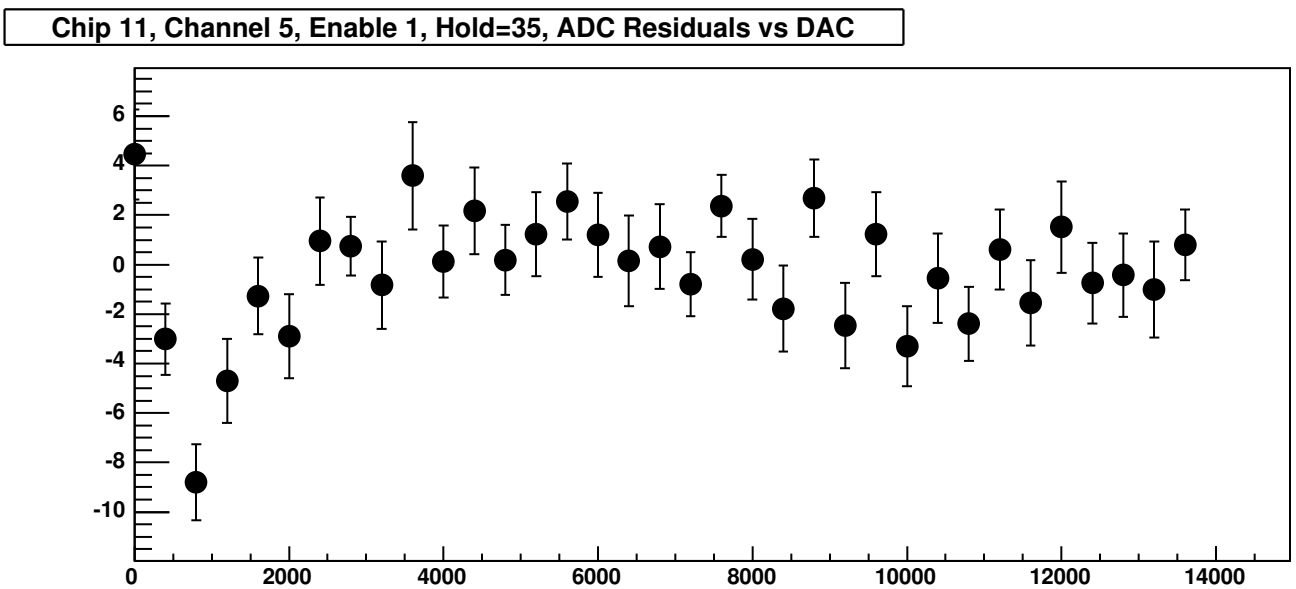

Chip 11, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.147e+05 / 23
p0 -945.4 ± 0.8857
p1 0.131 ± 0.0001762
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

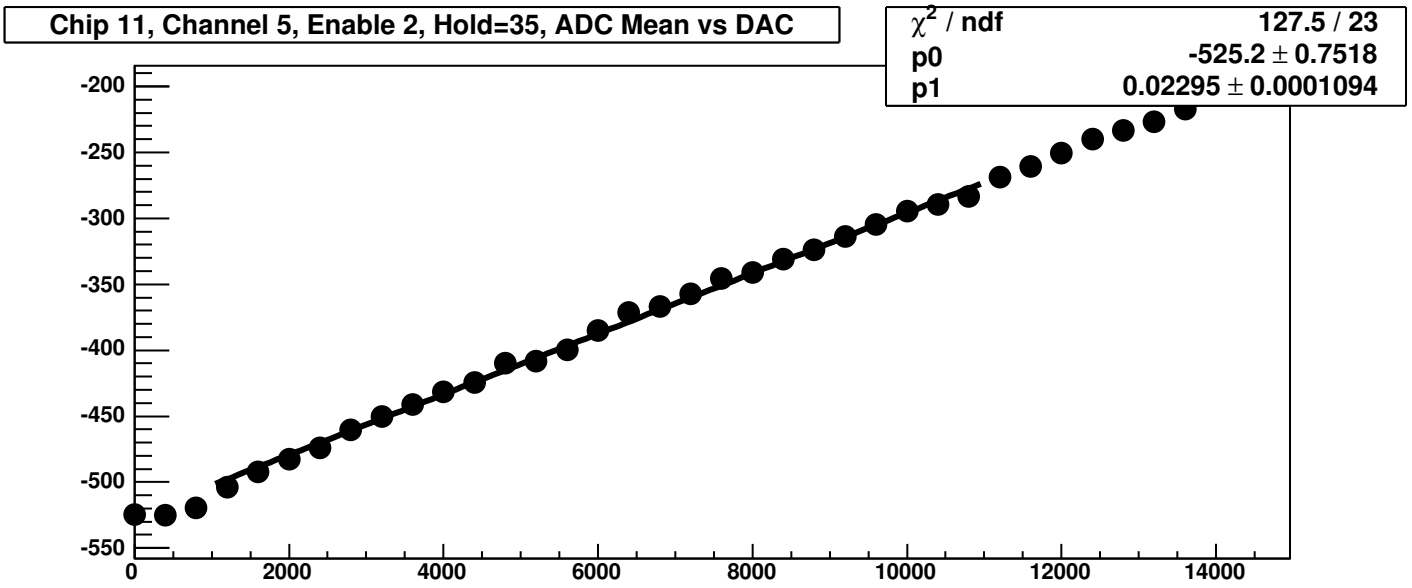

Chip 11, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

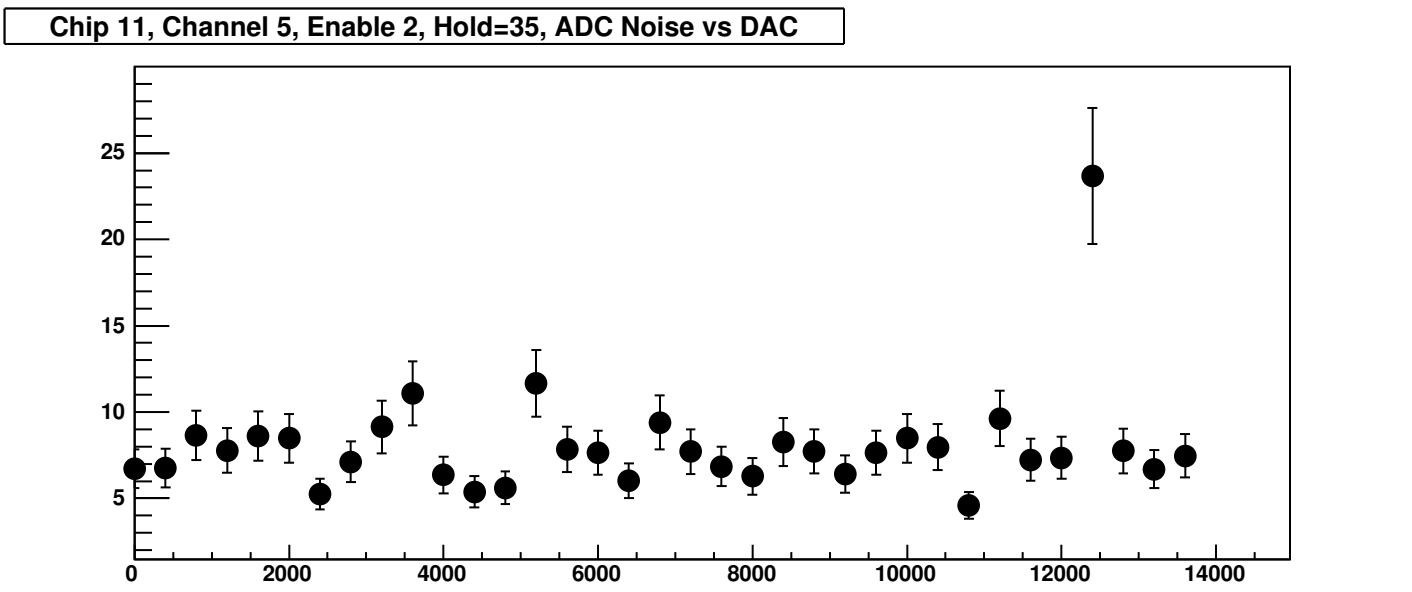

Chip 11, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

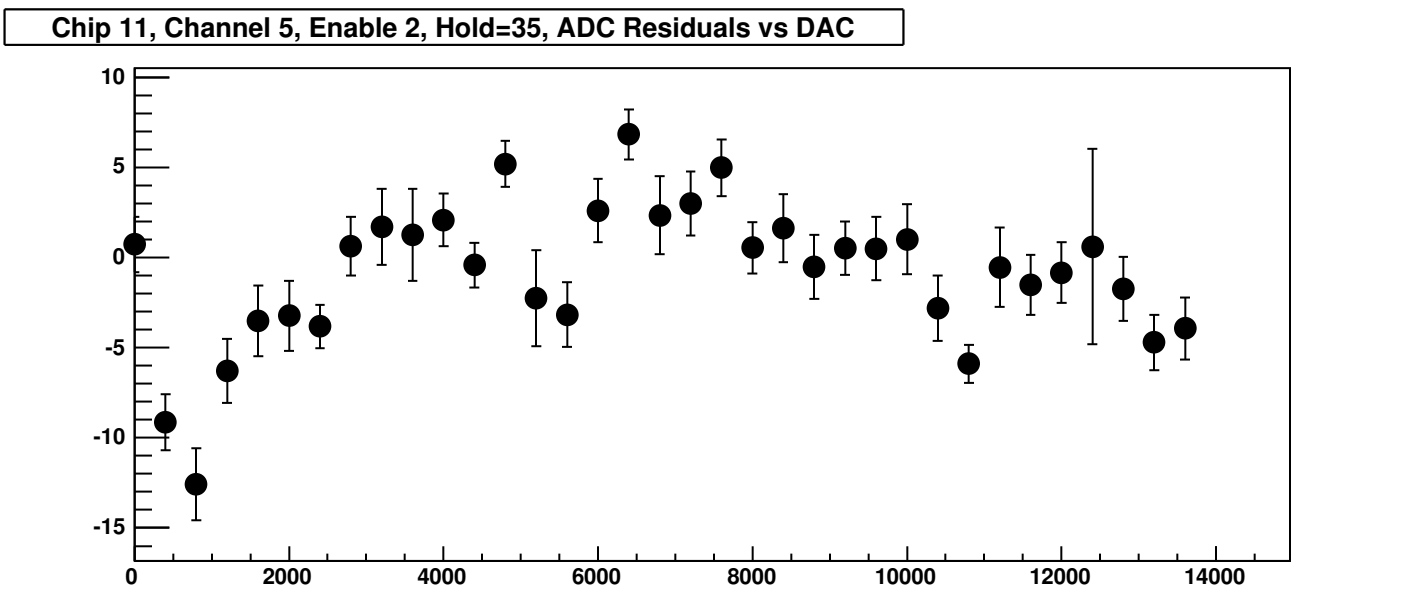

Chip 11, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.79 / 23
p0 -528 ± 0.7399
p1 0.02196 ± 0.0001116
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

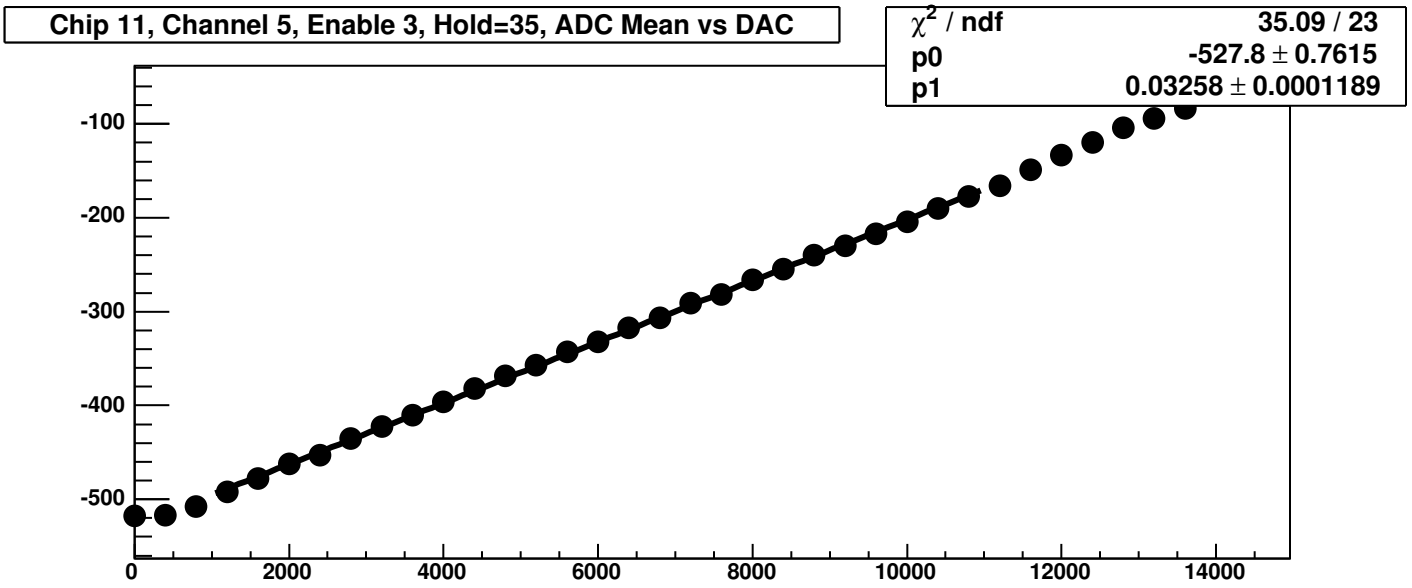

Chip 11, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

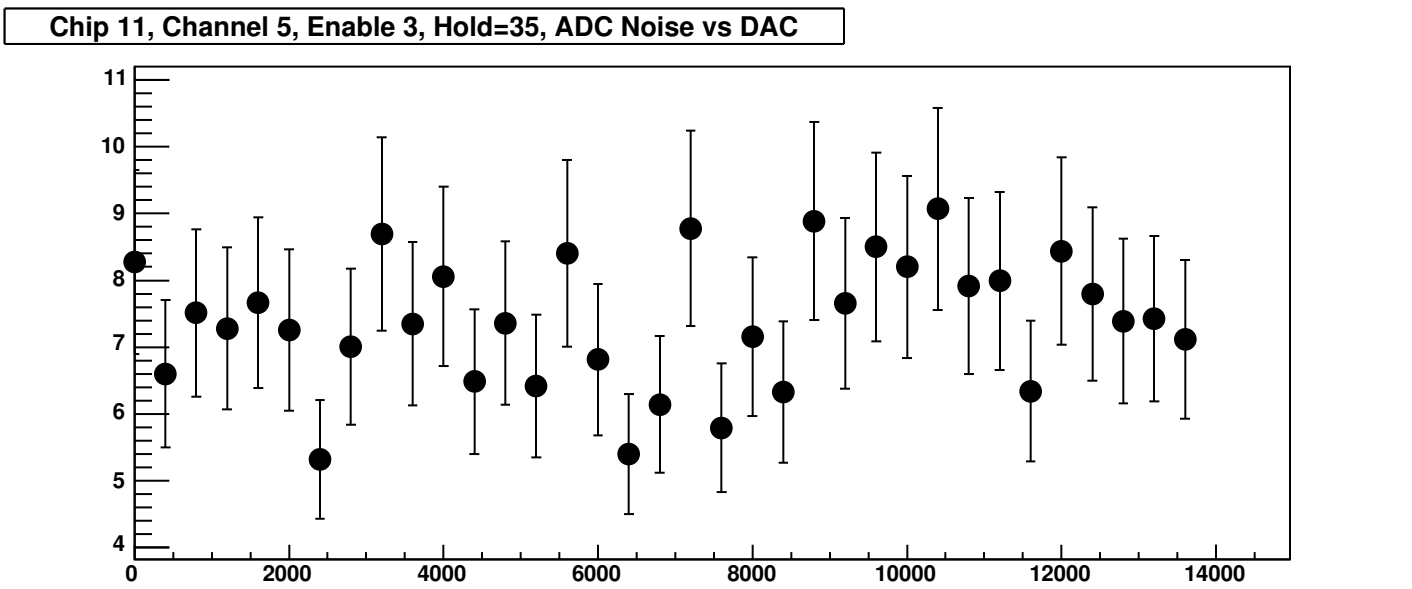

Chip 11, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

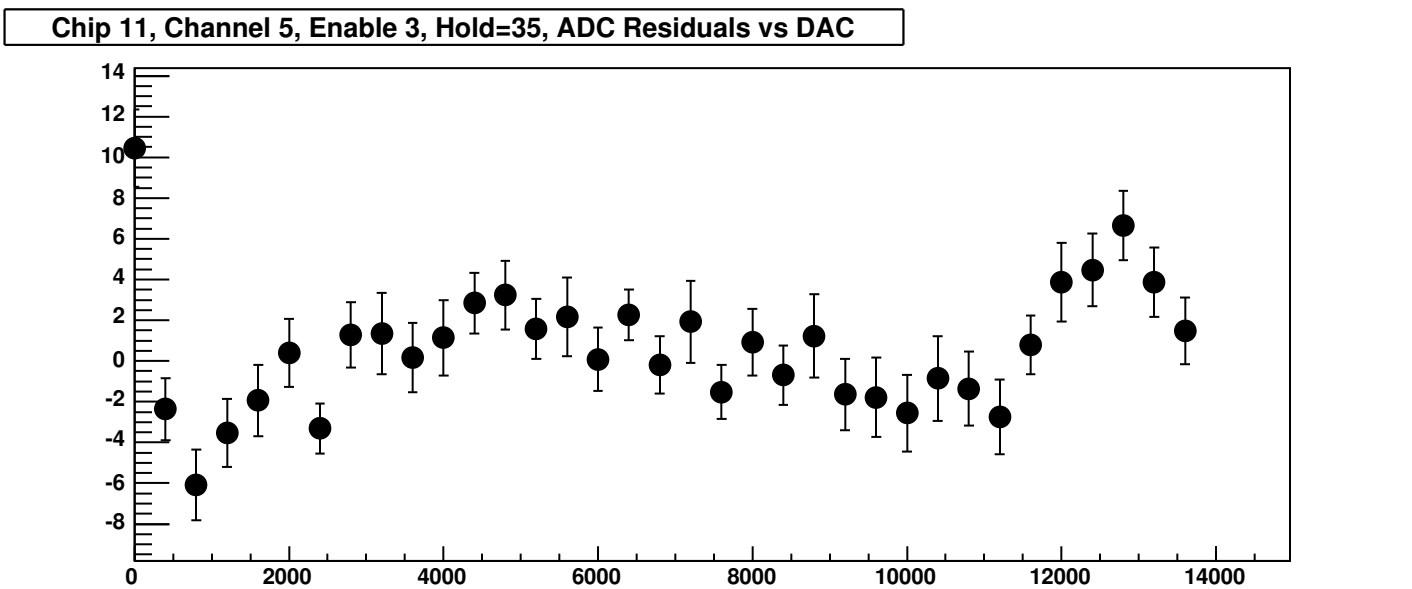

Chip 11, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 127.5 / 23
p0 -525.2 ± 0.7518
p1 0.02295 ± 0.0001094
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

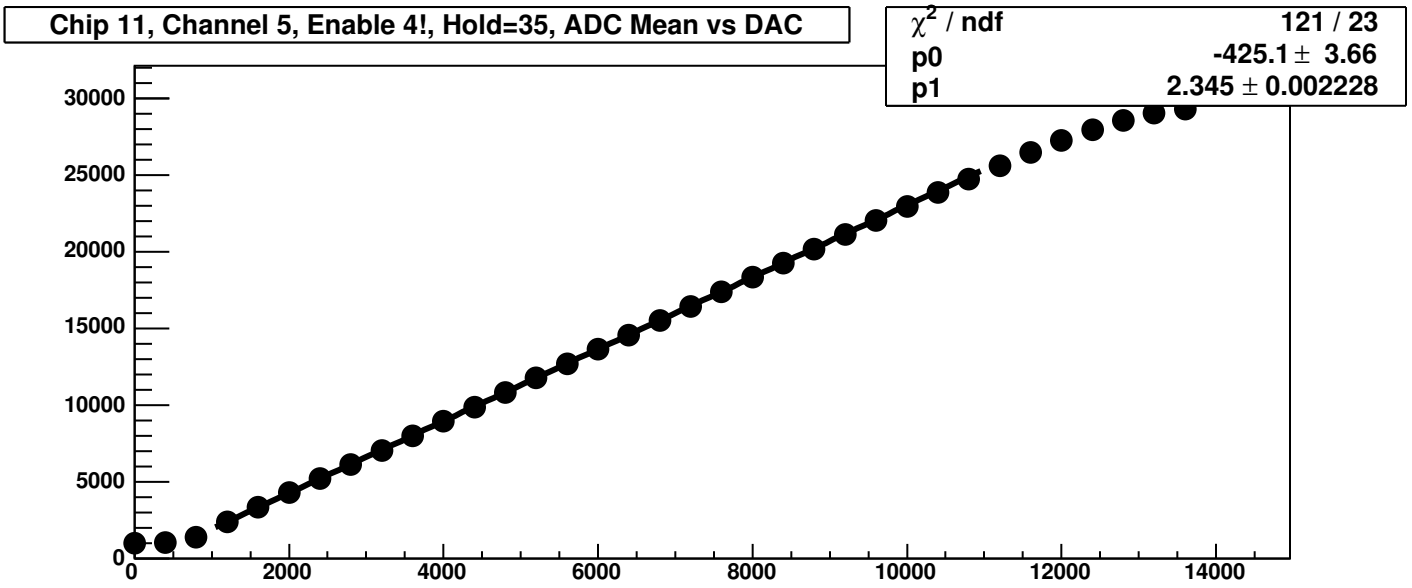

Chip 11, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

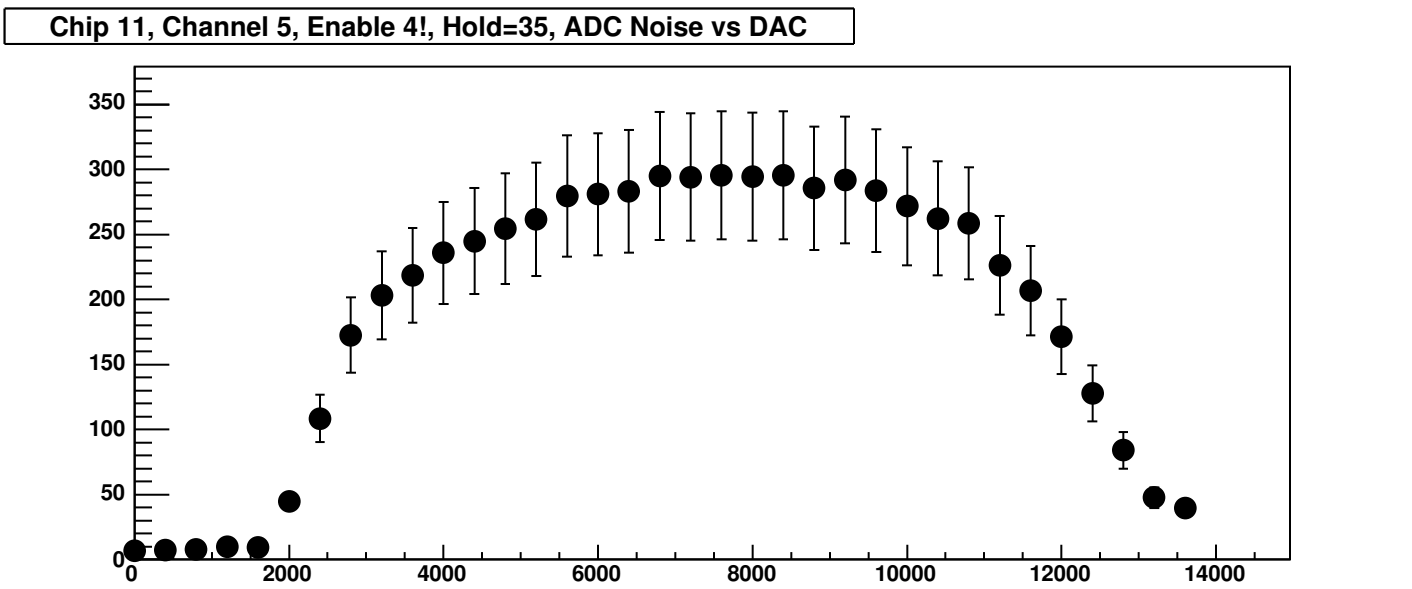

Chip 11, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

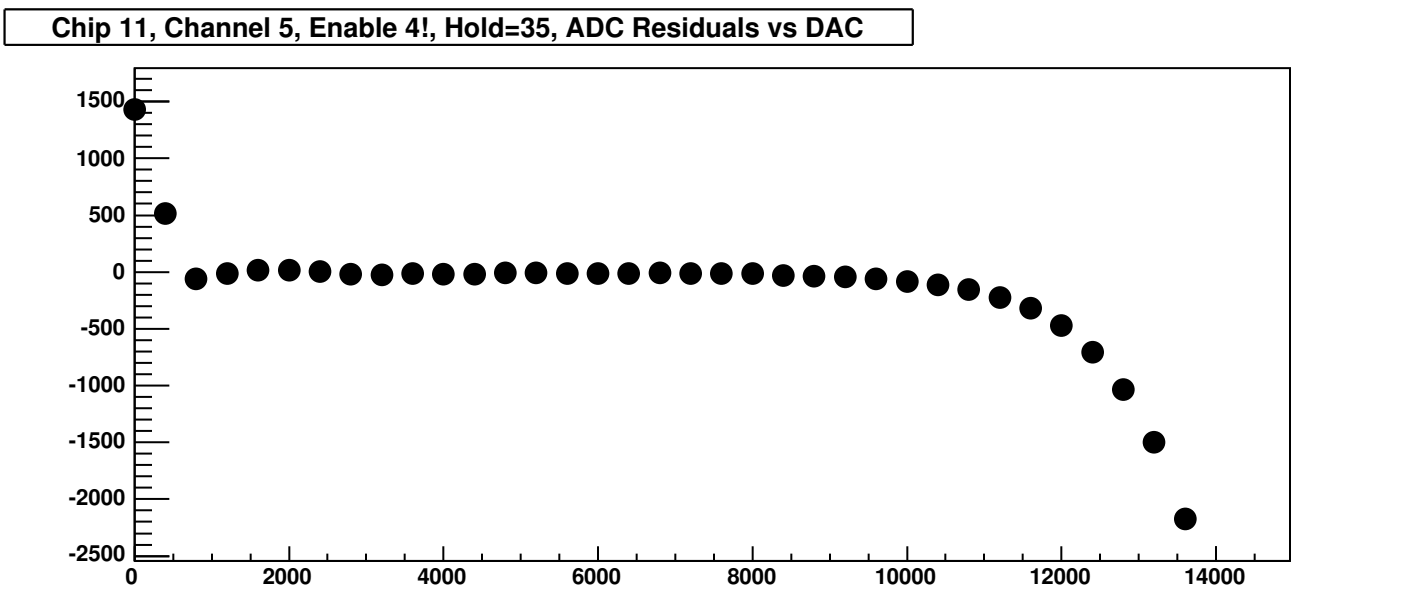

Chip 11, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.09 / 23
p0 -527.8 ± 0.7615
p1 0.03258 ± 0.0001189
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

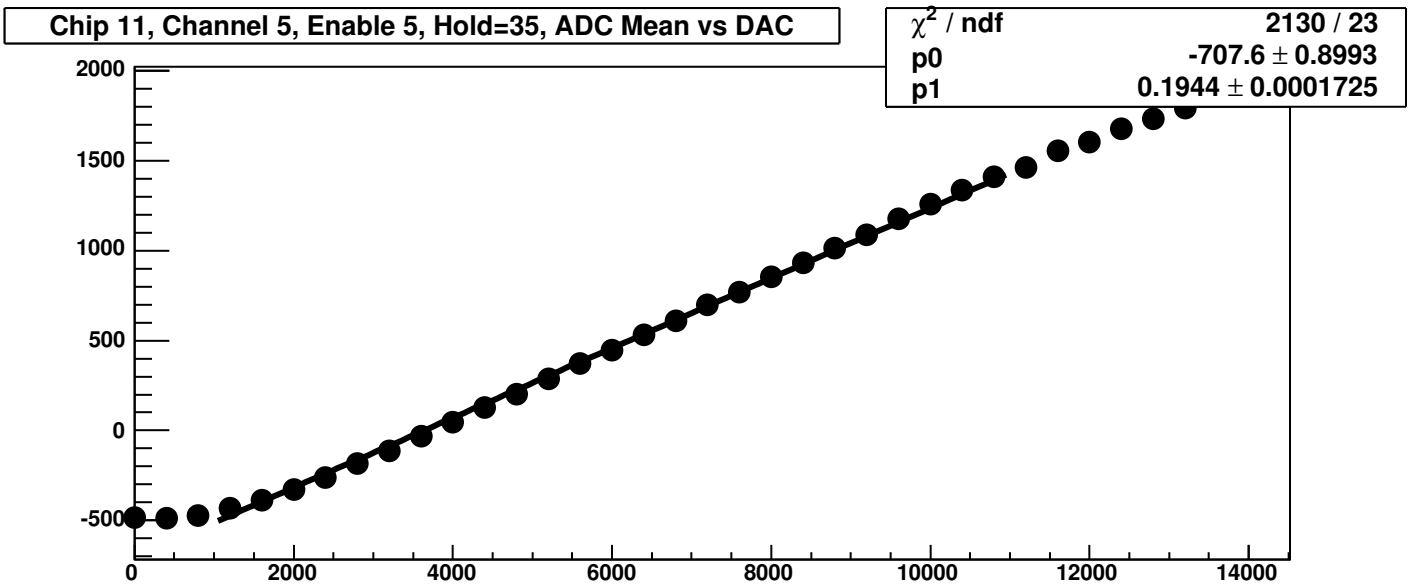

Chip 11, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

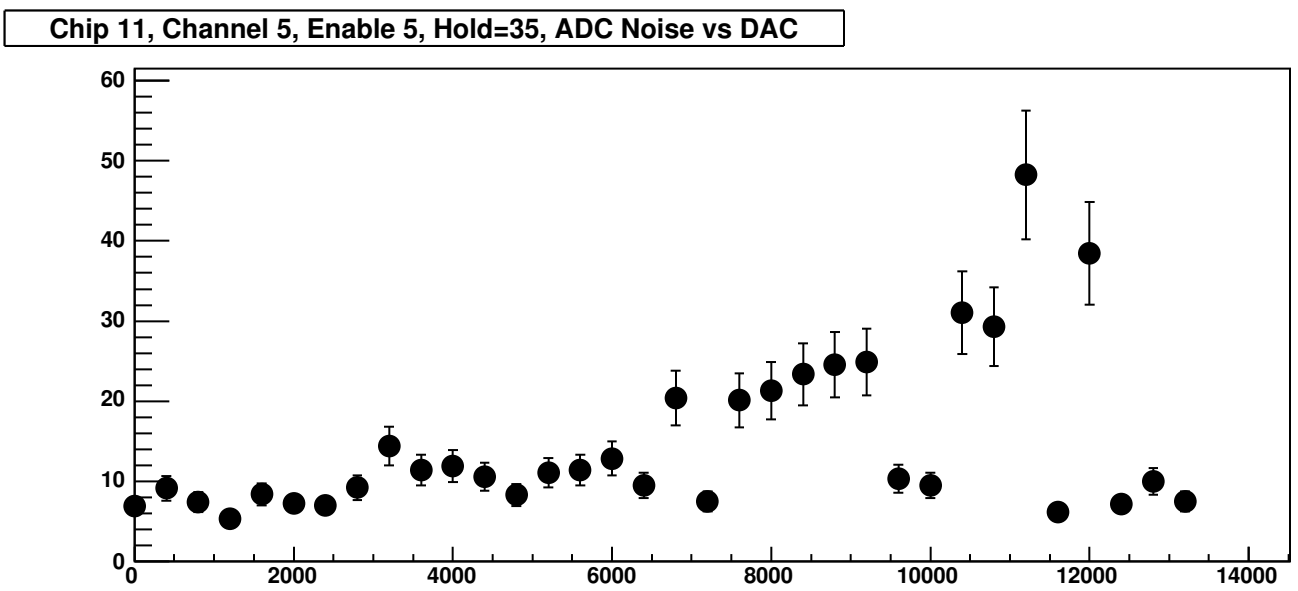

Chip 11, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

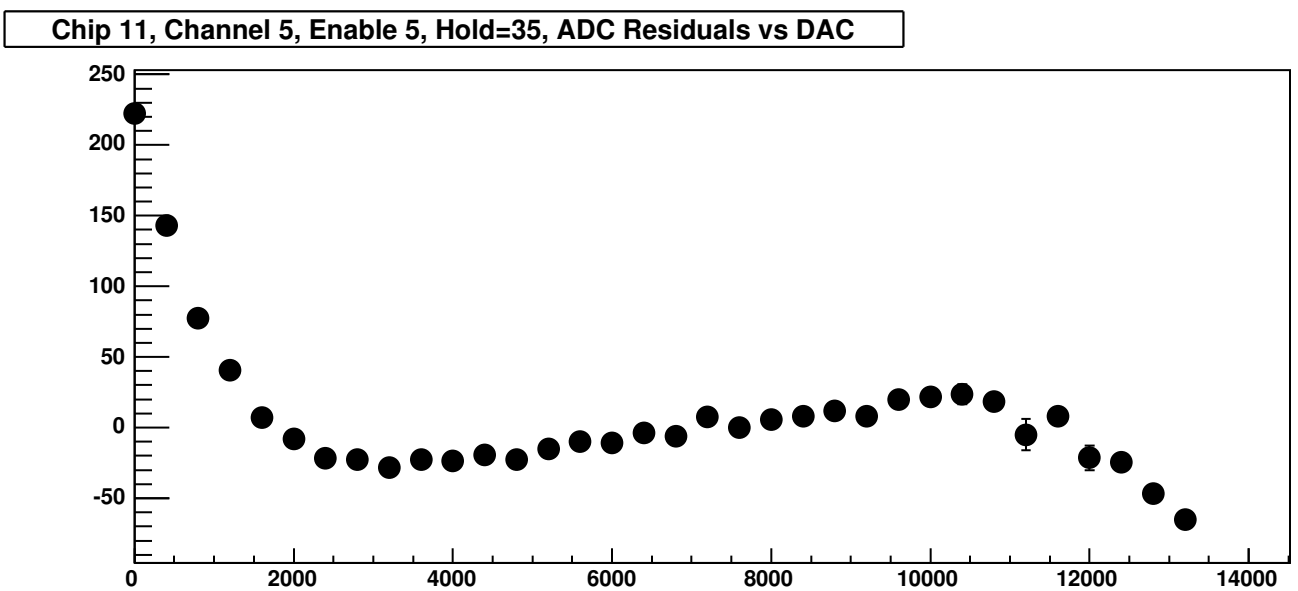

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 121 / 23
p0 -425.1 ± 3.66
p1 2.345 ± 0.002228
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

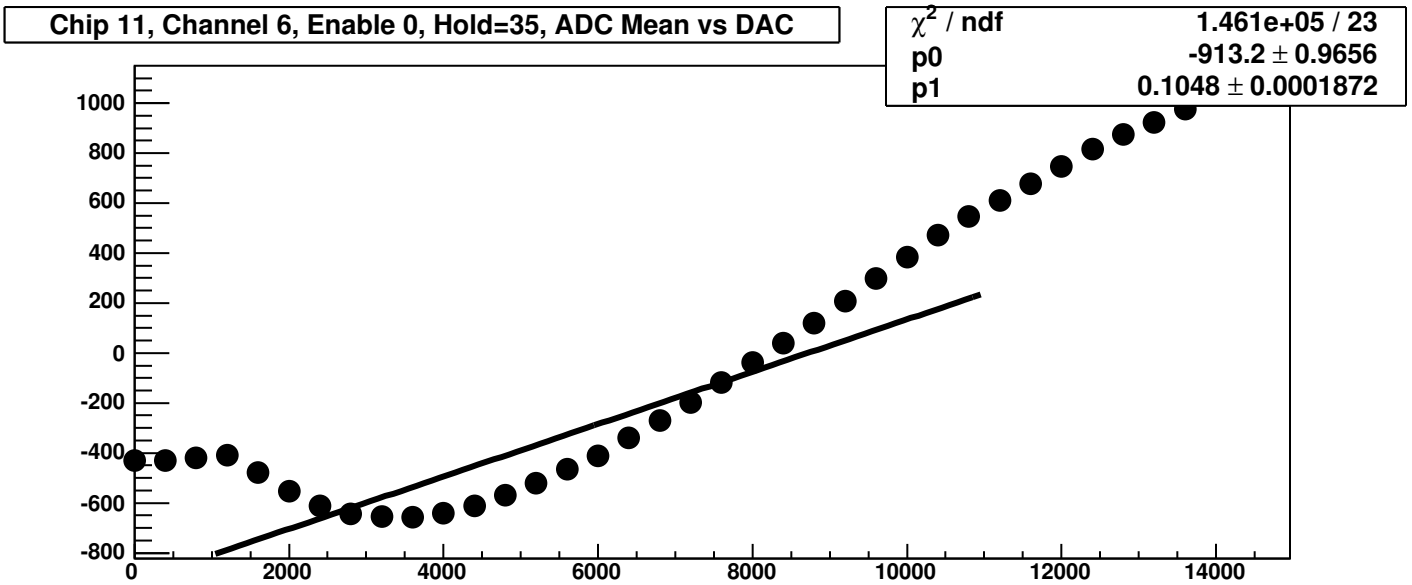

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

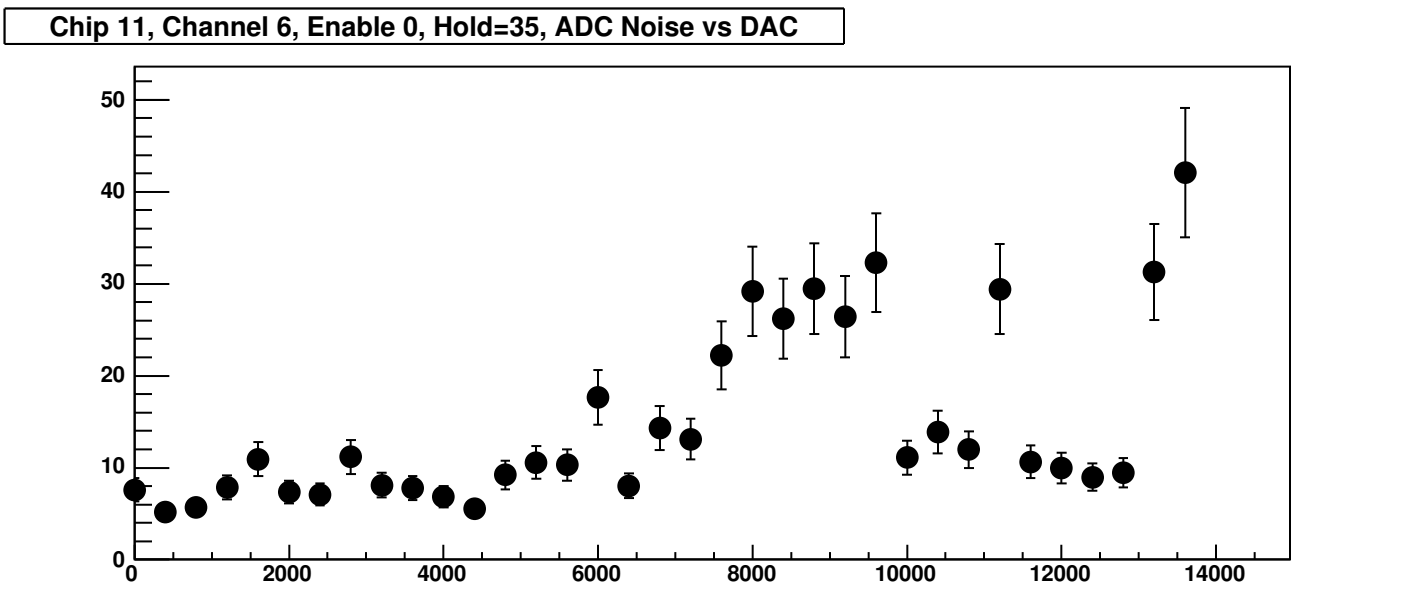

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

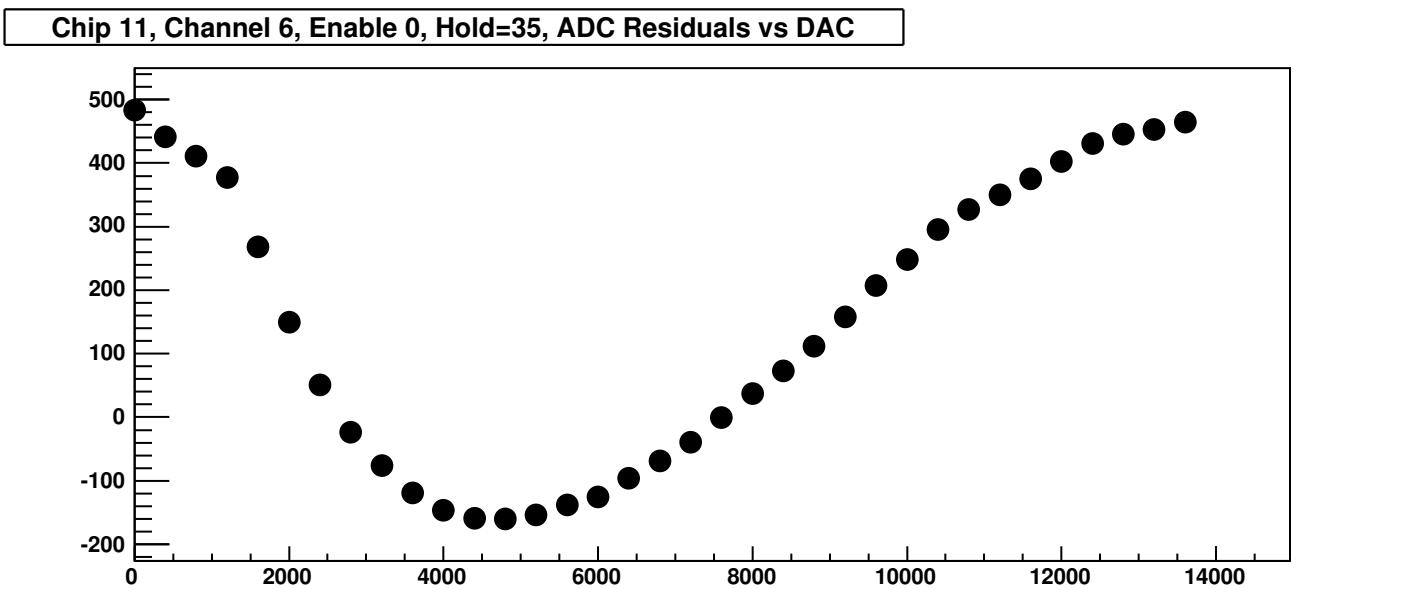

Chip 11, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2130 / 23
p0 -707.6 ± 0.8993
p1 0.1944 ± 0.0001725
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

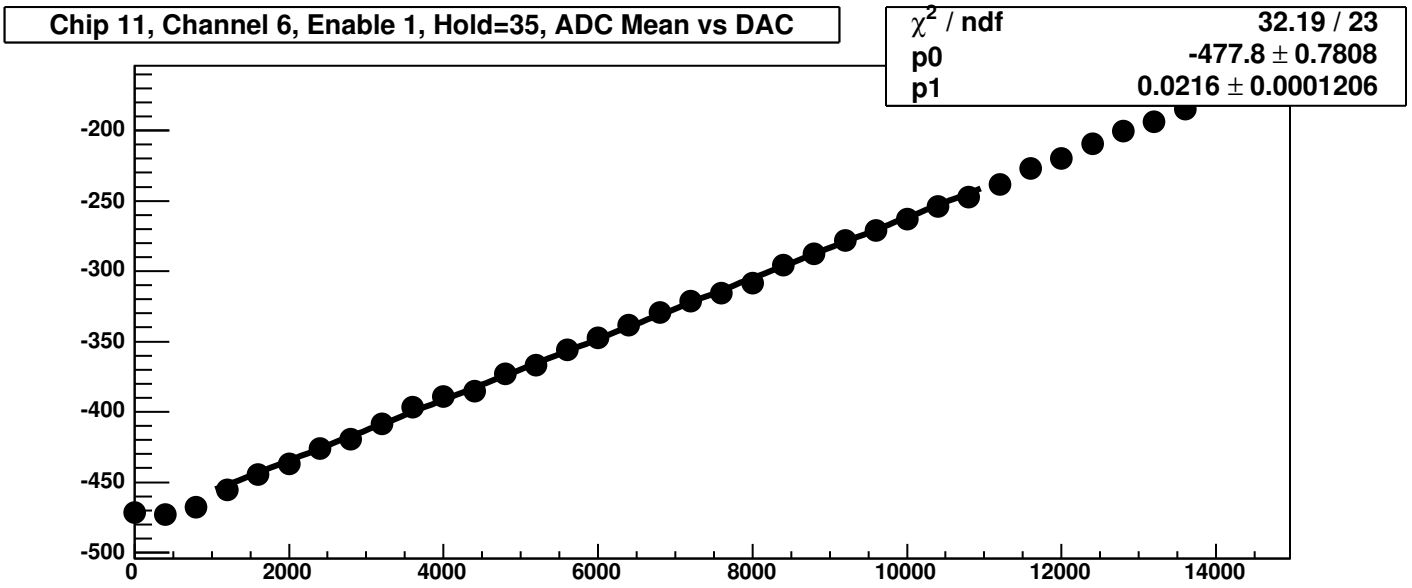

Chip 11, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

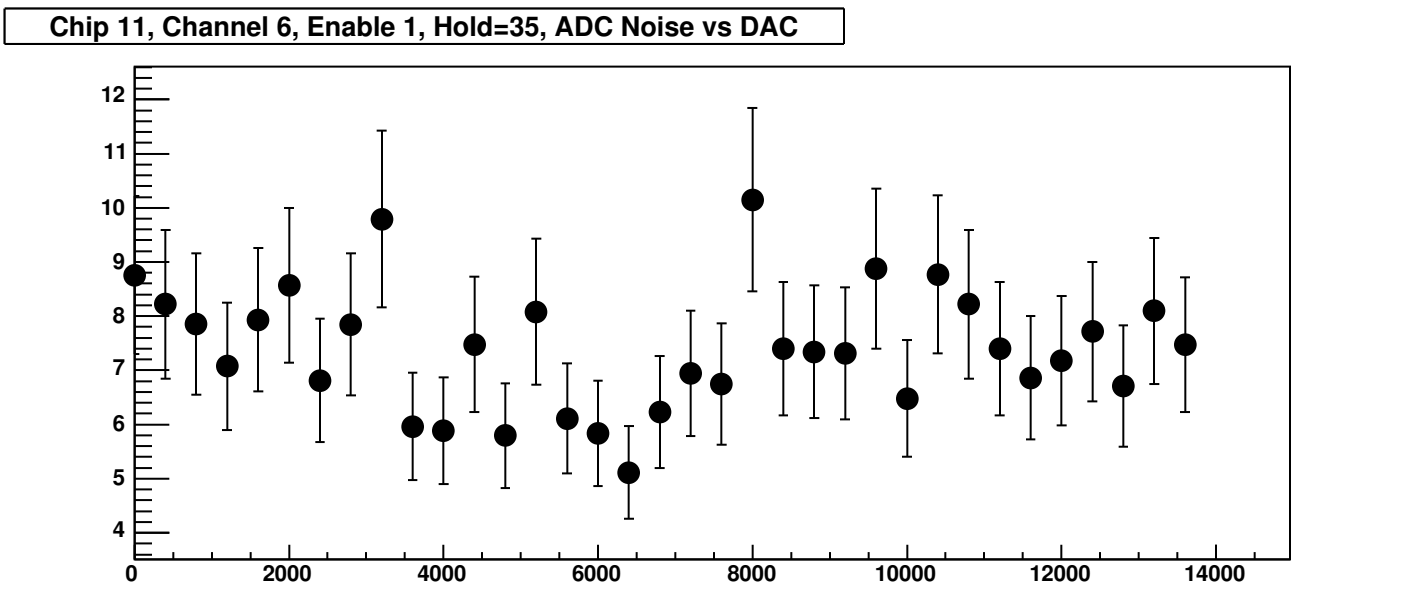

Chip 11, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

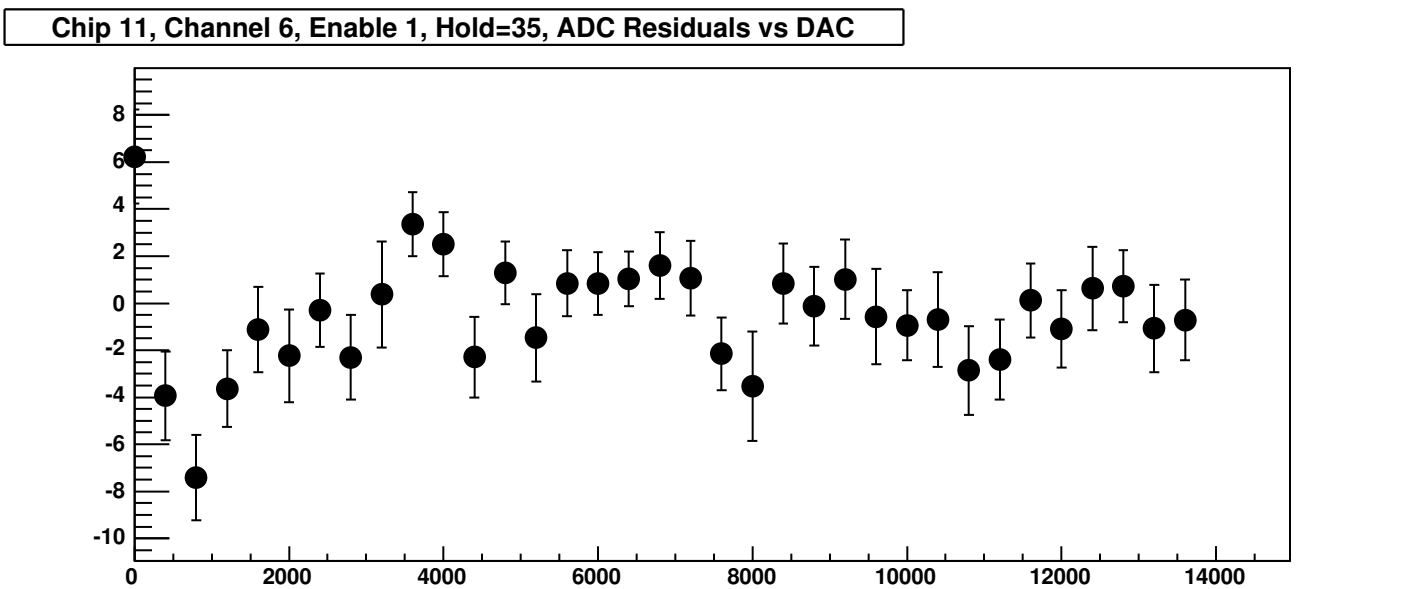

Chip 11, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.461e+05 / 23
p0 -913.2 ± 0.9656
p1 0.1048 ± 0.0001872
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

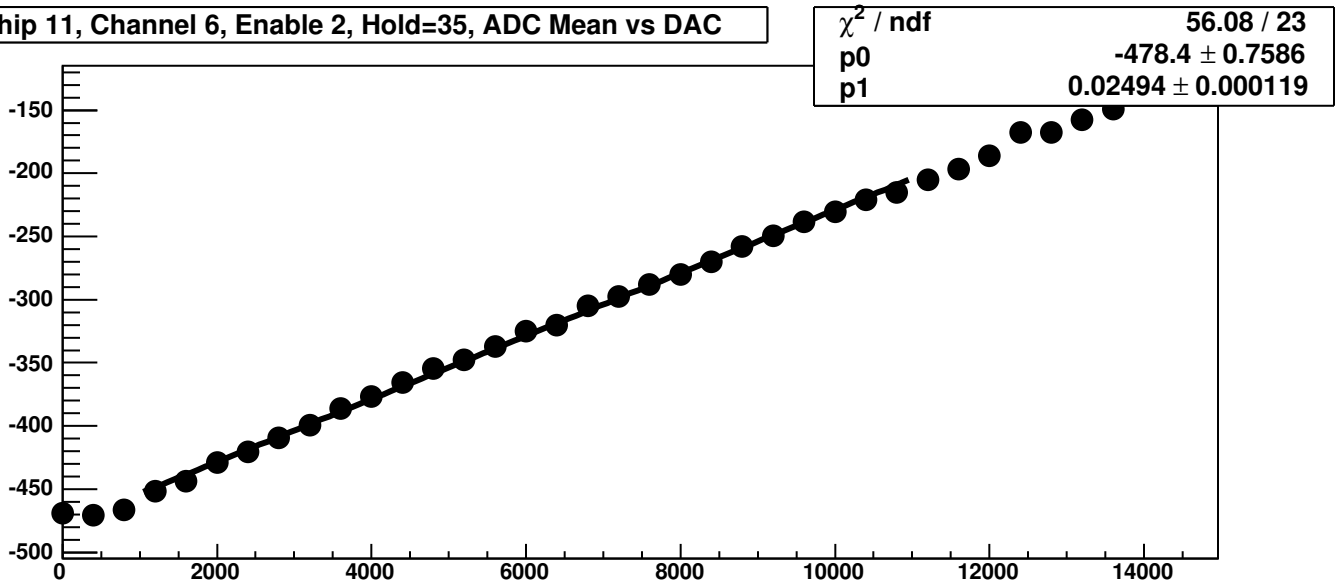

Chip 11, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.19 / 23
p0 -477.8 ± 0.7808
p1 0.0216 ± 0.0001206
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

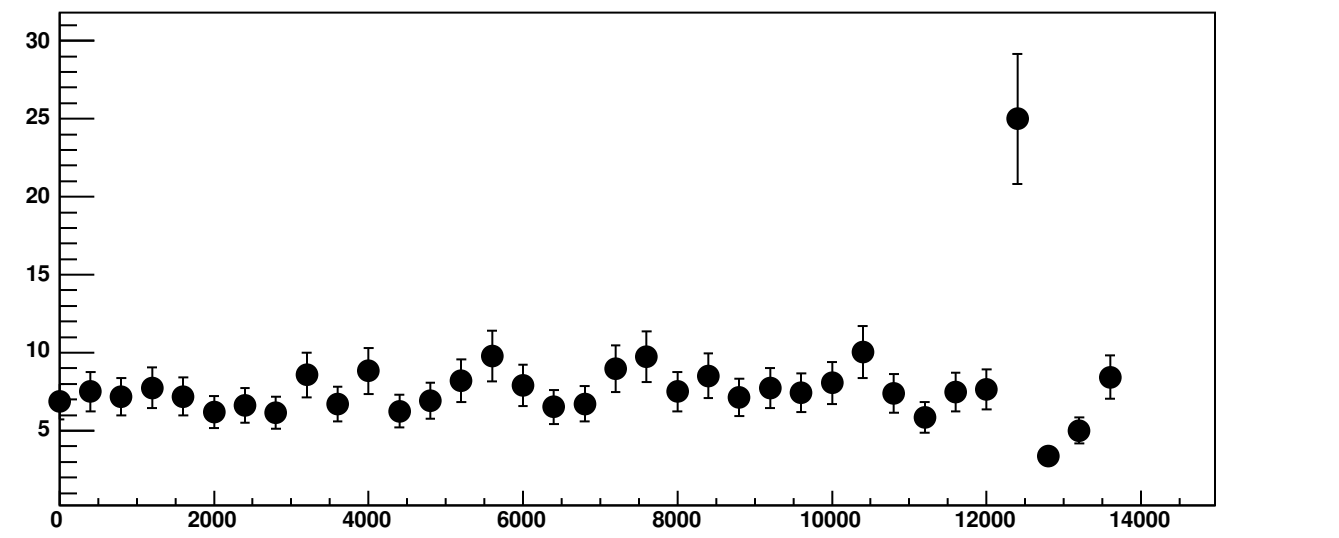

Chip 11, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

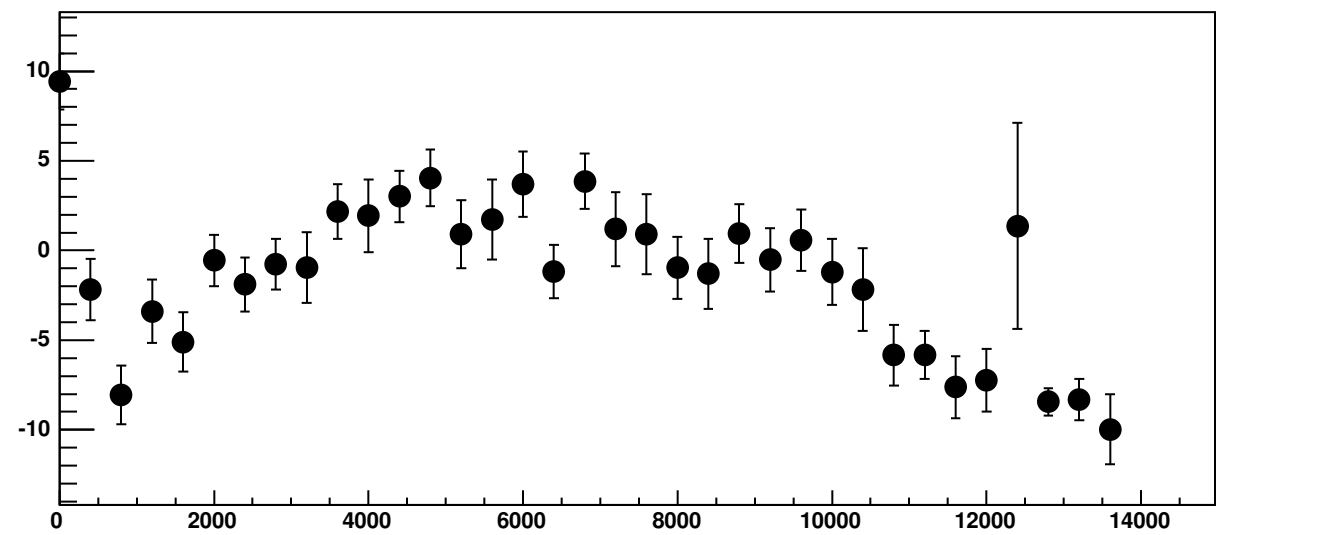
Chip 11, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



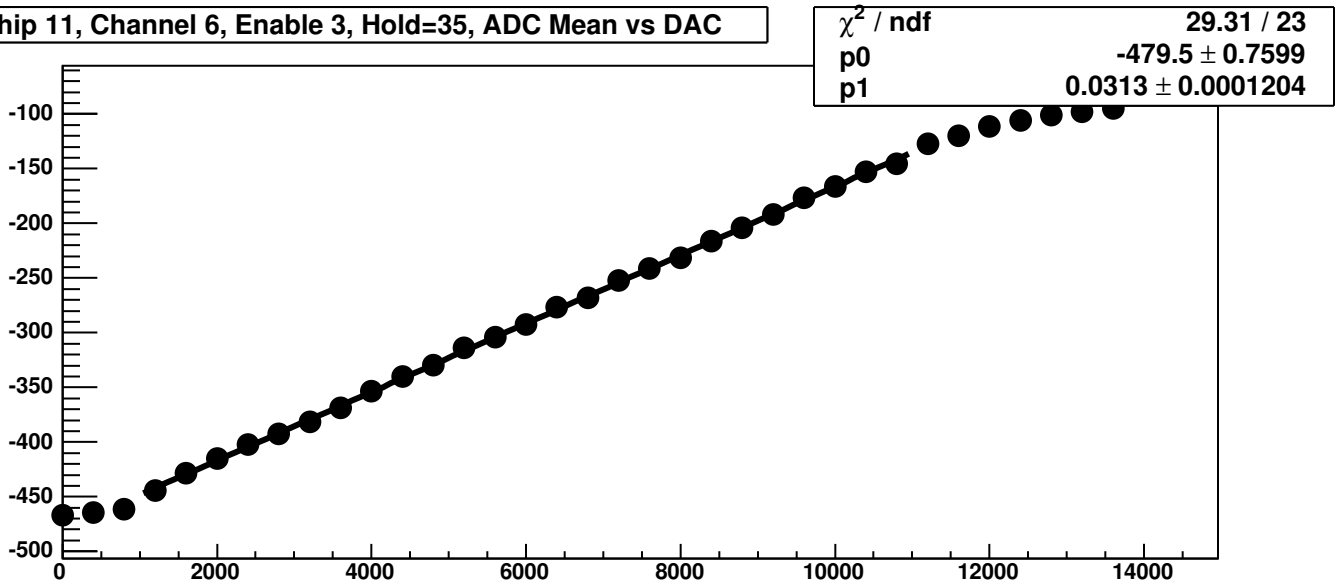
Chip 11, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



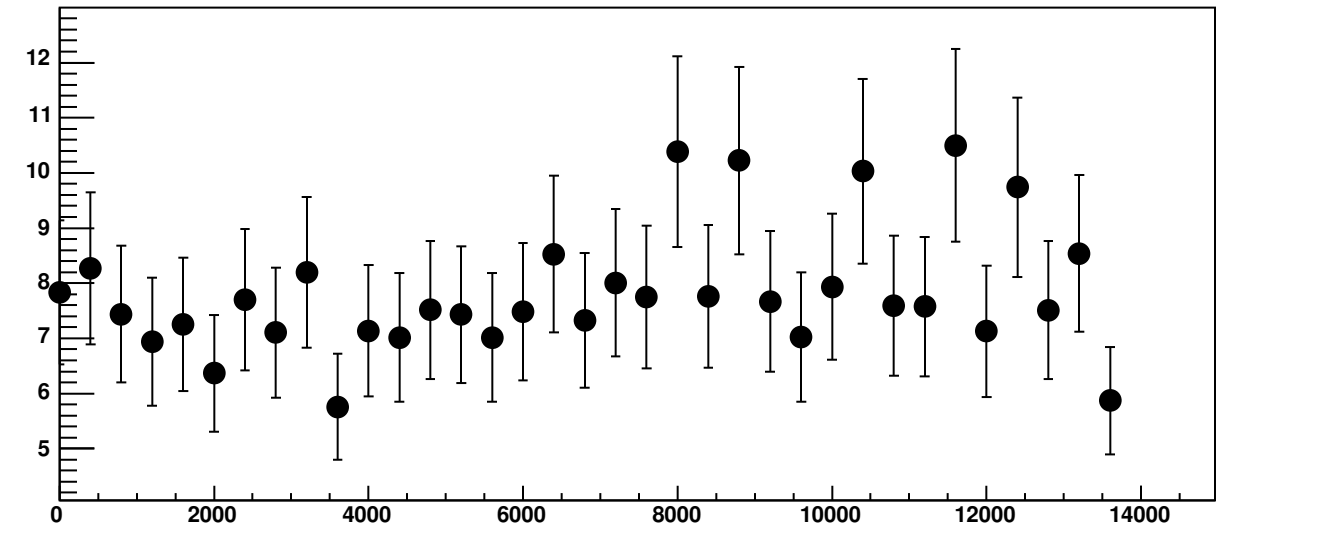
Chip 11, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



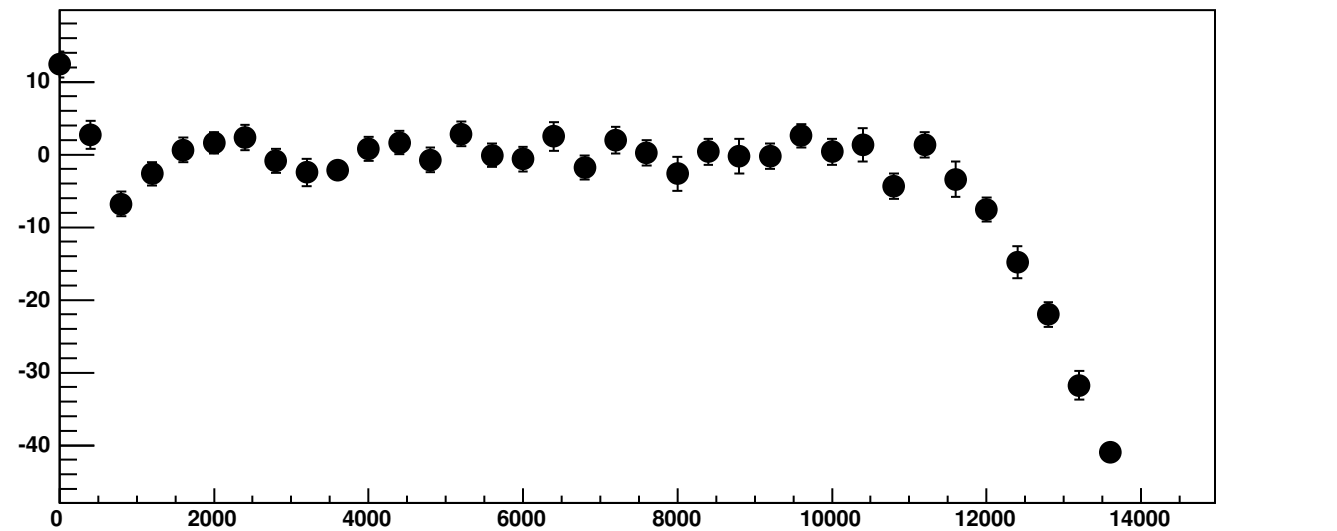
Chip 11, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



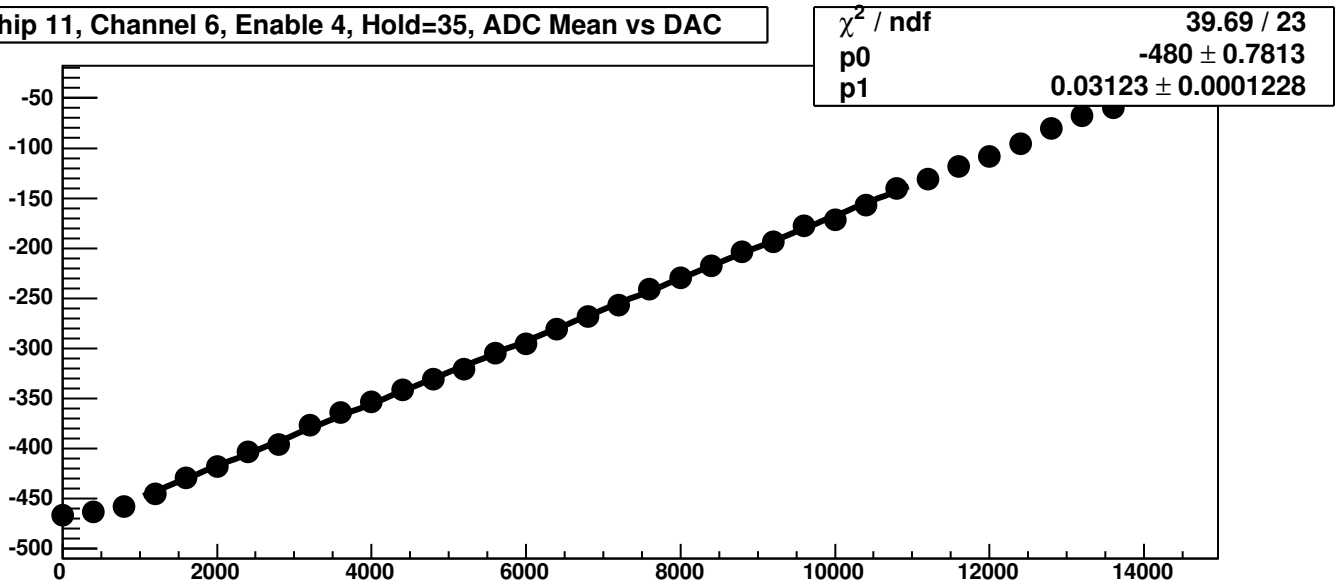
Chip 11, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



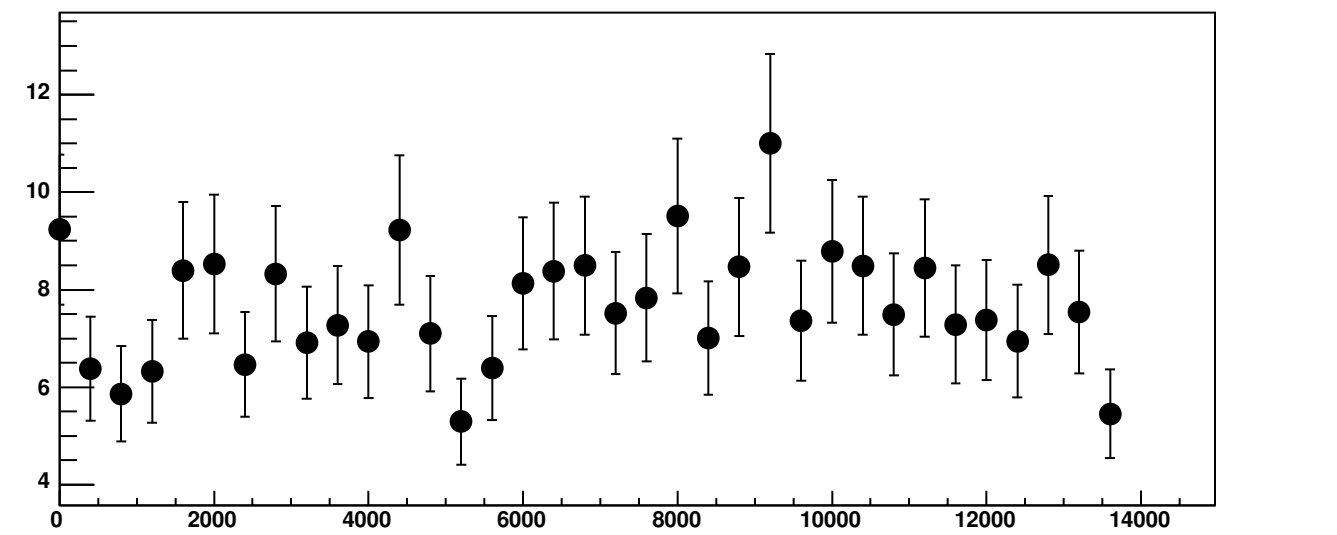
Chip 11, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



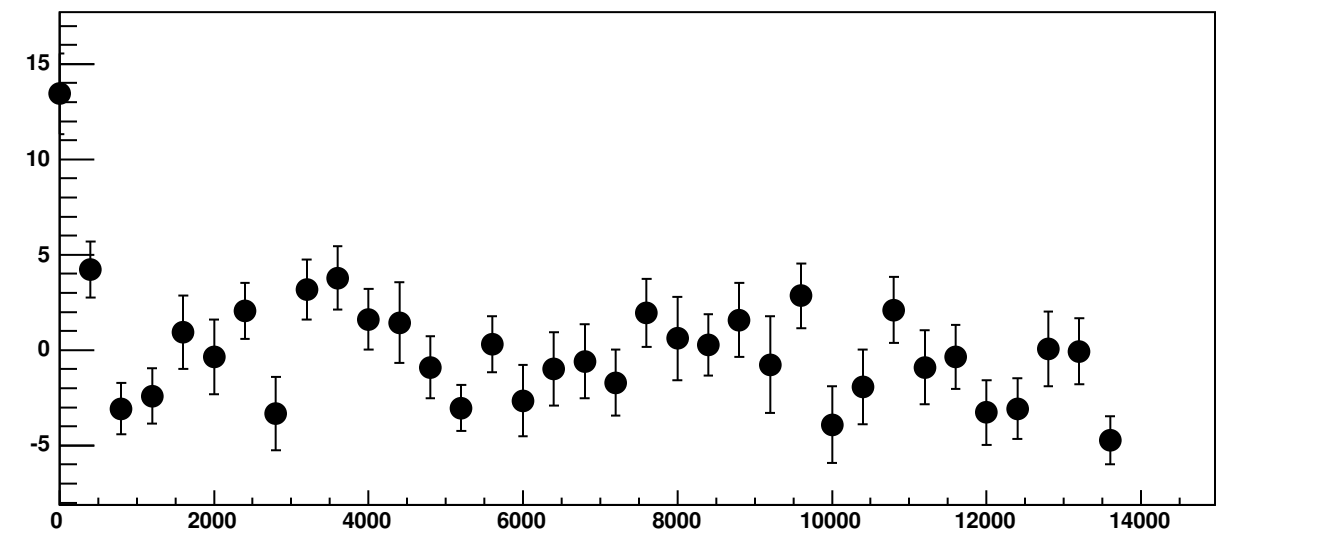
Chip 11, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

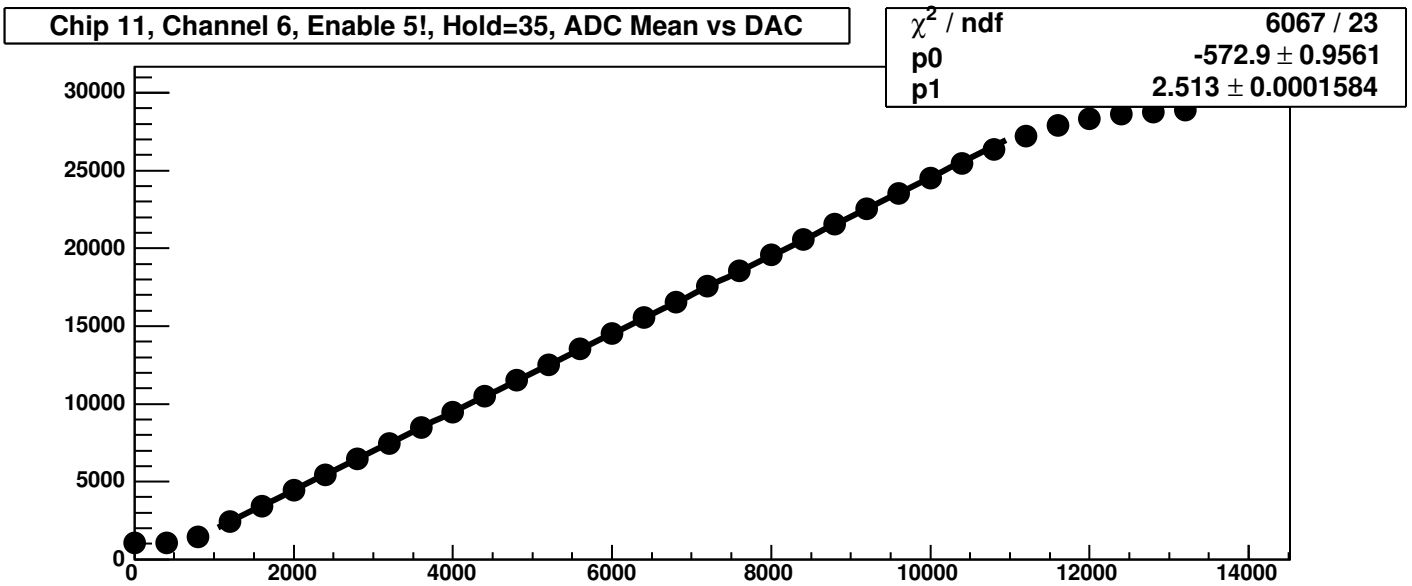


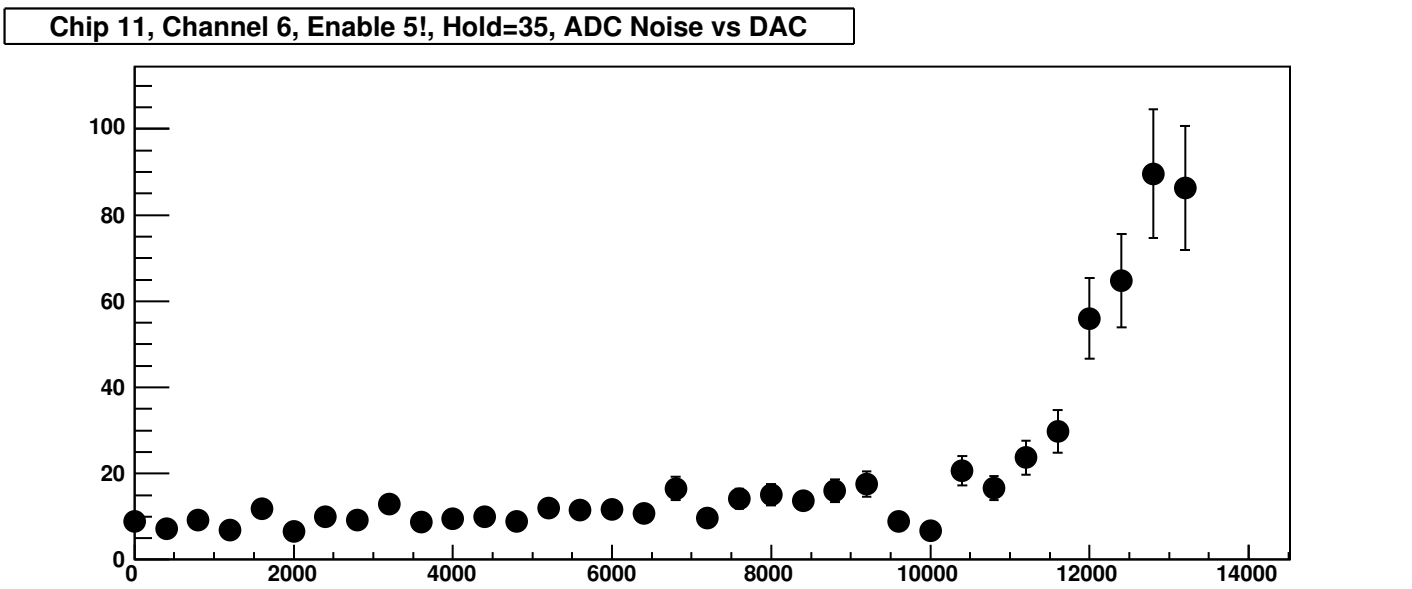
Chip 11, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

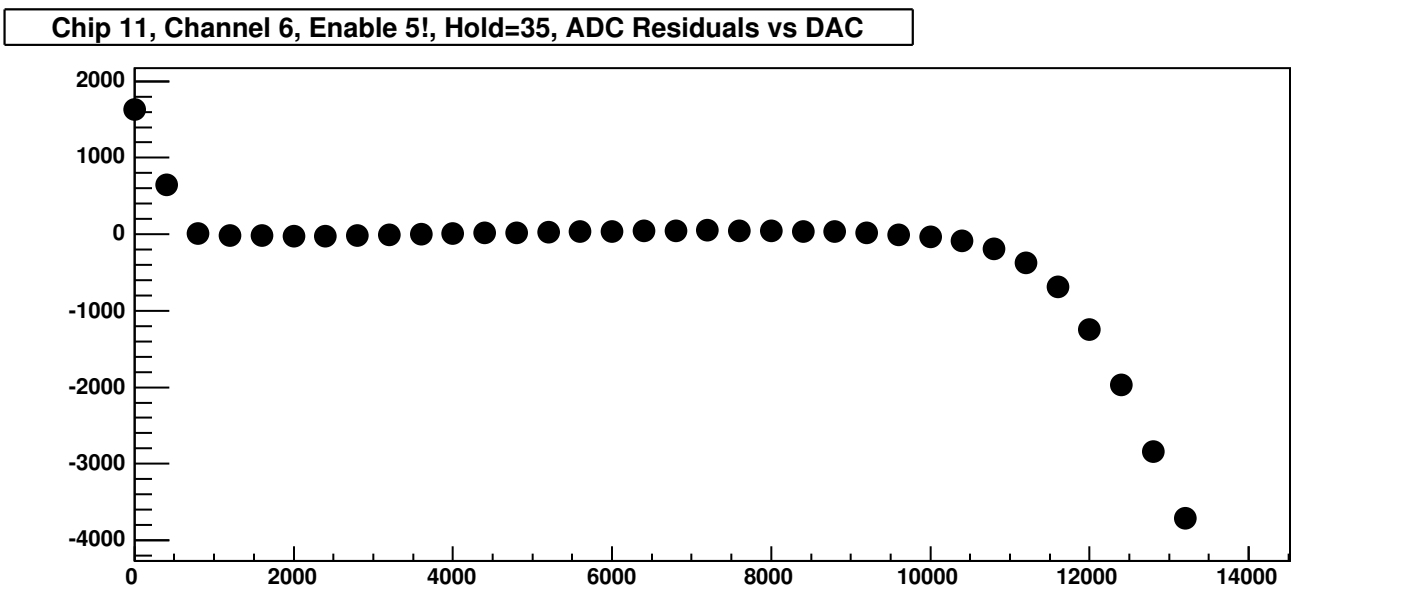


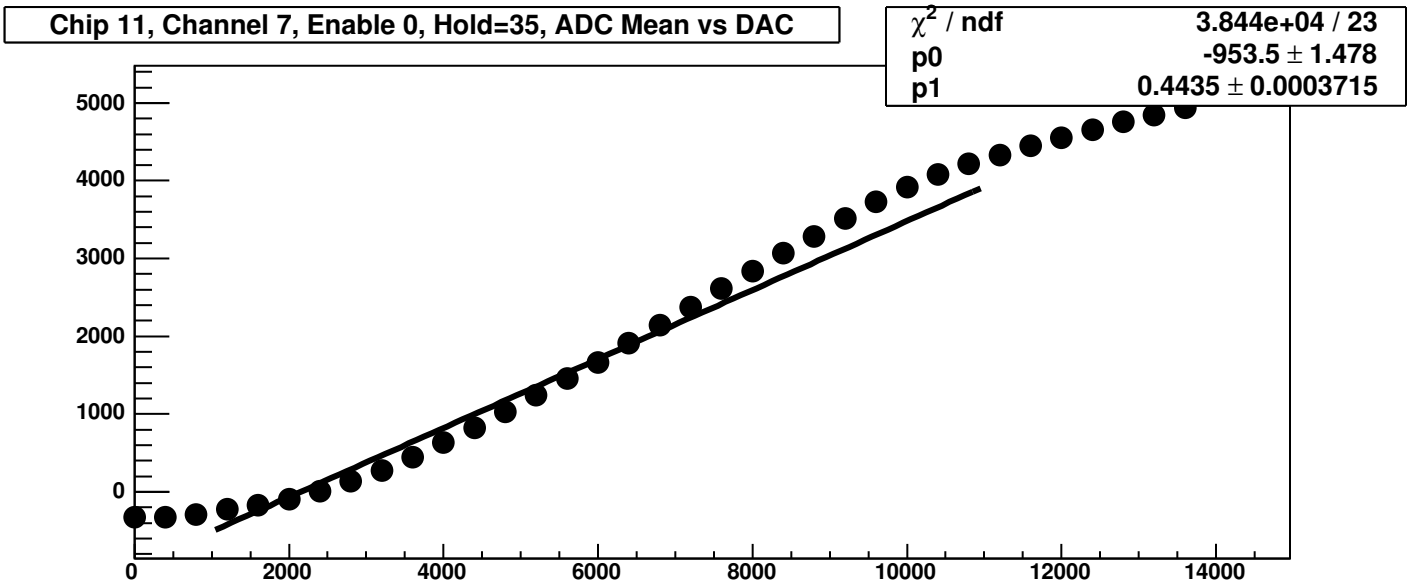
Chip 11, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

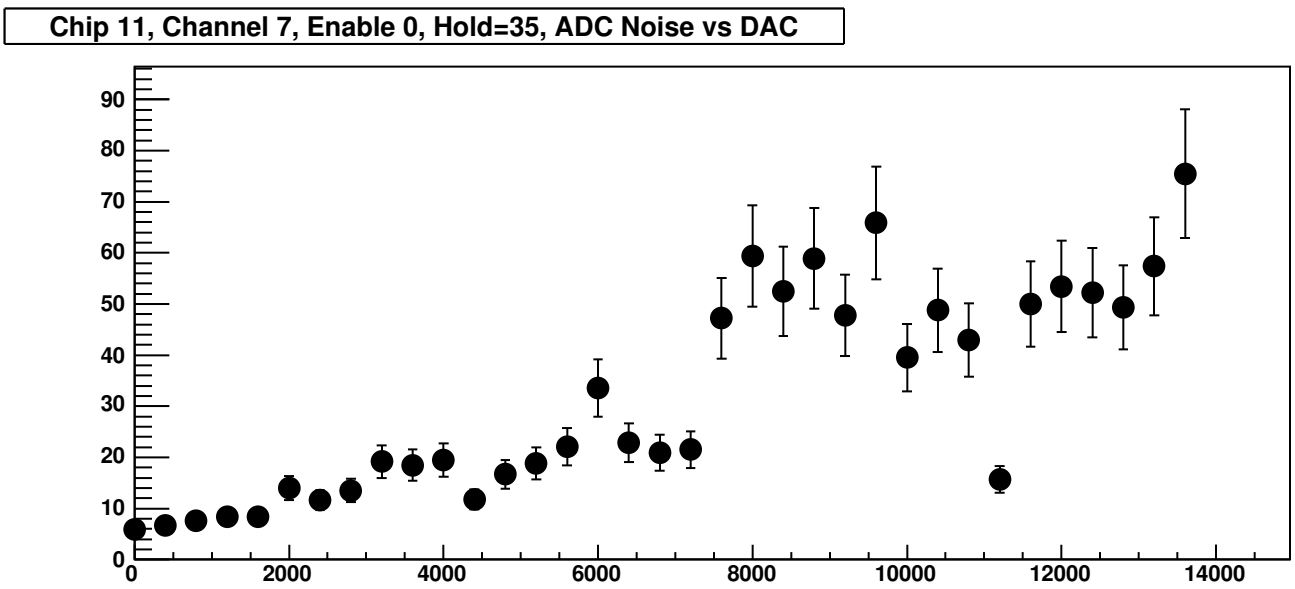


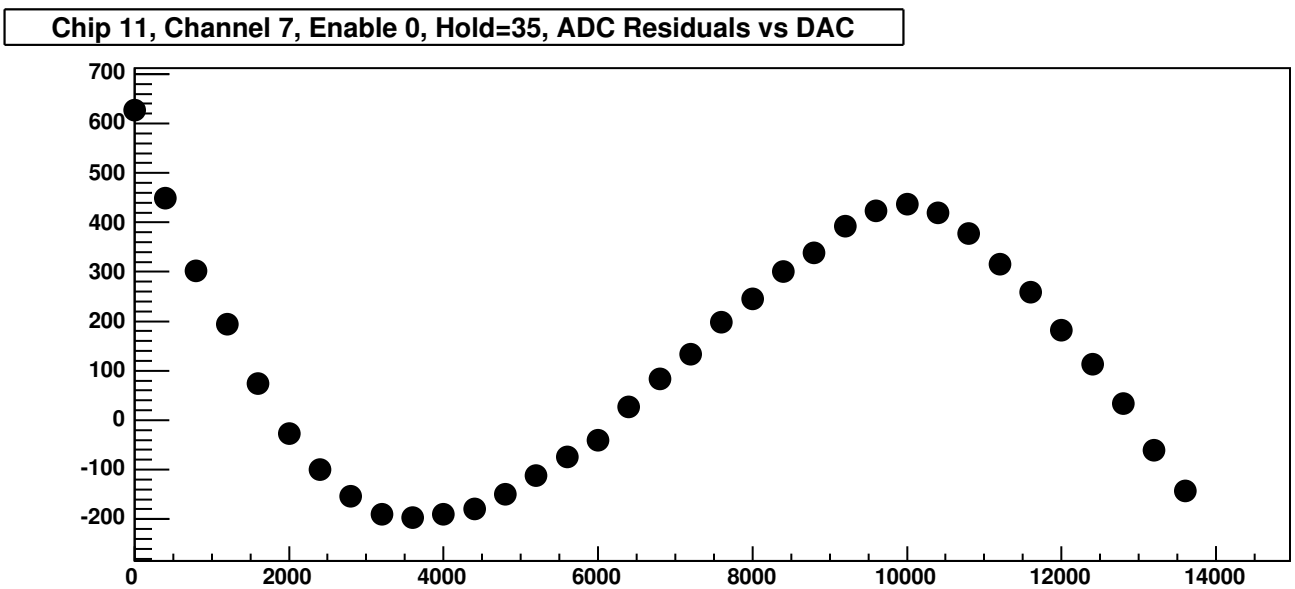

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 6067 / 23
p0 -572.9 ± 0.9561
p1 2.513 ± 0.0001584
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

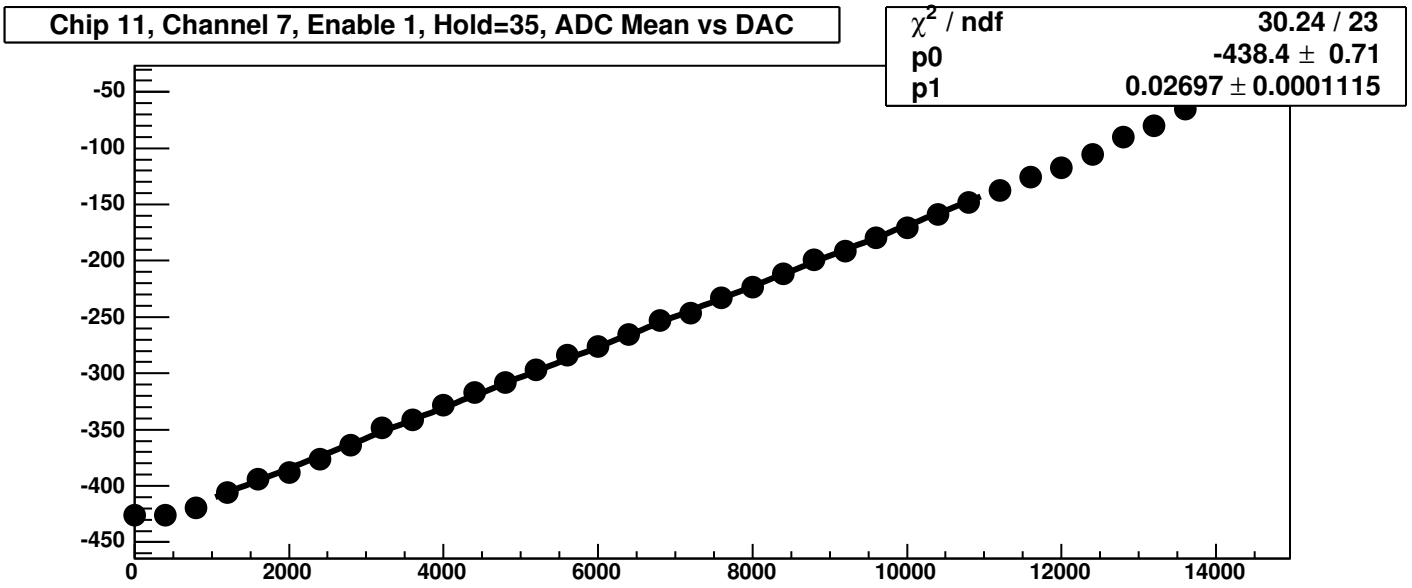

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

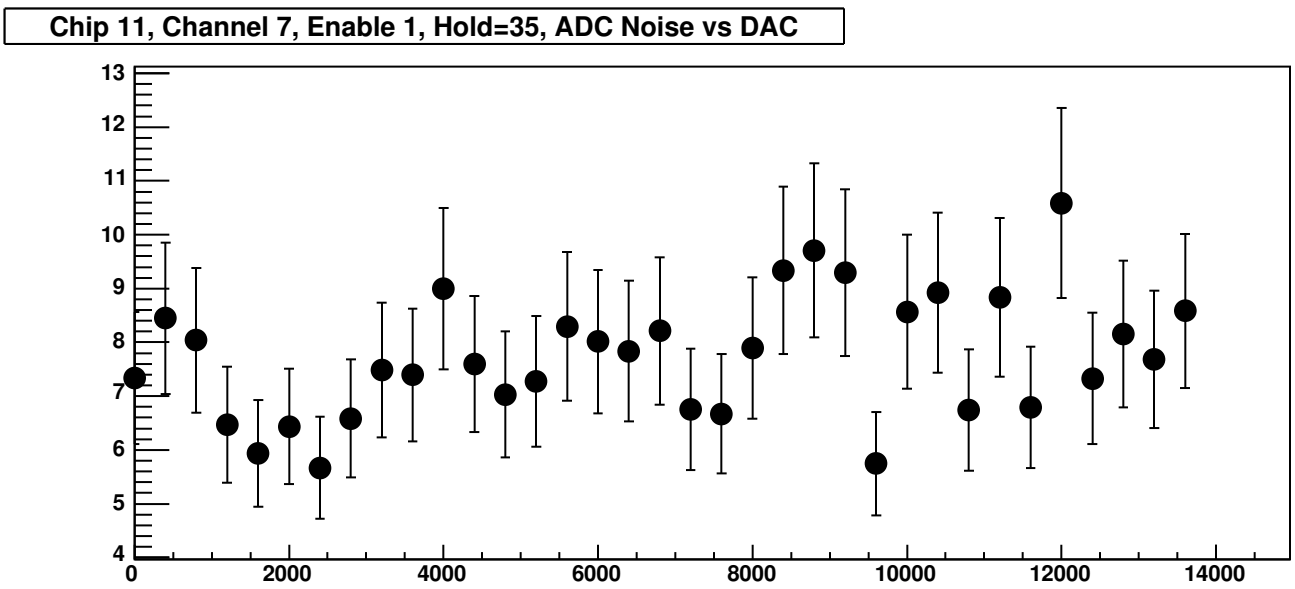

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

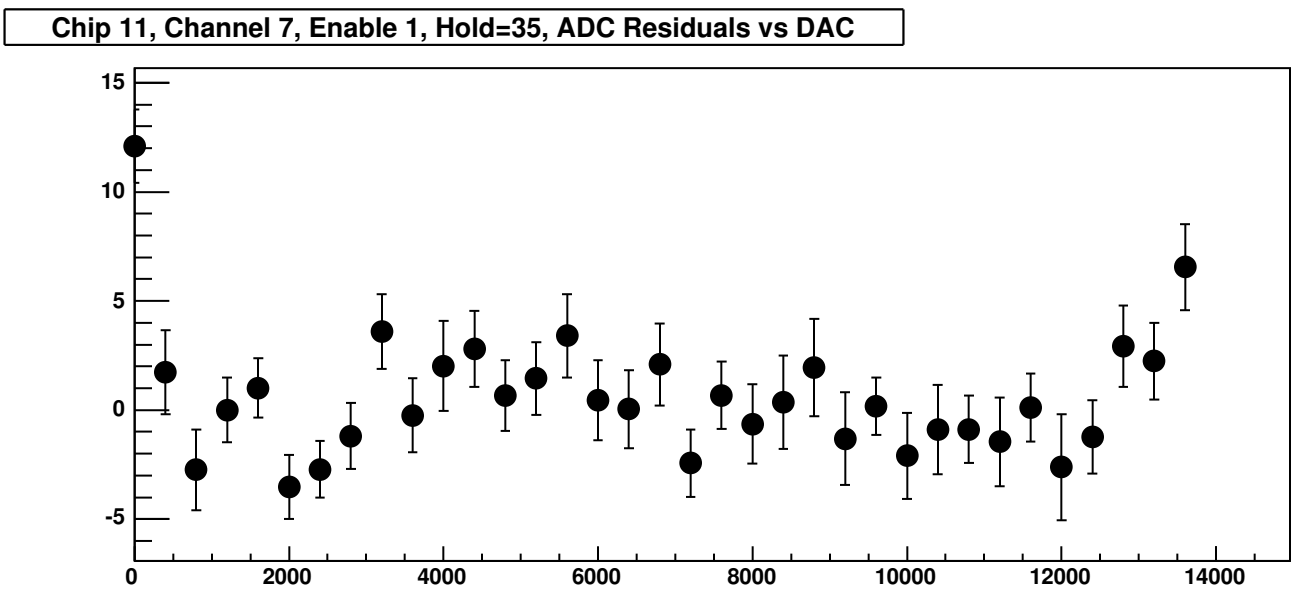

Chip 11, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.844e+04 / 23
p0 -953.5 ± 1.478
p1 0.4435 ± 0.0003715
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

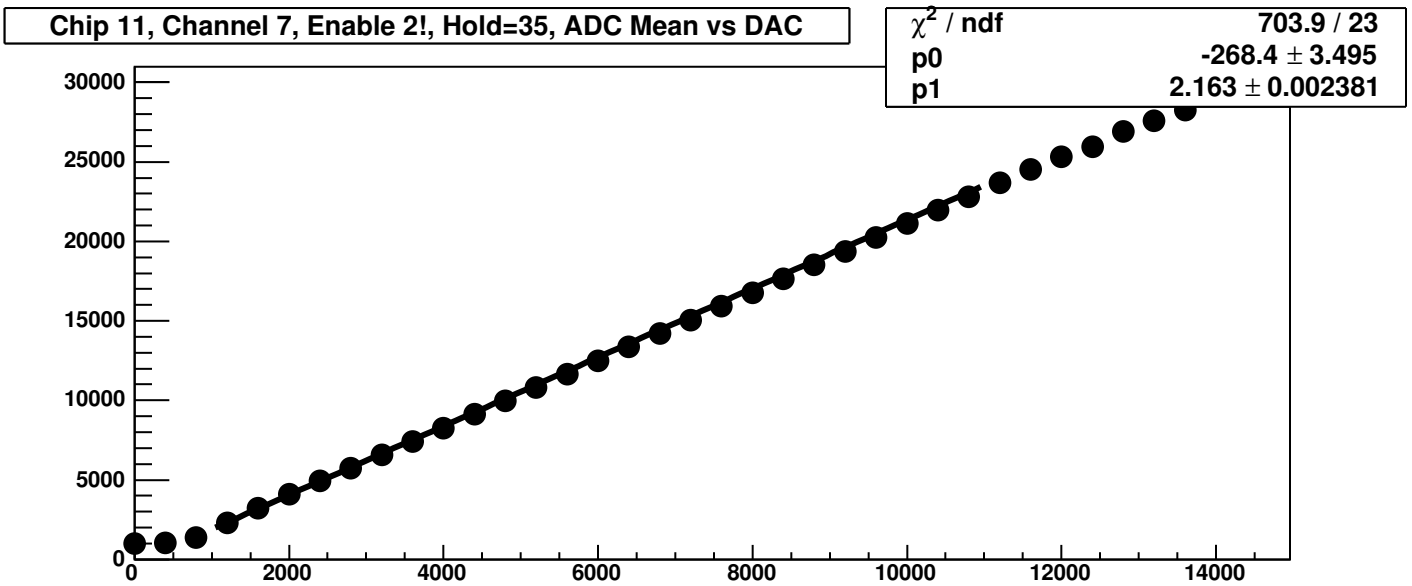

Chip 11, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

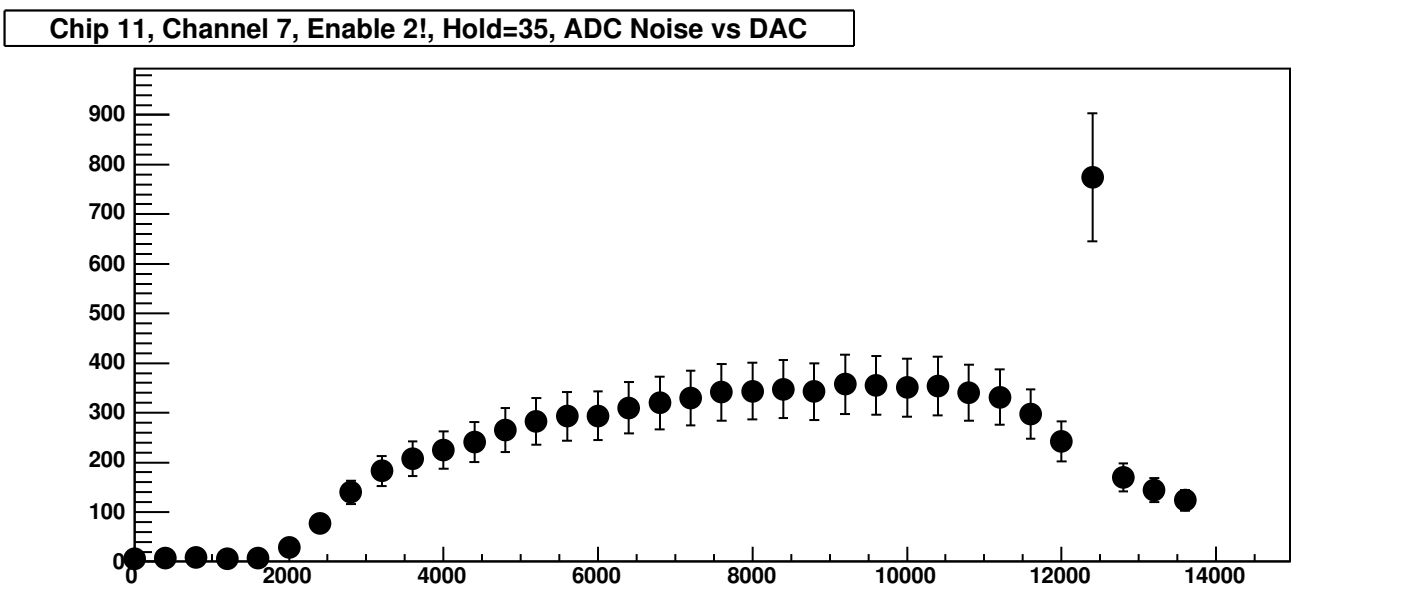

Chip 11, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
700
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

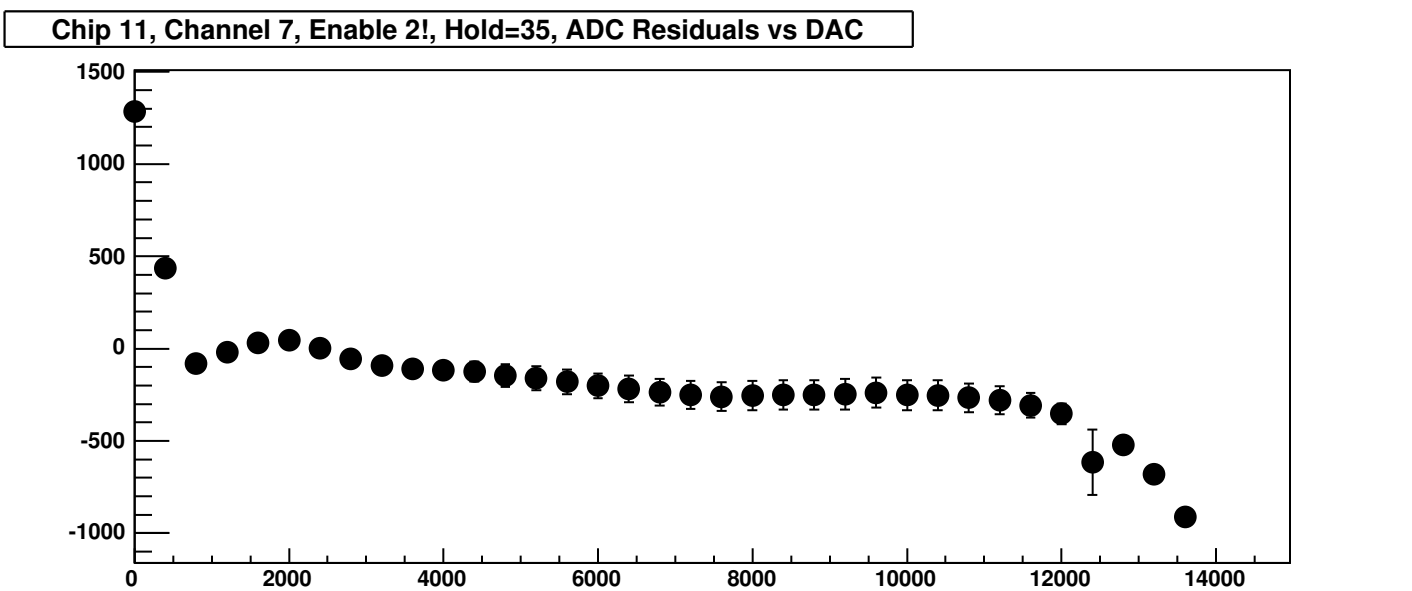

Chip 11, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.24 / 23
p0 -438.4 ± 0.71
p1 0.02697 ± 0.0001115
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

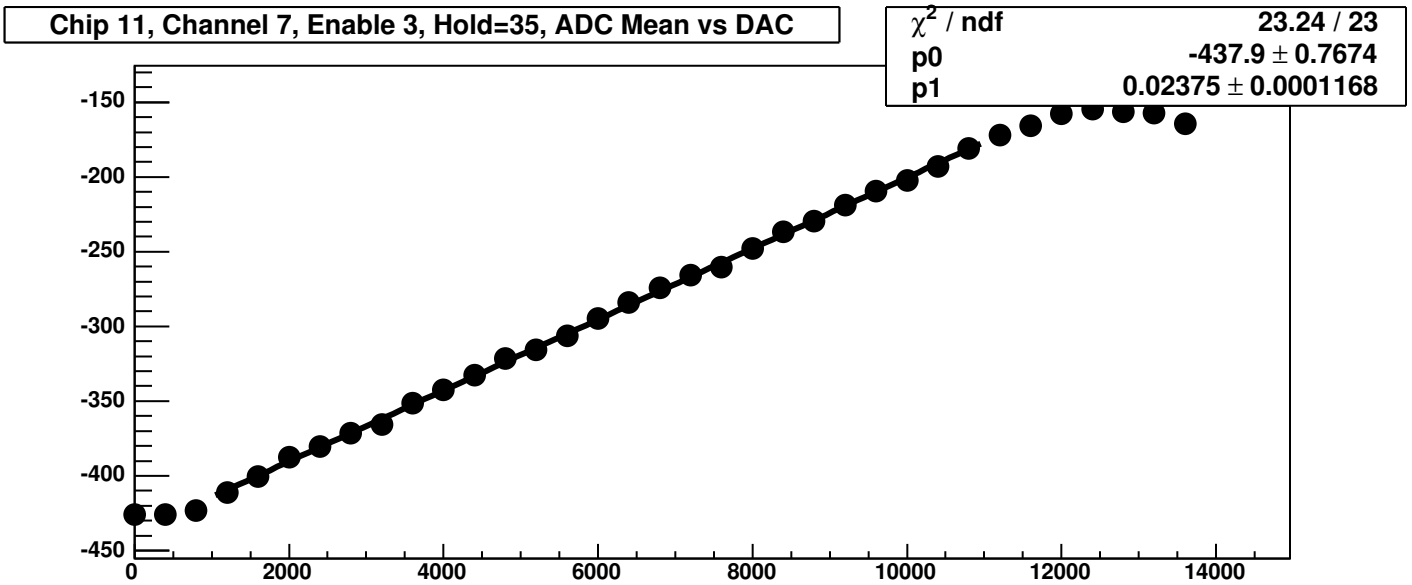

Chip 11, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

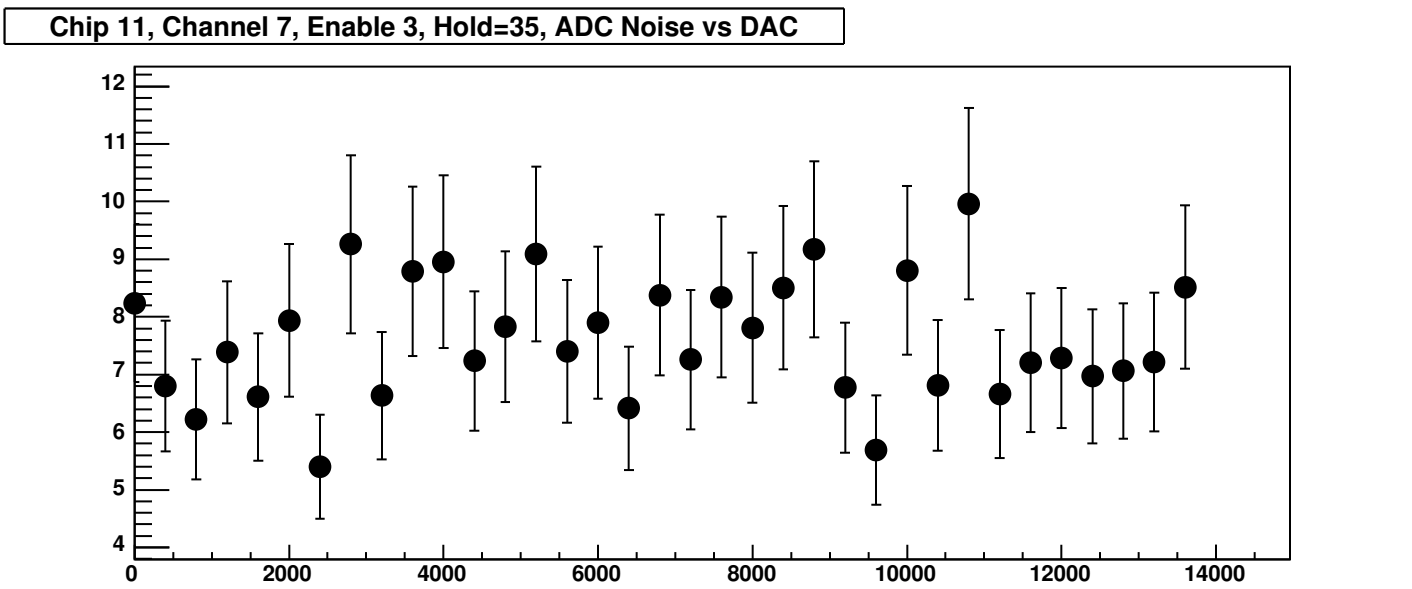

Chip 11, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

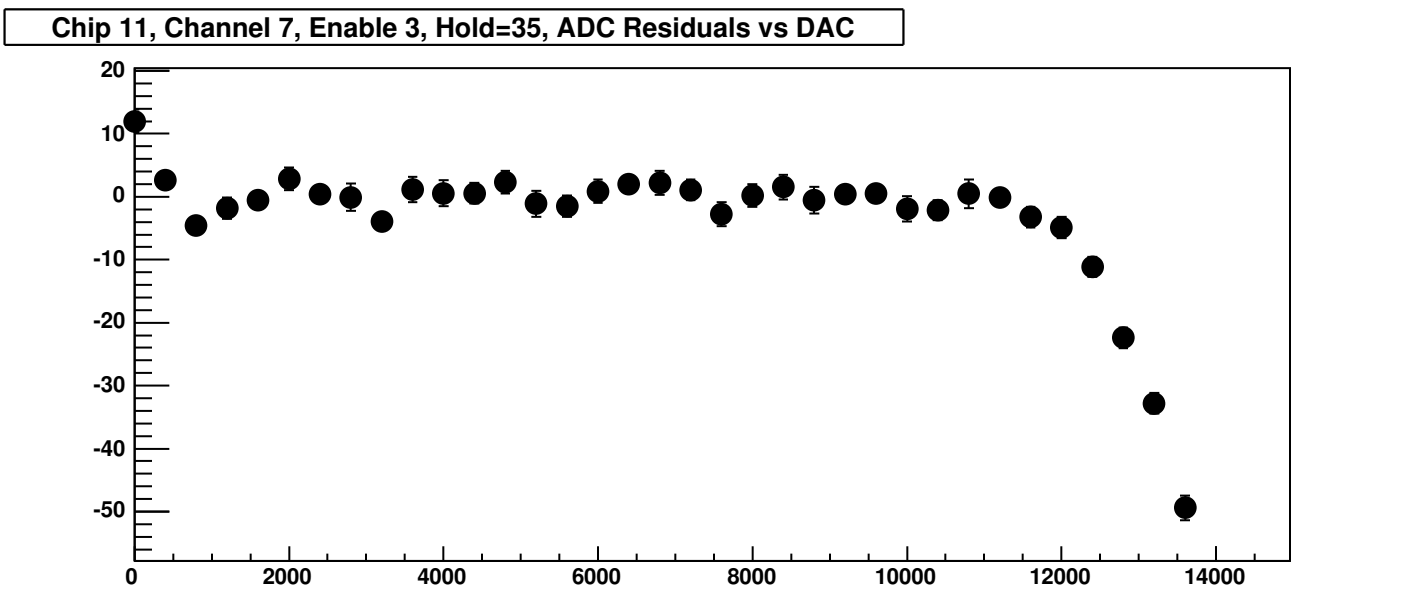

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 703.9 / 23
p0 -268.4 ± 3.495
p1 2.163 ± 0.002381
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

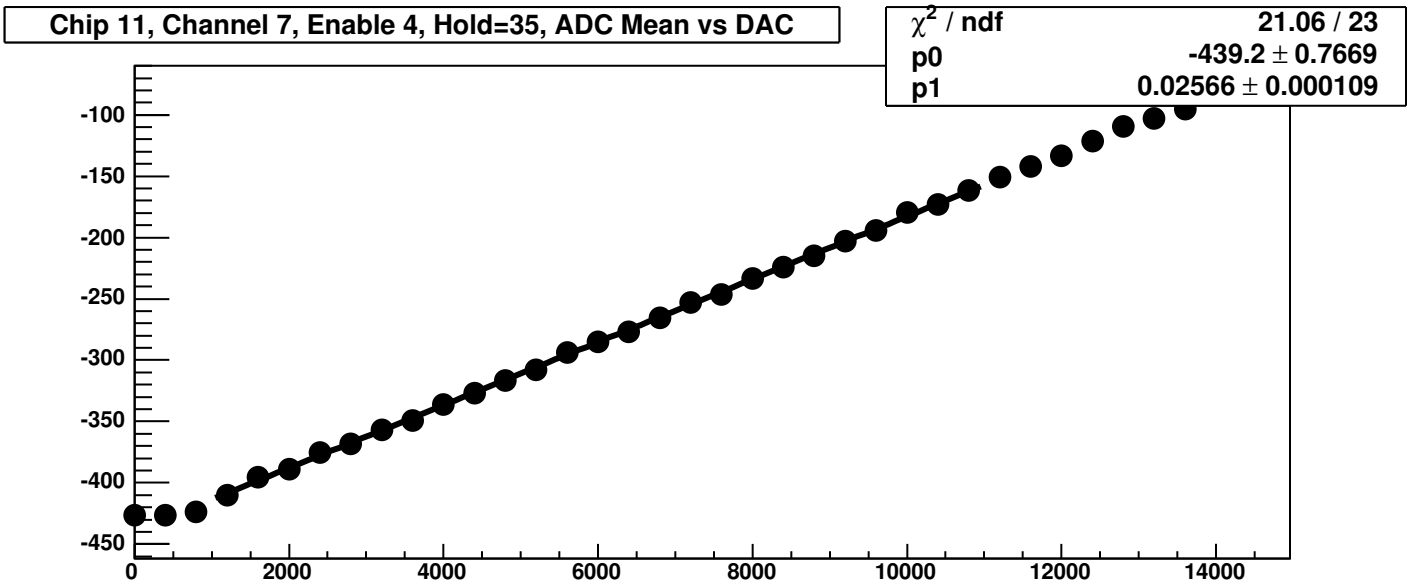

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

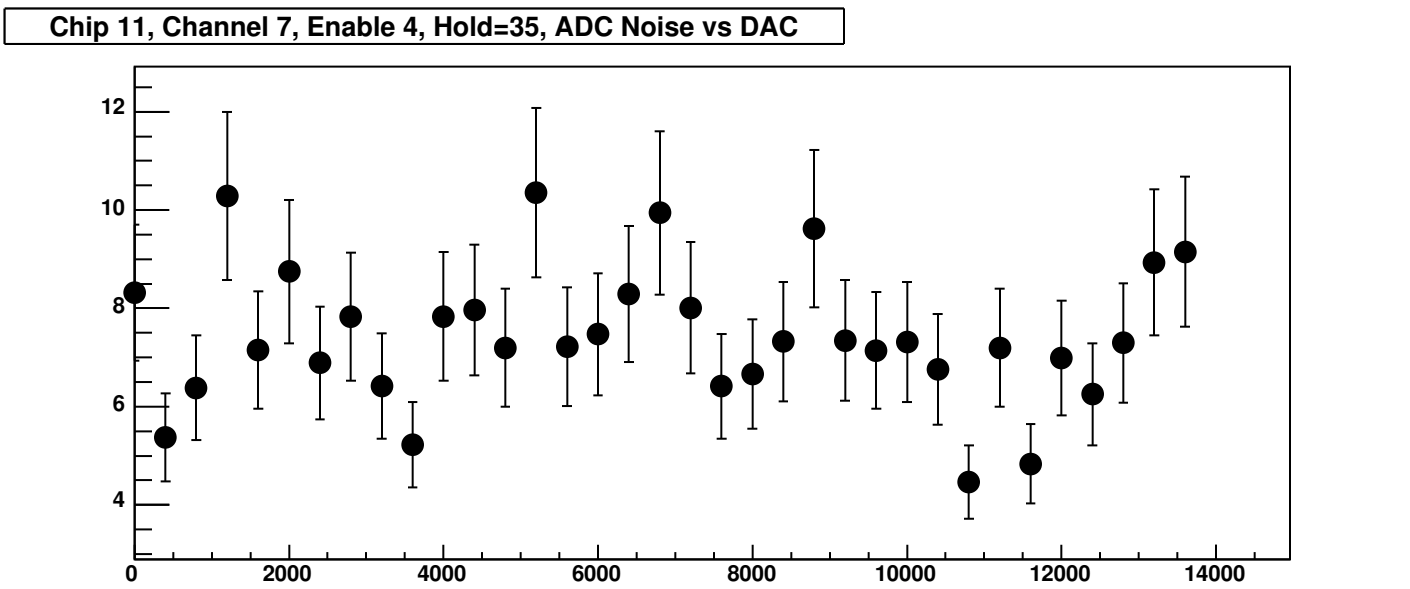

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

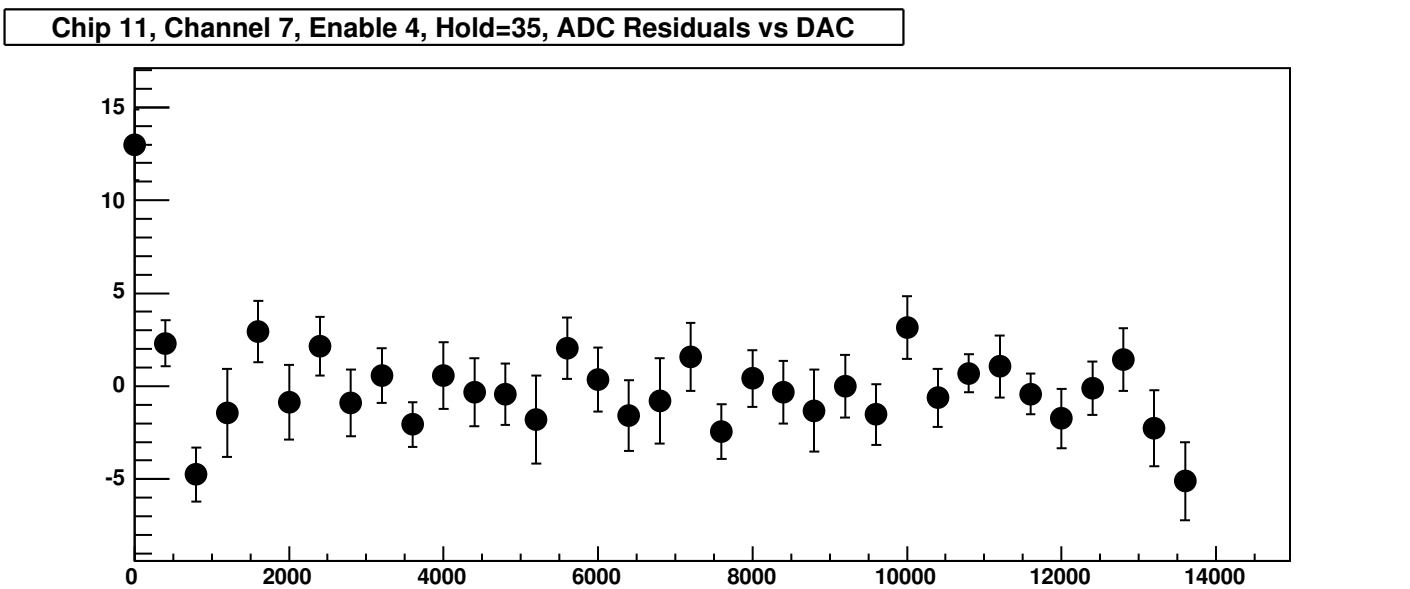

Chip 11, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.24 / 23
p0 -437.9 ± 0.7674
p1 0.02375 ± 0.0001168
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

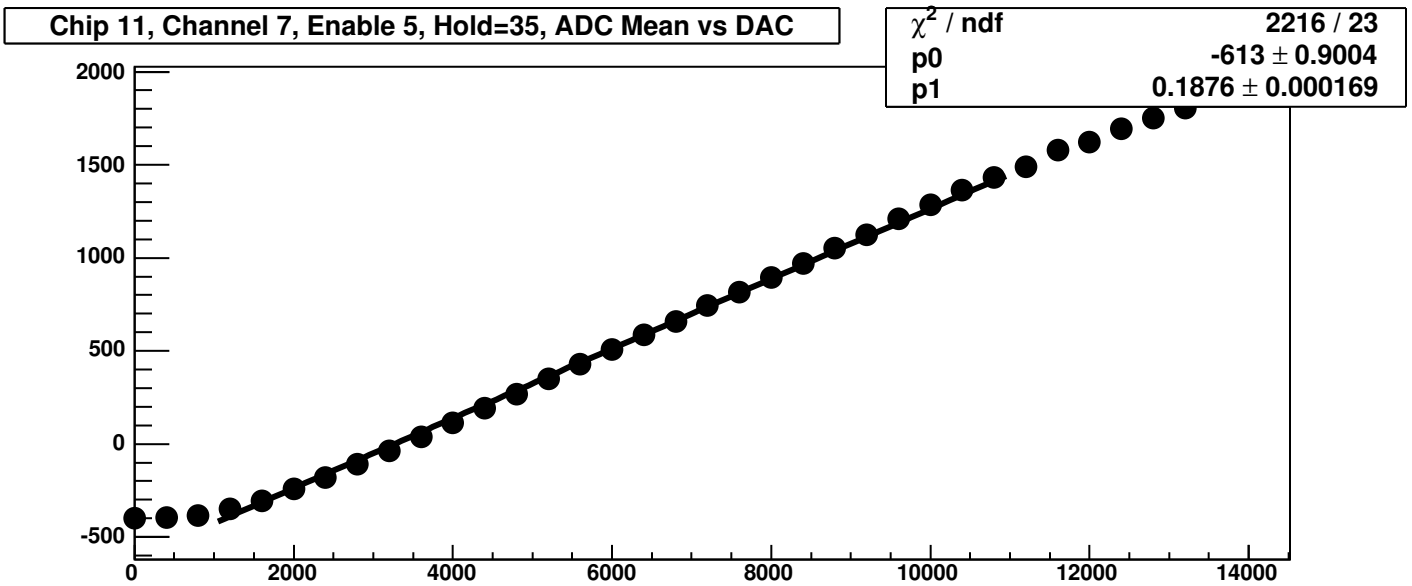

Chip 11, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

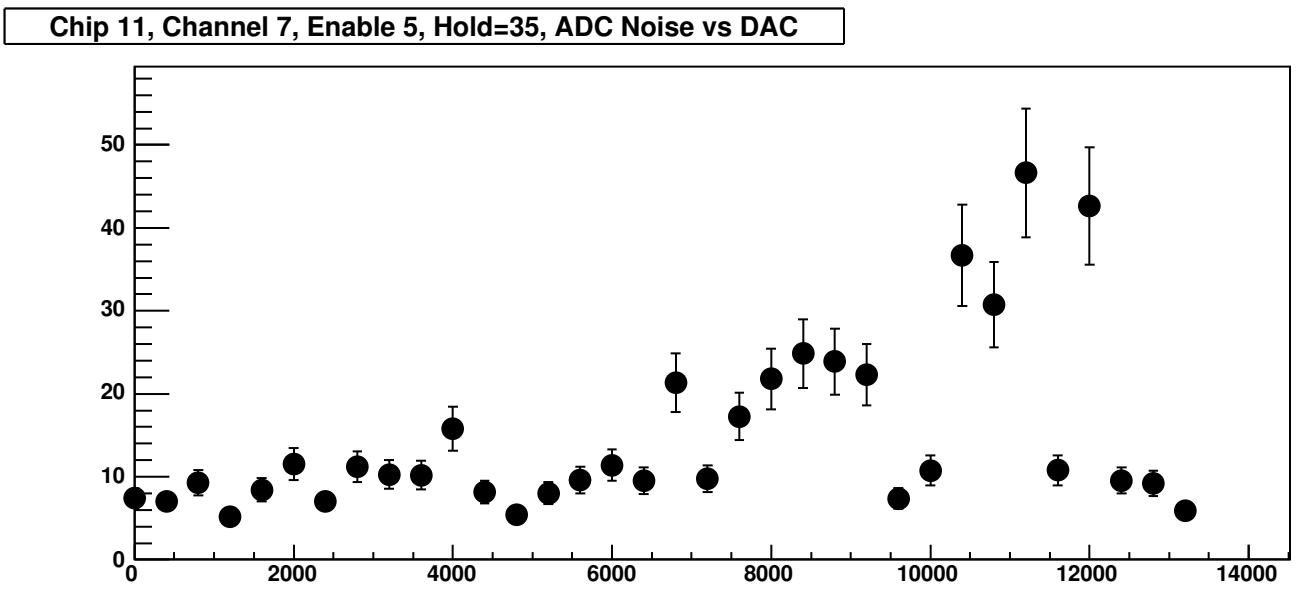

Chip 11, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

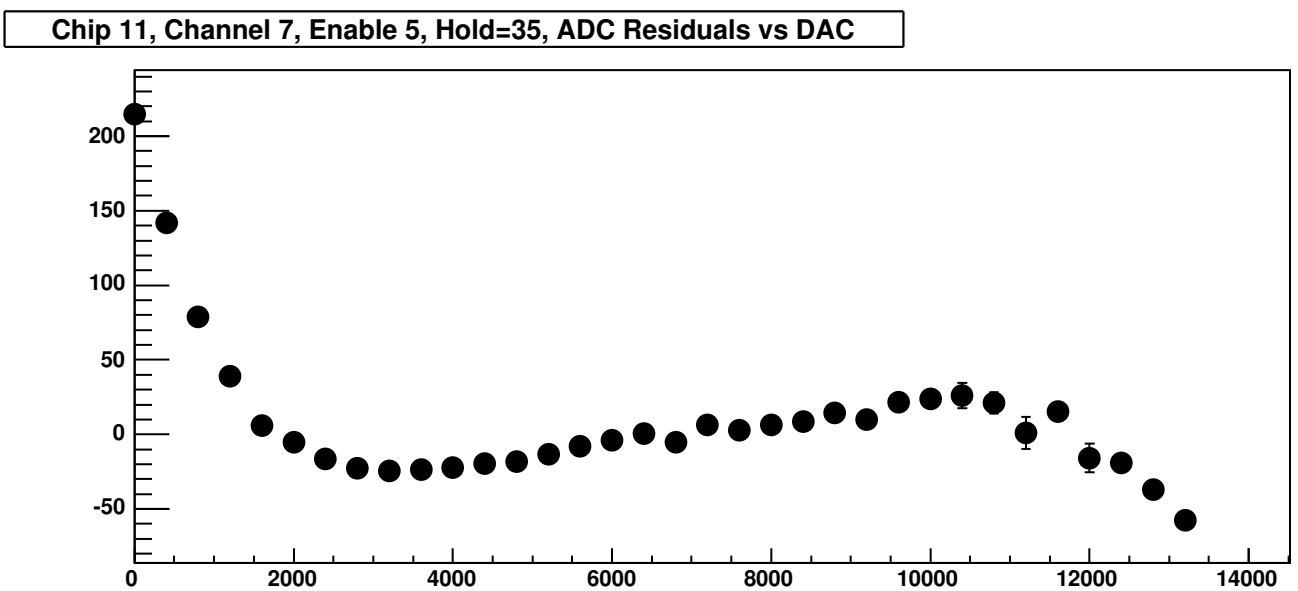

Chip 11, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.06 / 23
p0 -439.2 ± 0.7669
p1 0.02566 ± 0.000109
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

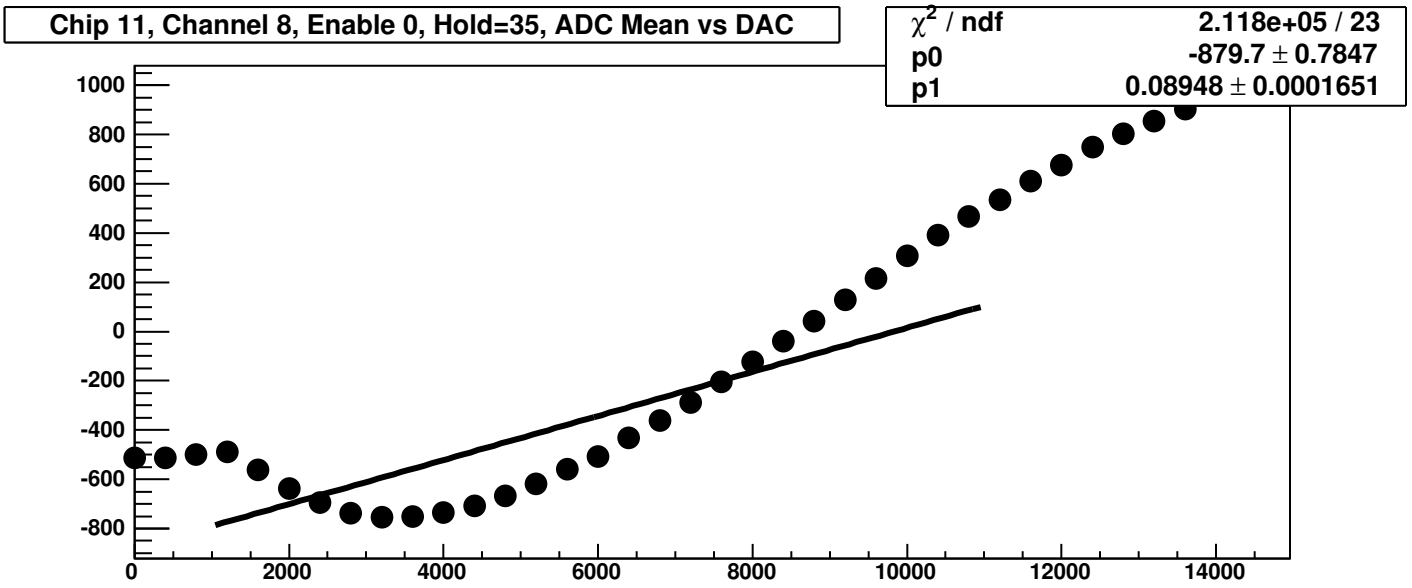

Chip 11, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

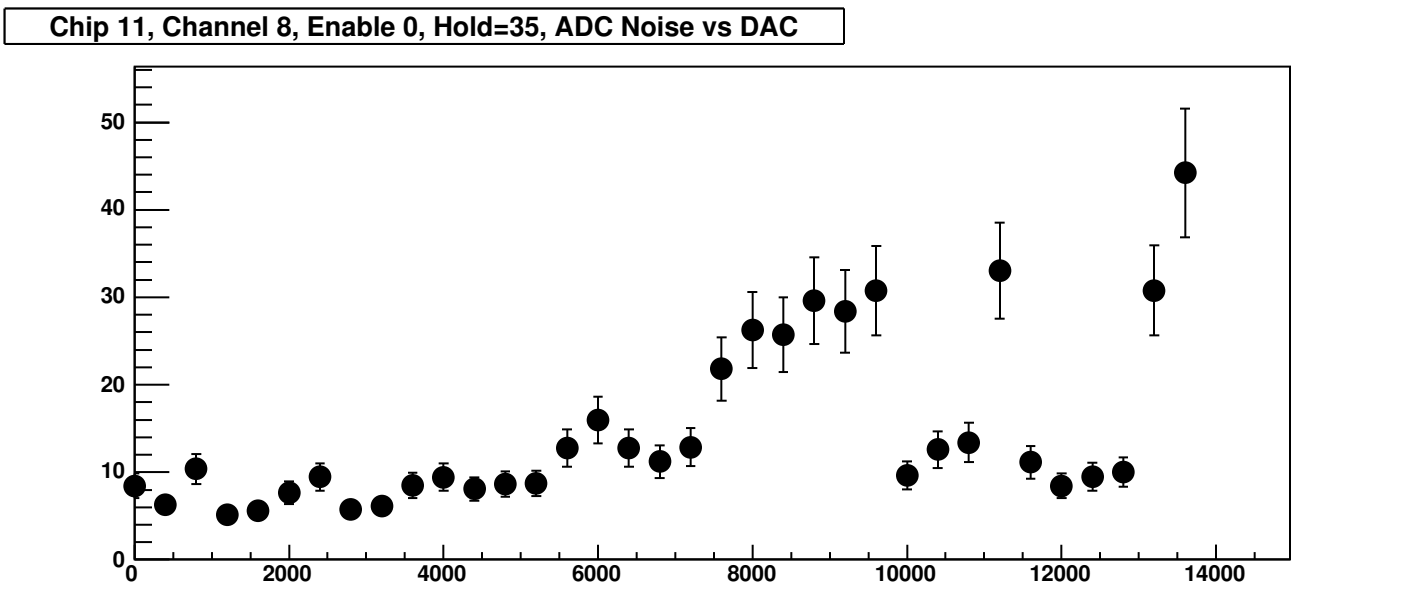

Chip 11, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

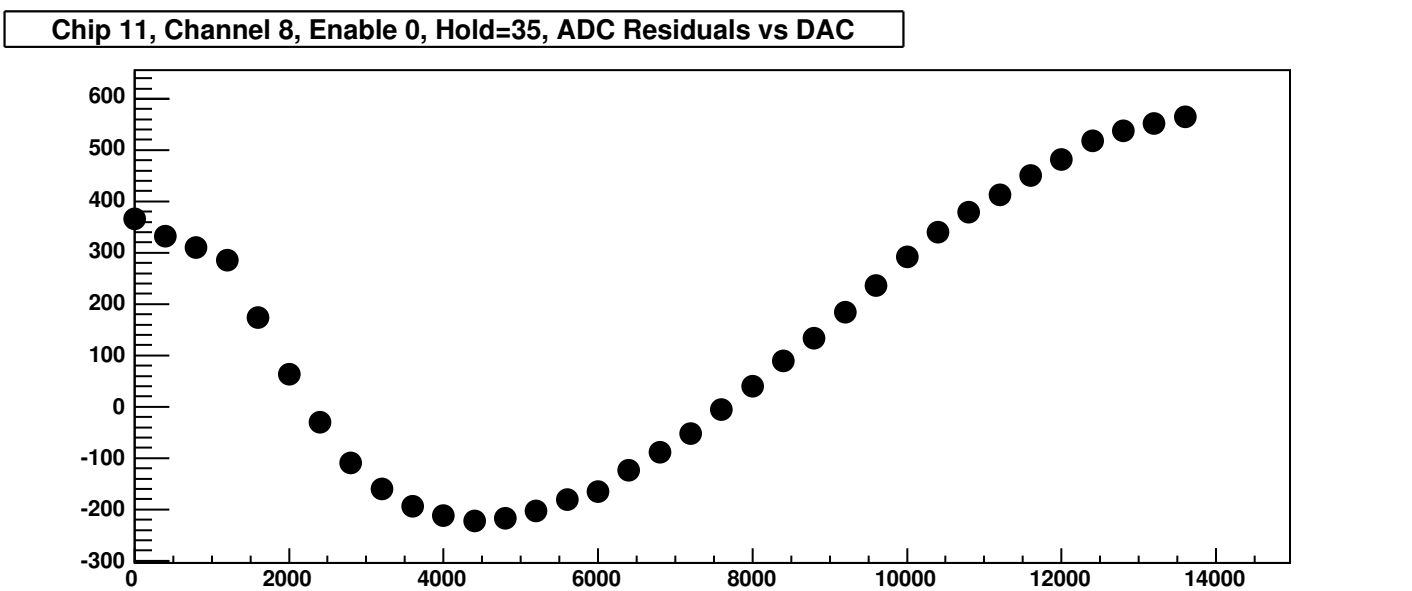

Chip 11, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2216 / 23
p0 -613 ± 0.9004
p1 0.1876 ± 0.000169
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

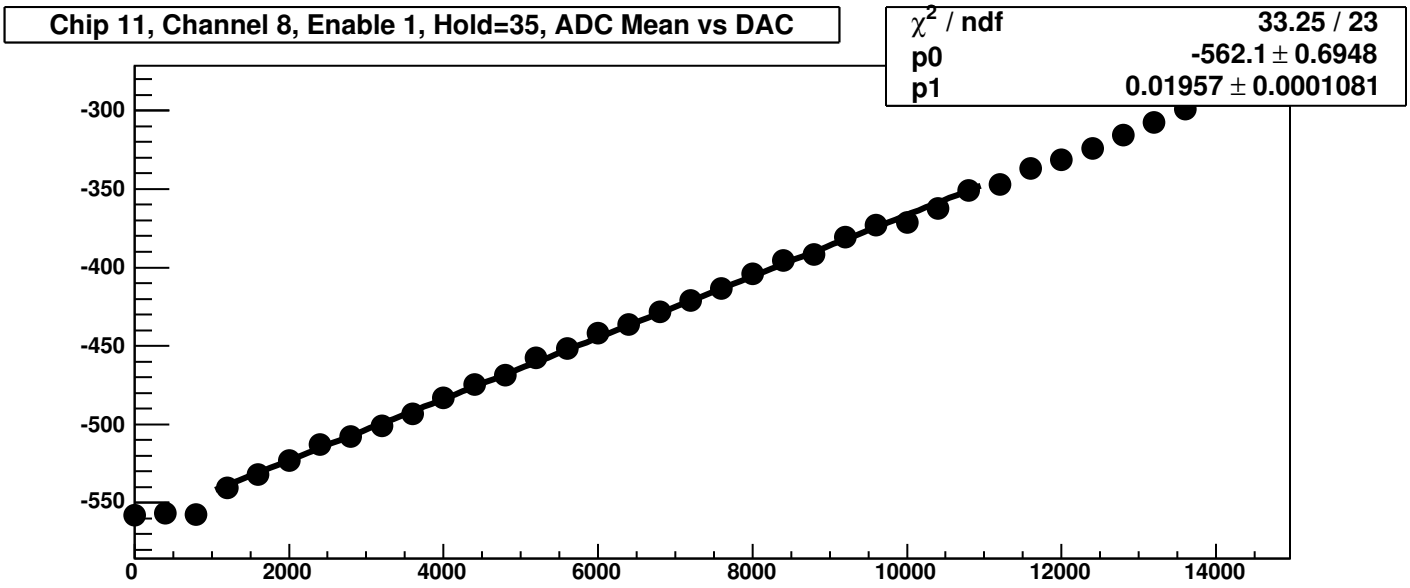

Chip 11, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

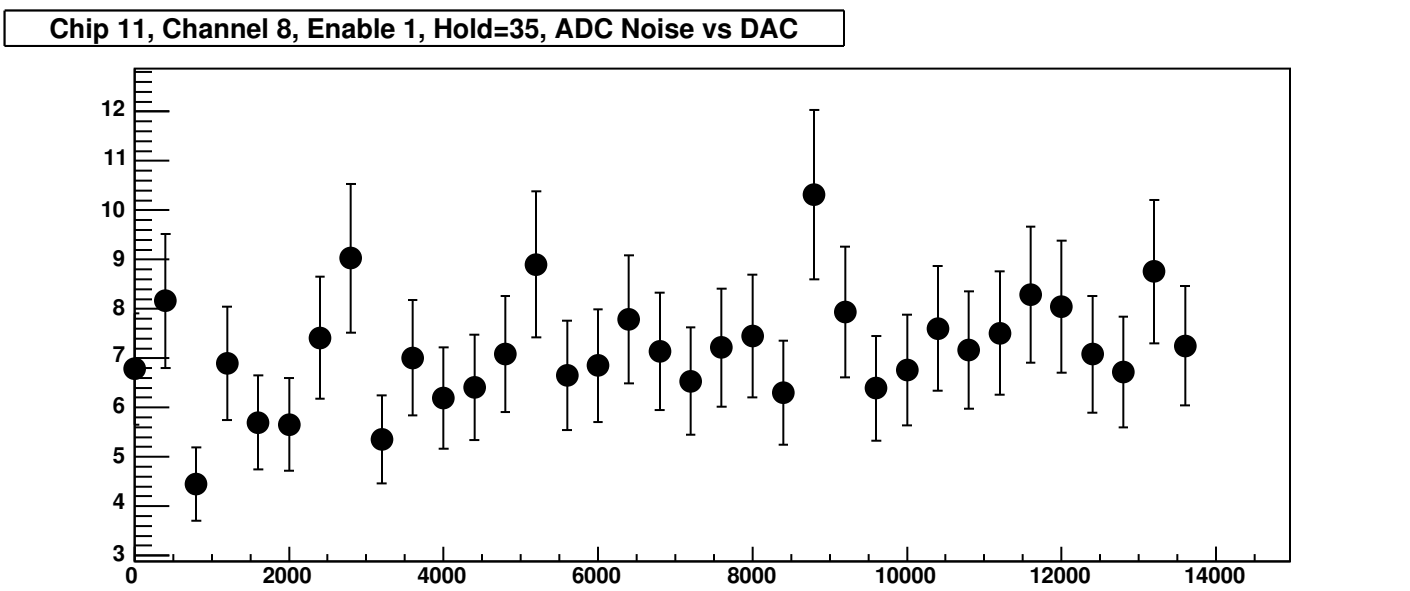

Chip 11, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

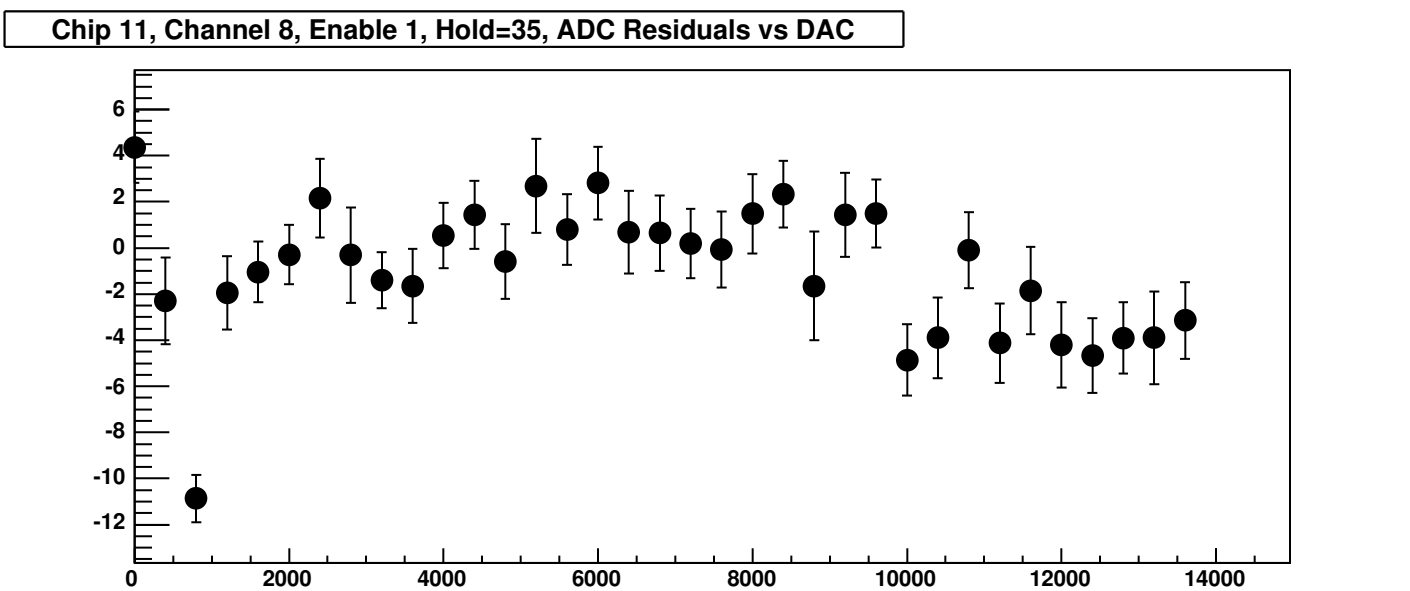

Chip 11, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.118e+05 / 23
p0 -879.7 ± 0.7847
p1 0.08948 ± 0.0001651
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

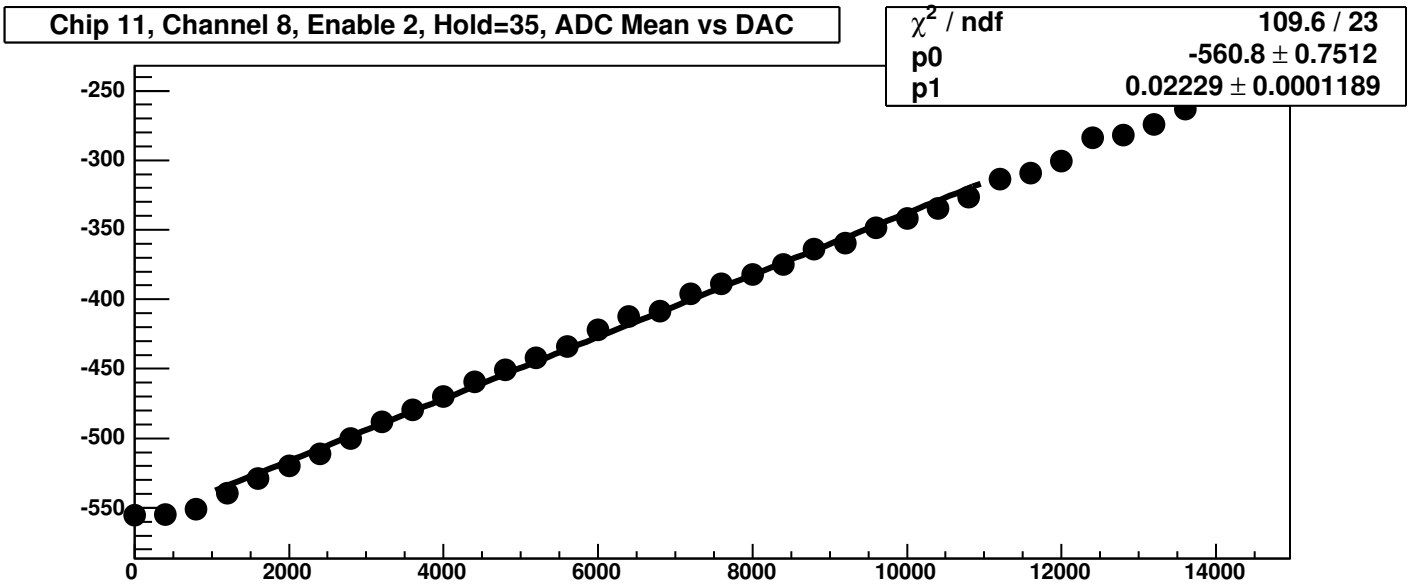

Chip 11, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

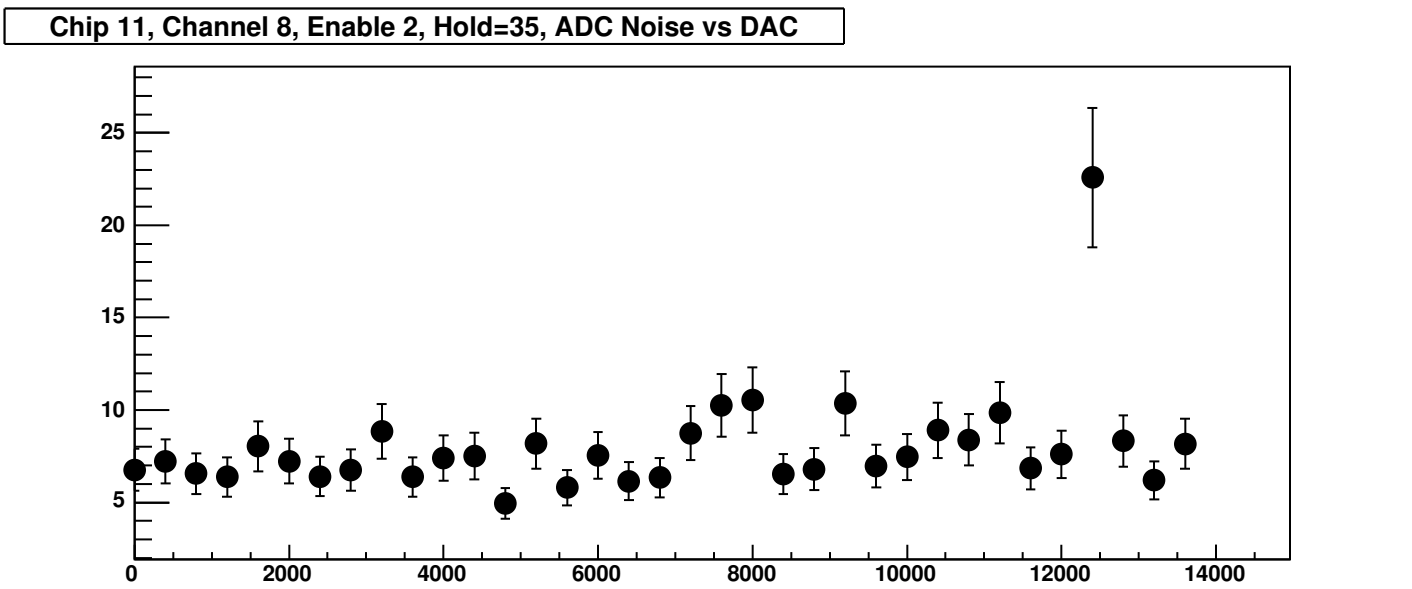

Chip 11, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

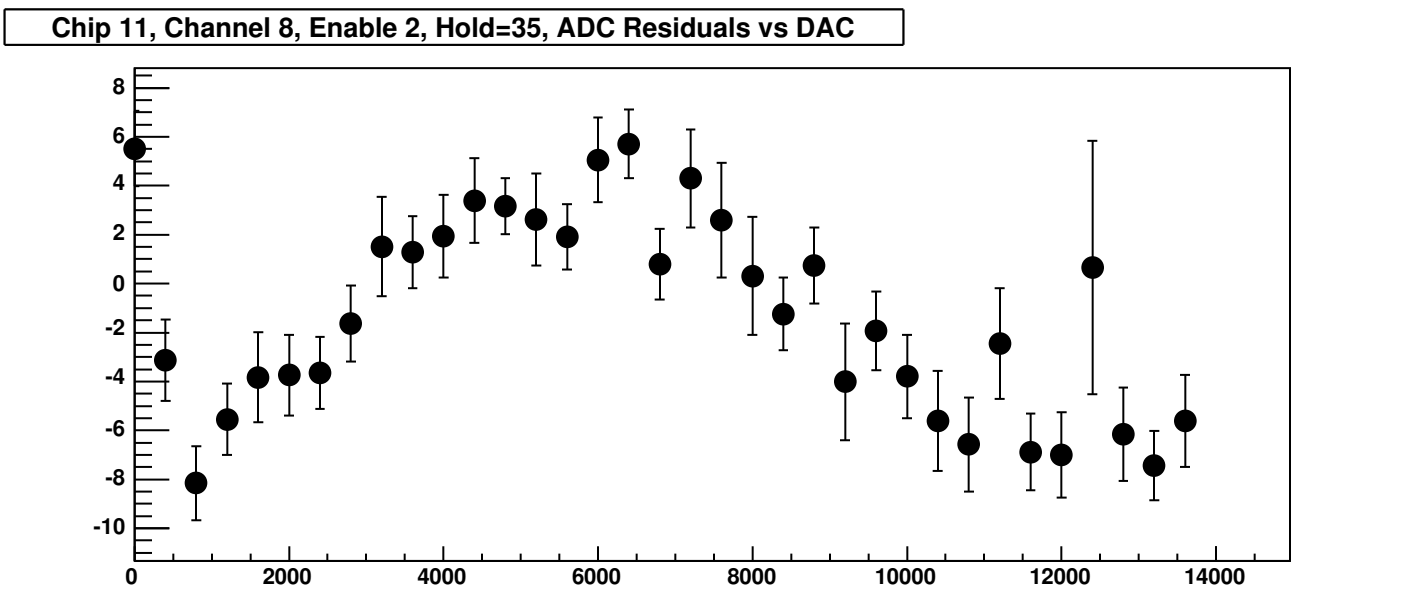

Chip 11, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.25 / 23
p0 -562.1 ± 0.6948
p1 0.01957 ± 0.0001081
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

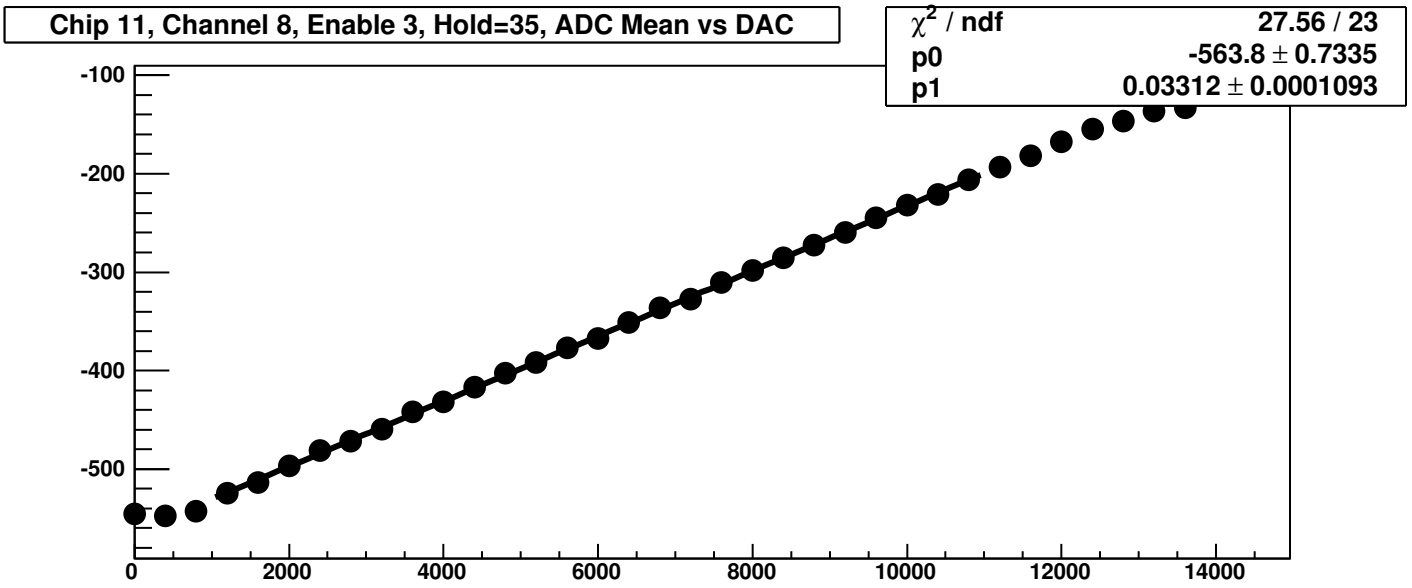

Chip 11, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

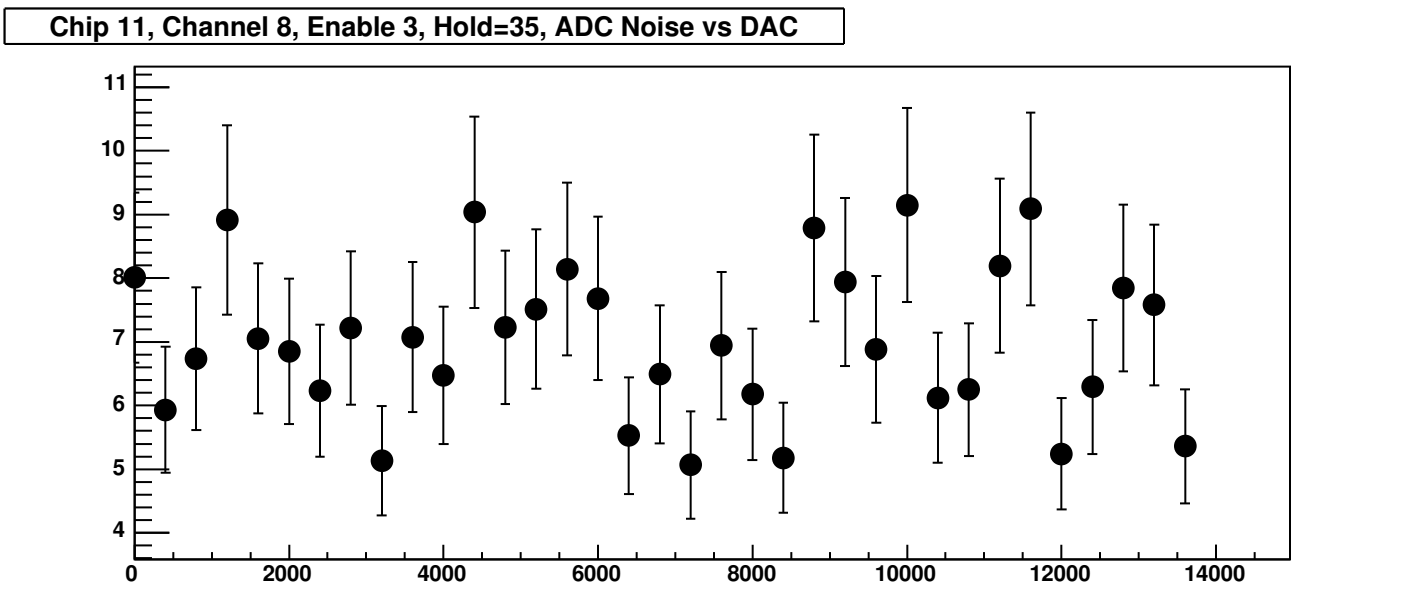

Chip 11, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

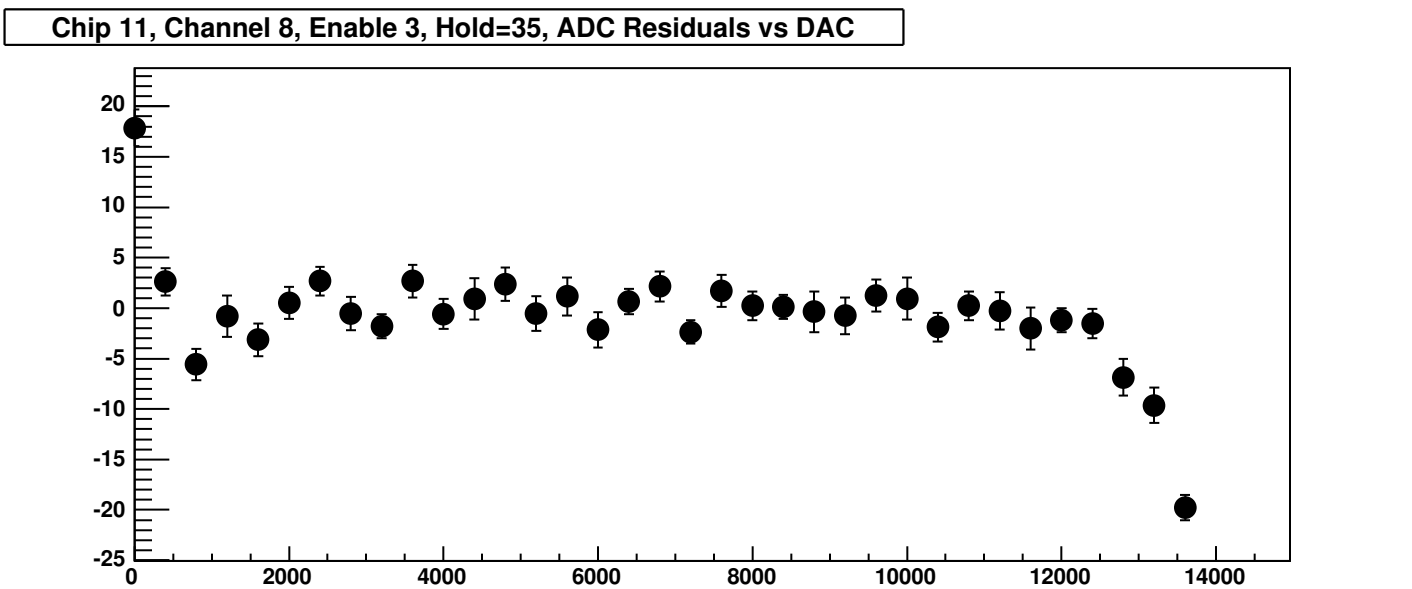

Chip 11, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 109.6 / 23
p0 -560.8 ± 0.7512
p1 0.02229 ± 0.0001189
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

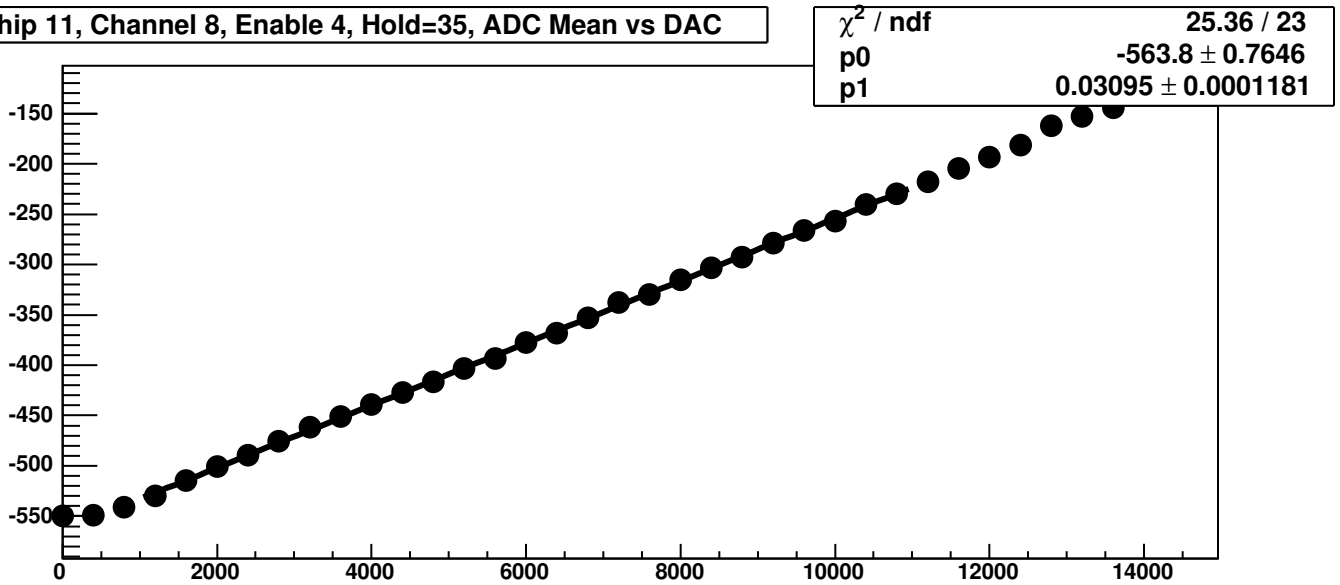

Chip 11, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.56 / 23
p0 -563.8 ± 0.7335
p1 0.03312 ± 0.0001093
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

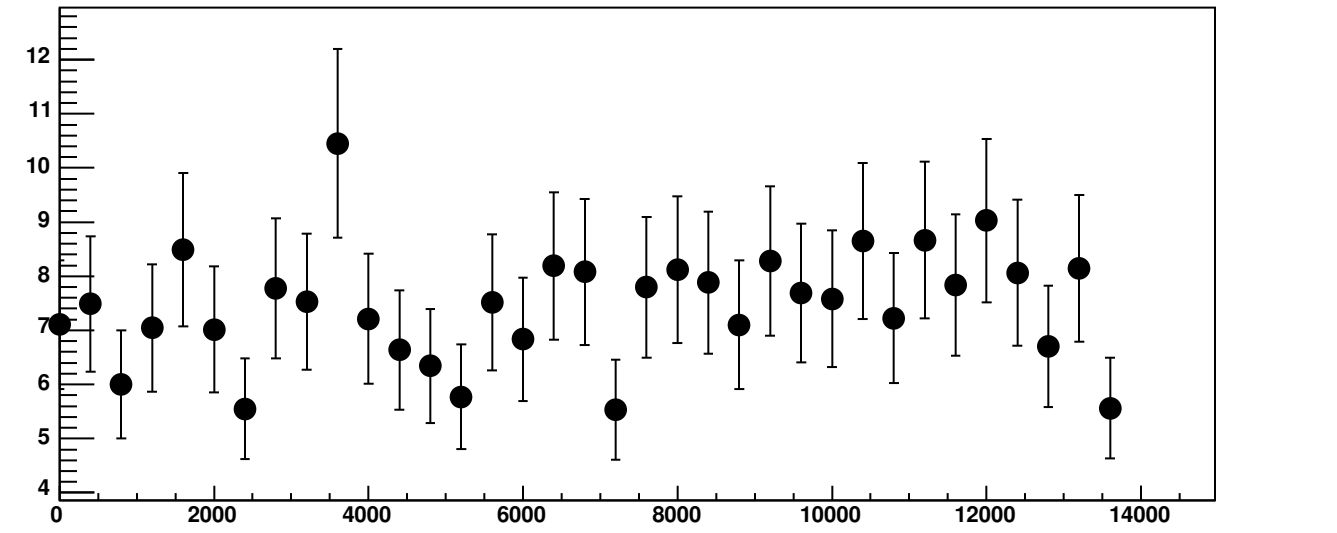

Chip 11, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

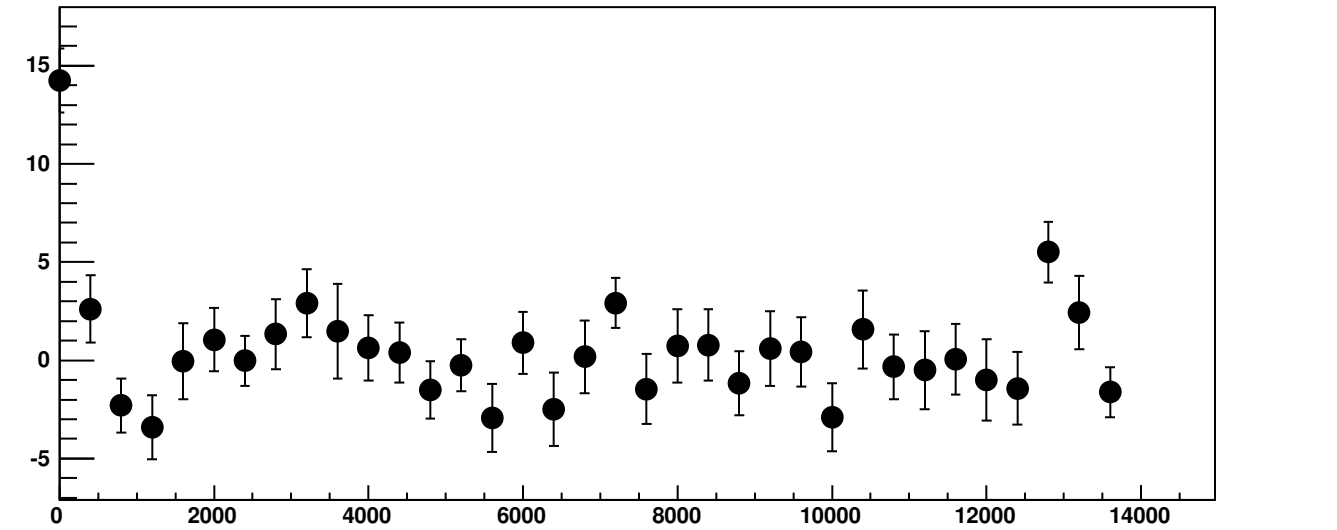
Chip 11, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

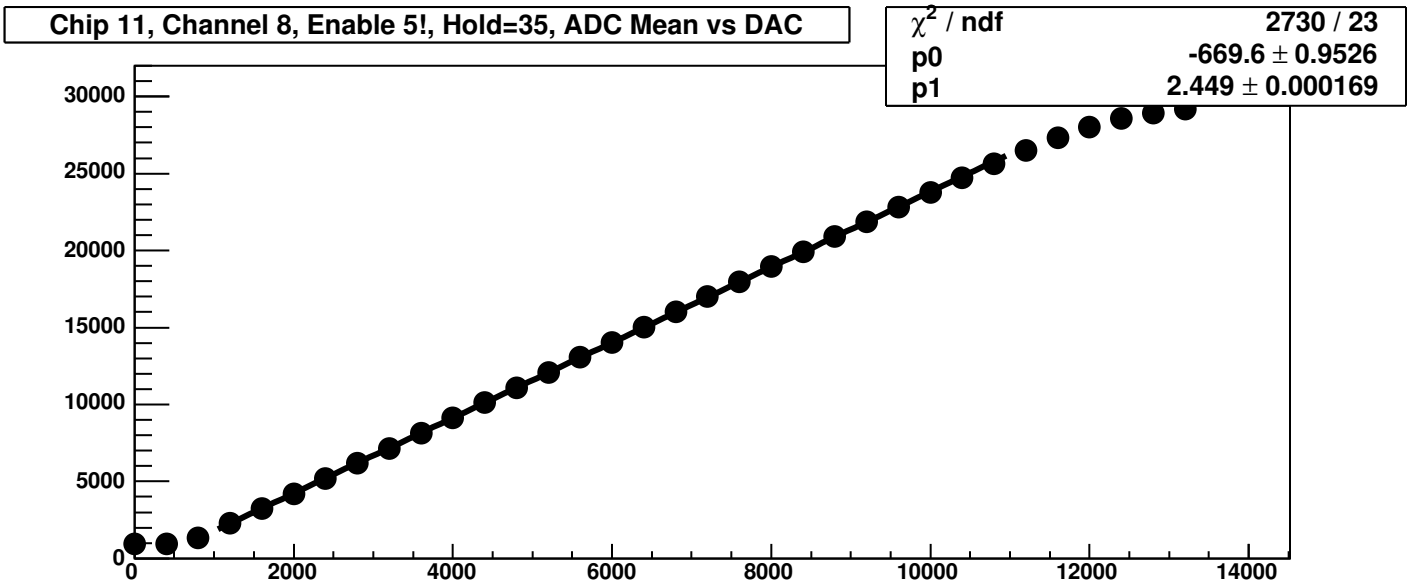


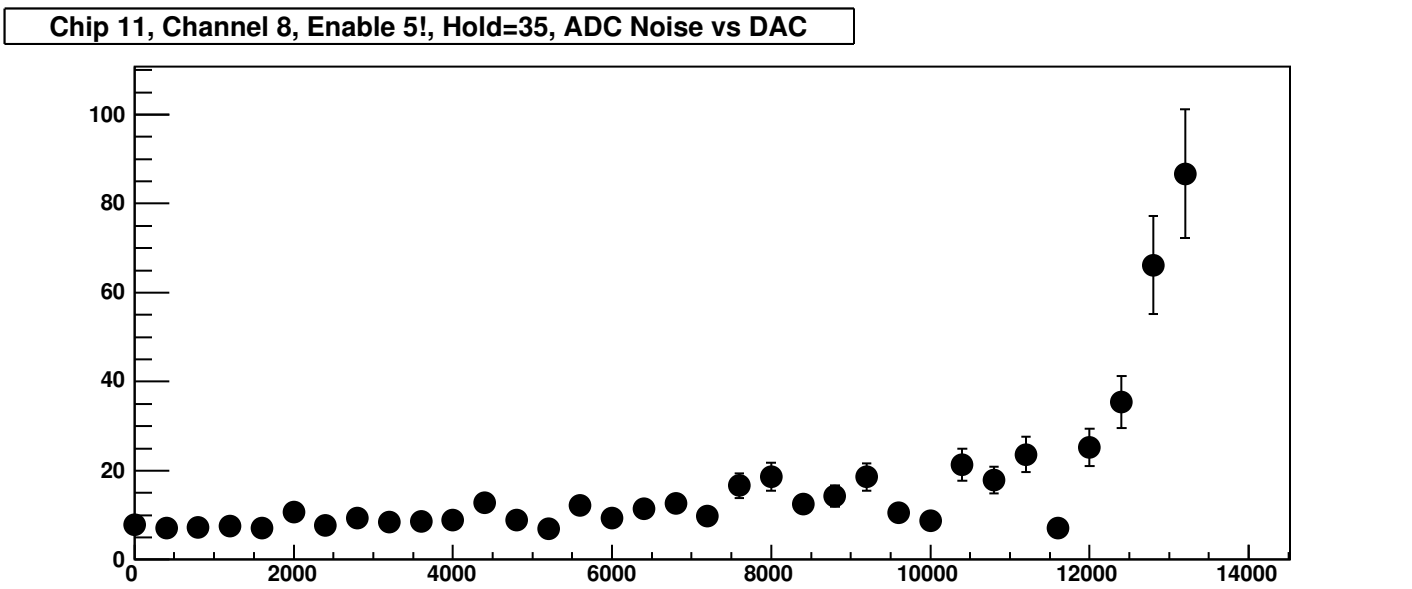
Chip 11, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

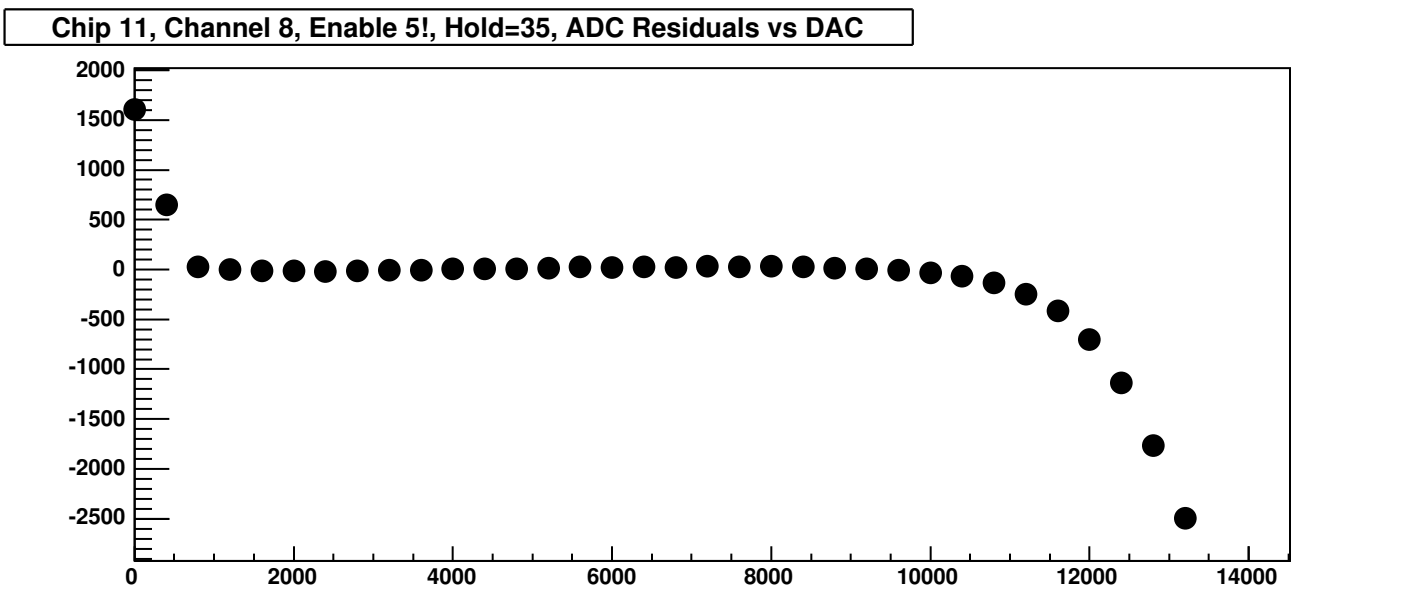


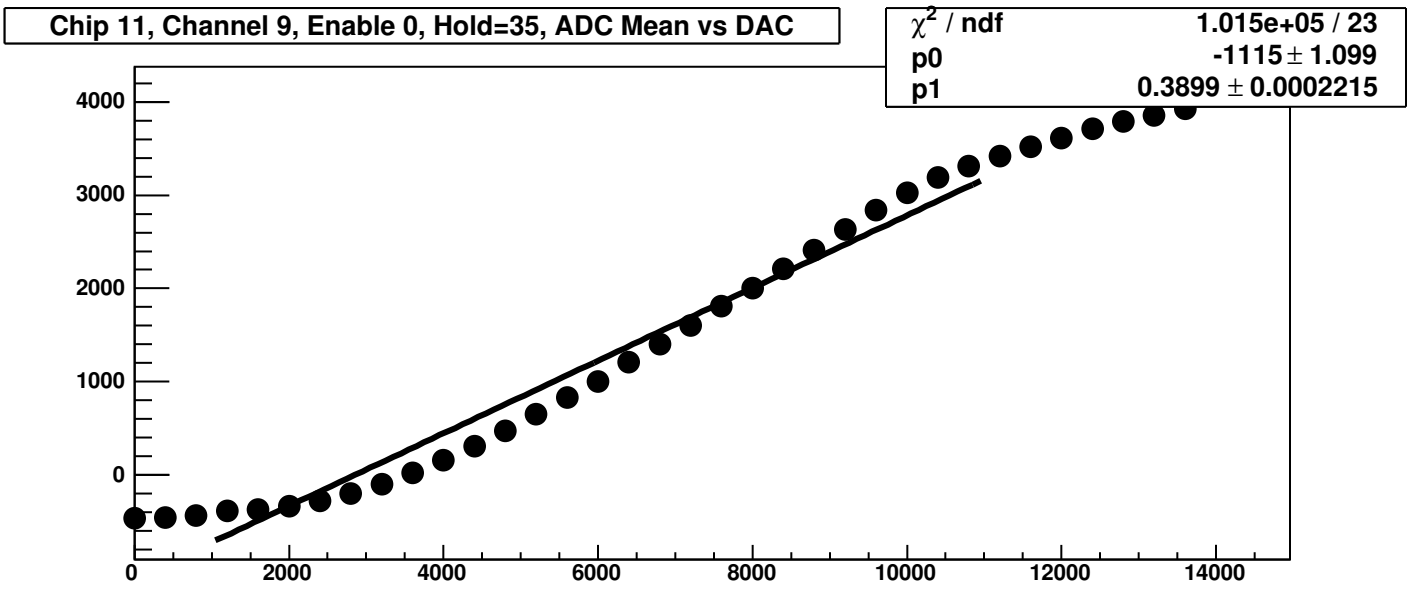
Chip 11, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

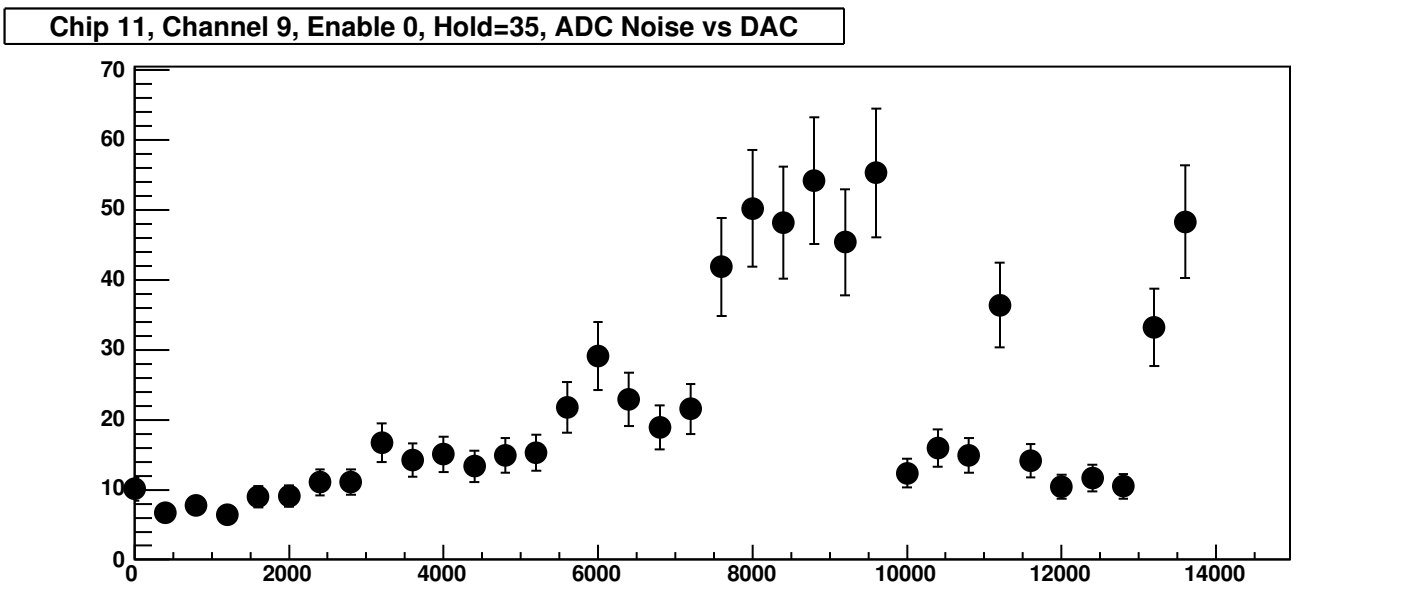


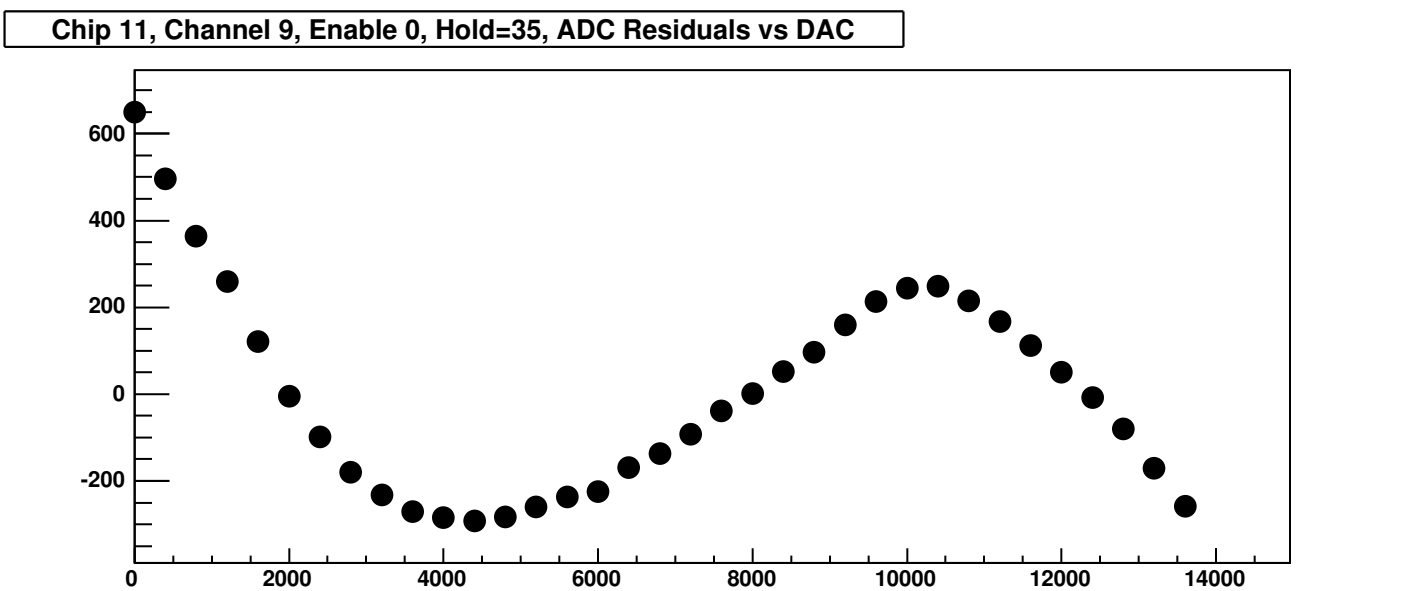

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2730 / 23
p0 -669.6 ± 0.9526
p1 2.449 ± 0.000169
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

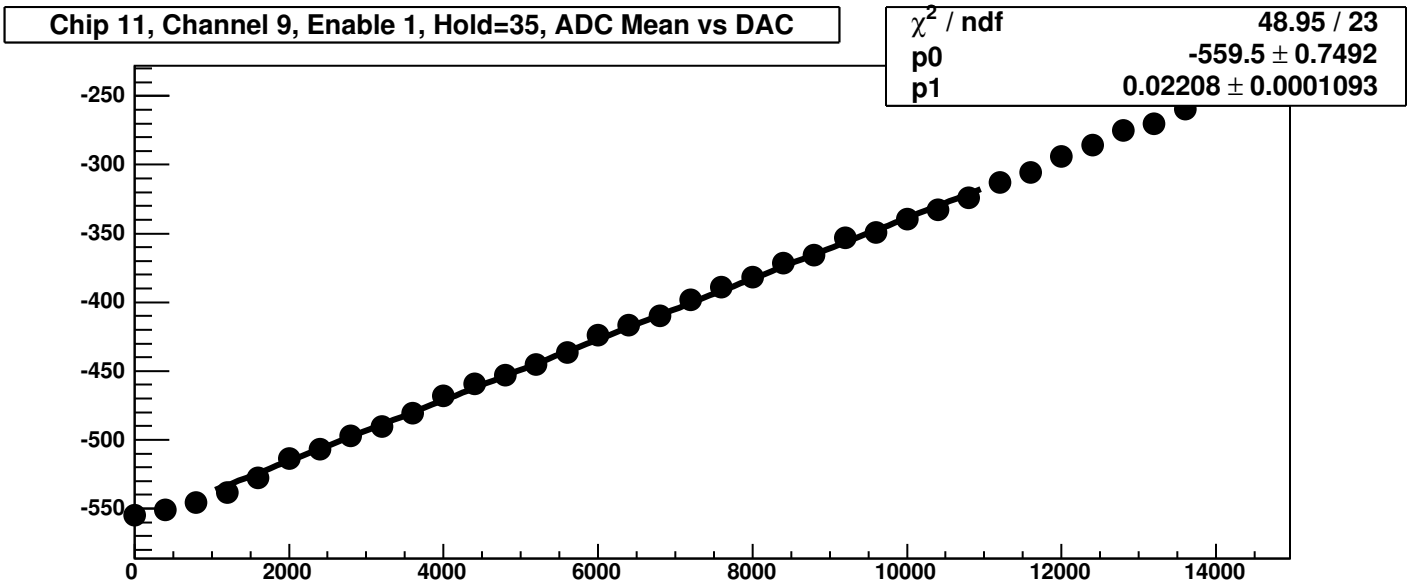

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

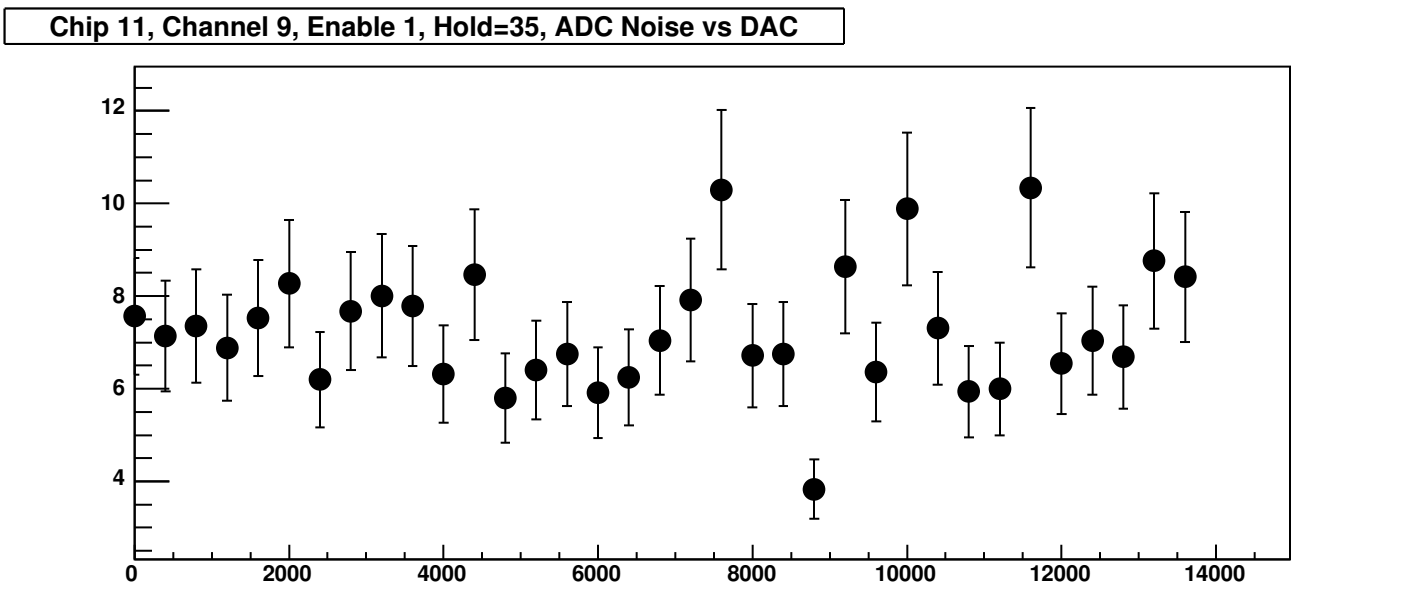

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

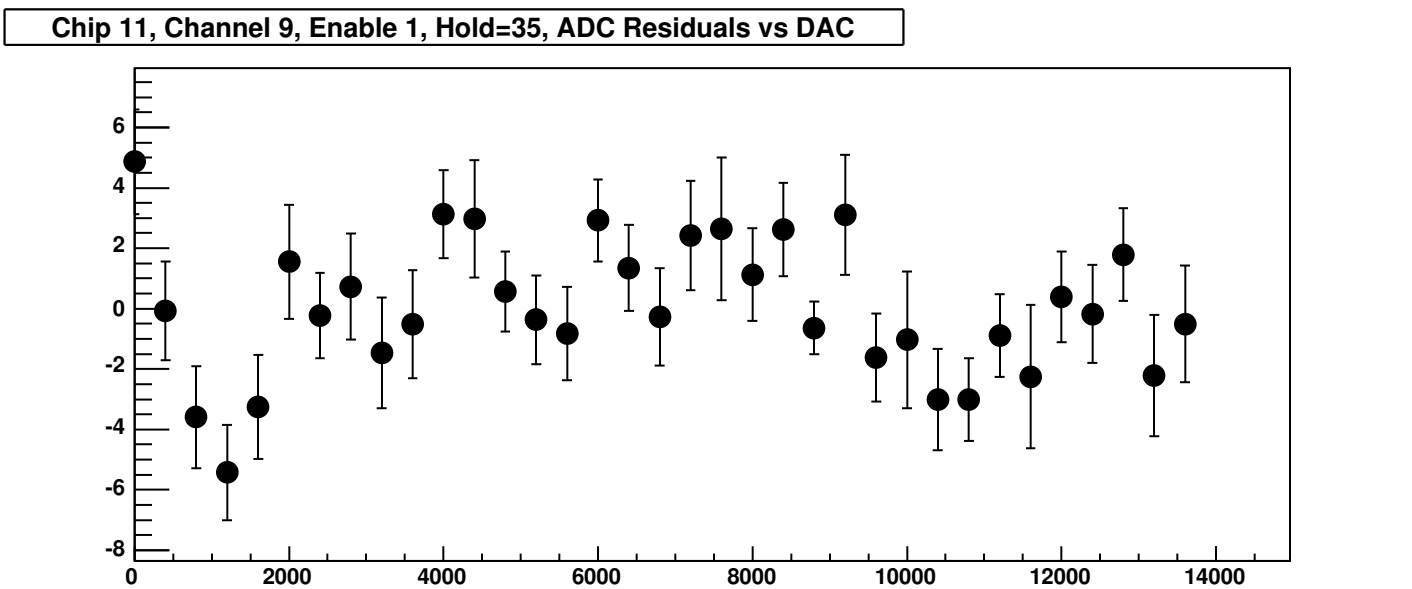

Chip 11, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.015e+05 / 23
p0 -1115 ± 1.099
p1 0.3899 ± 0.0002215
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

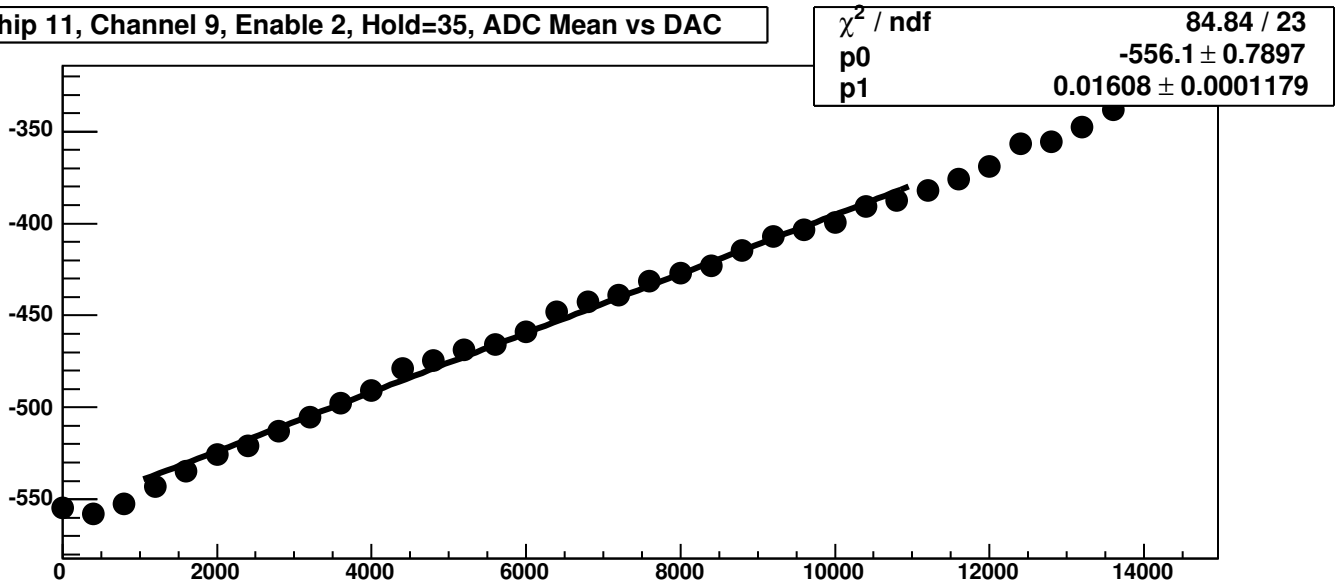

Chip 11, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 48.95 / 23
p0 -559.5 ± 0.7492
p1 0.02208 ± 0.0001093
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

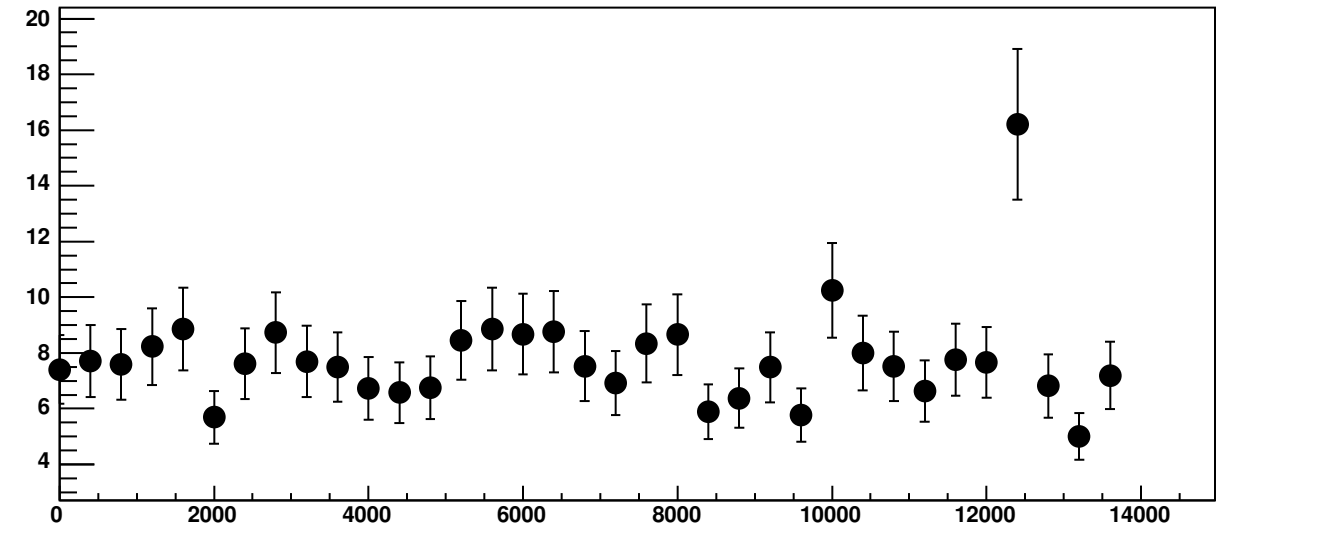

Chip 11, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

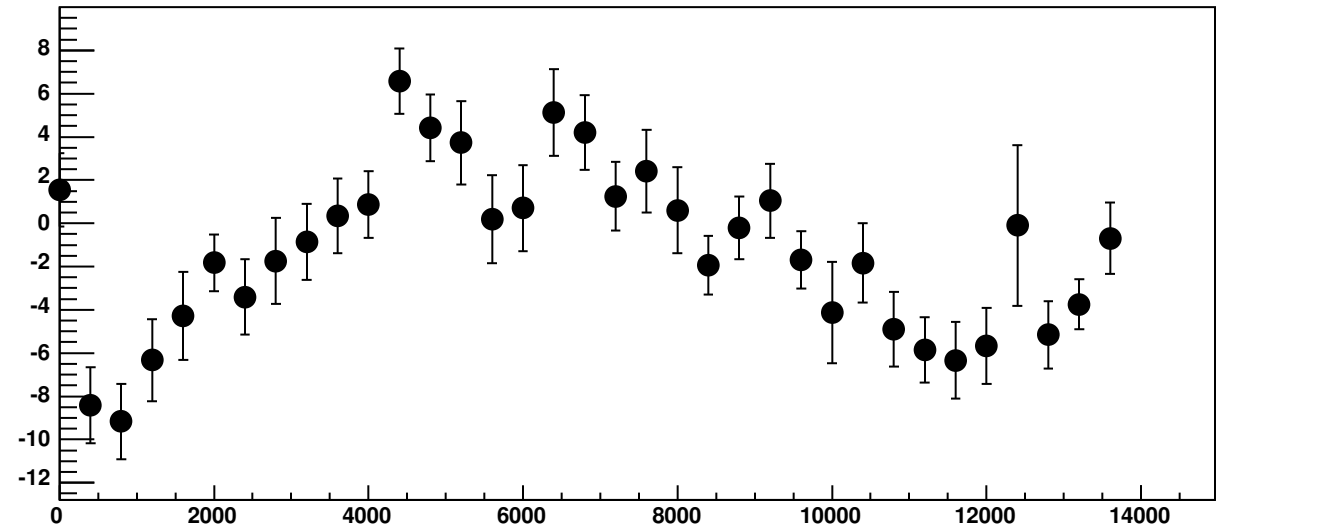
Chip 11, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

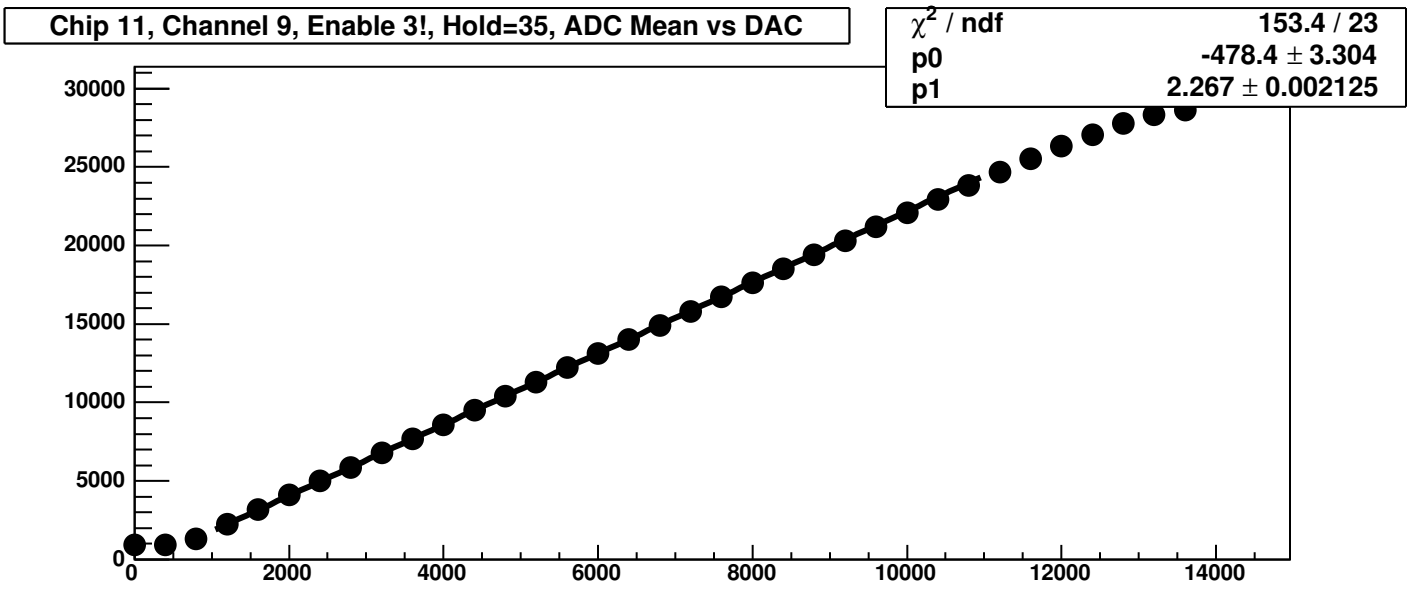


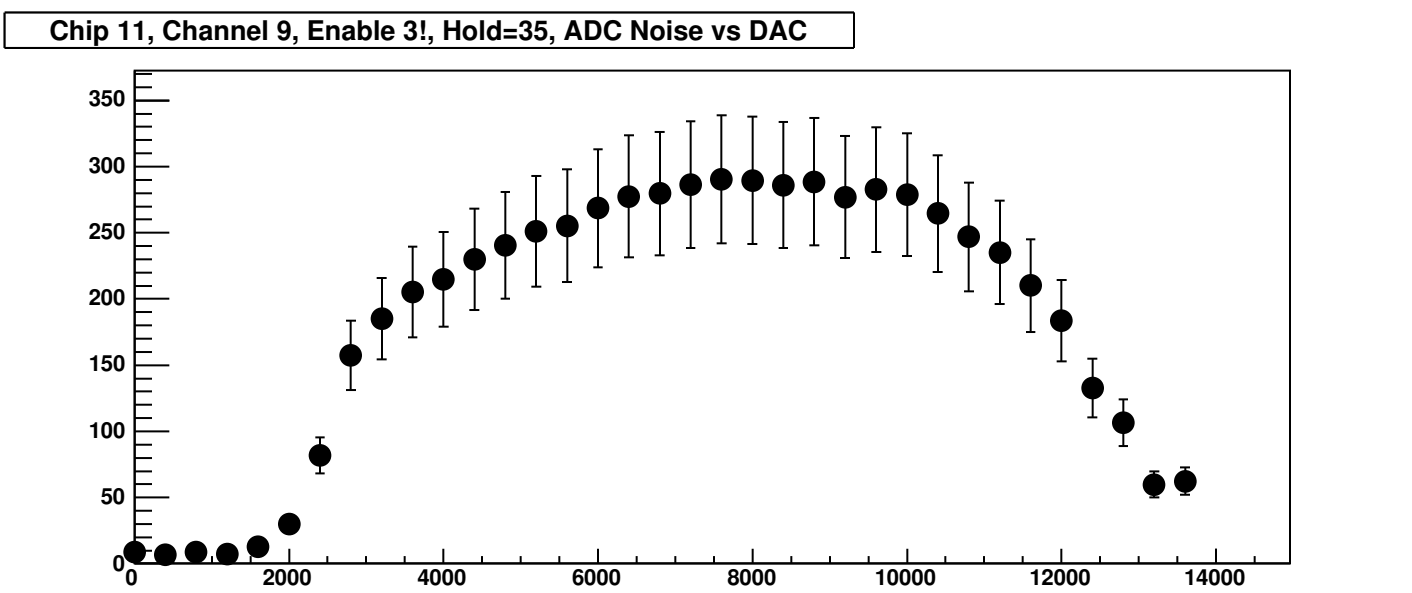
Chip 11, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

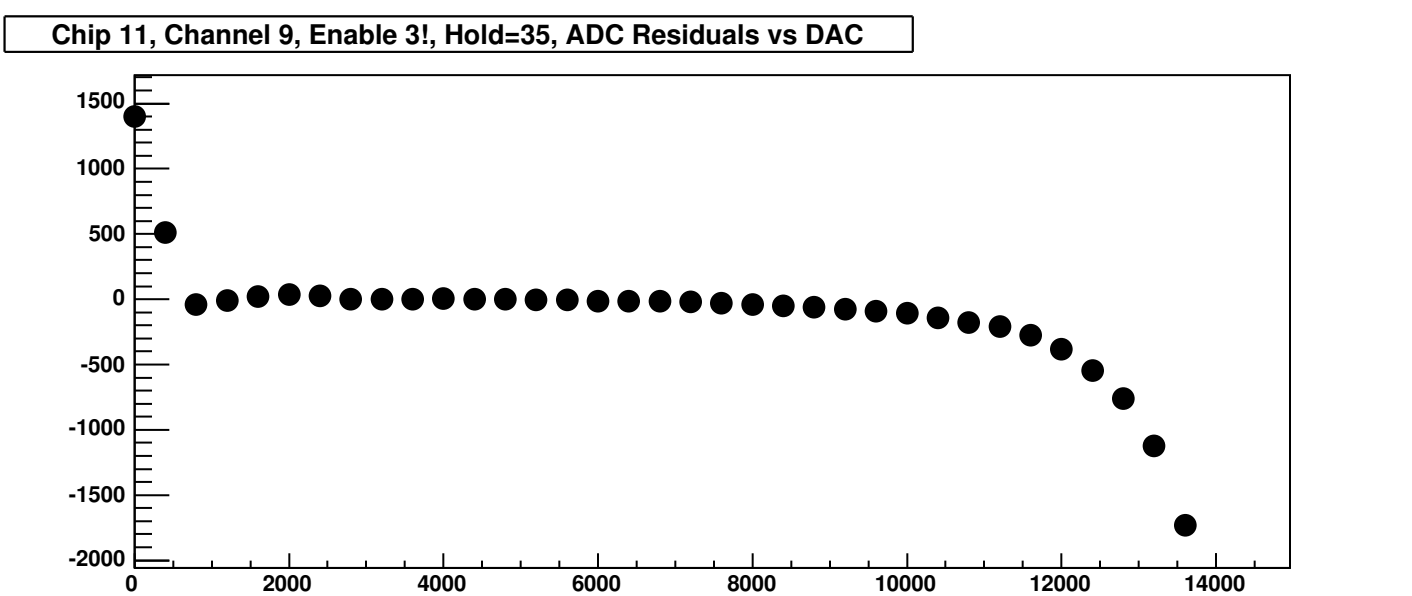


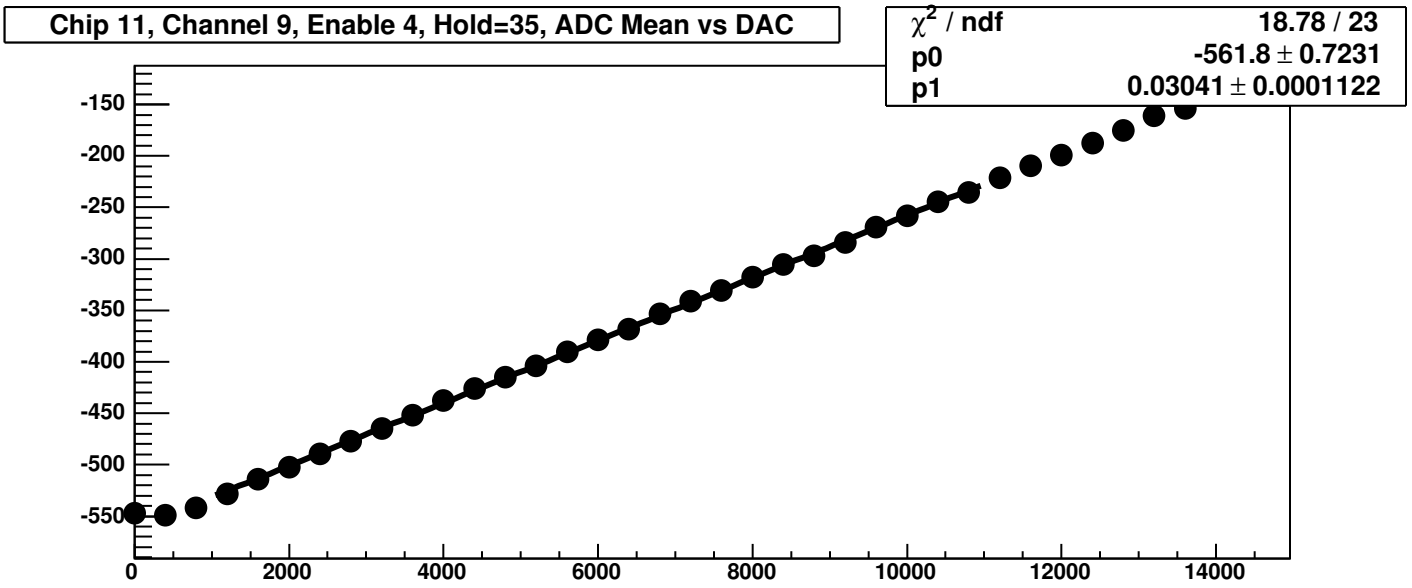
Chip 11, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

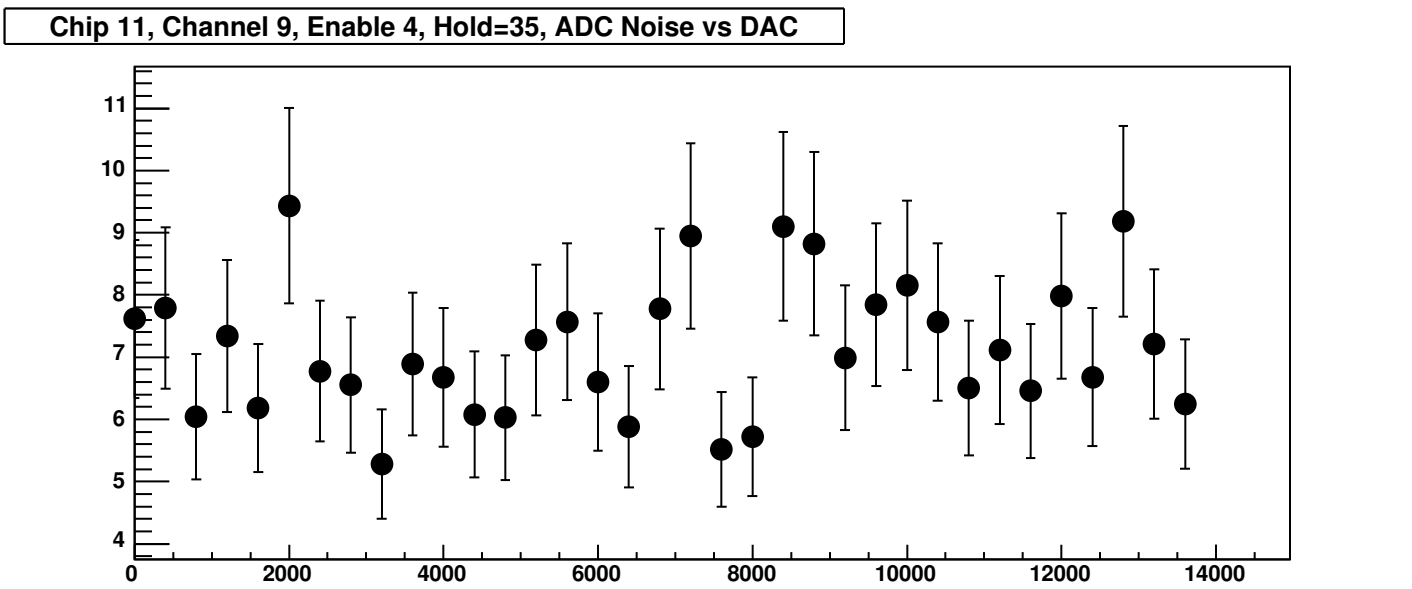


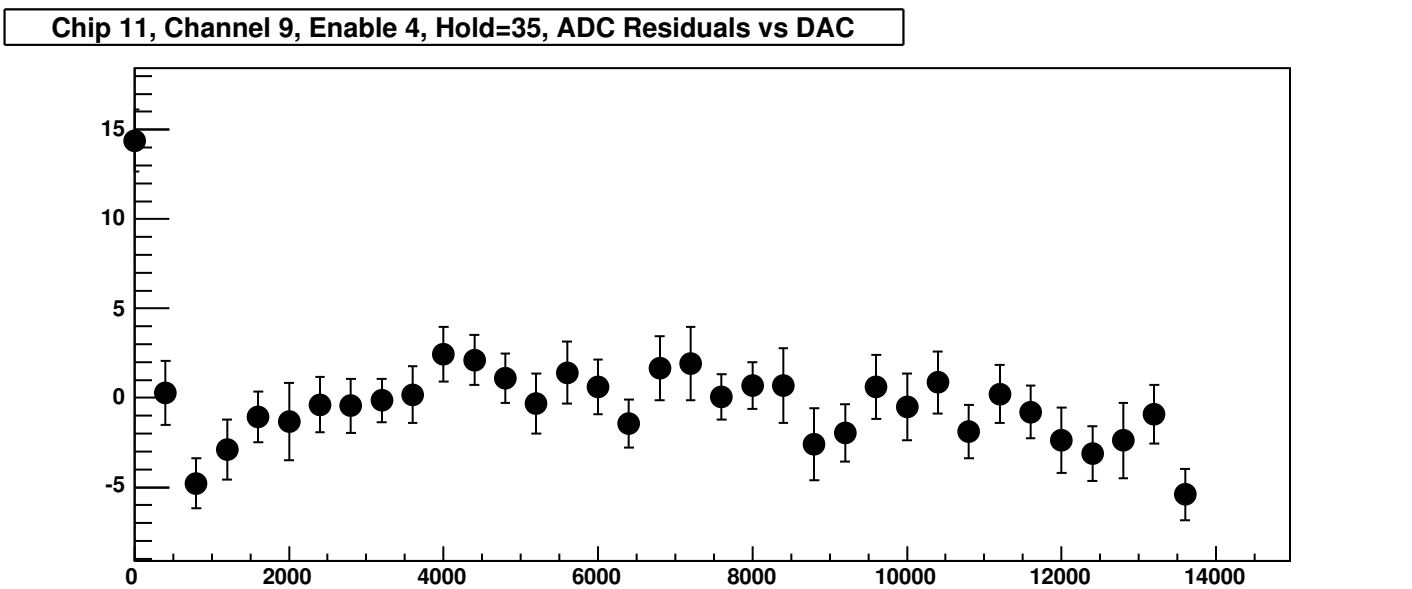

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 153.4 / 23
p0 -478.4 ± 3.304
p1 2.267 ± 0.002125
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

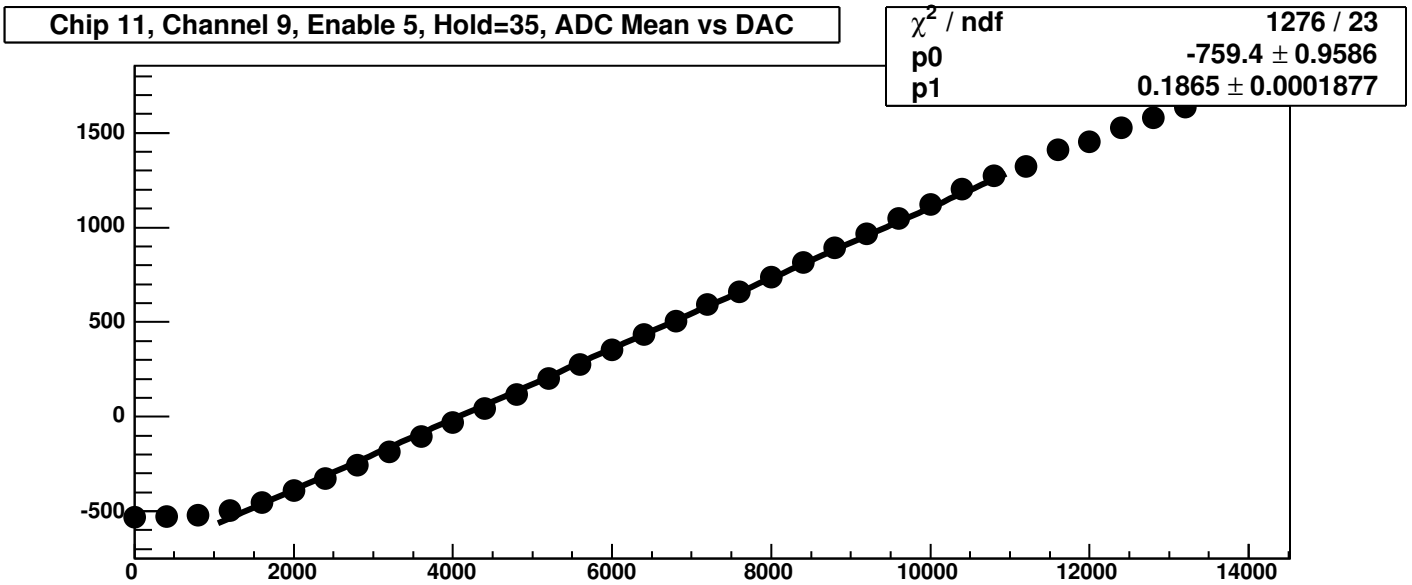

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

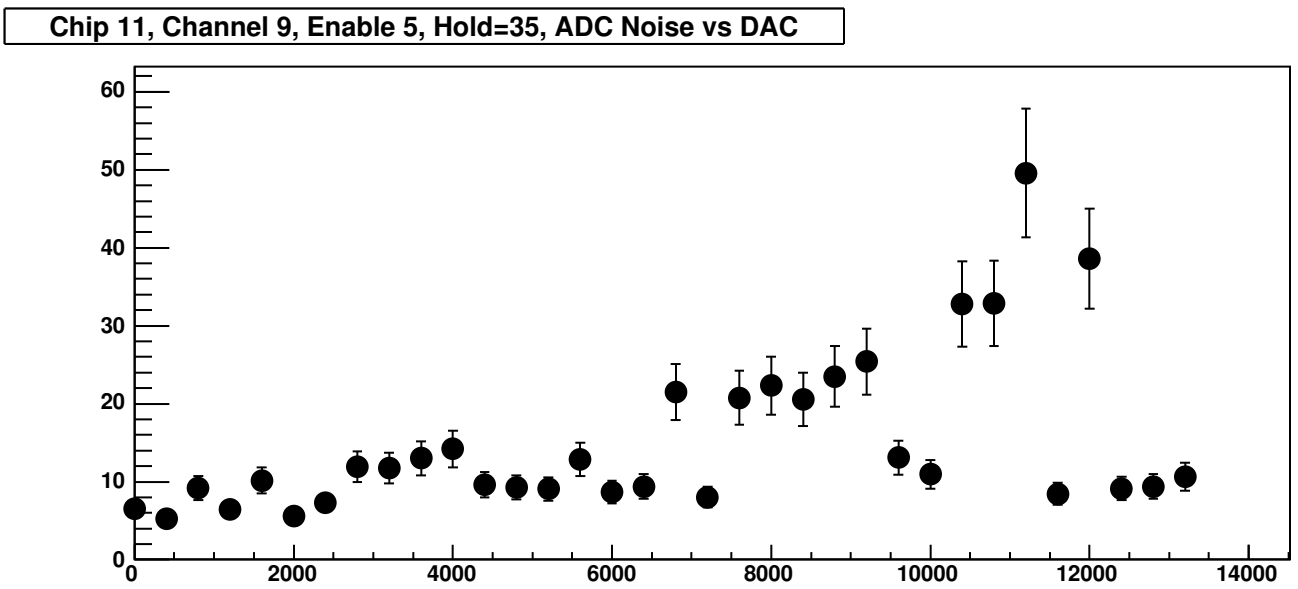

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

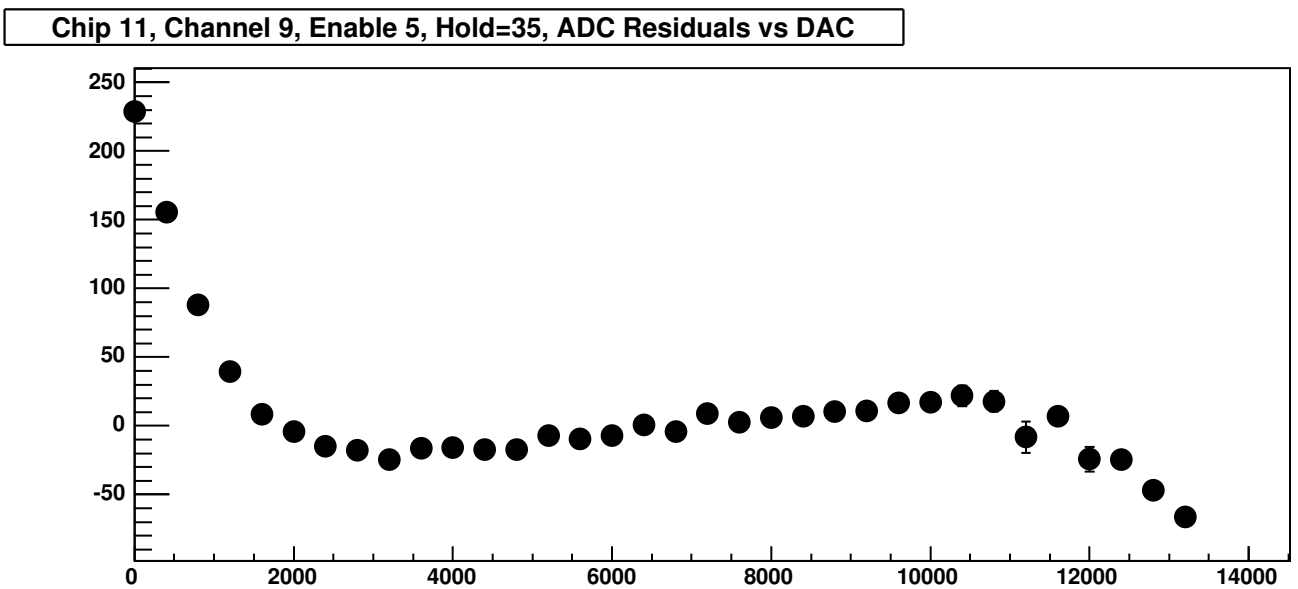

Chip 11, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.78 / 23
p0 -561.8 ± 0.7231
p1 0.03041 ± 0.0001122
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

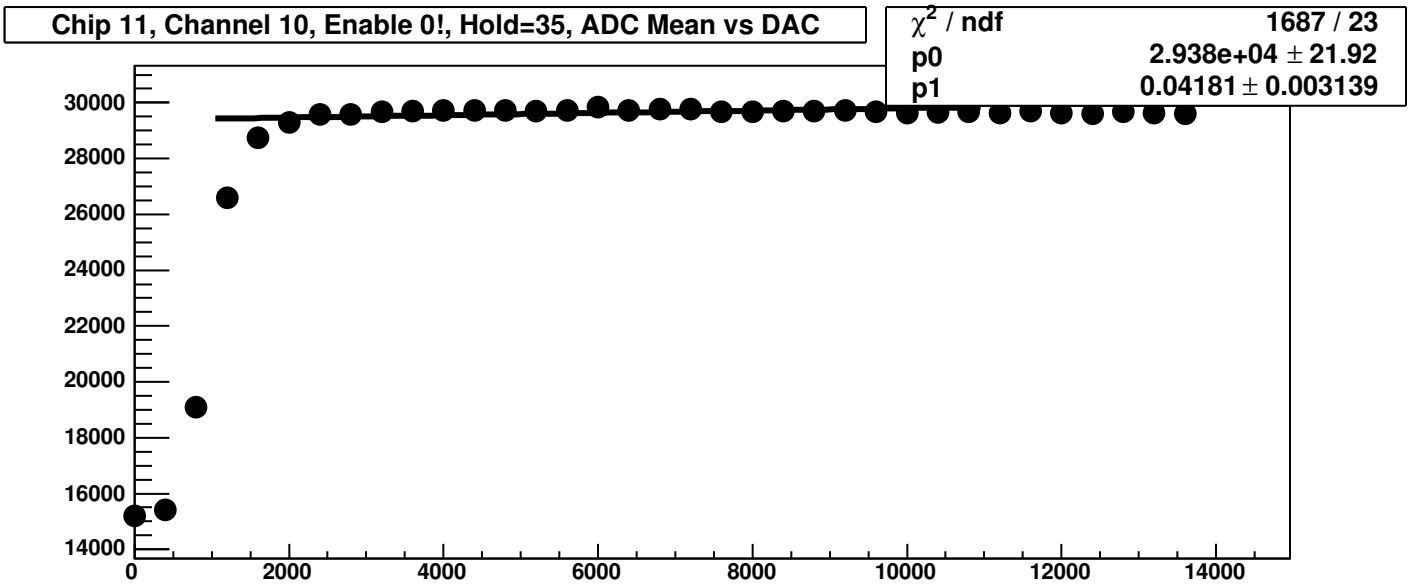

Chip 11, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

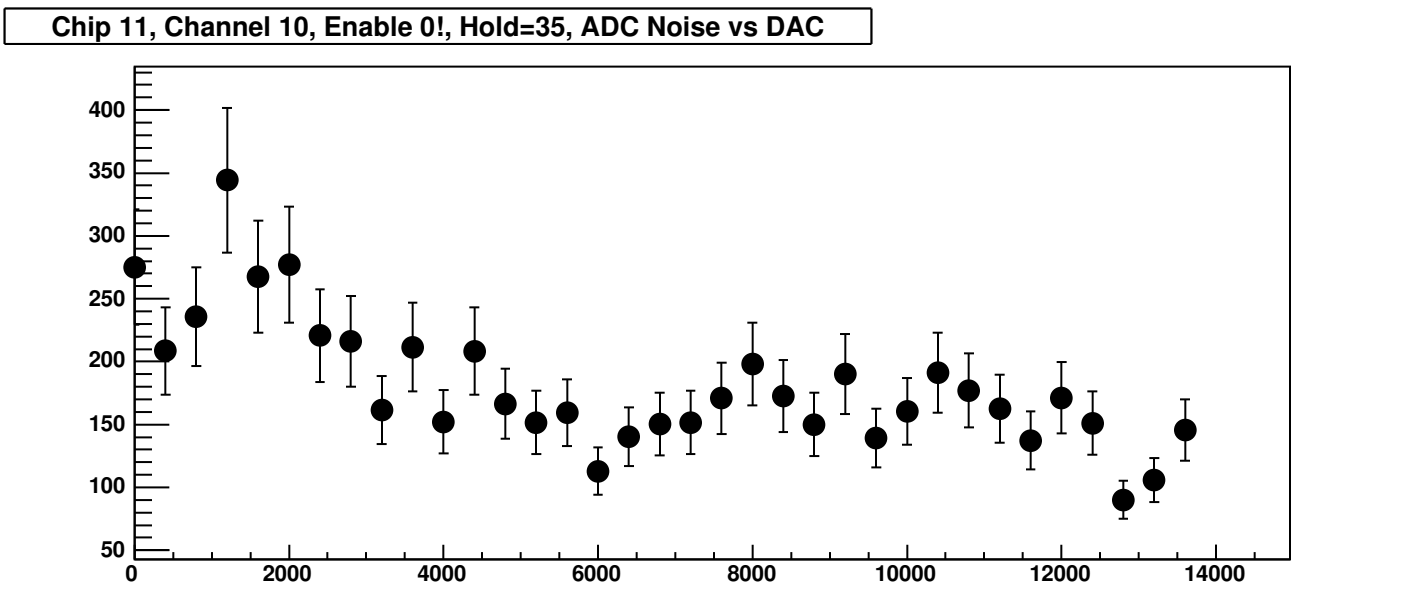

Chip 11, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

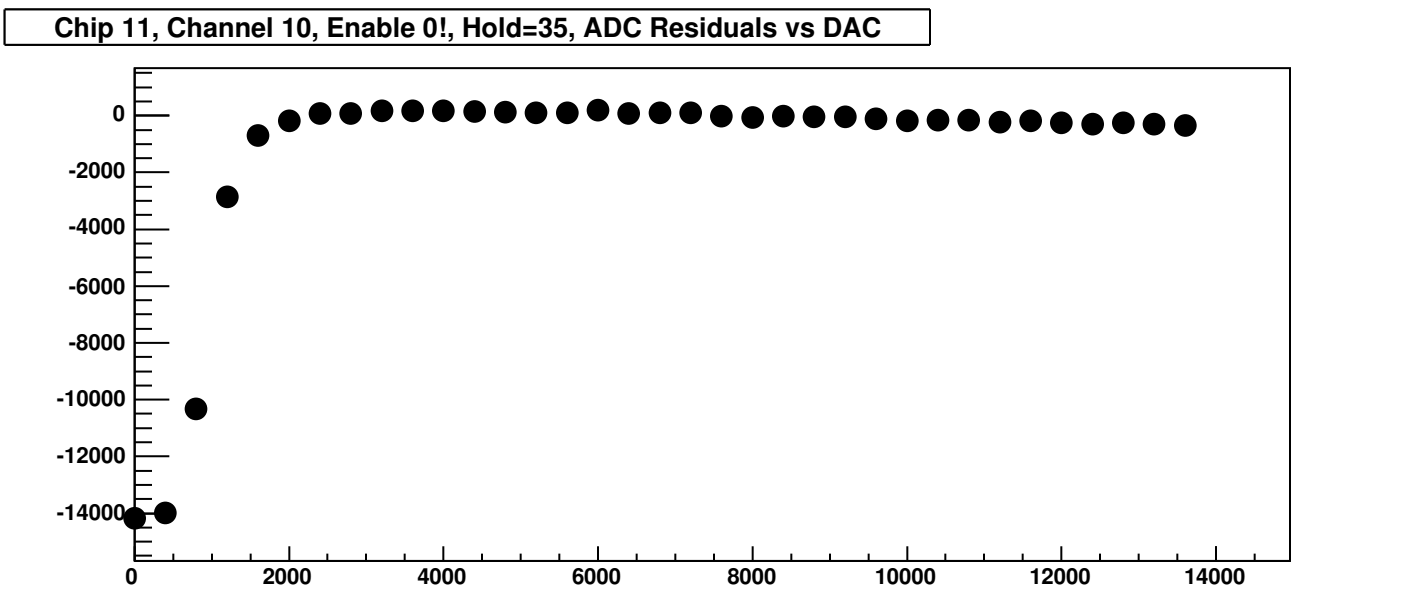

Chip 11, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1276 / 23
p0 -759.4 ± 0.9586
p1 0.1865 ± 0.0001877
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

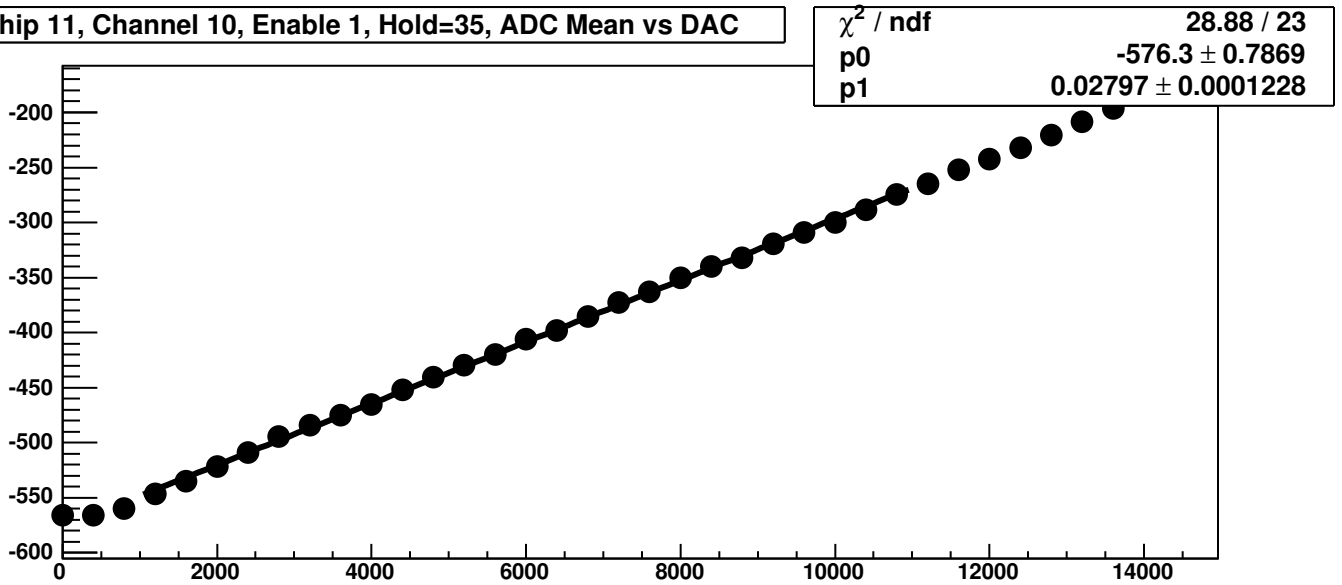

Chip 11, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1687 / 23
p0 2.938e+04 ± 21.92
p1 0.04181 ± 0.003139
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

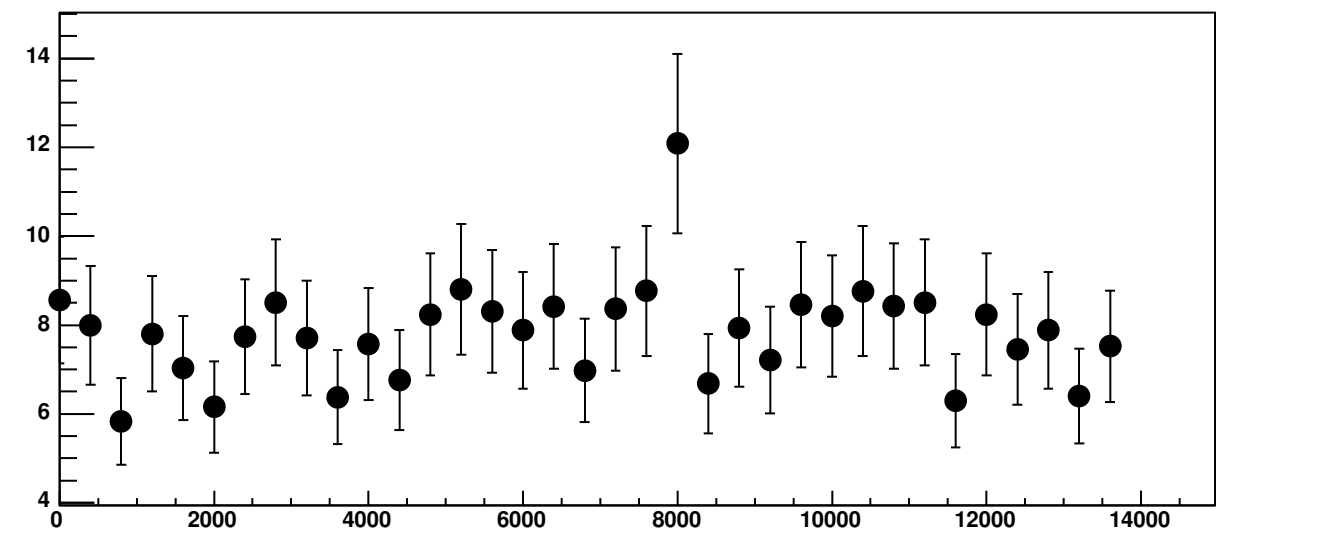

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

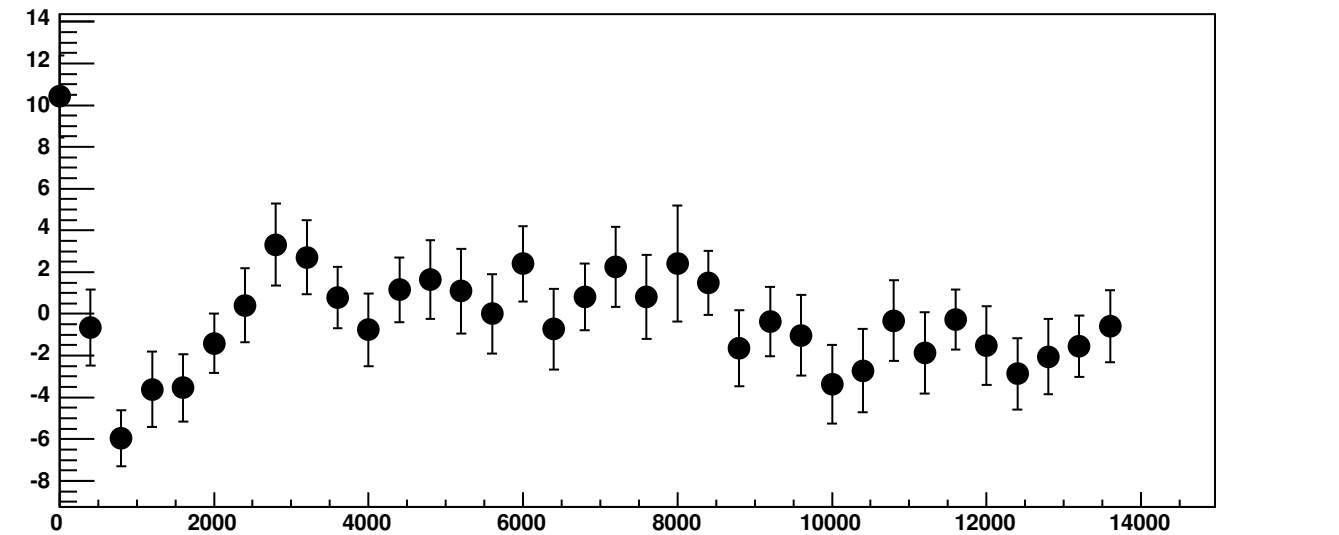
Chip 11, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

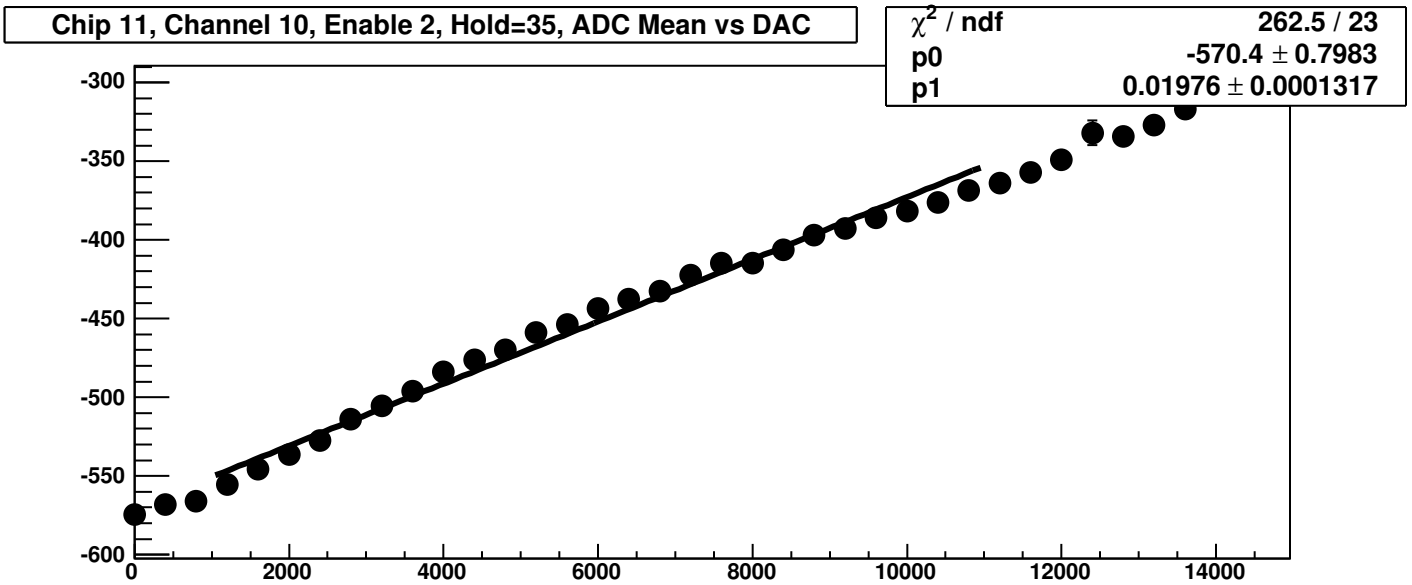


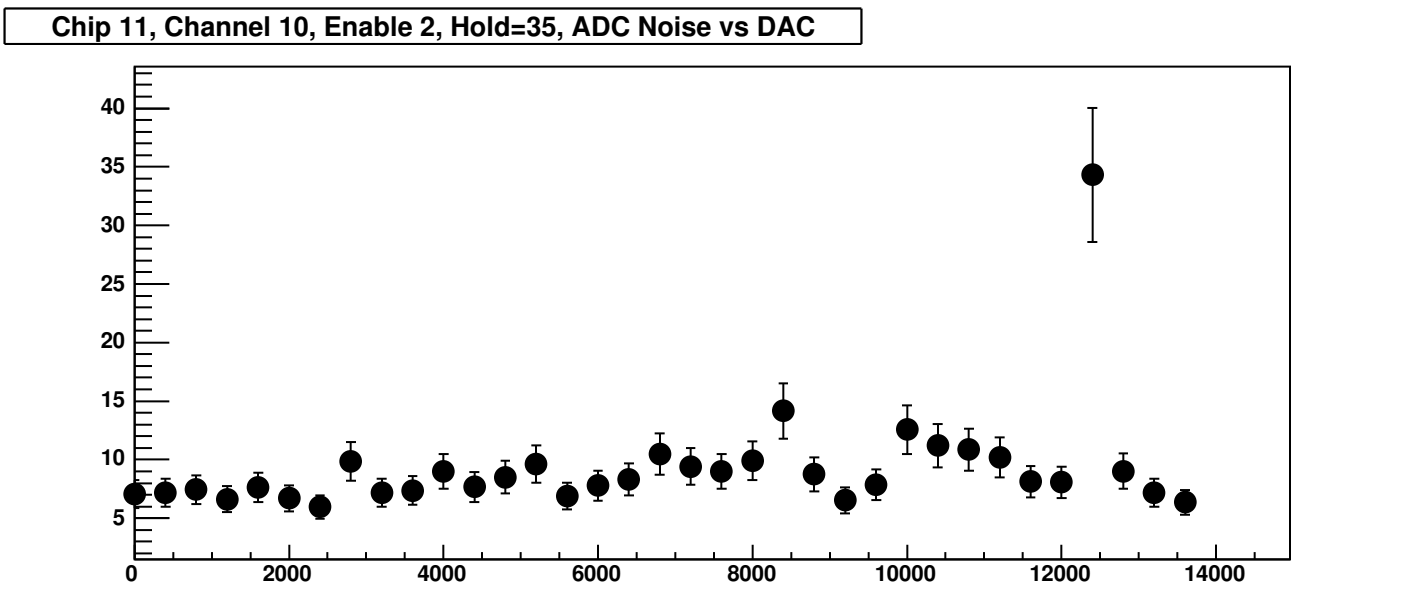
Chip 11, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

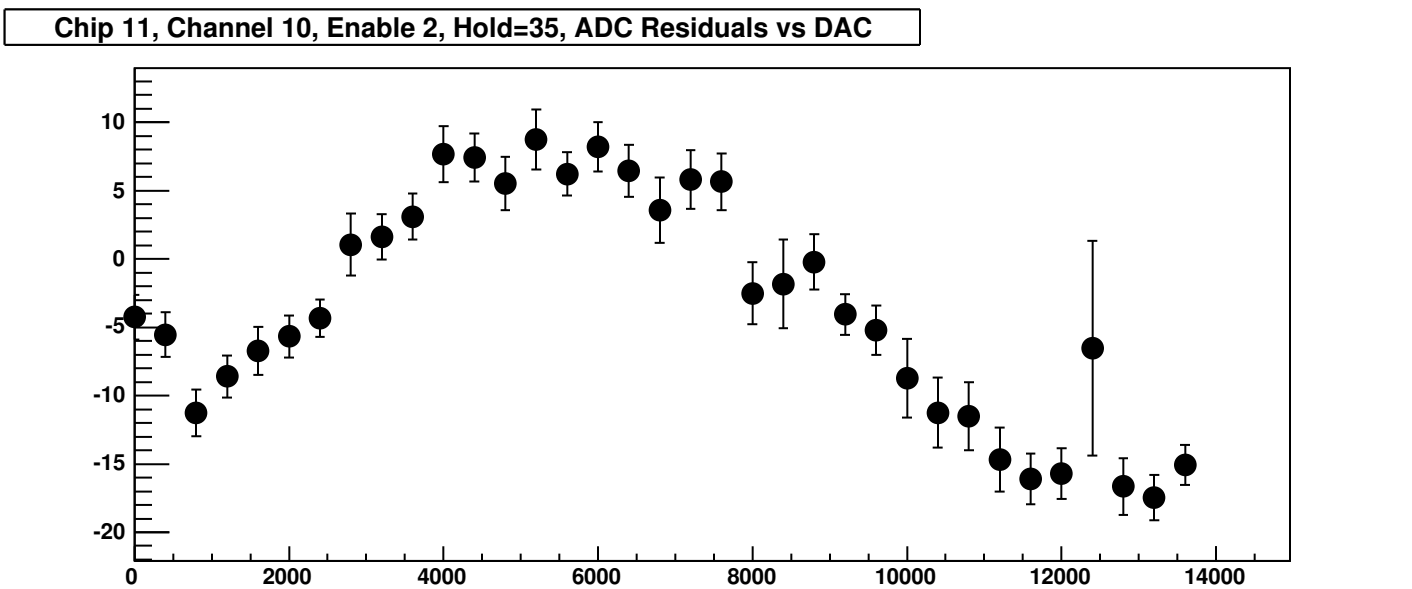


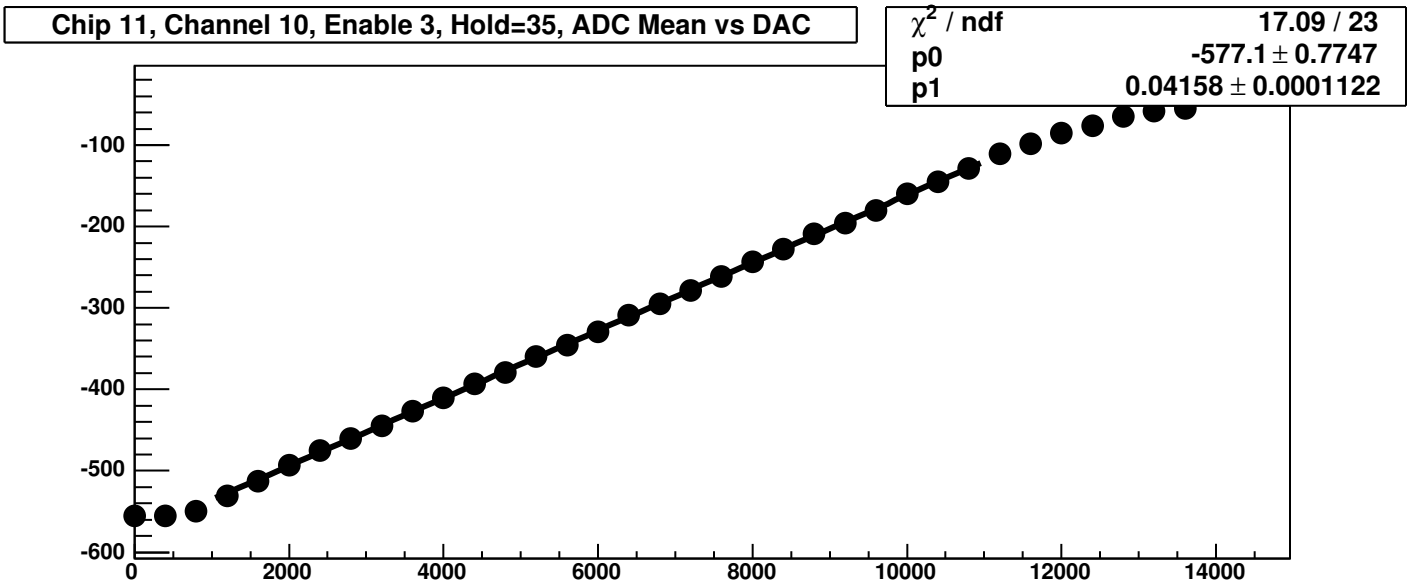
Chip 11, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

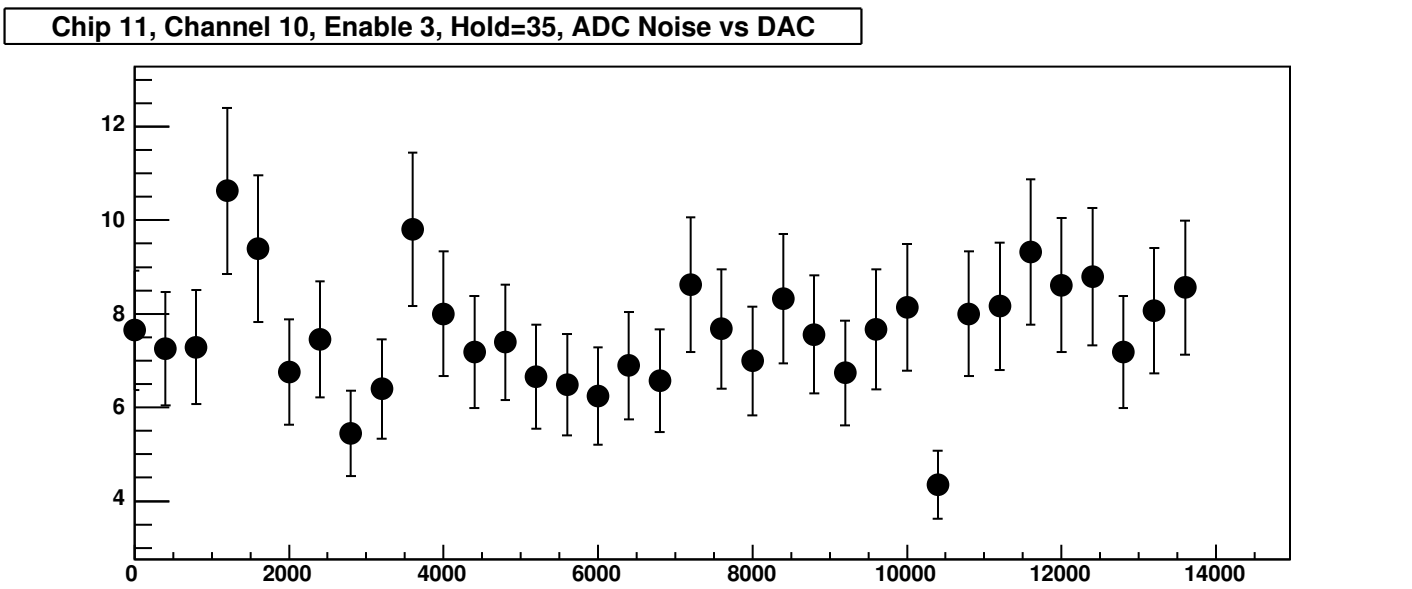


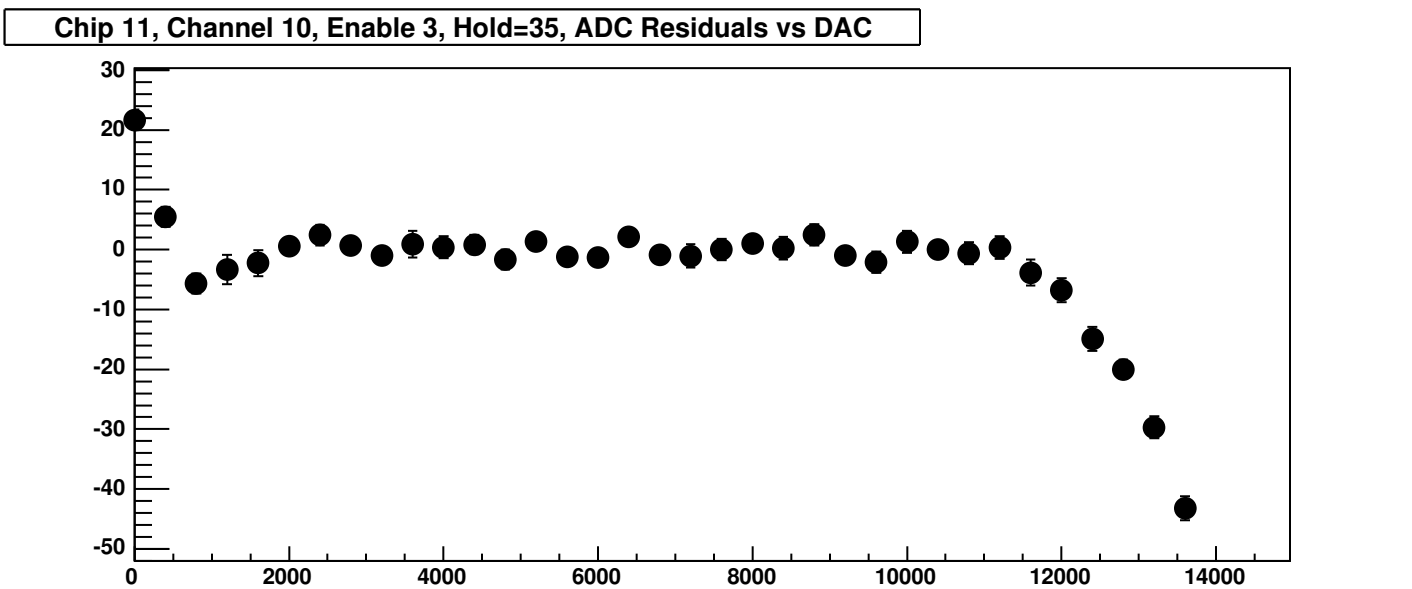

Chip 11, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 262.5 / 23
p0 -570.4 ± 0.7983
p1 0.01976 ± 0.0001317
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

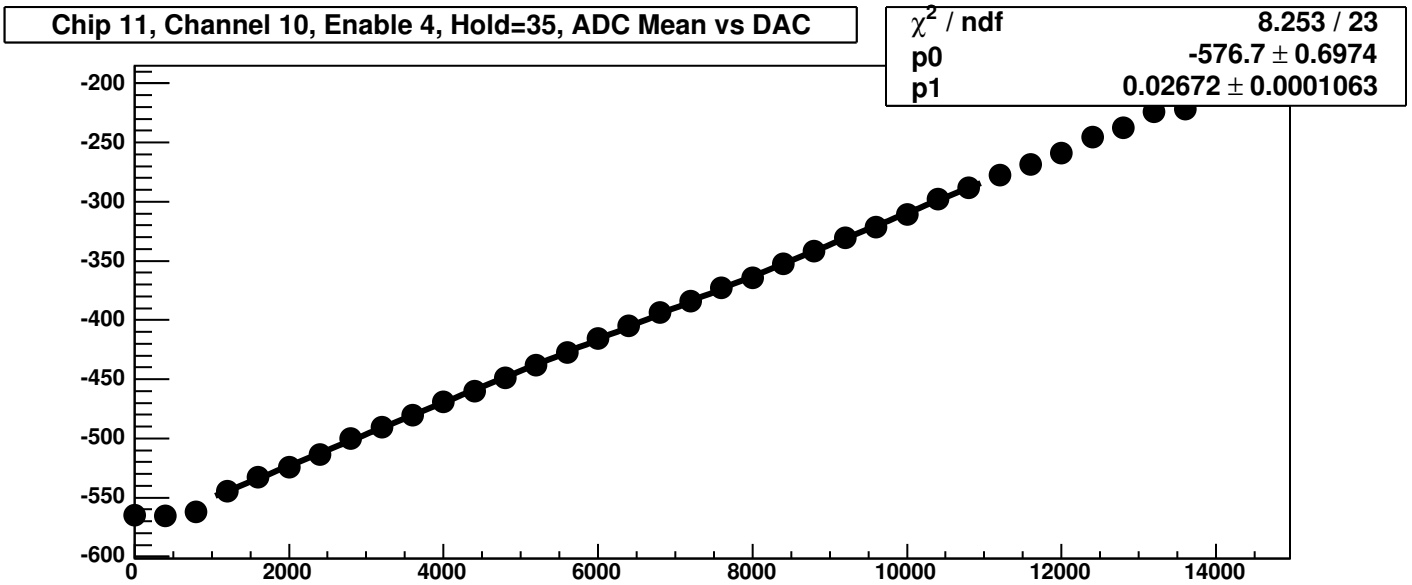

Chip 11, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

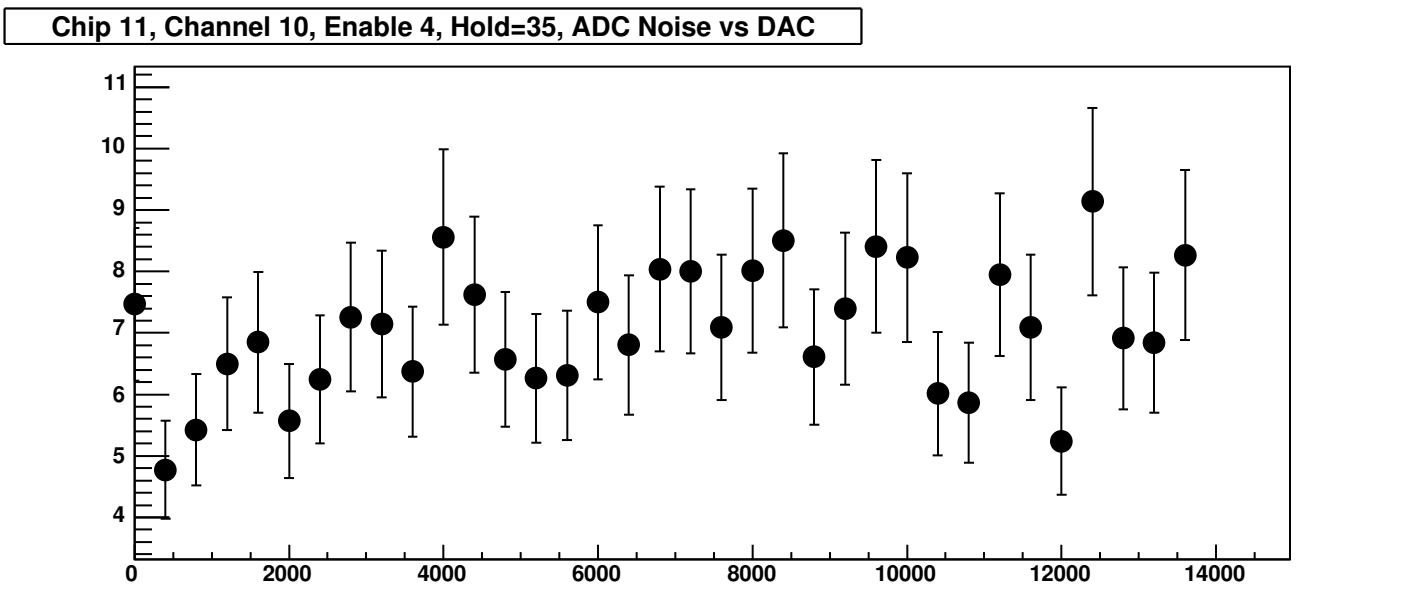

Chip 11, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

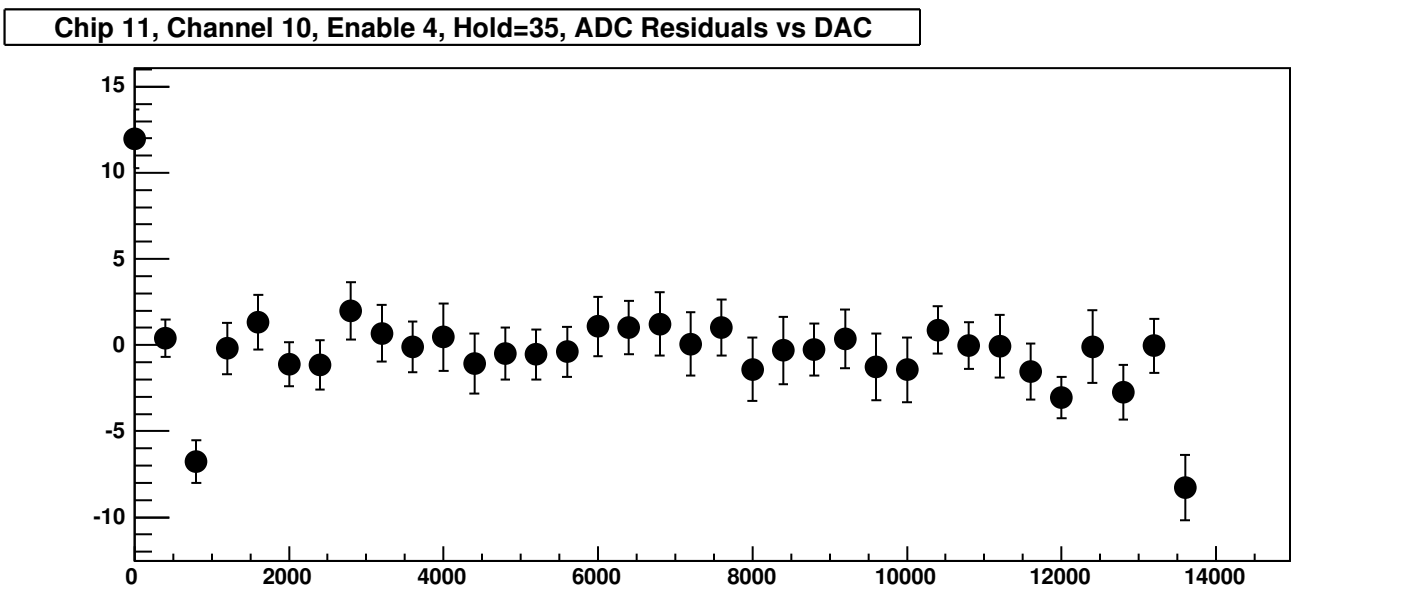

Chip 11, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.09 / 23
p0 -577.1 ± 0.7747
p1 0.04158 ± 0.0001122
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

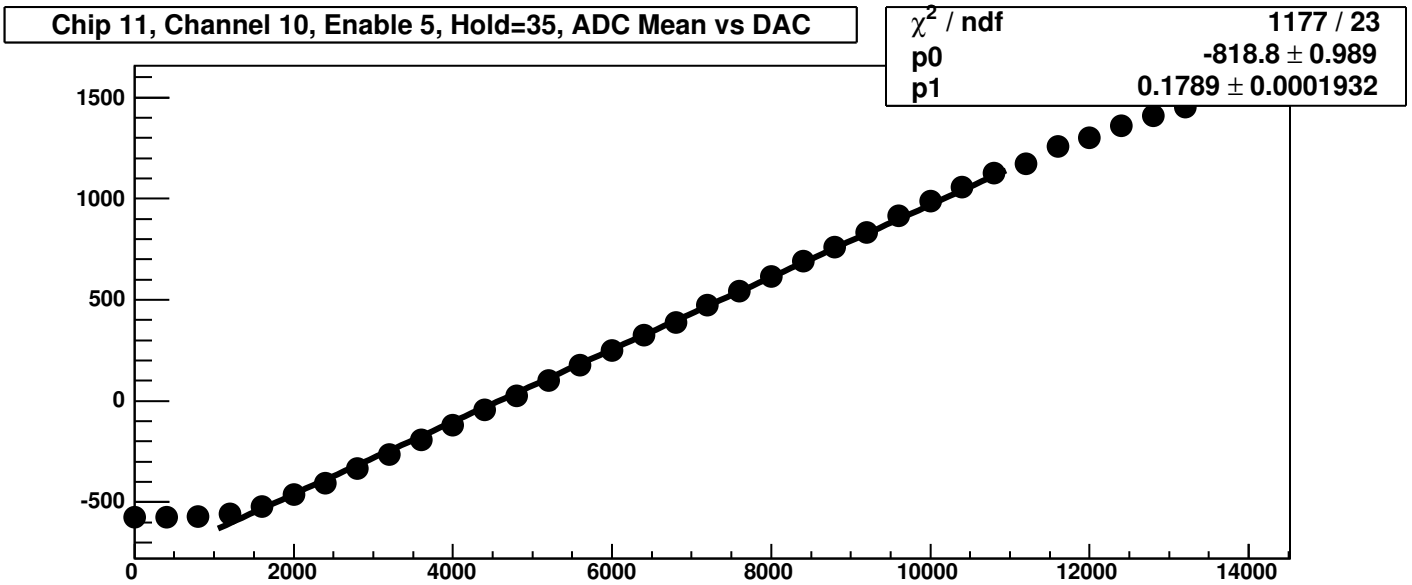

Chip 11, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

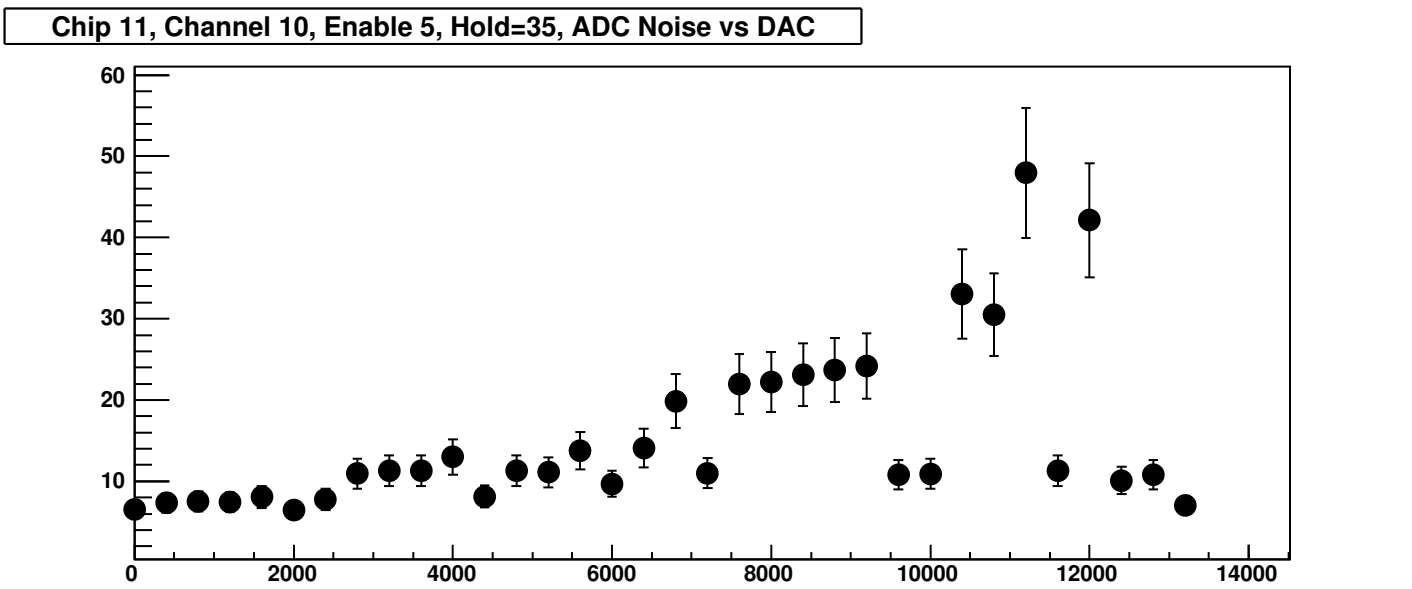

Chip 11, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

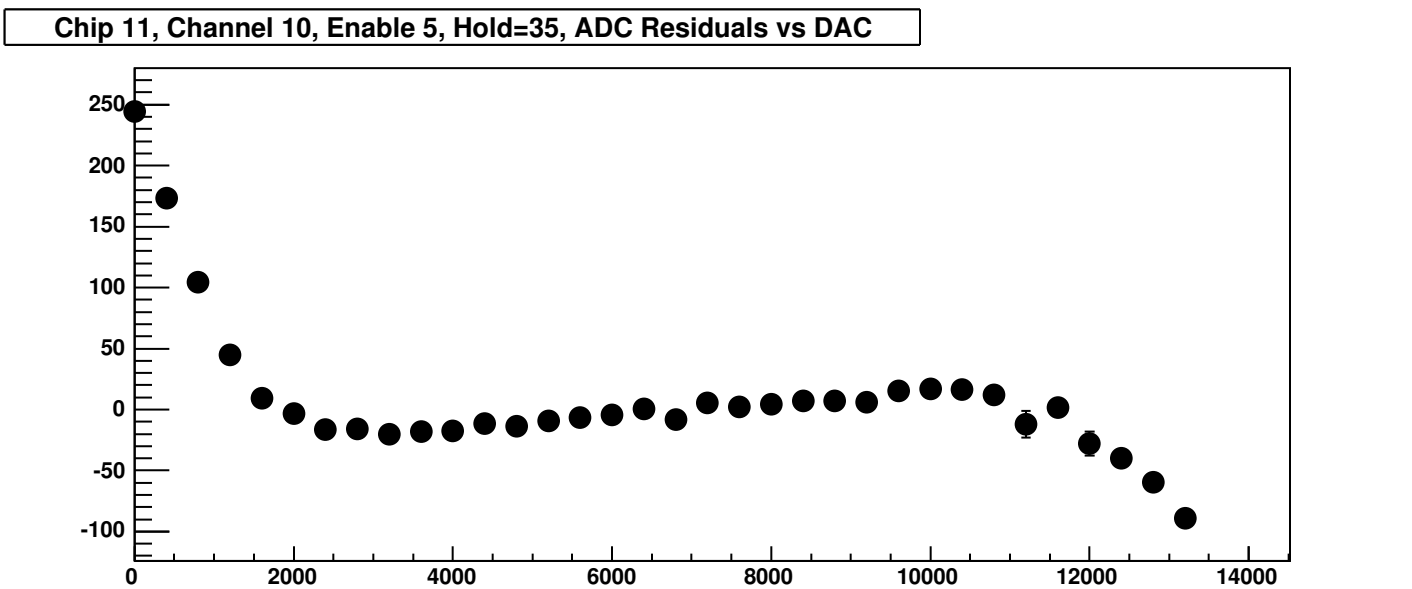

Chip 11, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 8.253 / 23
p0 -576.7 ± 0.6974
p1 0.02672 ± 0.0001063
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

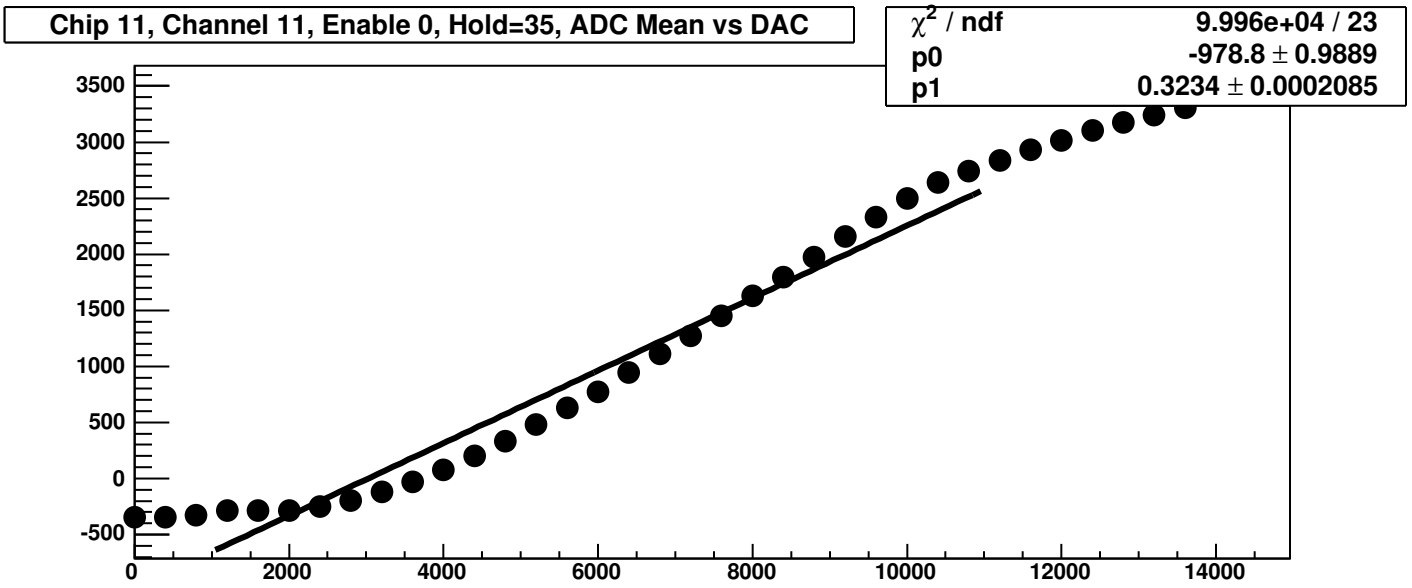

Chip 11, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

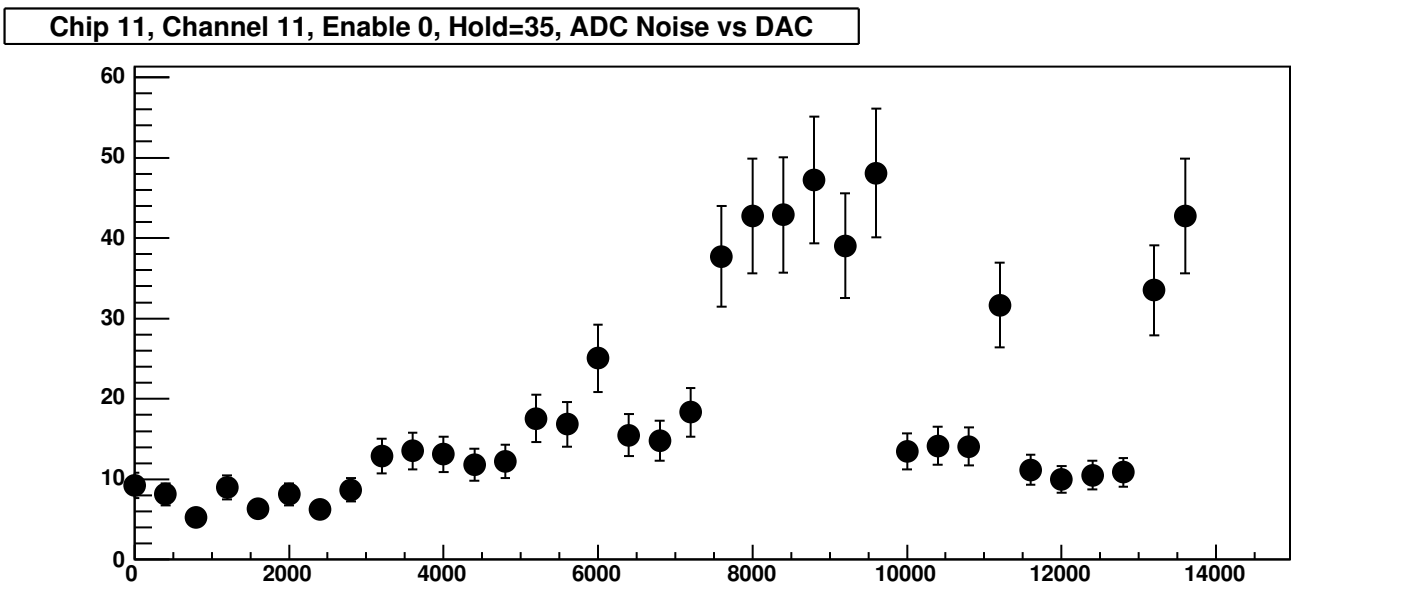

Chip 11, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

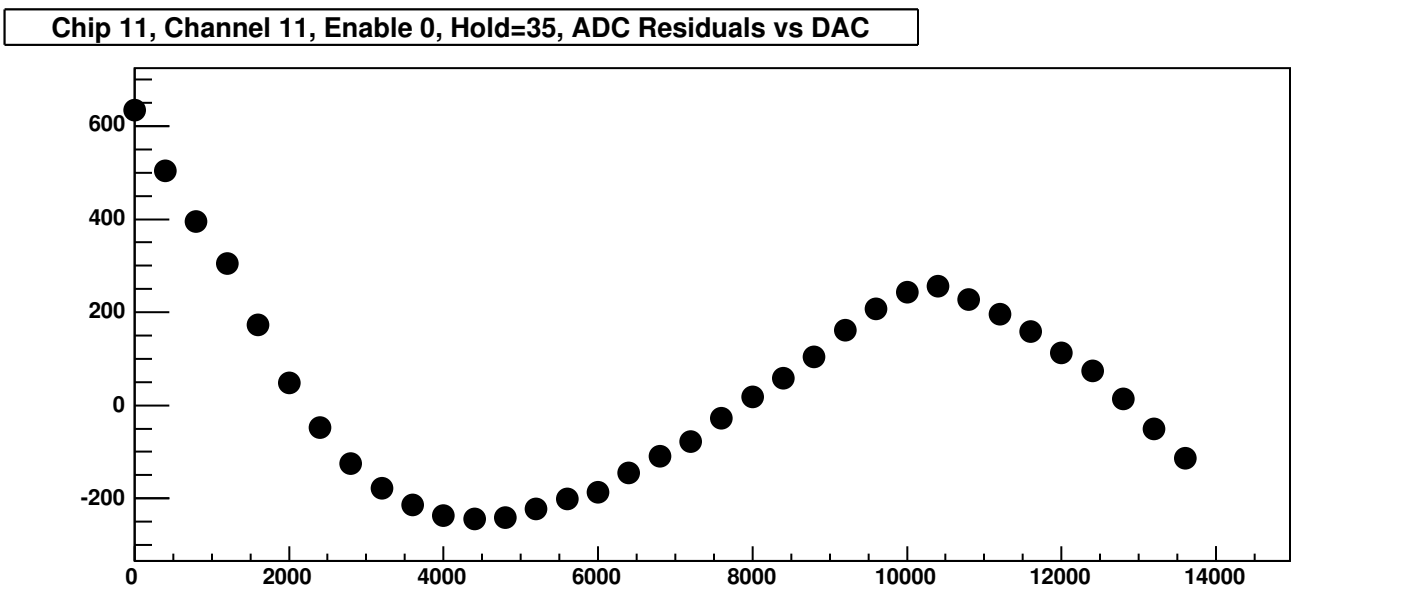

Chip 11, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1177 / 23
p0 -818.8 ± 0.989
p1 0.1789 ± 0.0001932
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

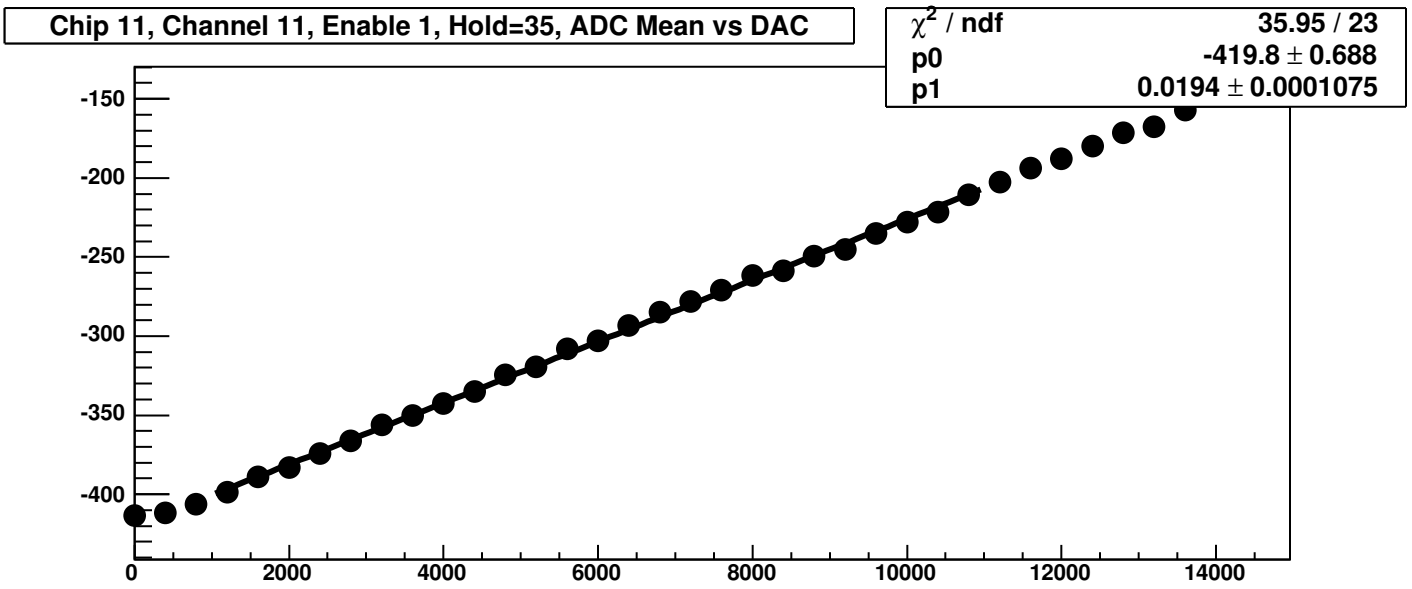

Chip 11, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

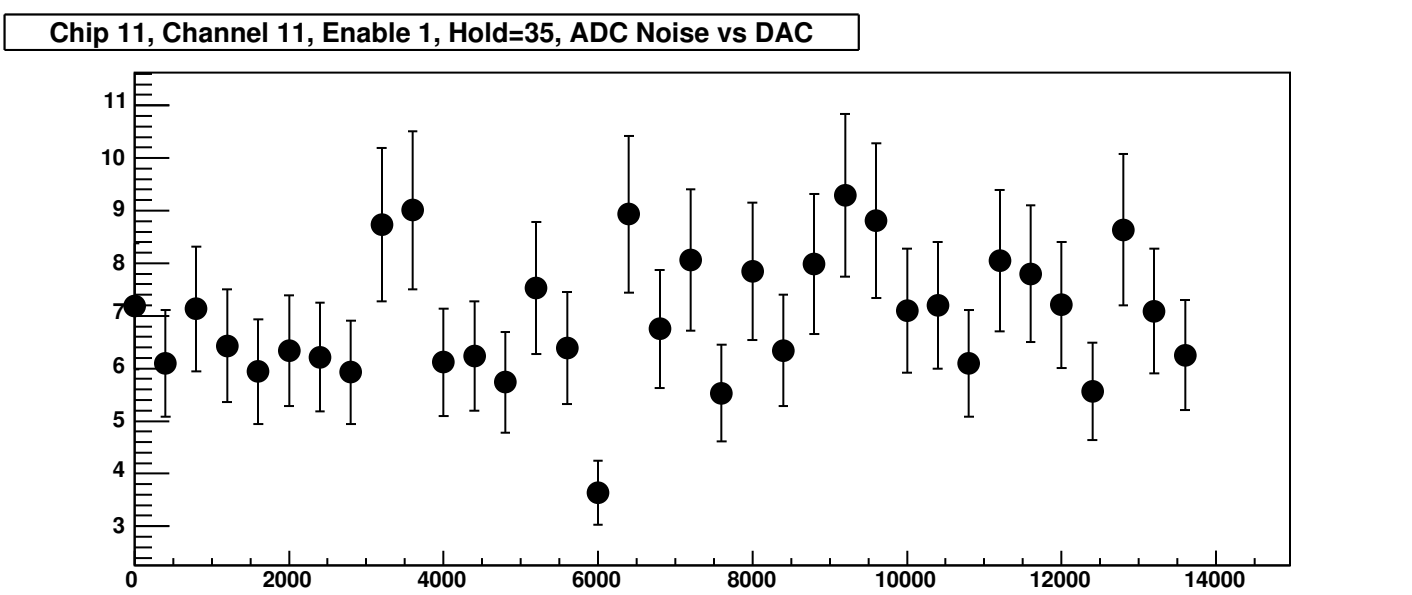

Chip 11, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

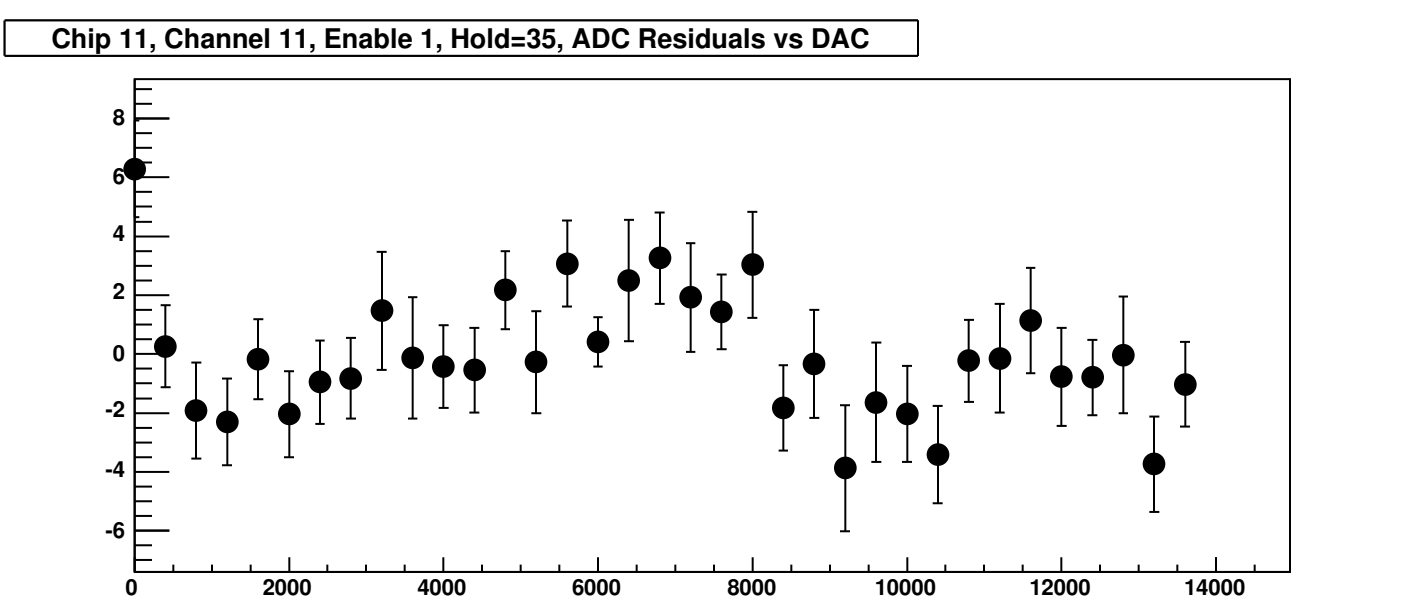

Chip 11, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9.996e+04 / 23
p0 -978.8 ± 0.9889
p1 0.3234 ± 0.0002085
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

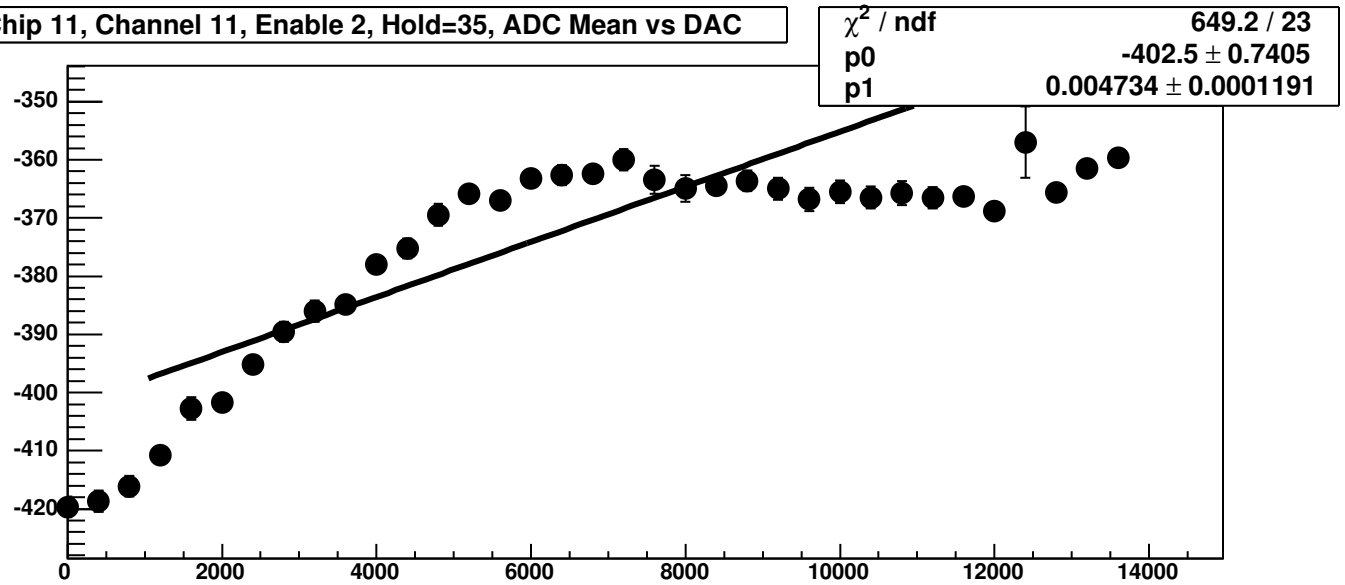

Chip 11, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.95 / 23
p0 -419.8 ± 0.688
p1 0.0194 ± 0.0001075
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

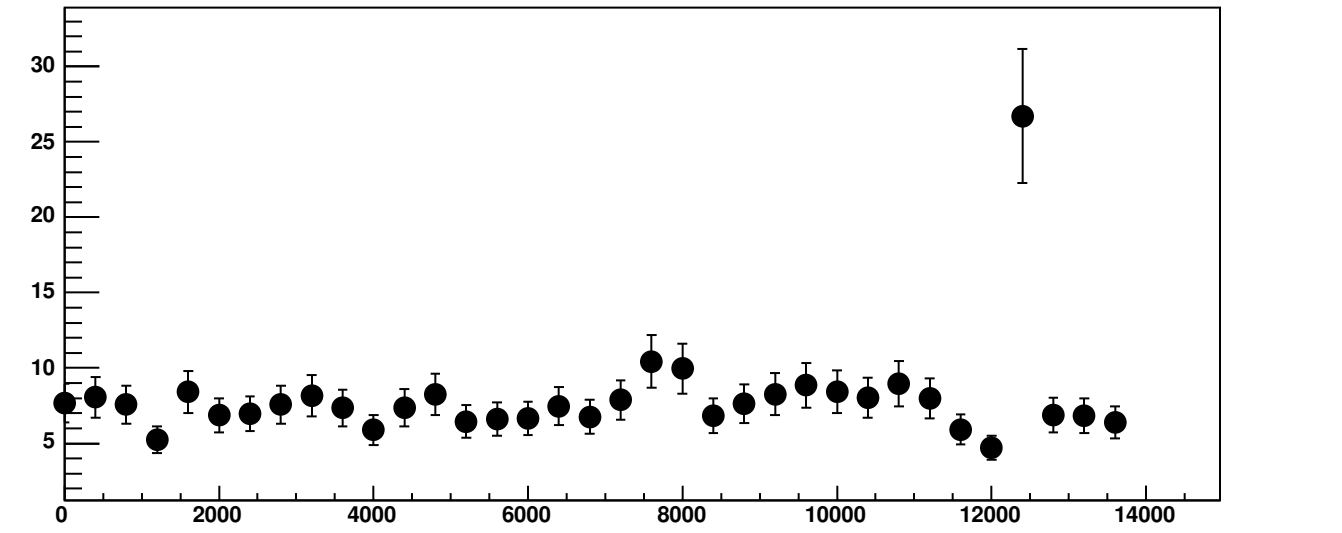

Chip 11, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

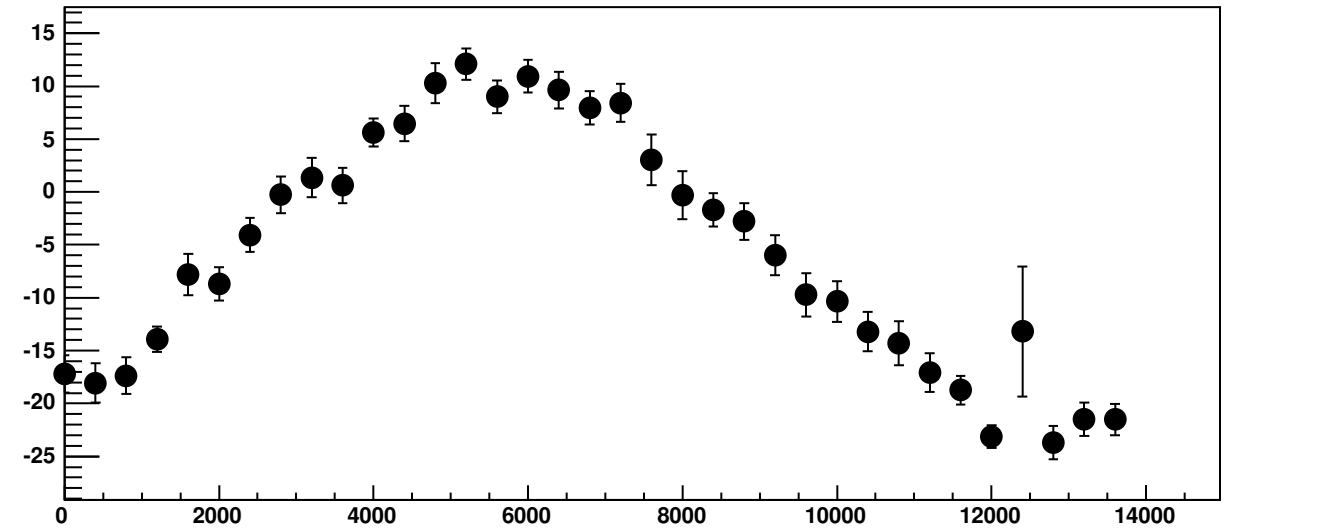
Chip 11, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

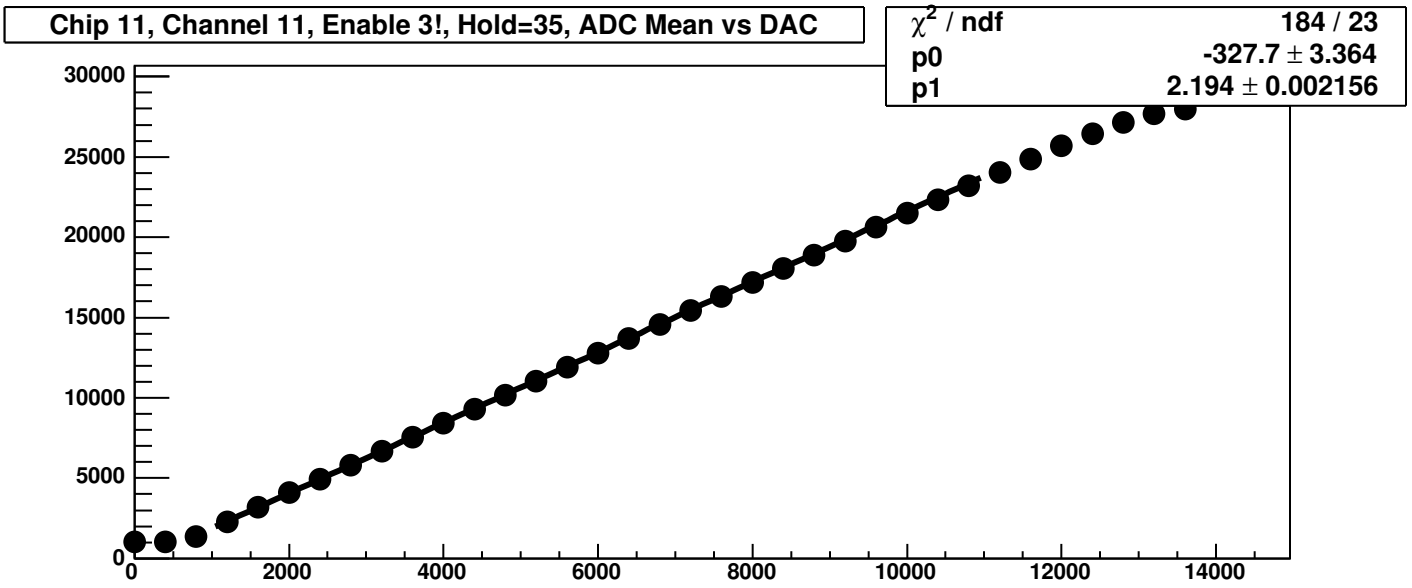


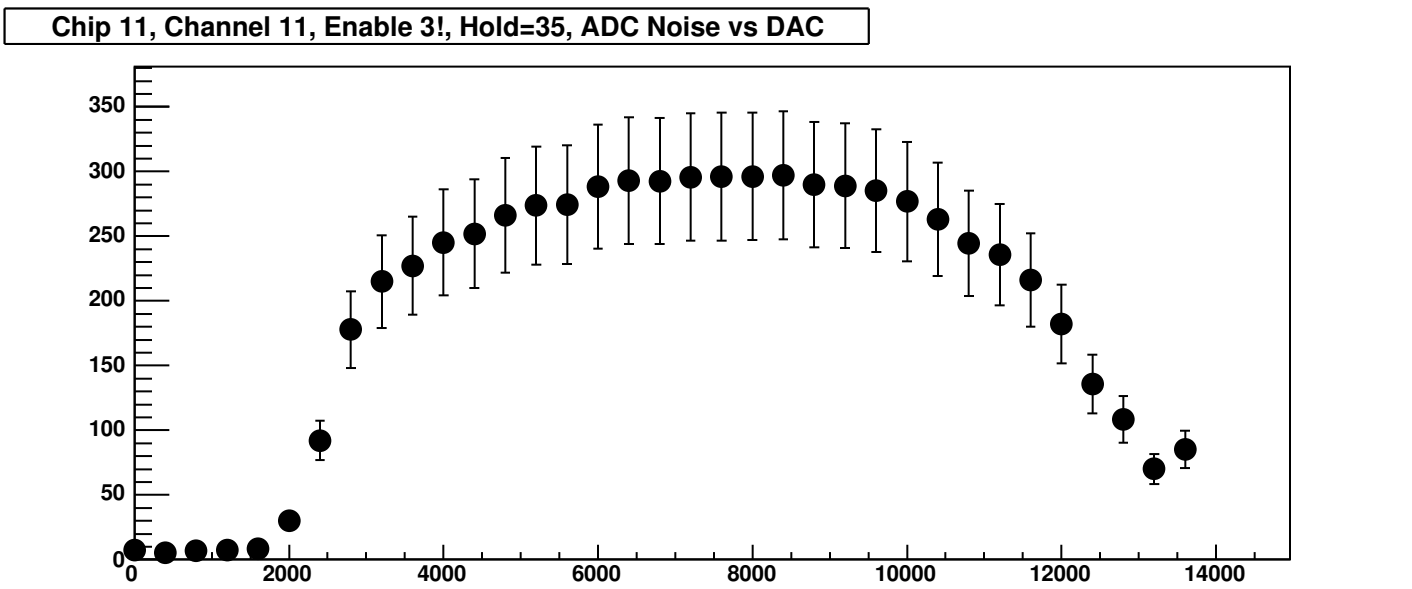
Chip 11, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

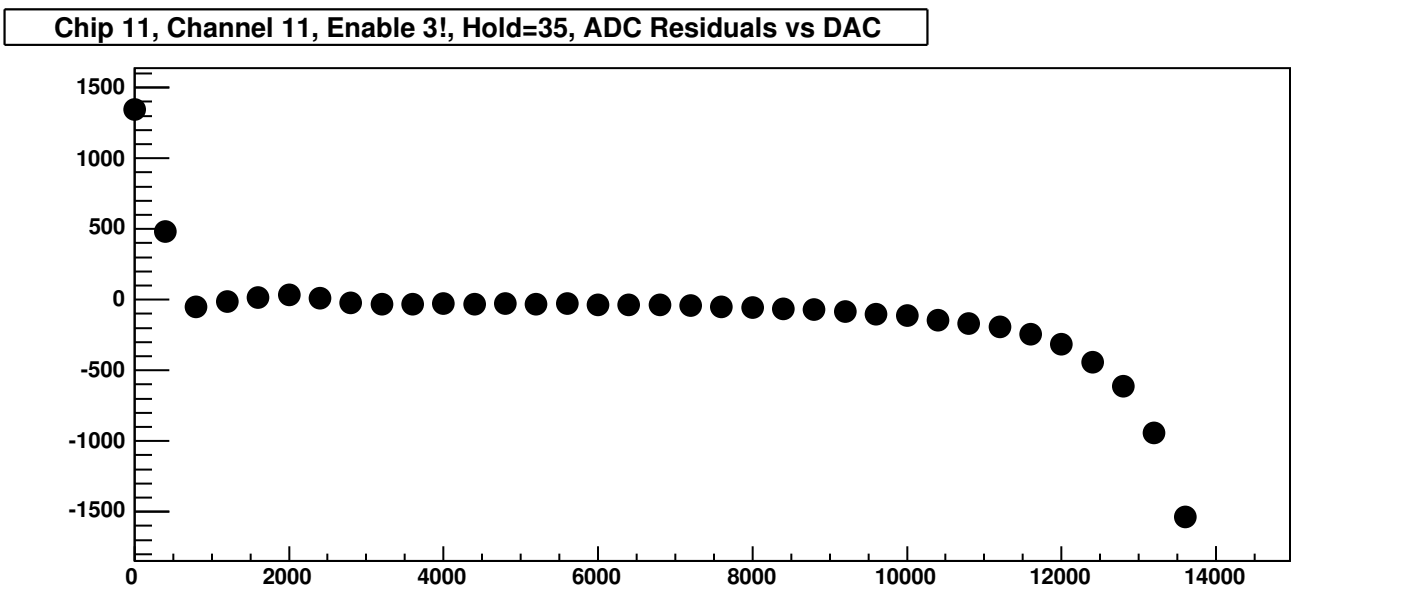


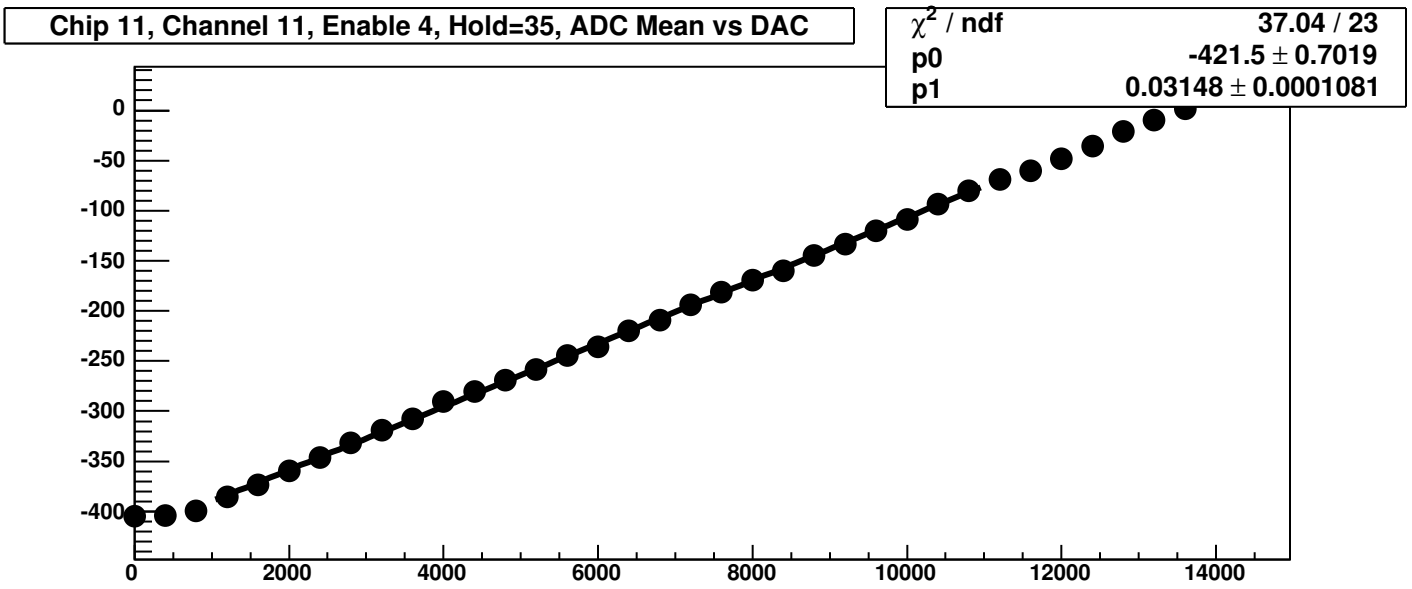
Chip 11, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

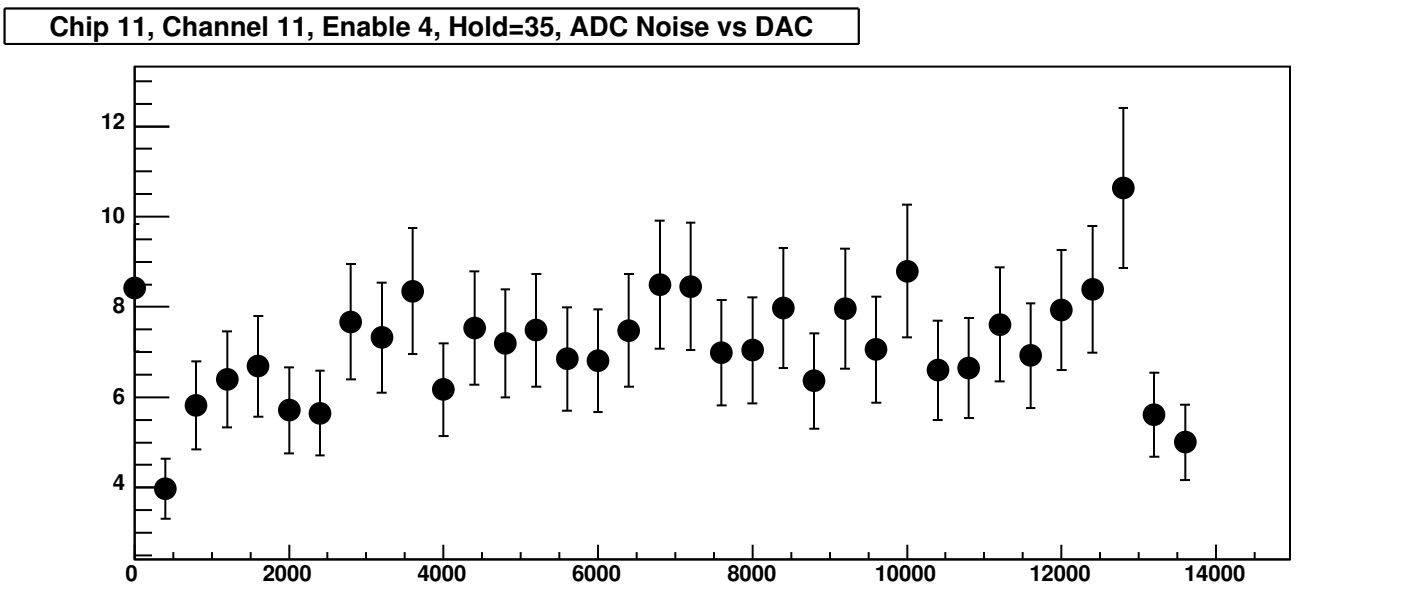


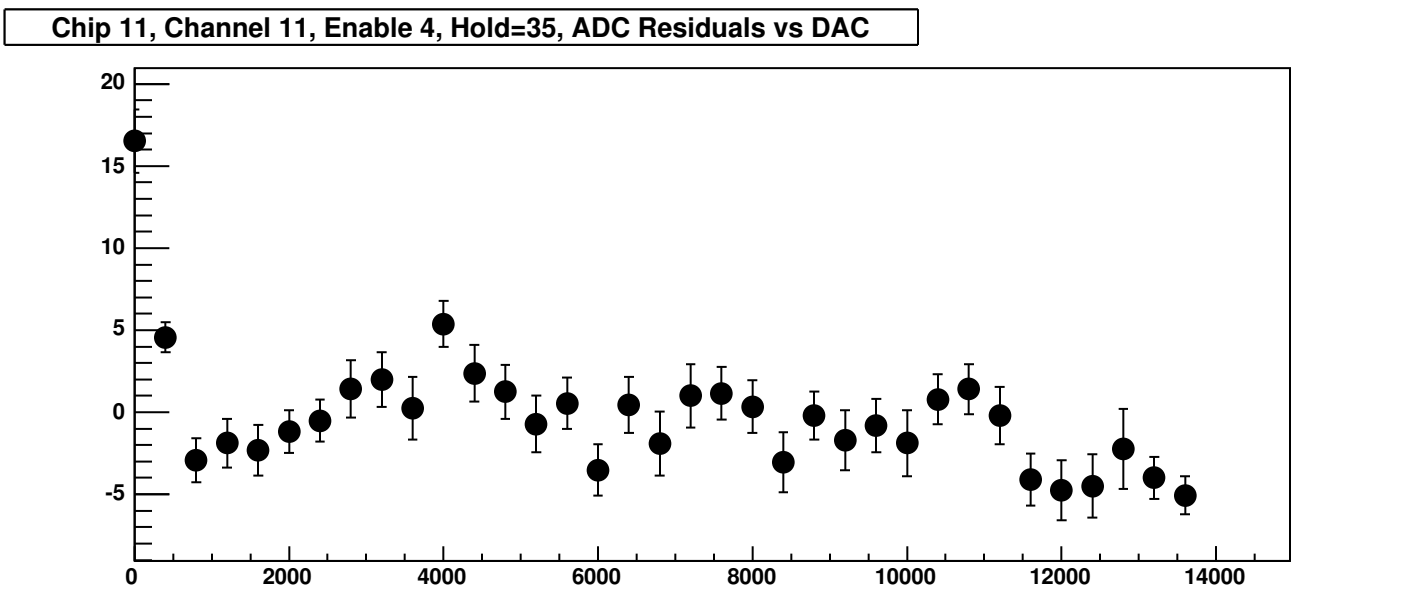

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 184 / 23
p0 -327.7 ± 3.364
p1 2.194 ± 0.002156
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

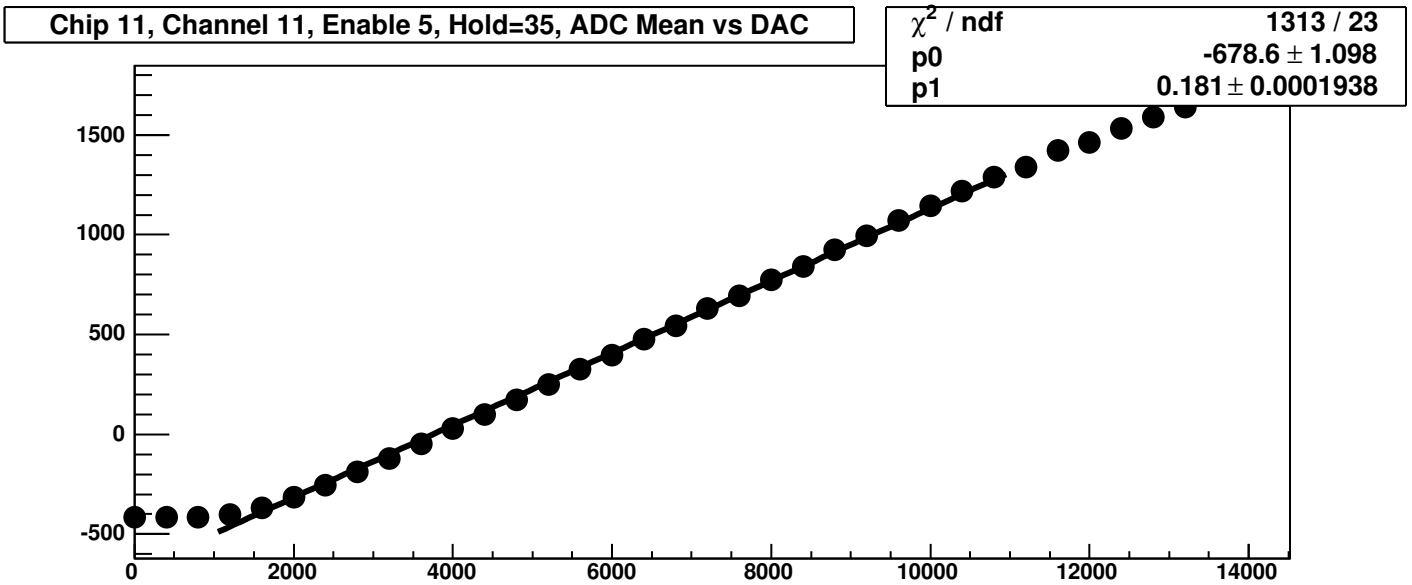

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

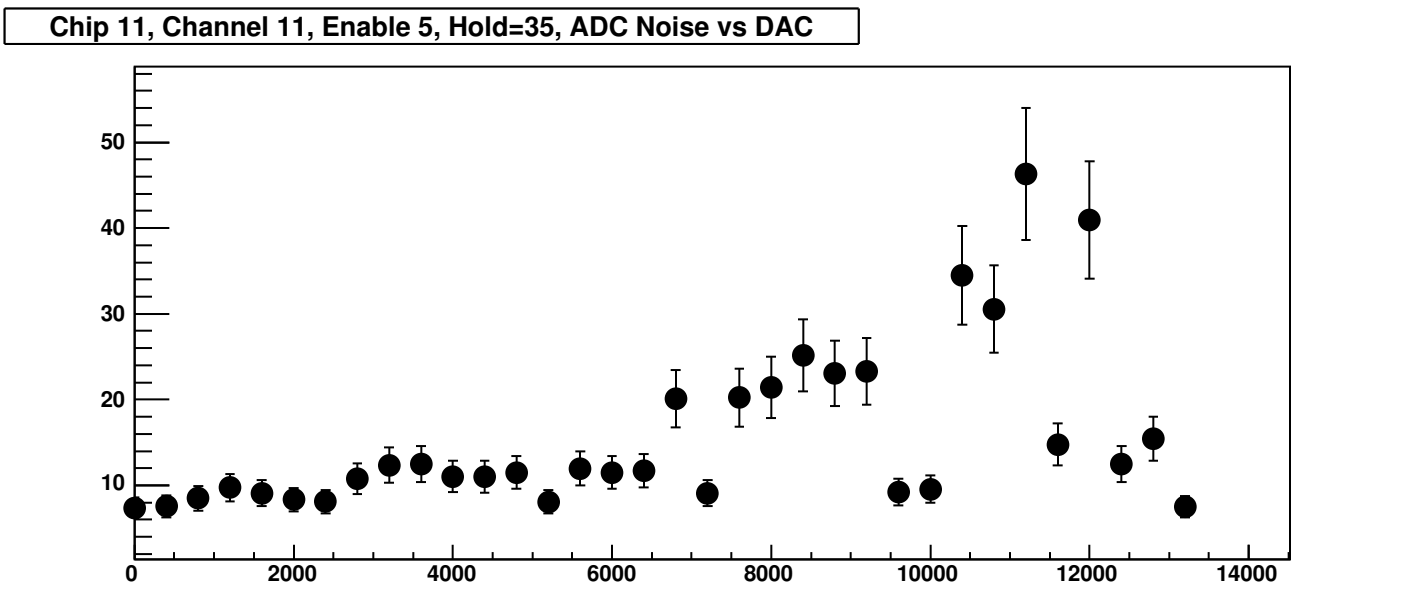

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

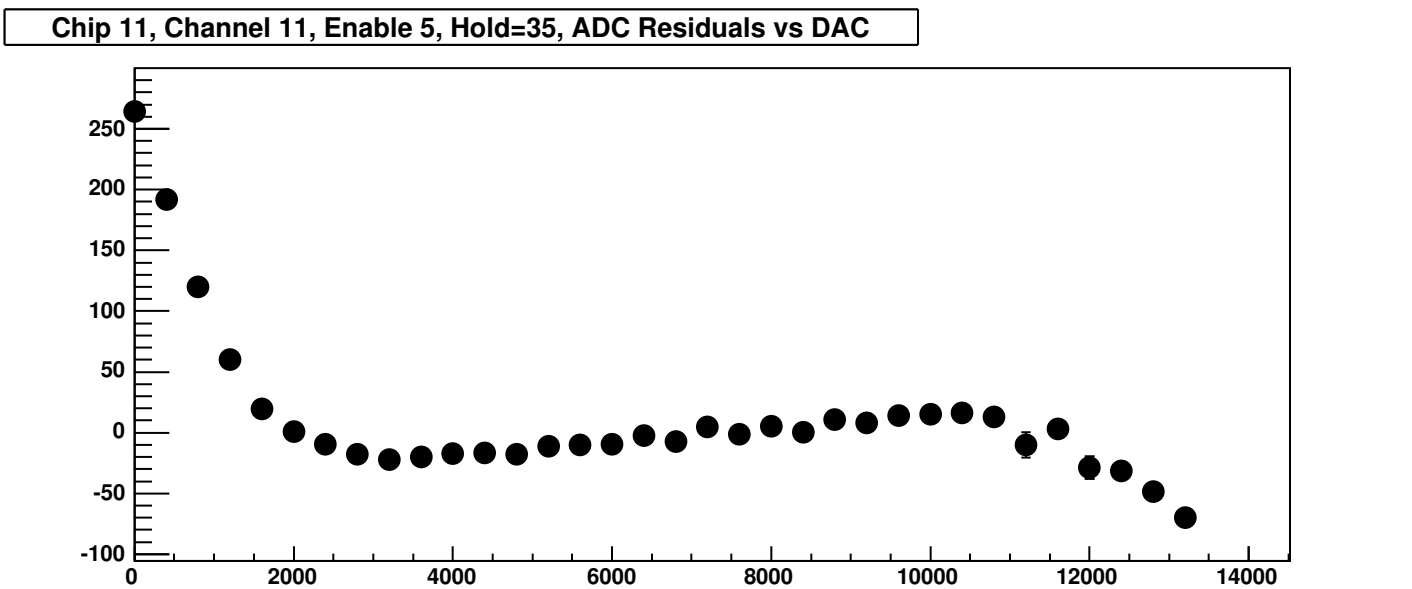

Chip 11, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.04 / 23
p0 -421.5 ± 0.7019
p1 0.03148 ± 0.0001081
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

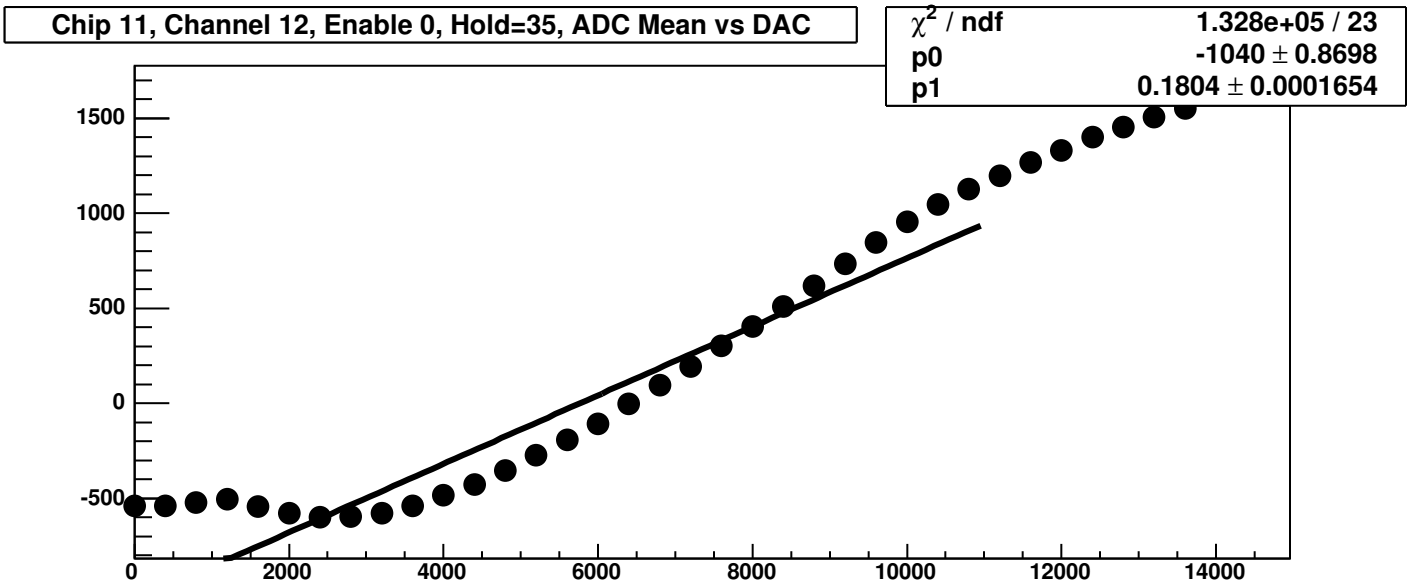

Chip 11, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

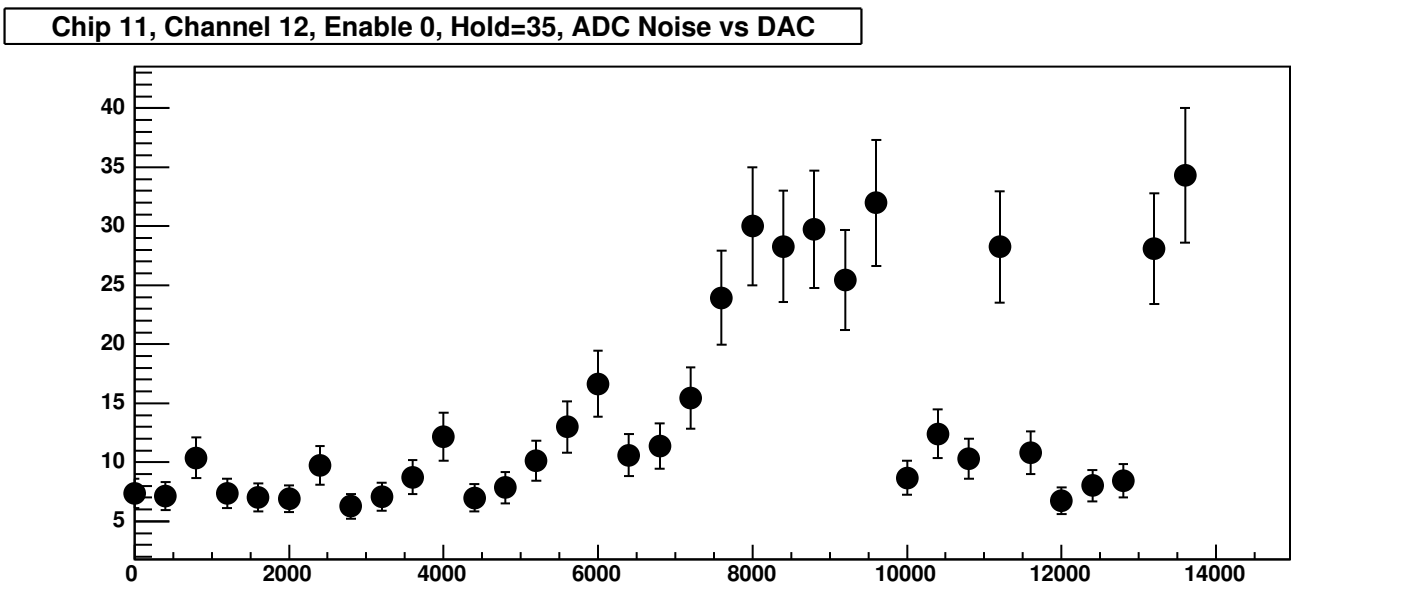

Chip 11, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

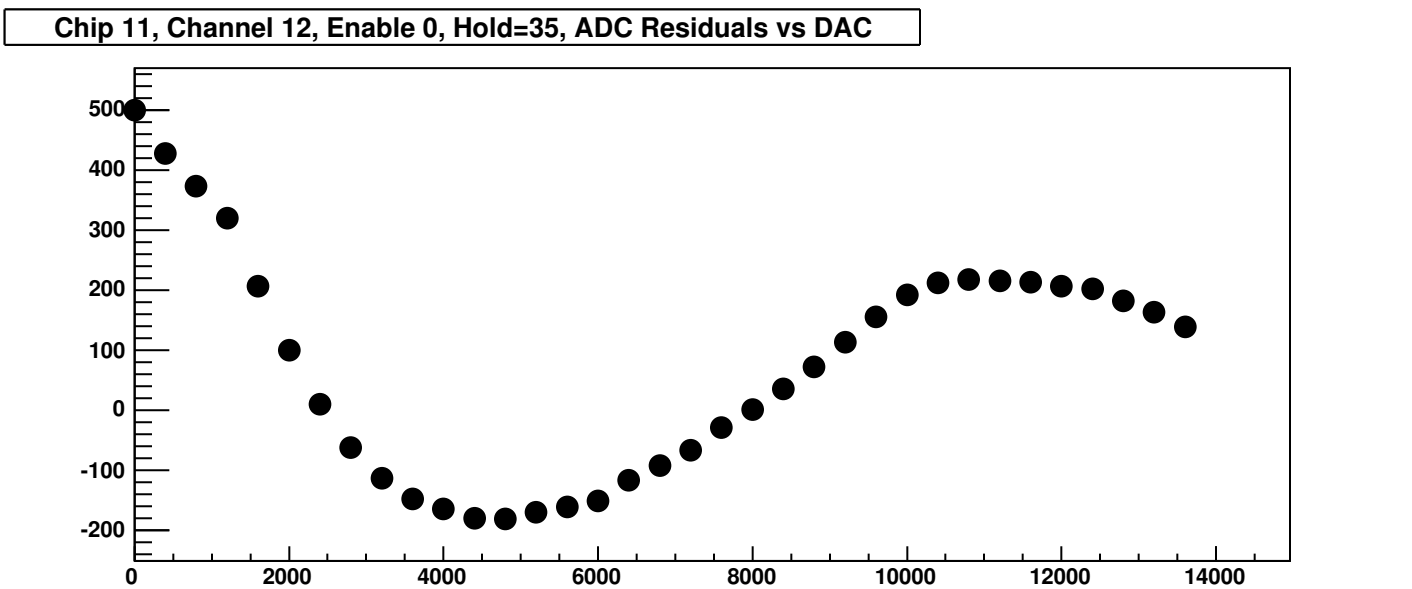

Chip 11, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1313 / 23
p0 -678.6 ± 1.098
p1 0.181 ± 0.0001938
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

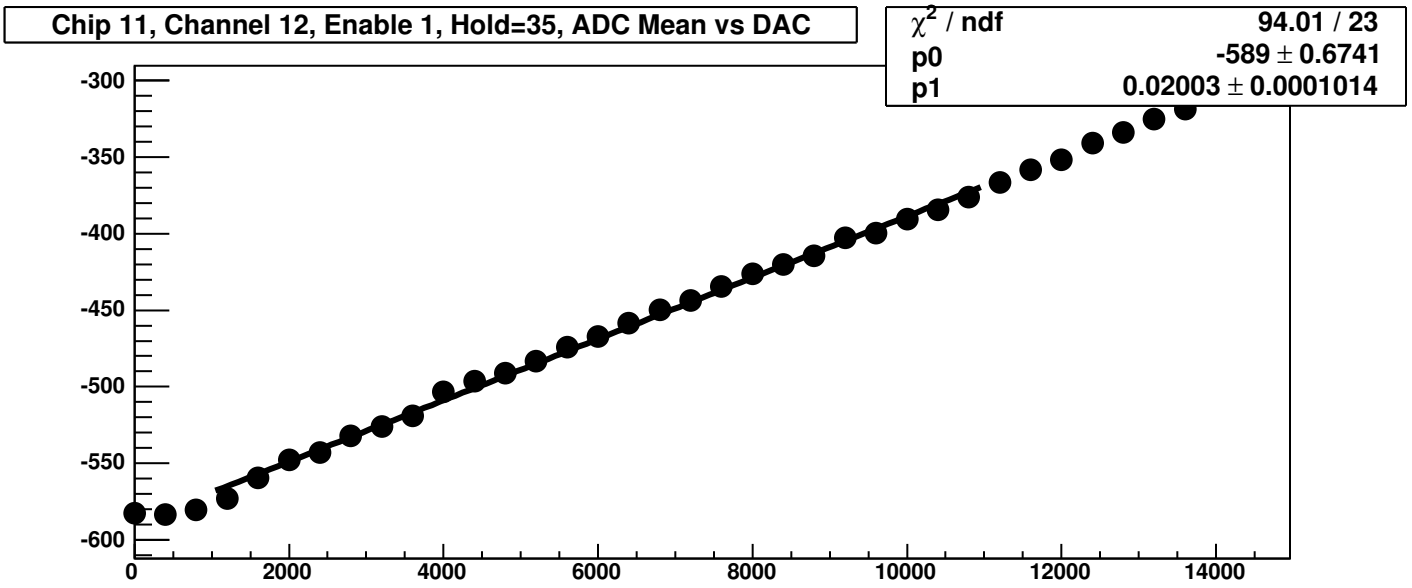

Chip 11, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

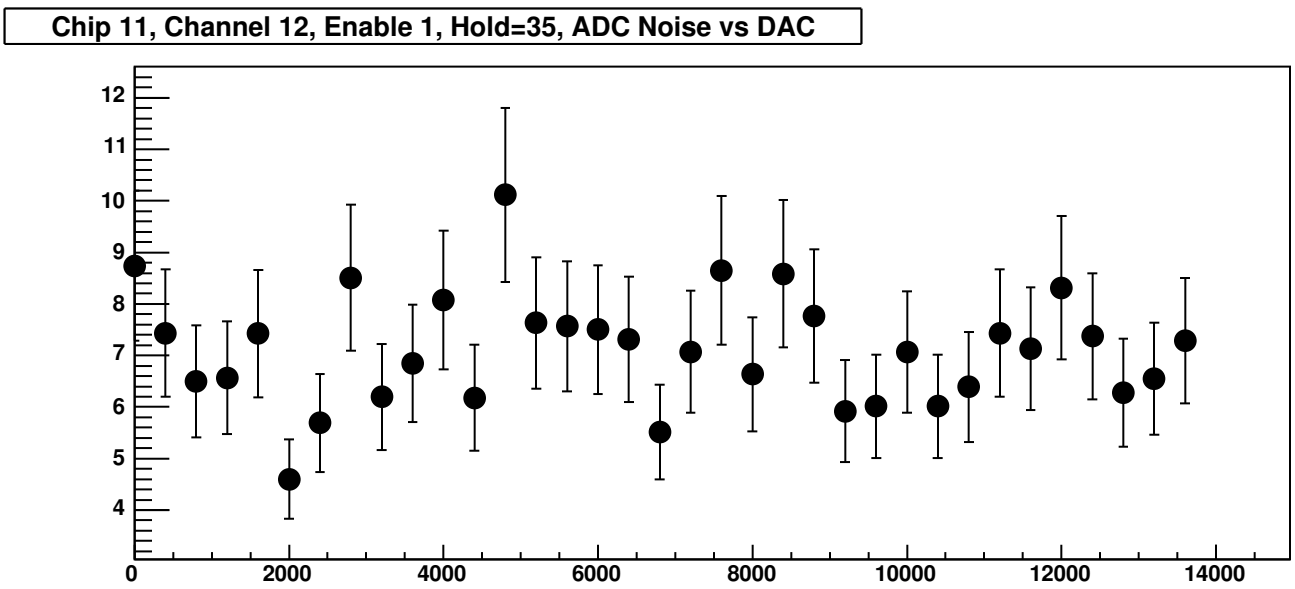

Chip 11, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

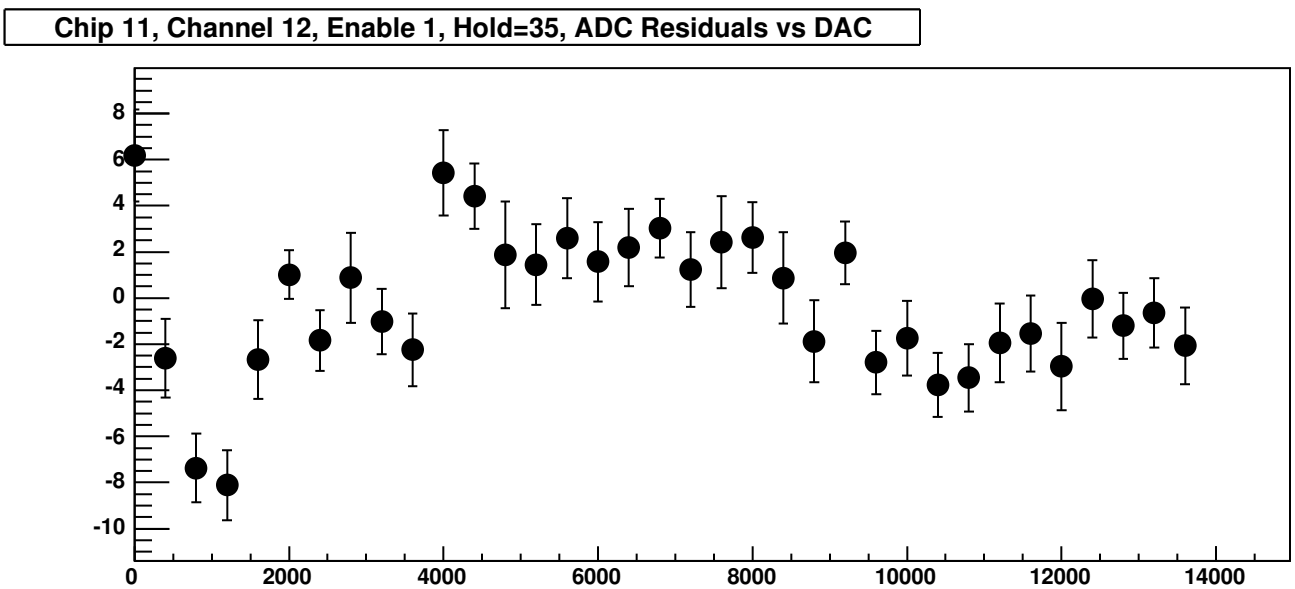

Chip 11, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.328e+05 / 23
p0 -1040 ± 0.8698
p1 0.1804 ± 0.0001654
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

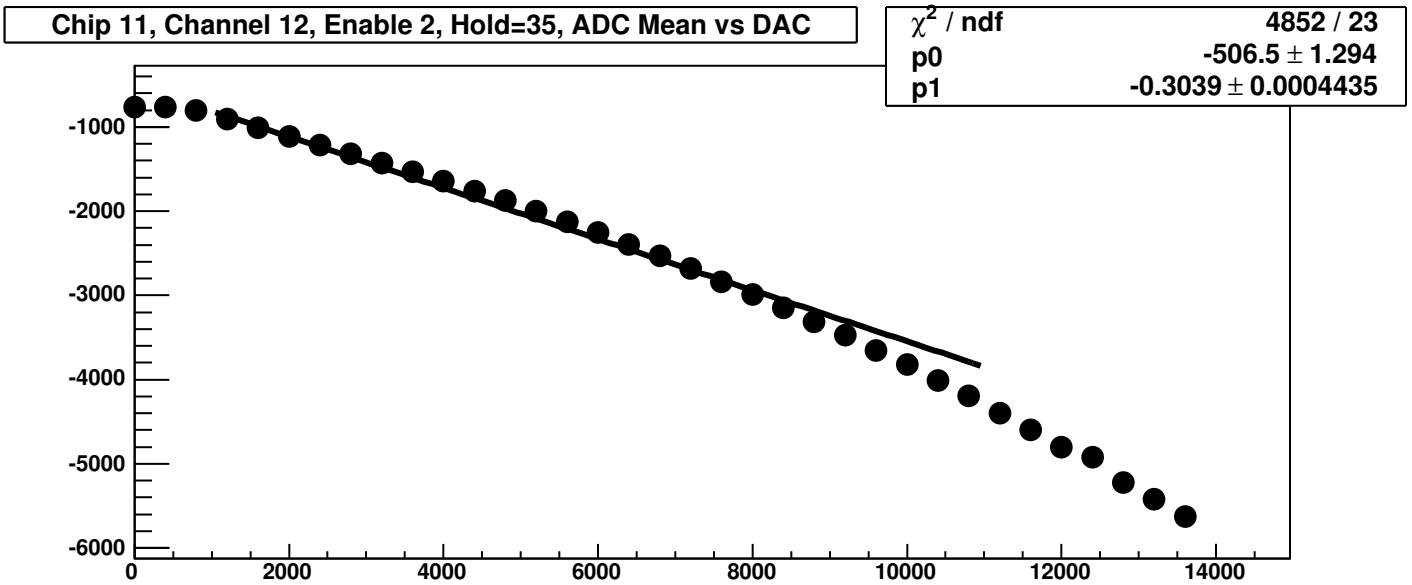

Chip 11, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

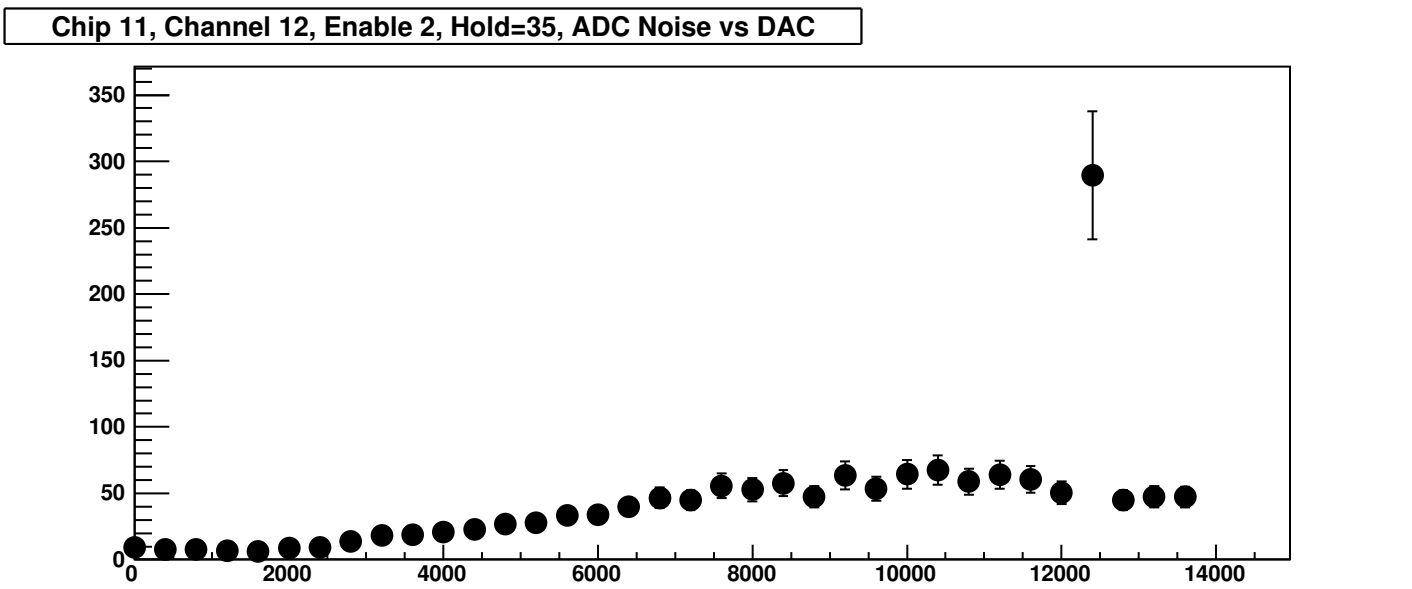

Chip 11, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

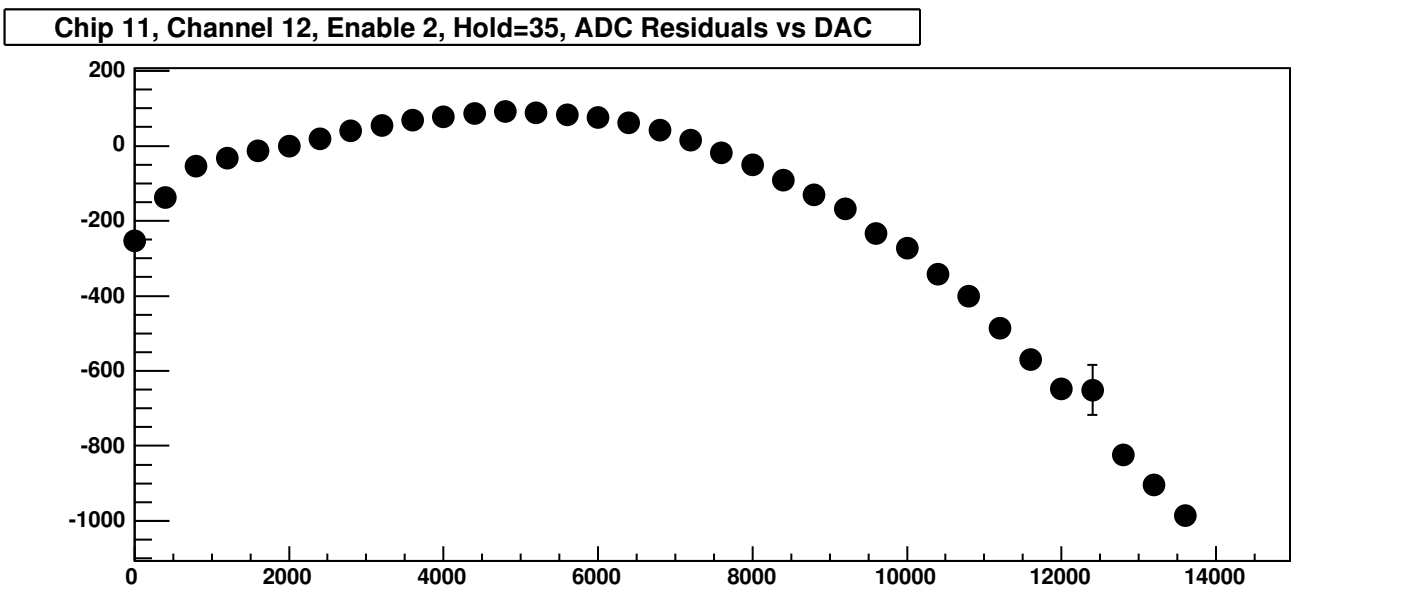

Chip 11, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 94.01 / 23
p0 -589 ± 0.6741
p1 0.02003 ± 0.0001014
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

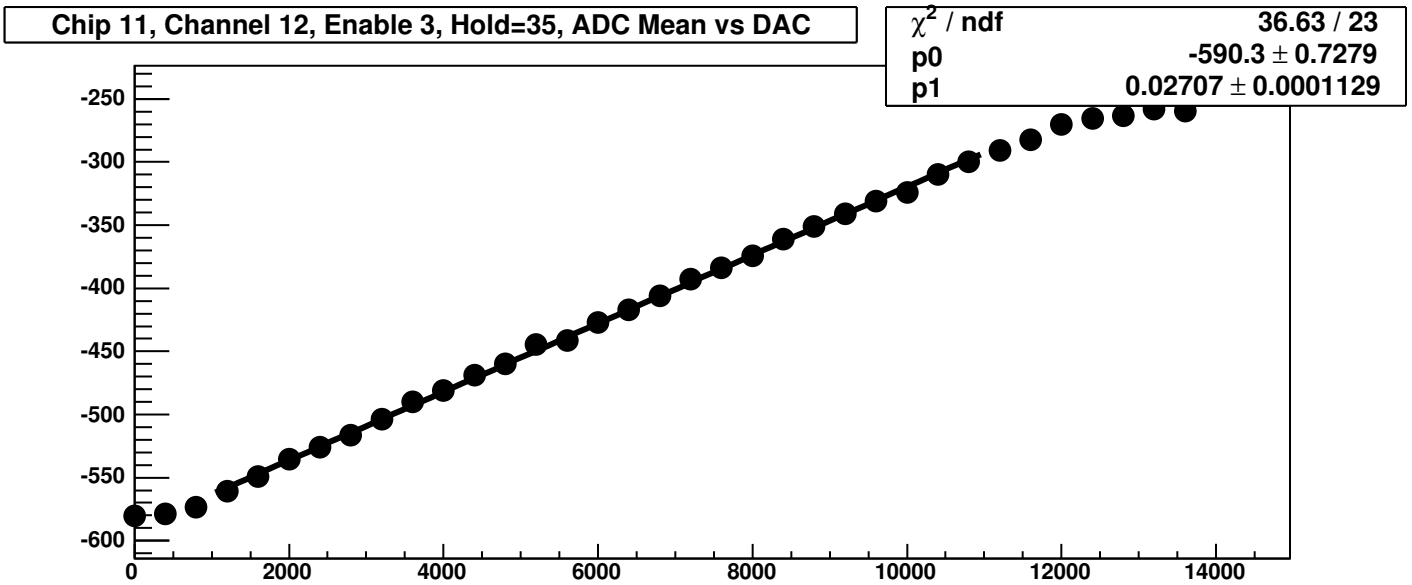

Chip 11, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

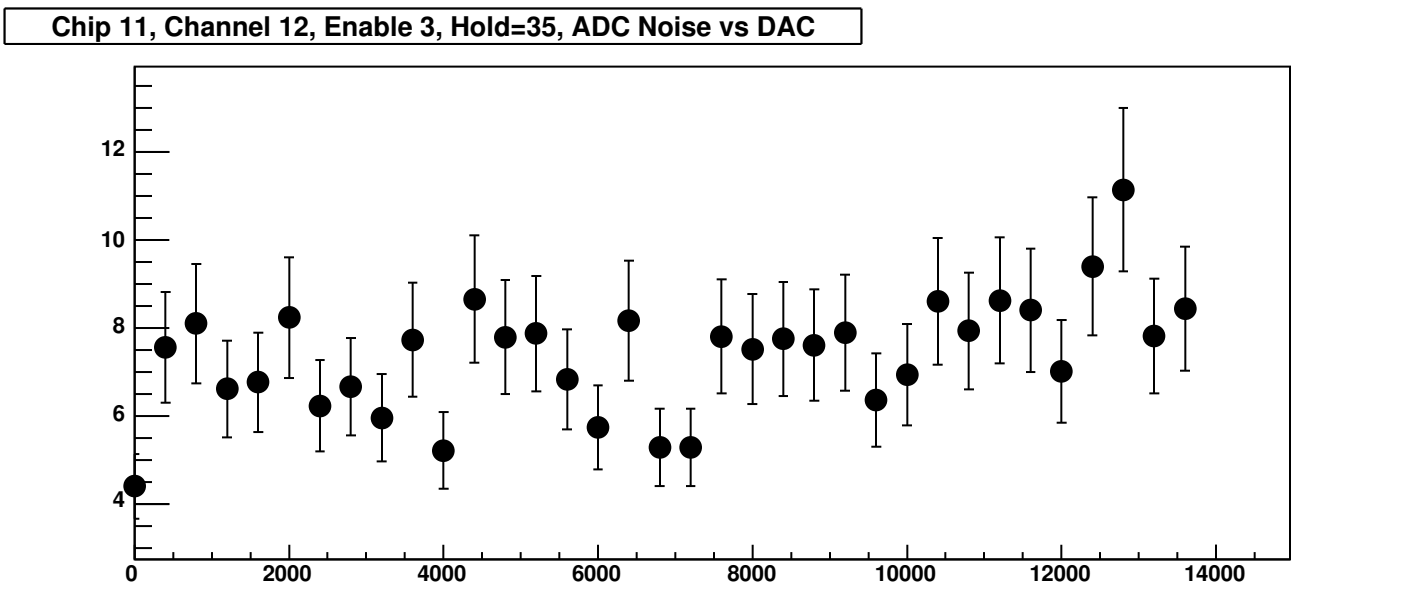

Chip 11, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

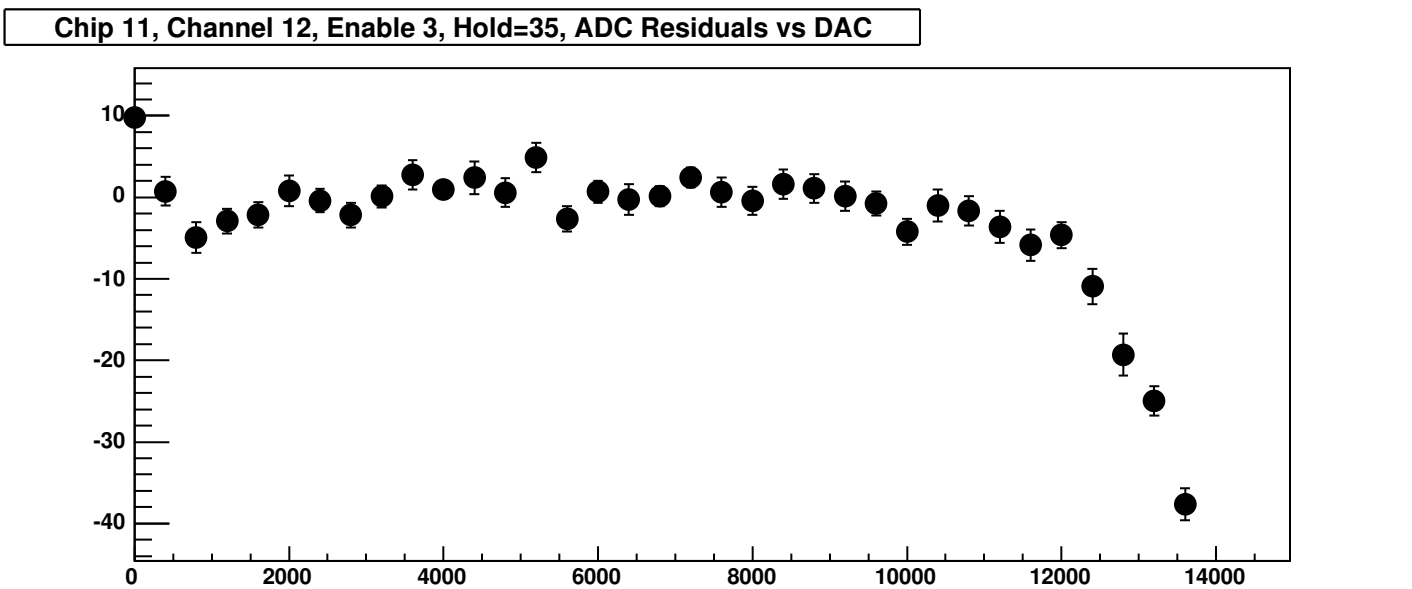

Chip 11, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4852 / 23
p0 -506.5 ± 1.294
p1 -0.3039 ± 0.0004435
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

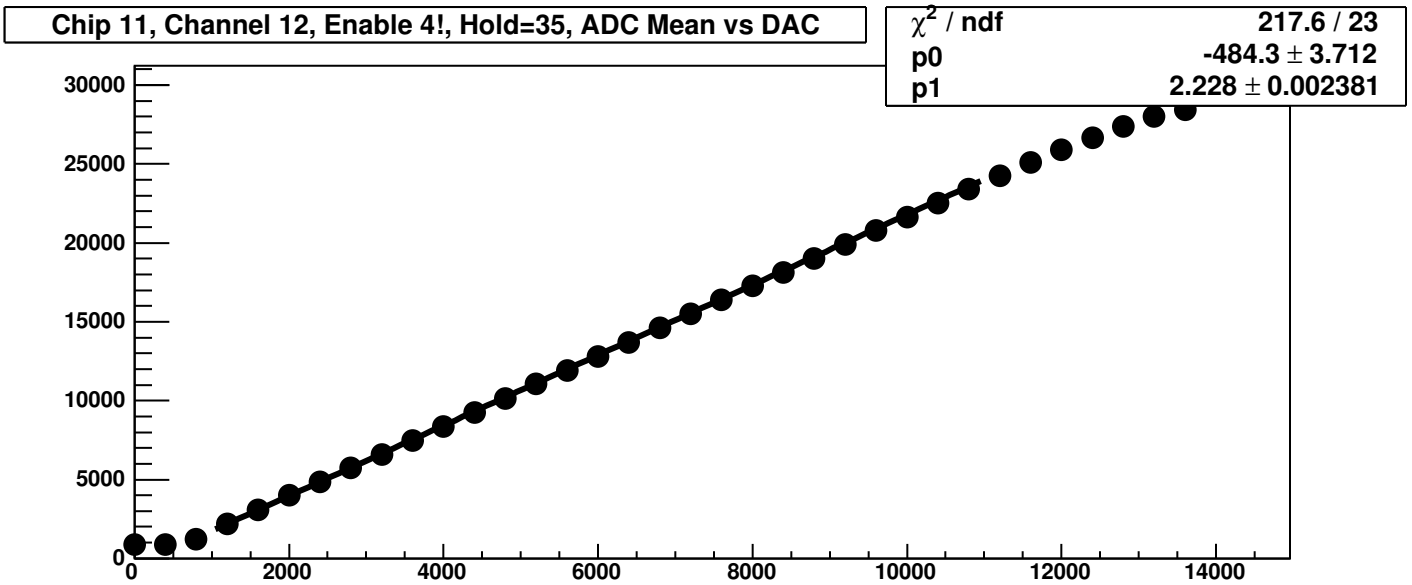

Chip 11, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

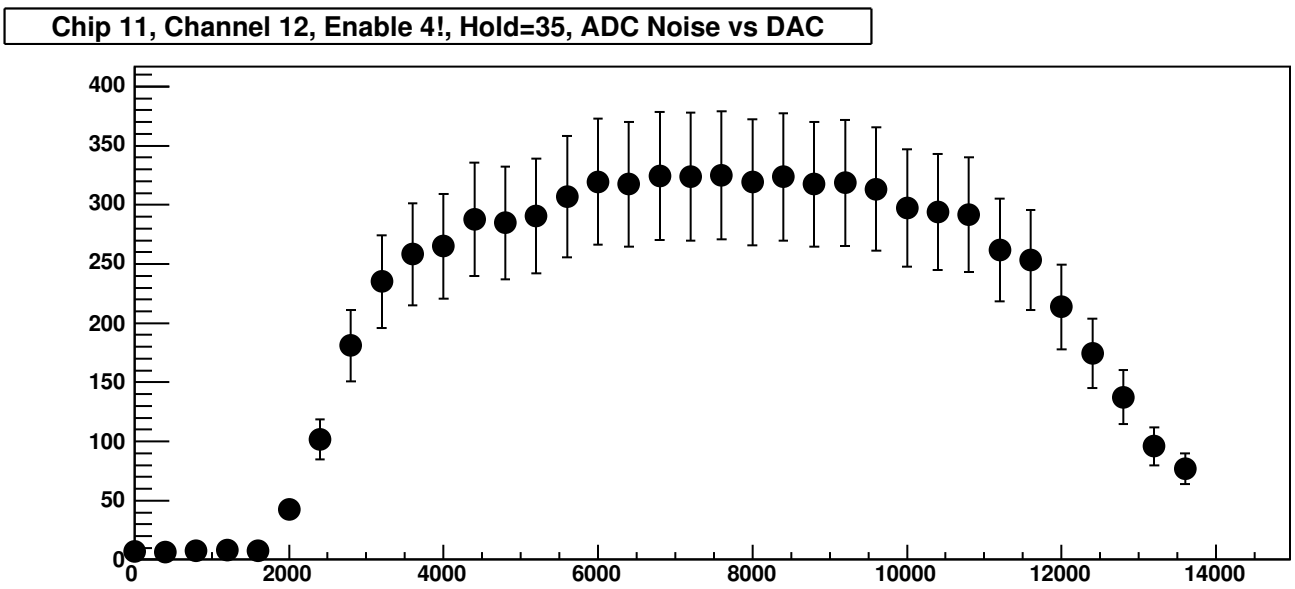

Chip 11, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

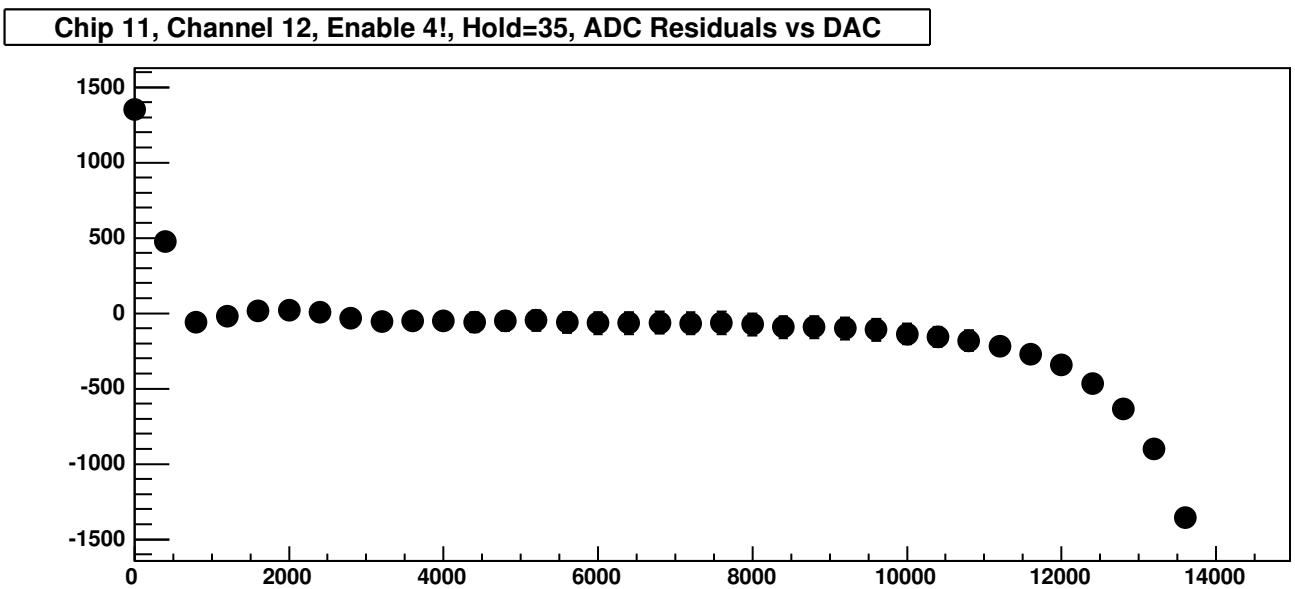

Chip 11, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.63 / 23
p0 -590.3 ± 0.7279
p1 0.02707 ± 0.0001129
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

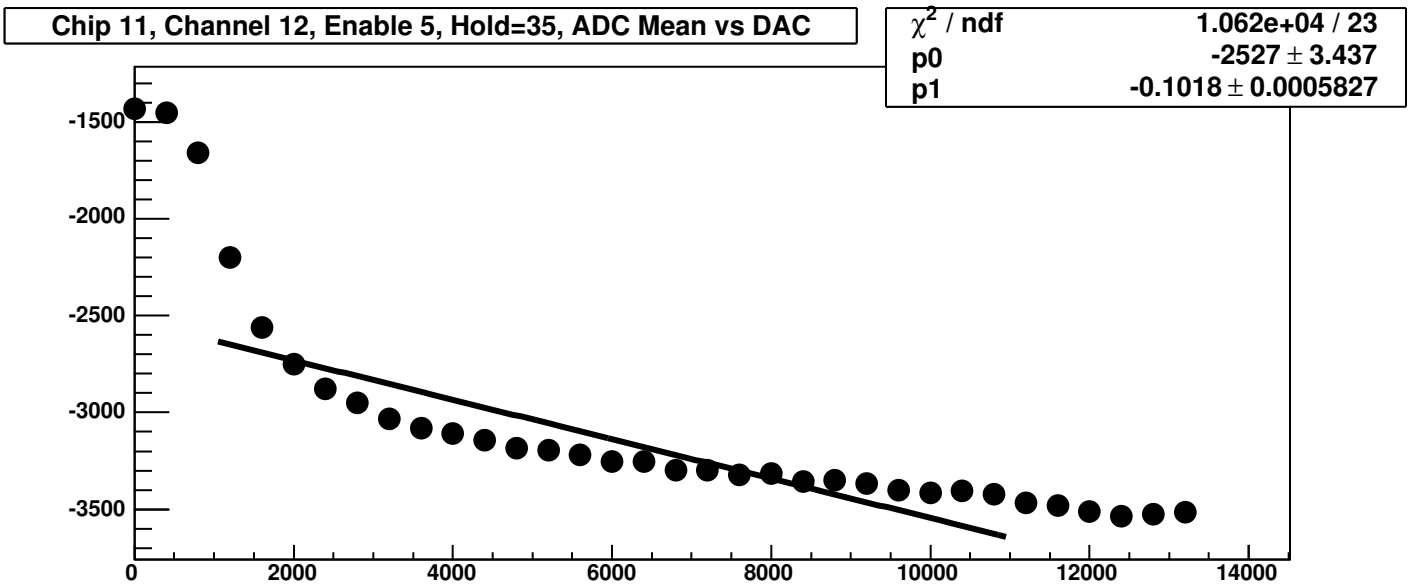

Chip 11, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

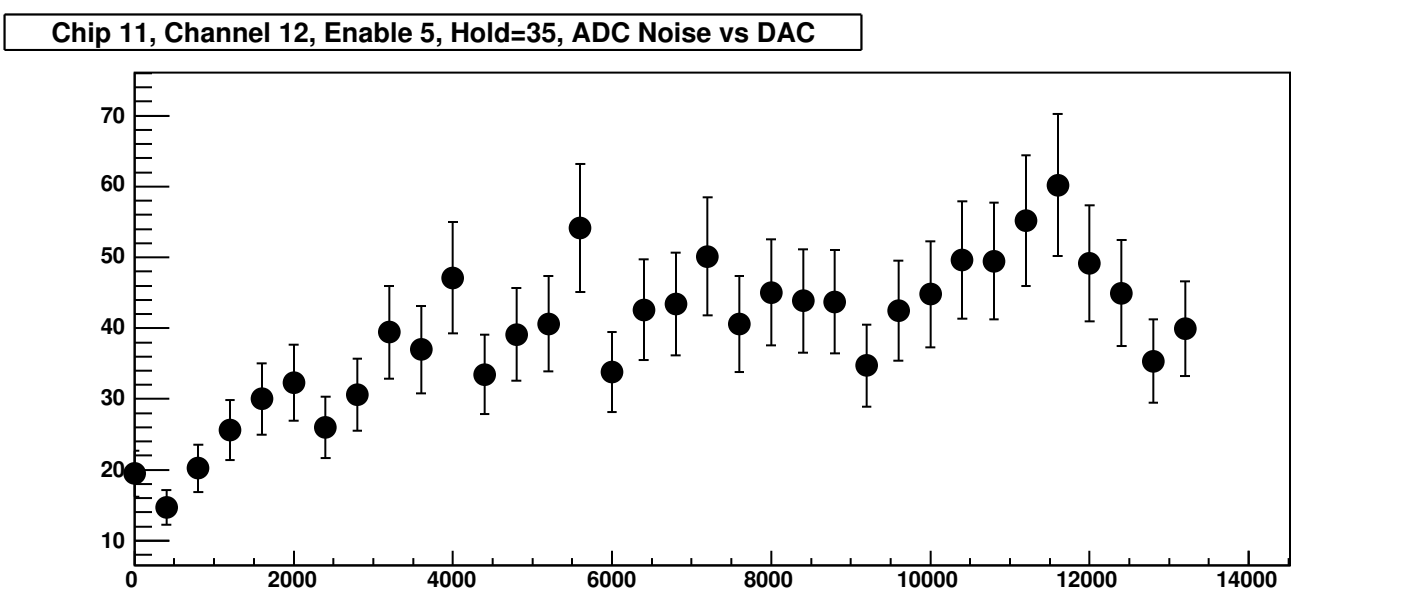

Chip 11, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

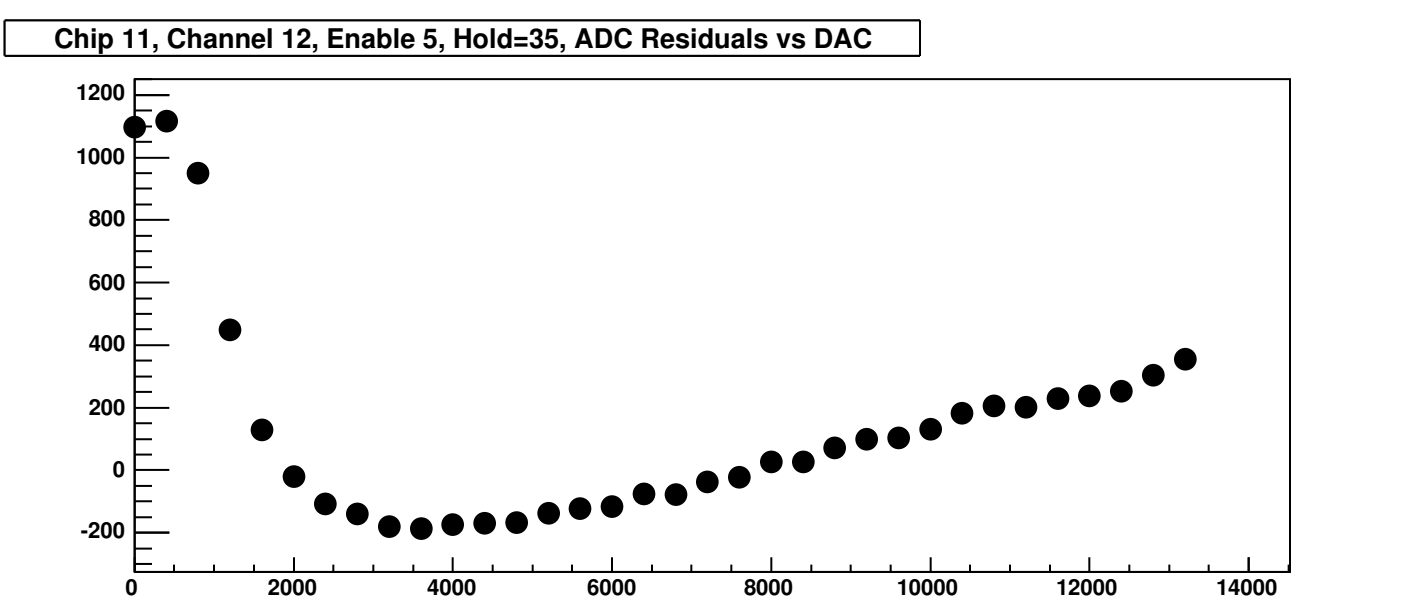

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 217.6 / 23
p0 -484.3 ± 3.712
p1 2.228 ± 0.002381
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

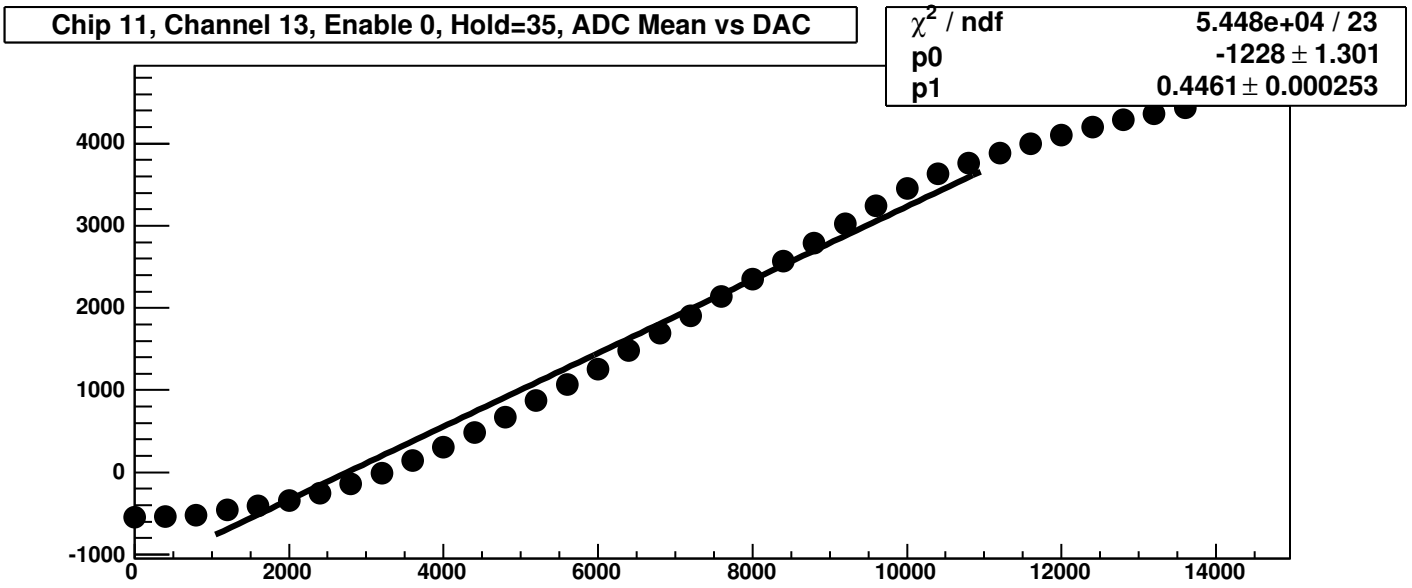

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

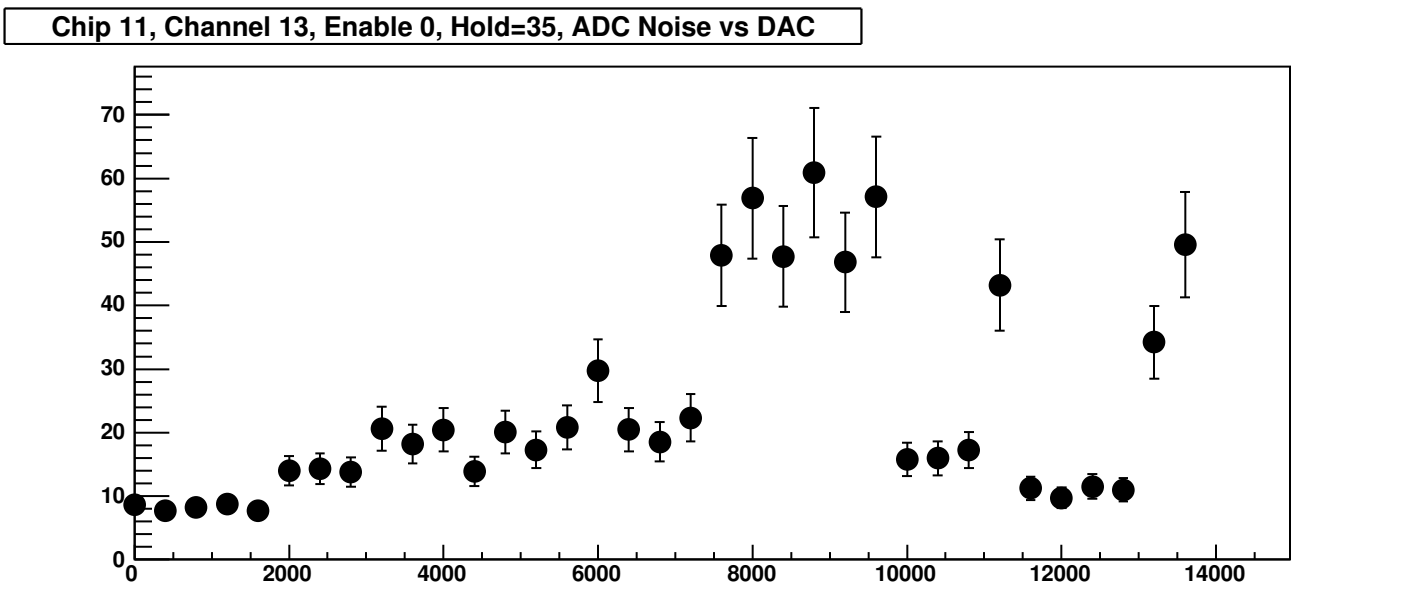

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

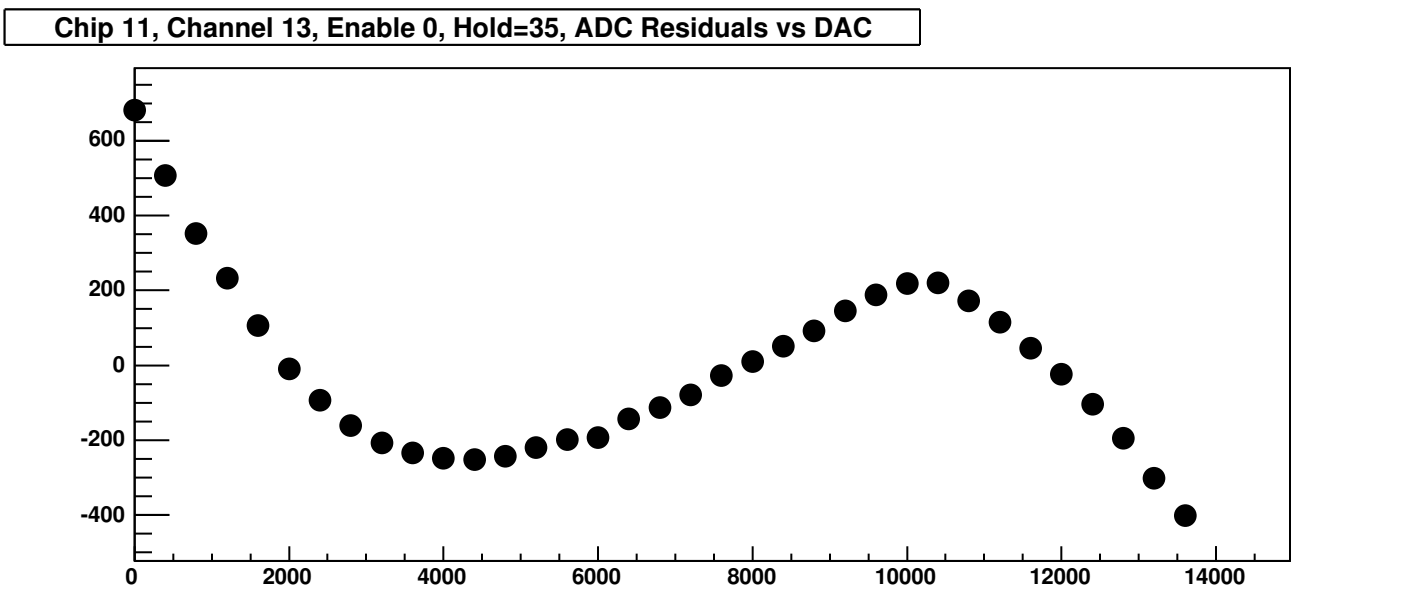

Chip 11, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.062e+04 / 23
p0 -2527 ± 3.437
p1 -0.1018 ± 0.0005827
-1500
-2000
-2500
-3000
-3500
0
2000
4000
6000
8000
10000
12000
14000

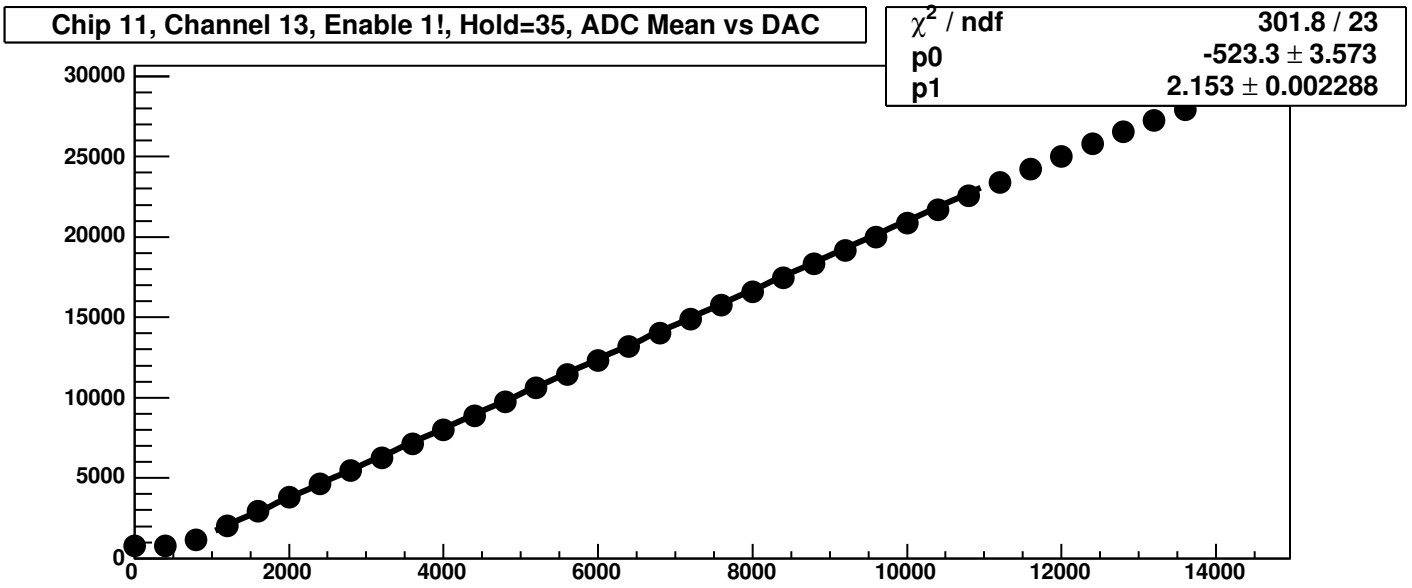

Chip 11, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

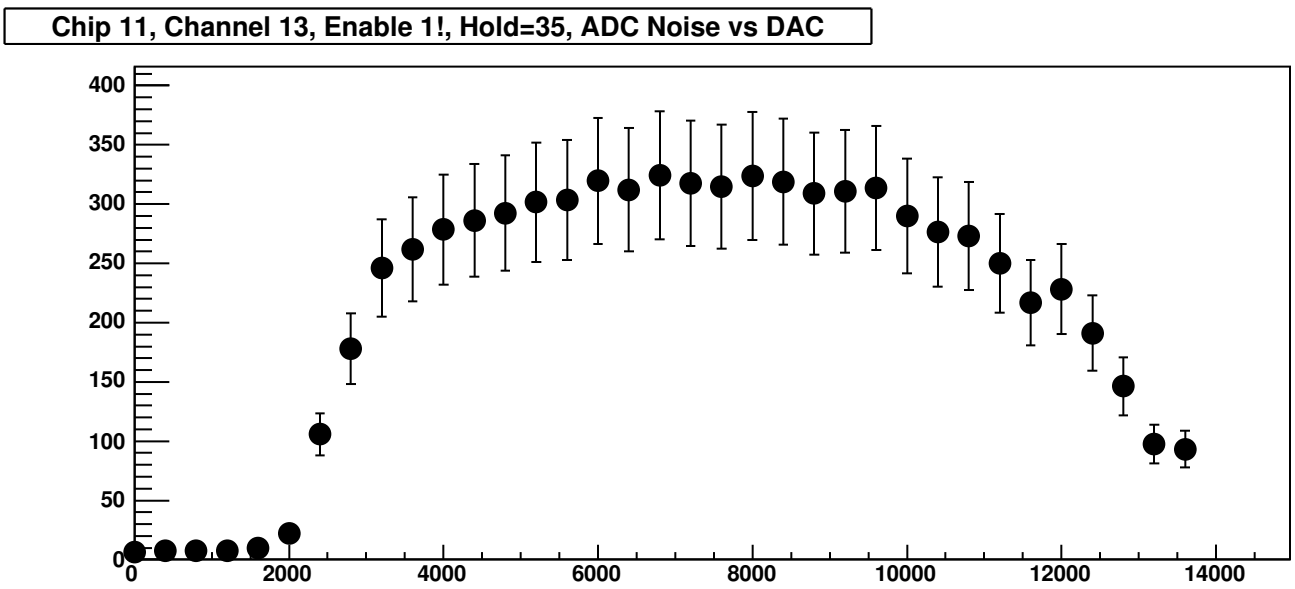

Chip 11, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

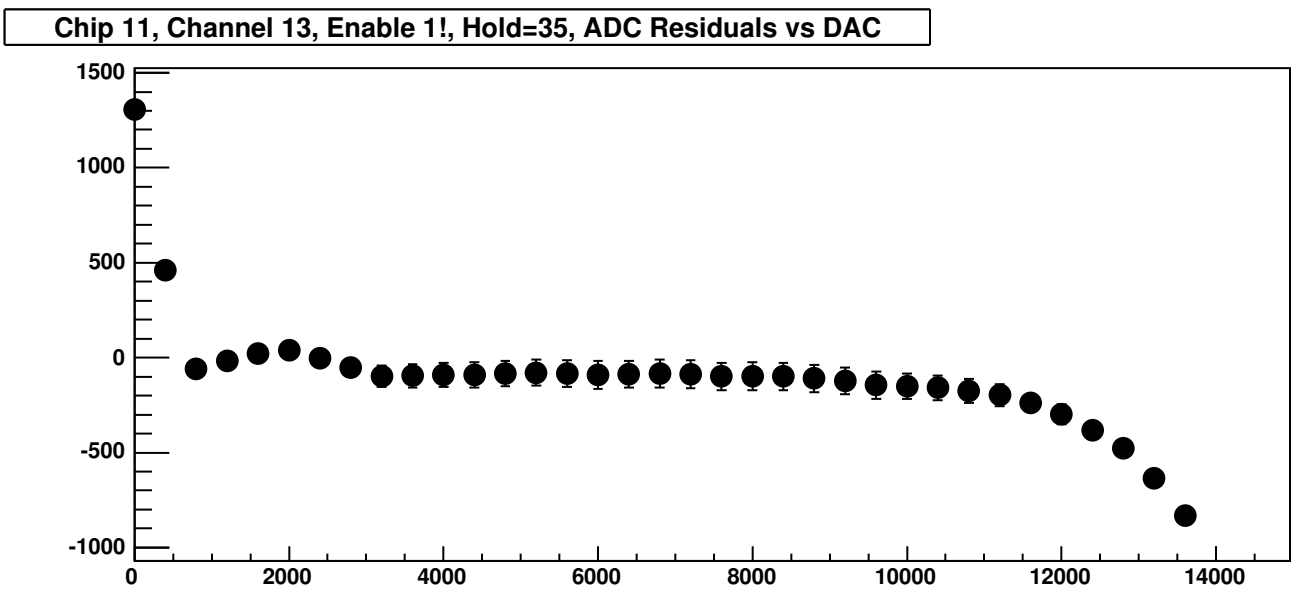

Chip 11, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.448e+04 / 23
p0 -1228 ± 1.301
p1 0.4461 ± 0.000253
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

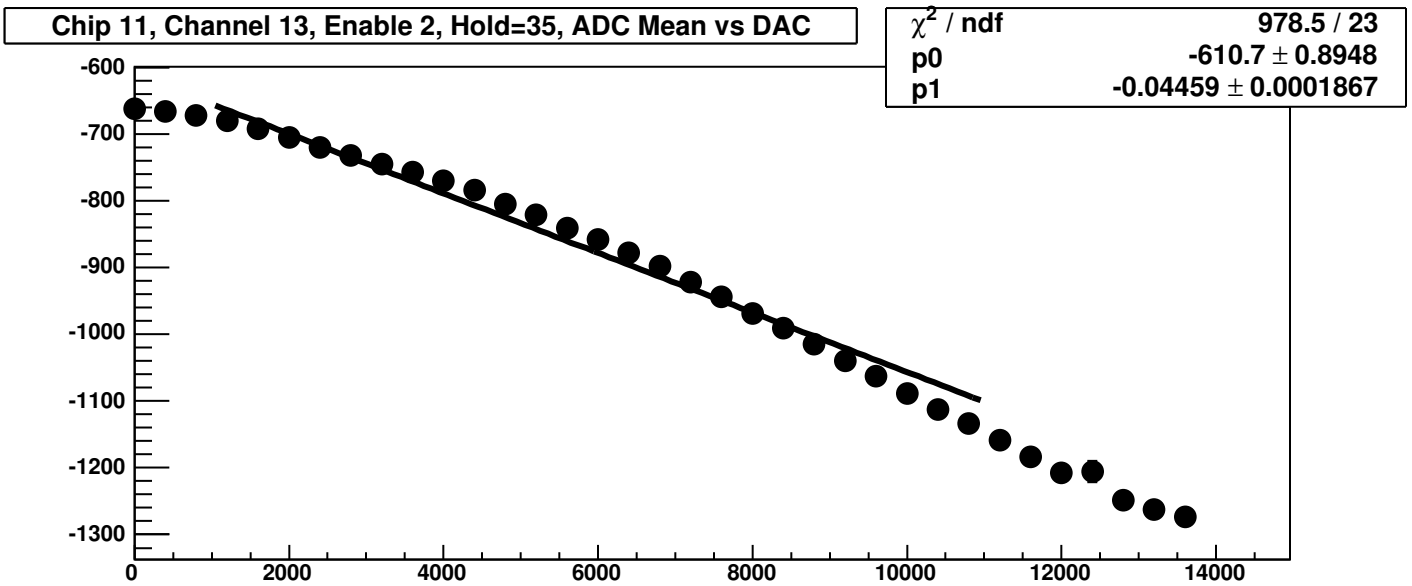

Chip 11, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

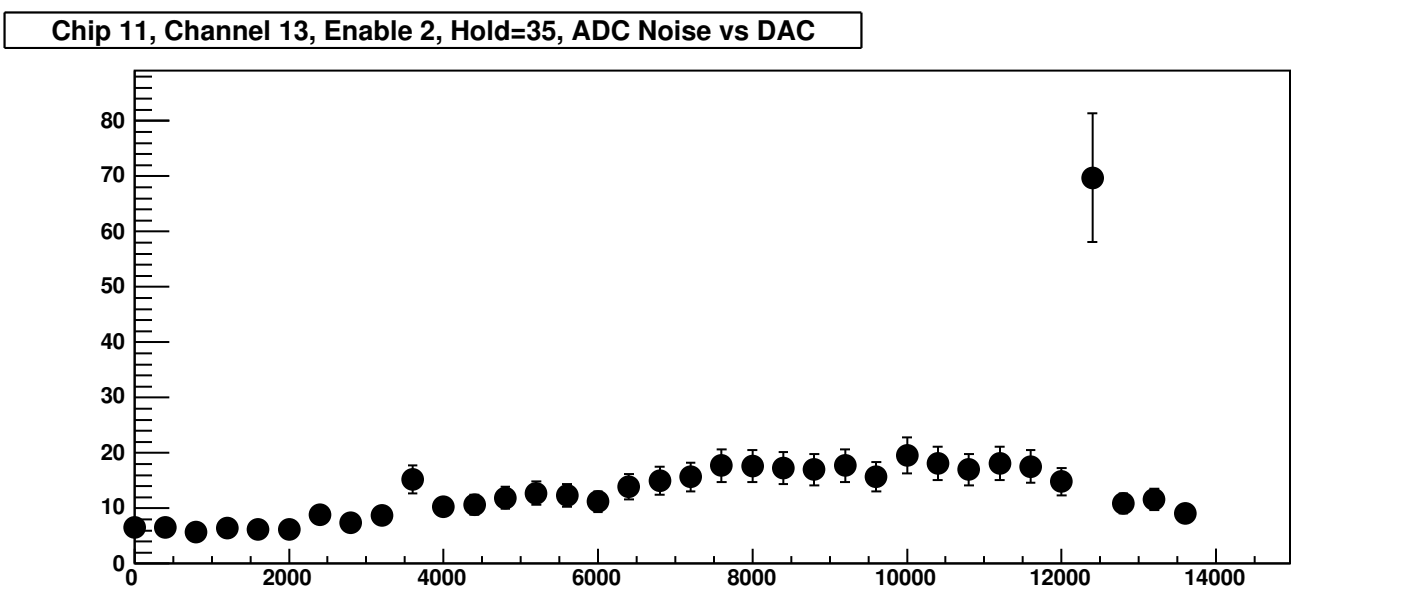

Chip 11, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

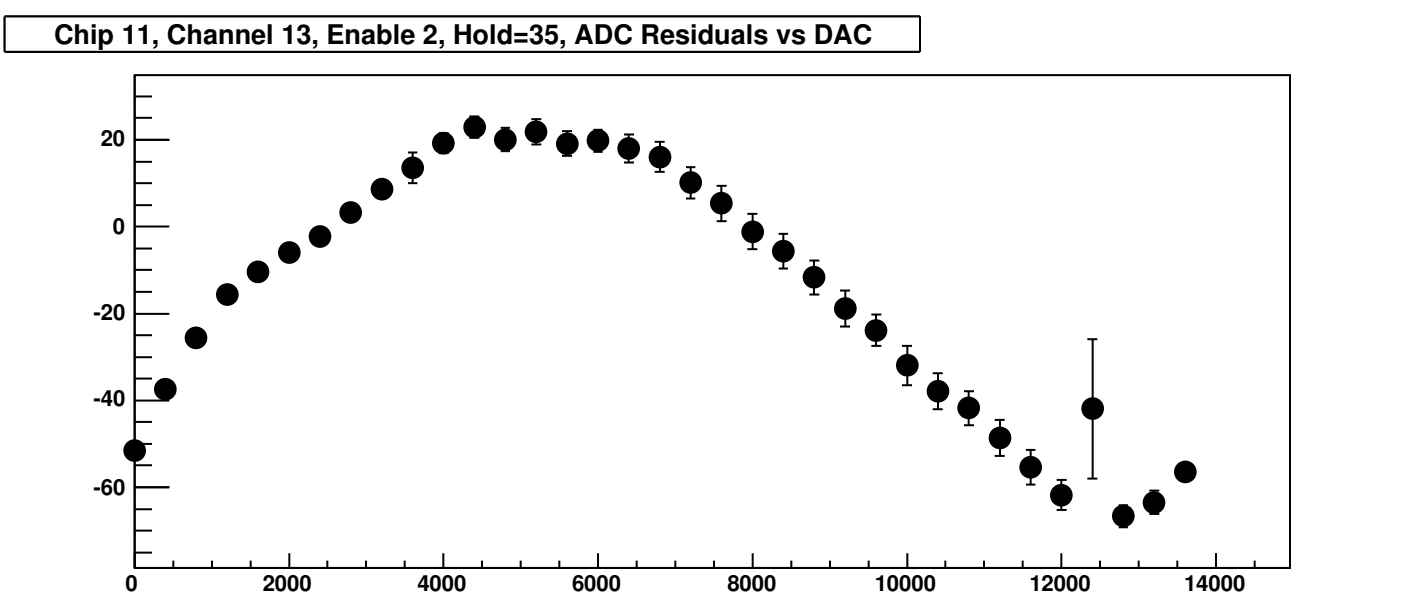

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 301.8 / 23
p0 -523.3 ± 3.573
p1 2.153 ± 0.002288
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

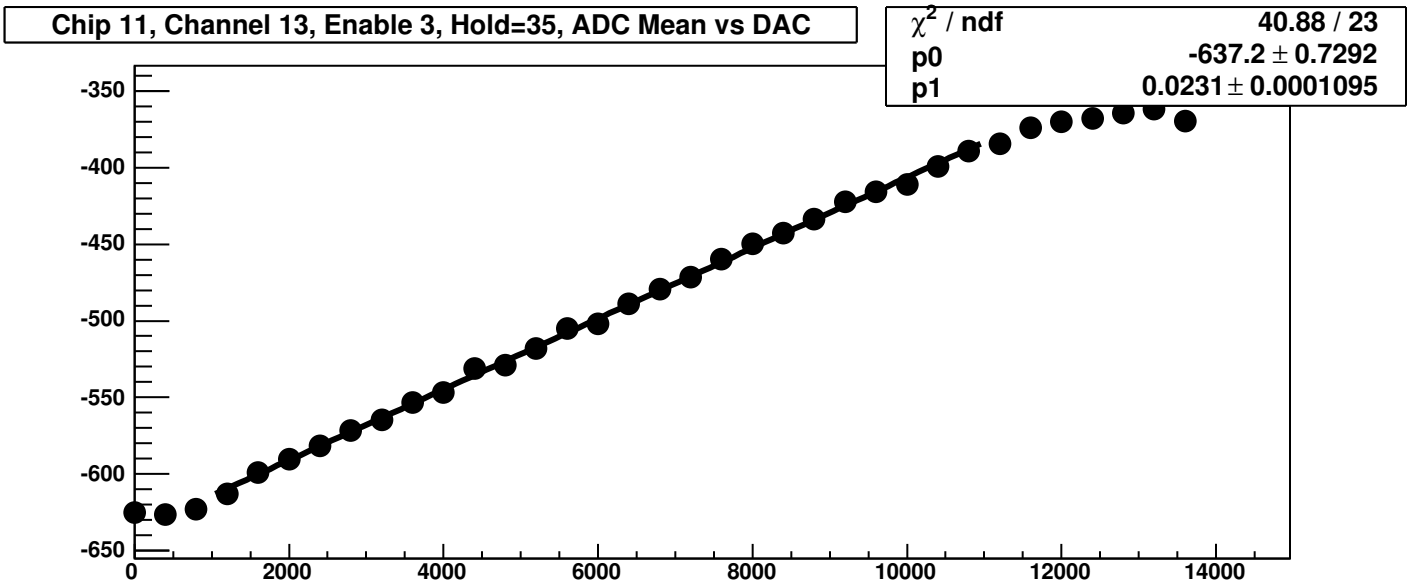

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

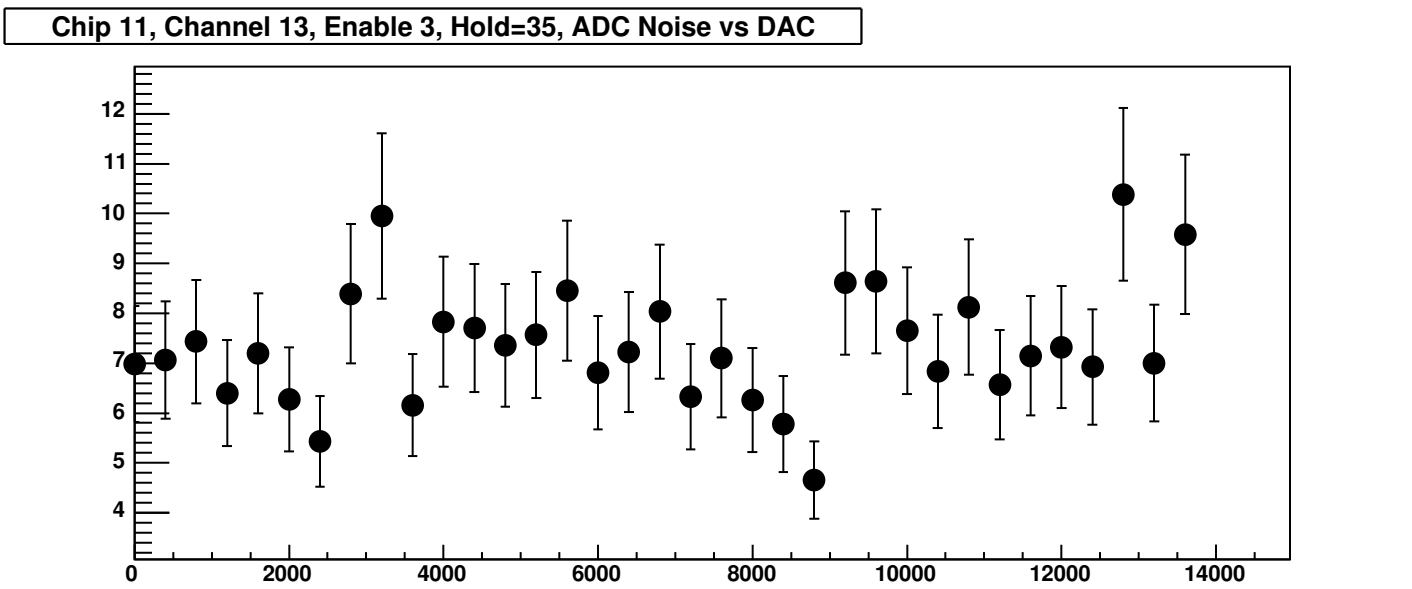

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

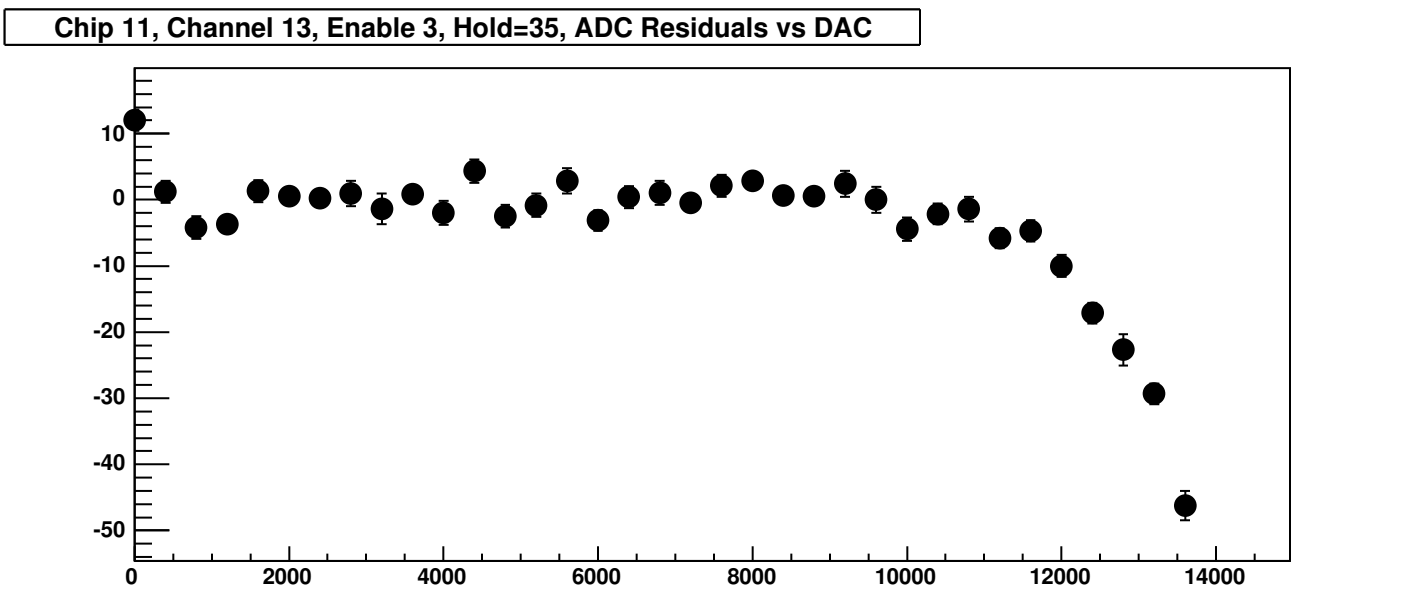

Chip 11, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 978.5 / 23
p0 -610.7 ± 0.8948
p1 -0.04459 ± 0.0001867
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

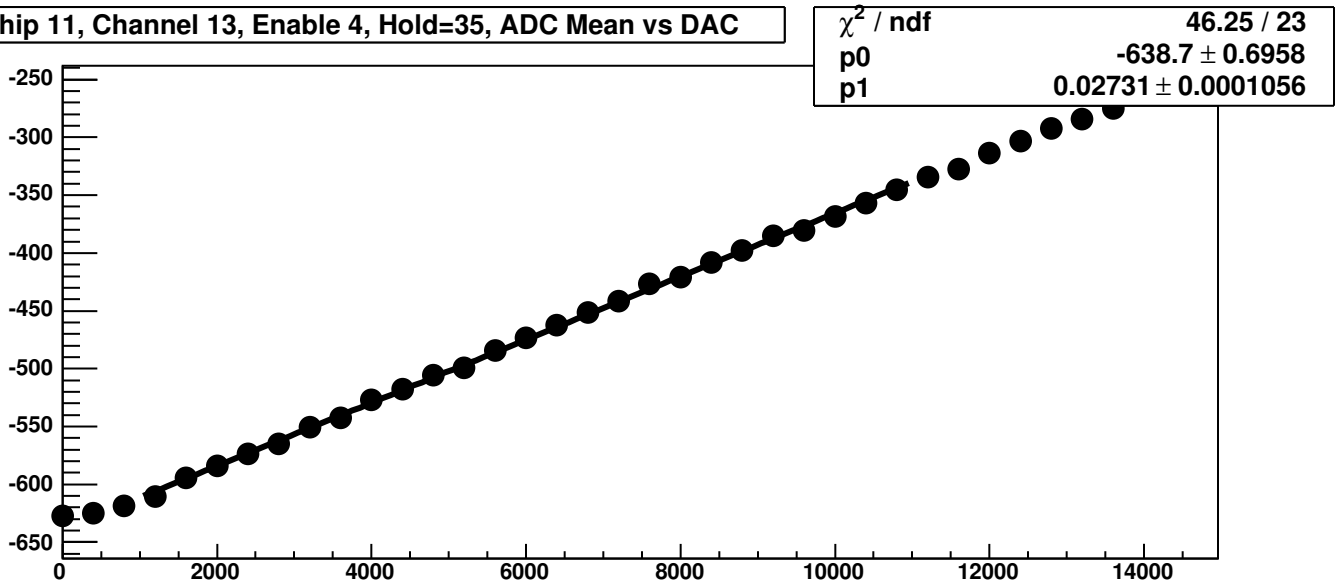

Chip 11, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.88 / 23
p0 -637.2 ± 0.7292
p1 0.0231 ± 0.0001095
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

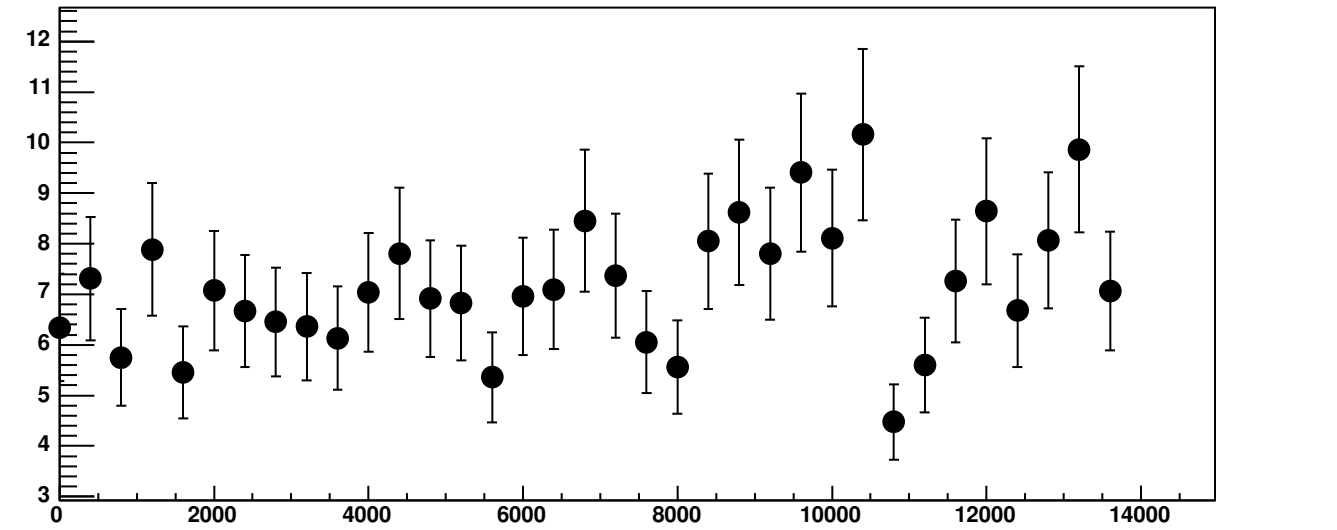

Chip 11, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

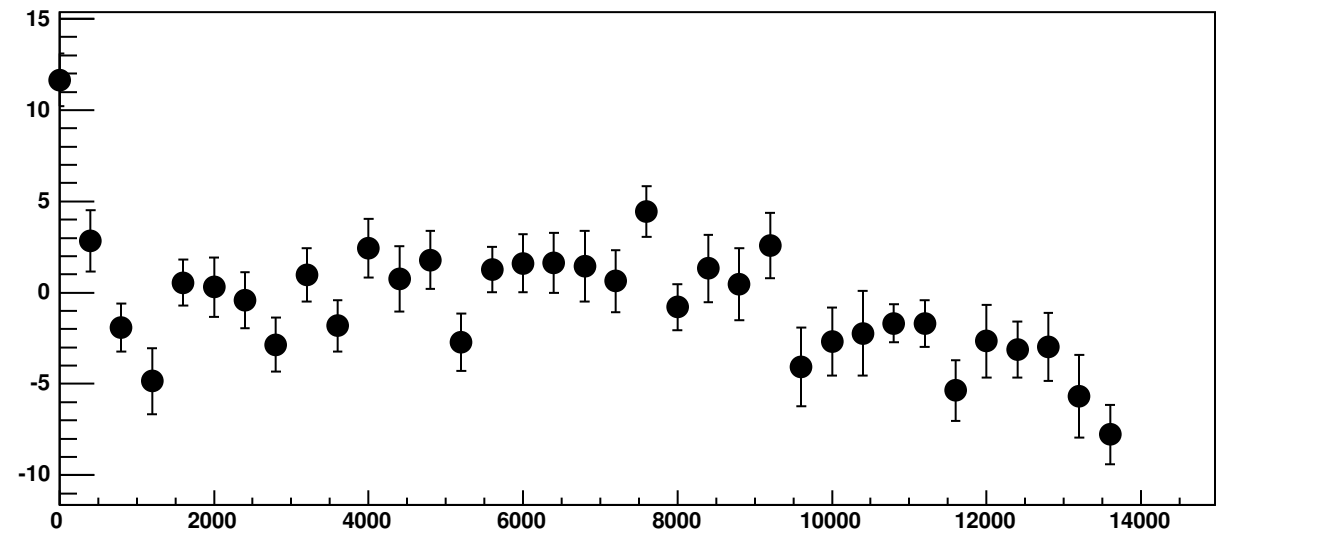
Chip 11, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

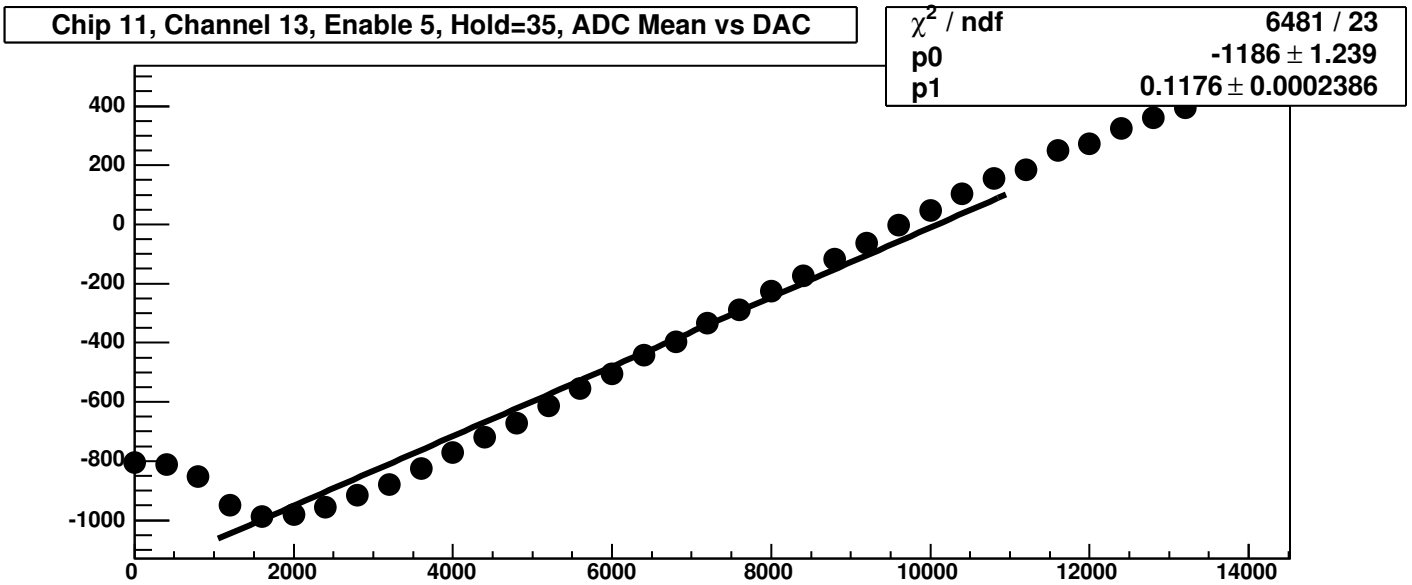


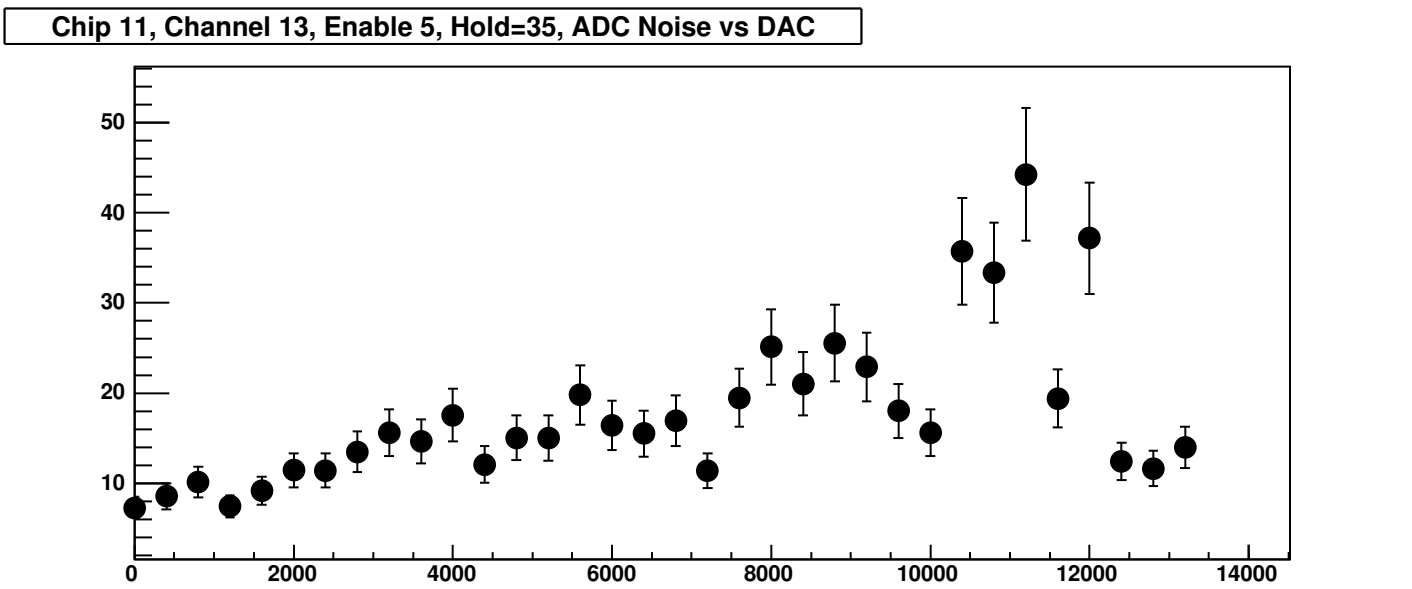
Chip 11, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

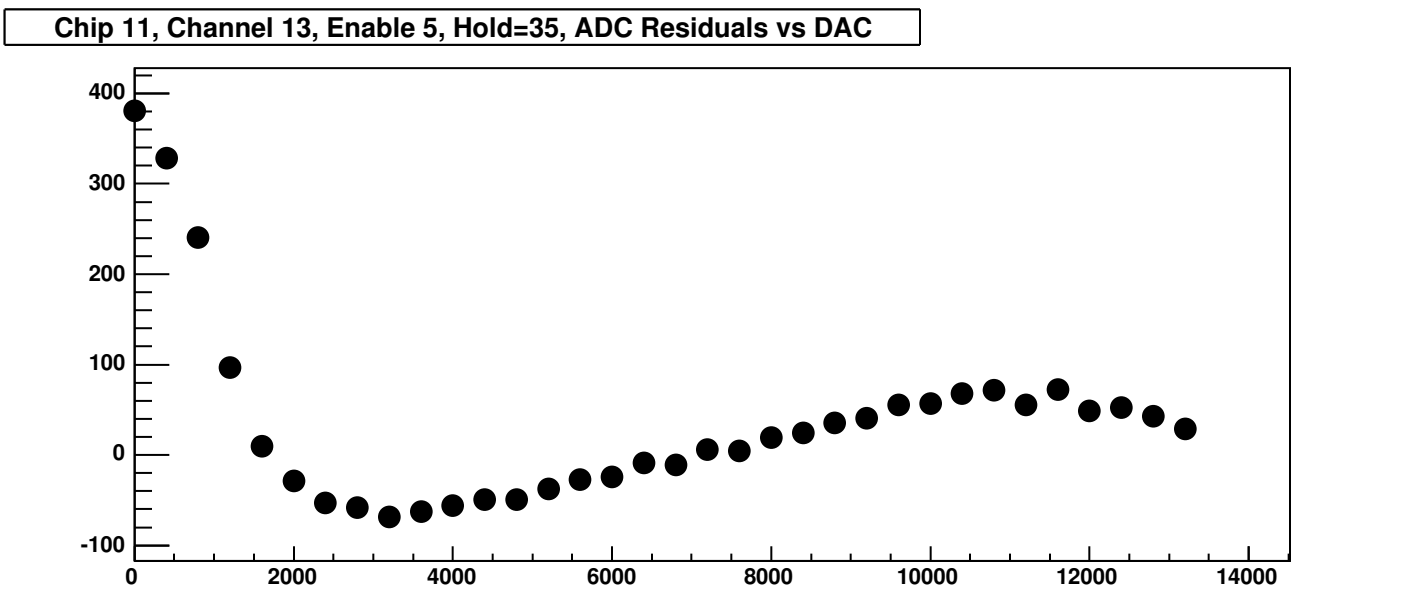


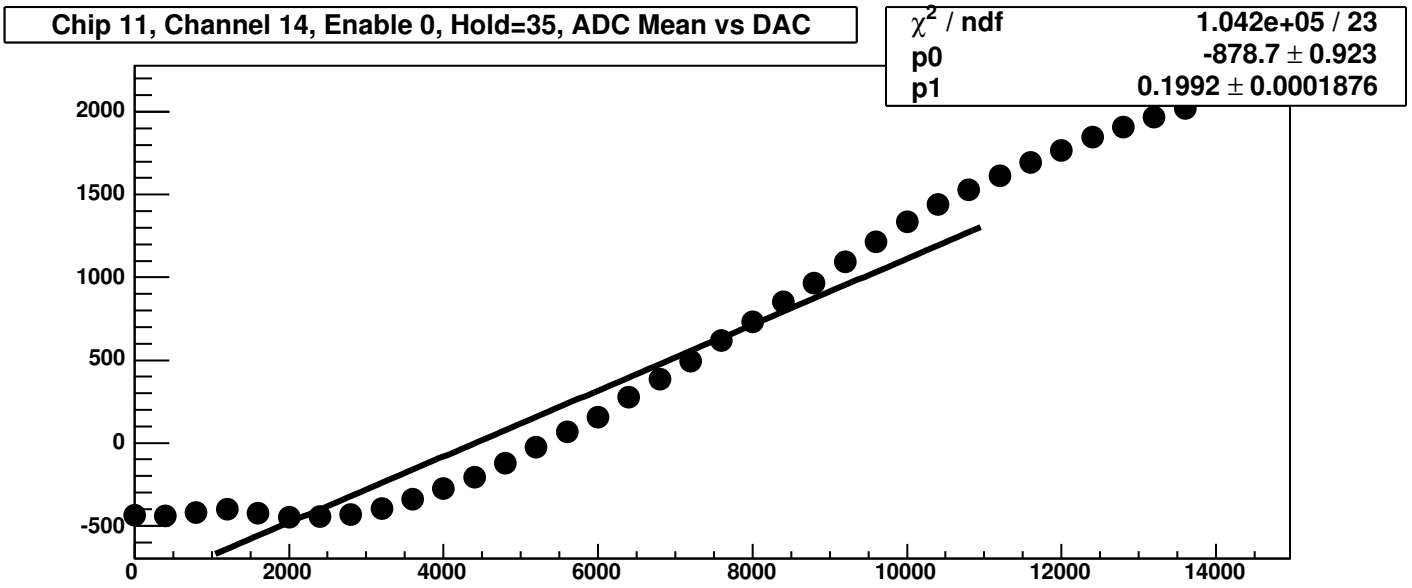
Chip 11, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

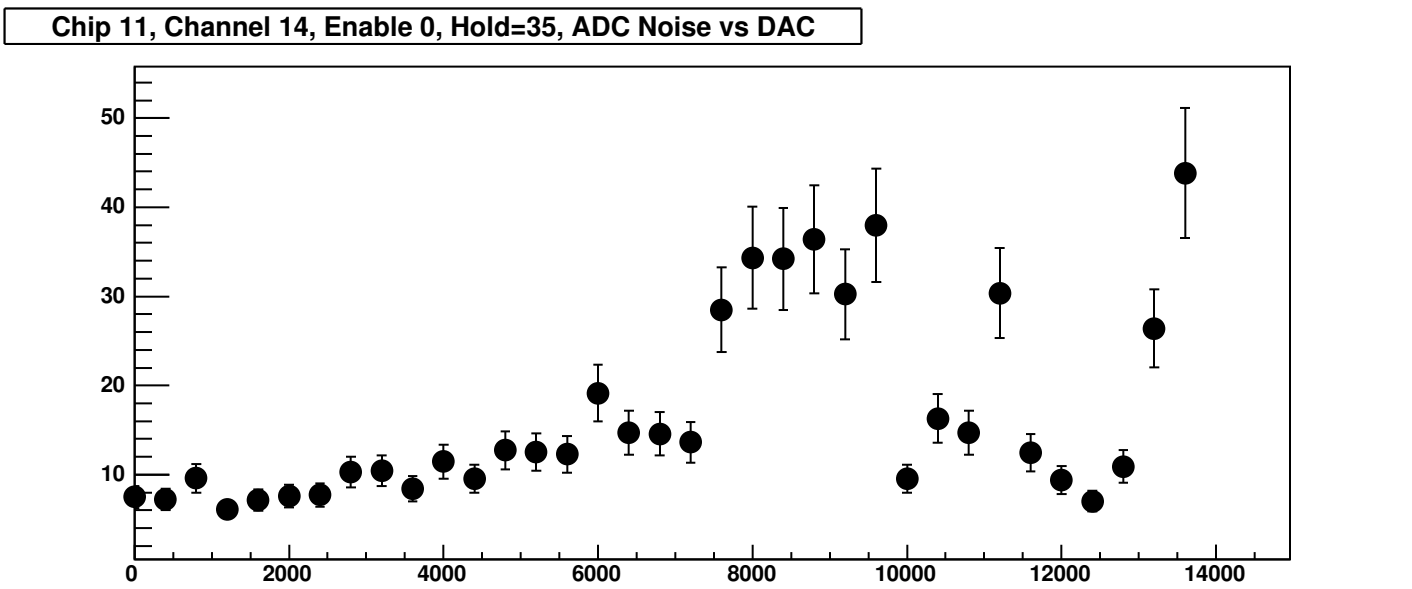


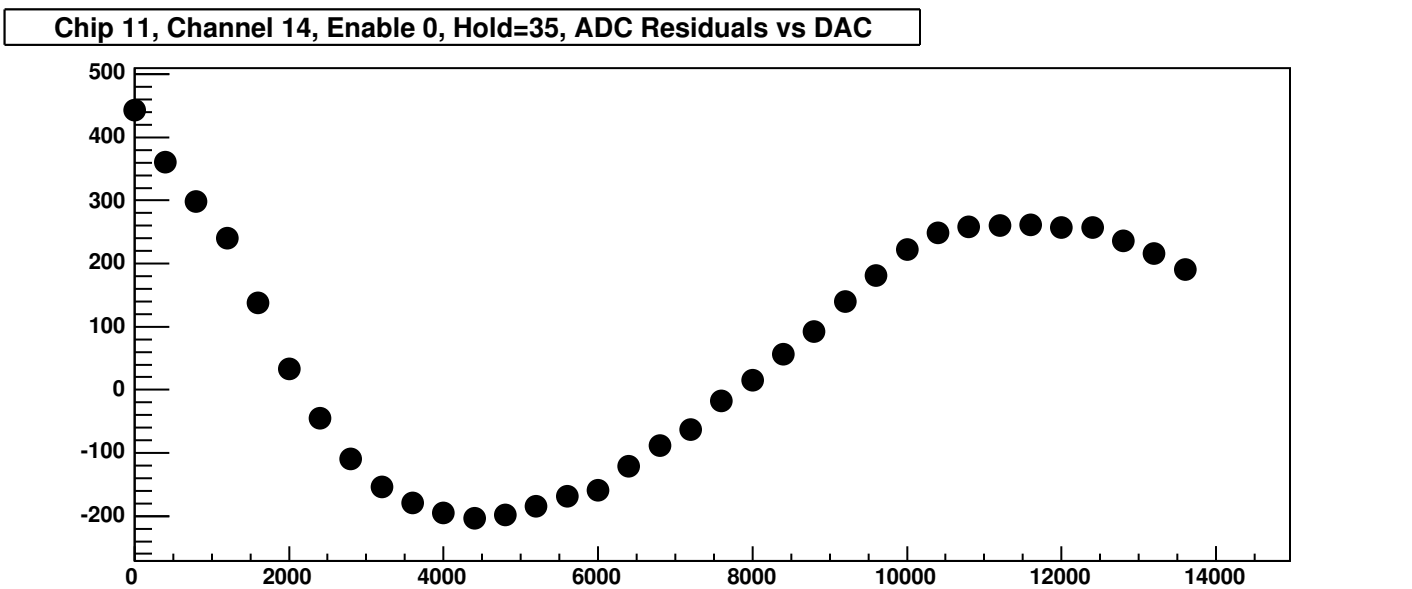

Chip 11, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6481 / 23
p0 -1186 ± 1.239
p1 0.1176 ± 0.0002386
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

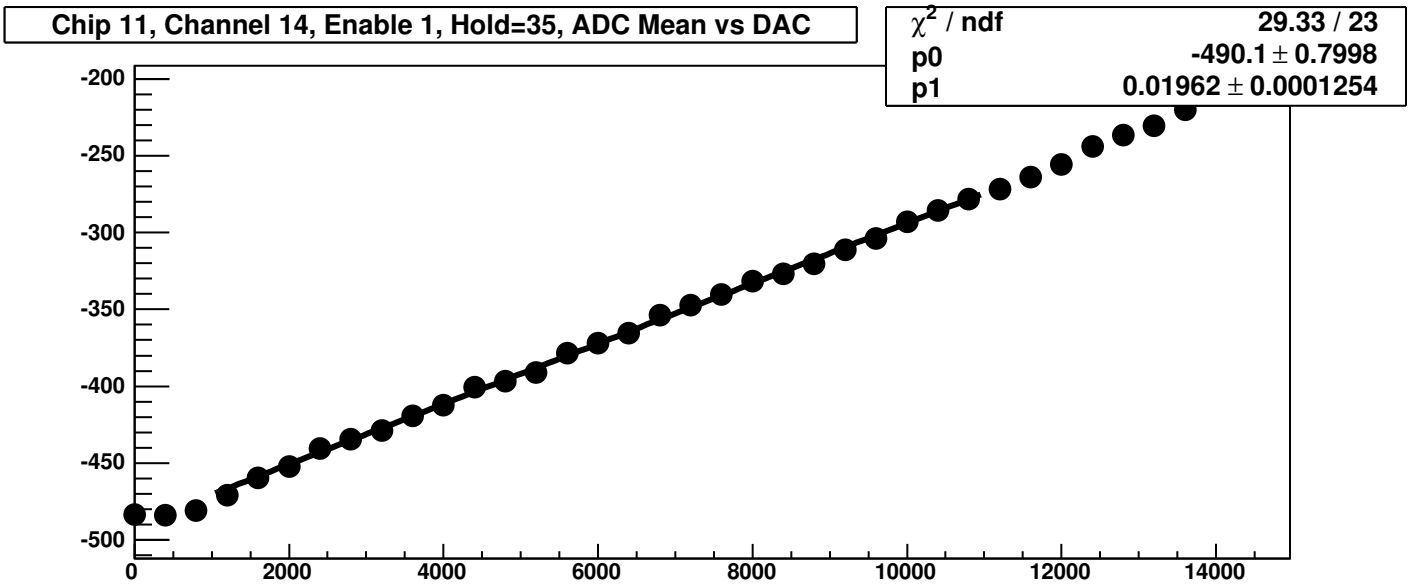

Chip 11, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

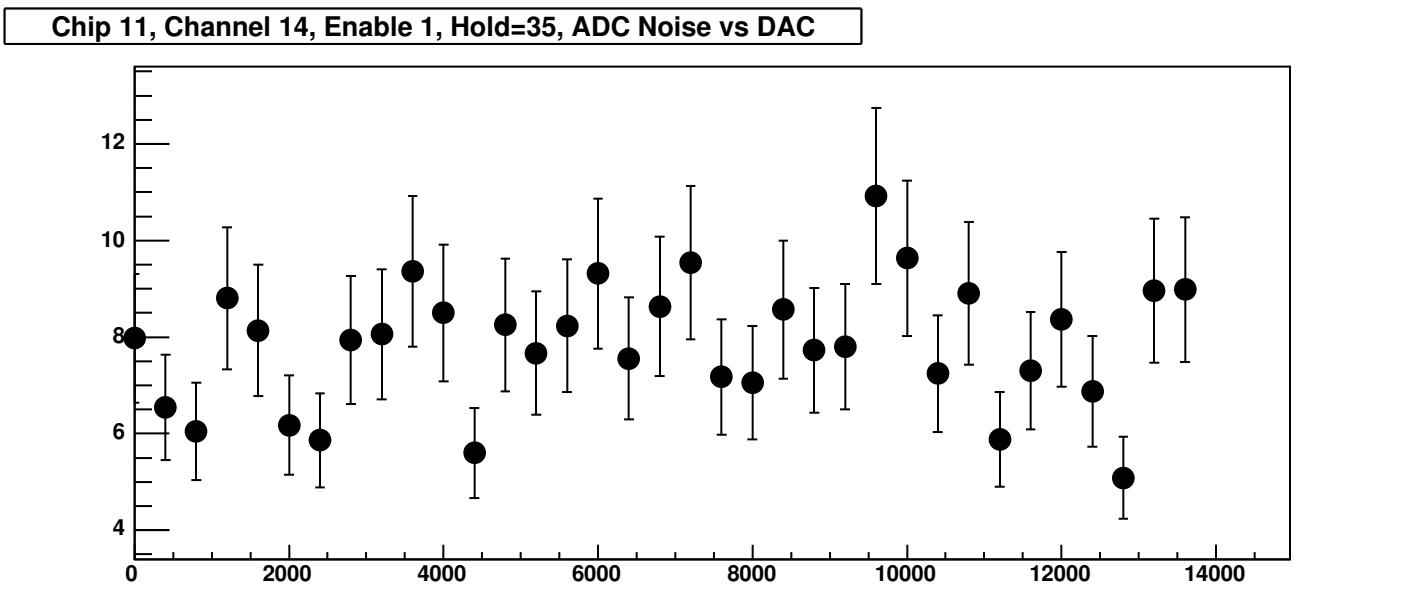

Chip 11, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

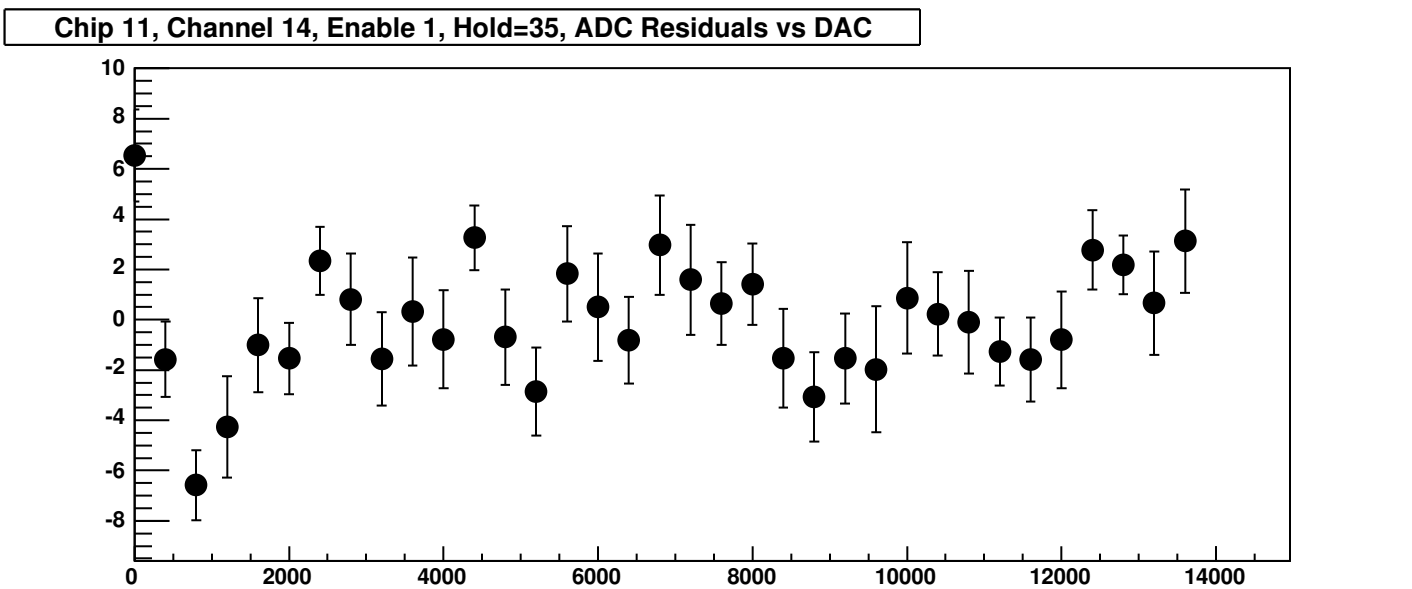

Chip 11, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.042e+05 / 23
p0 -878.7 ± 0.923
p1 0.1992 ± 0.0001876
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

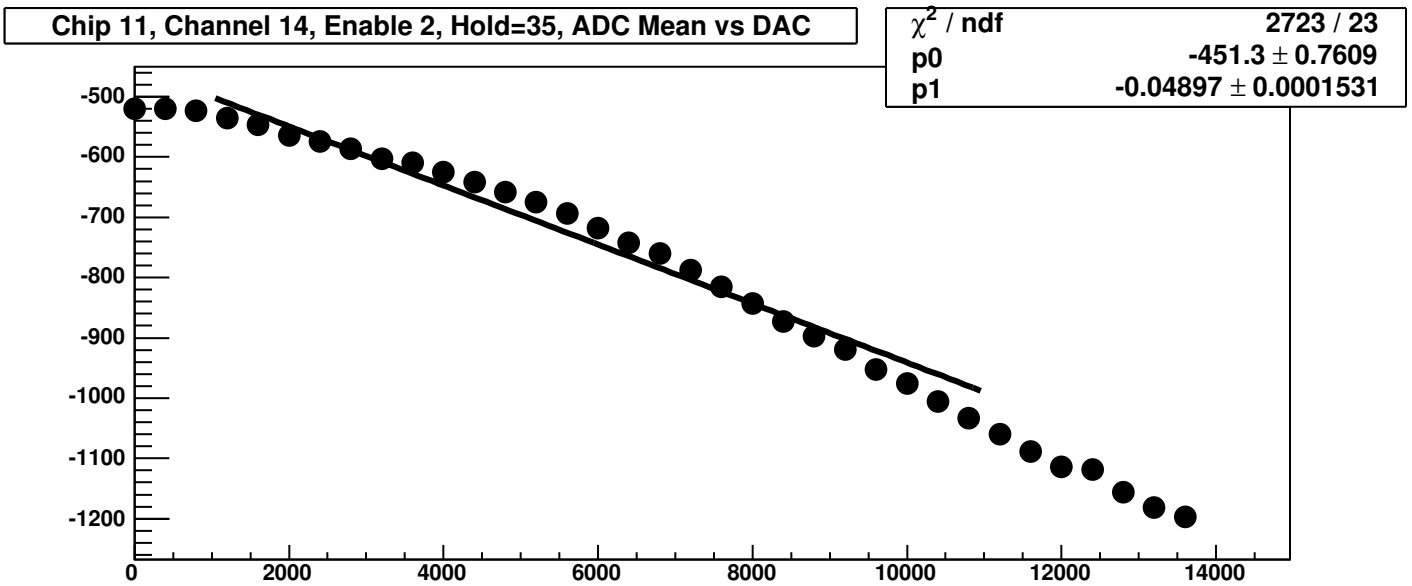

Chip 11, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

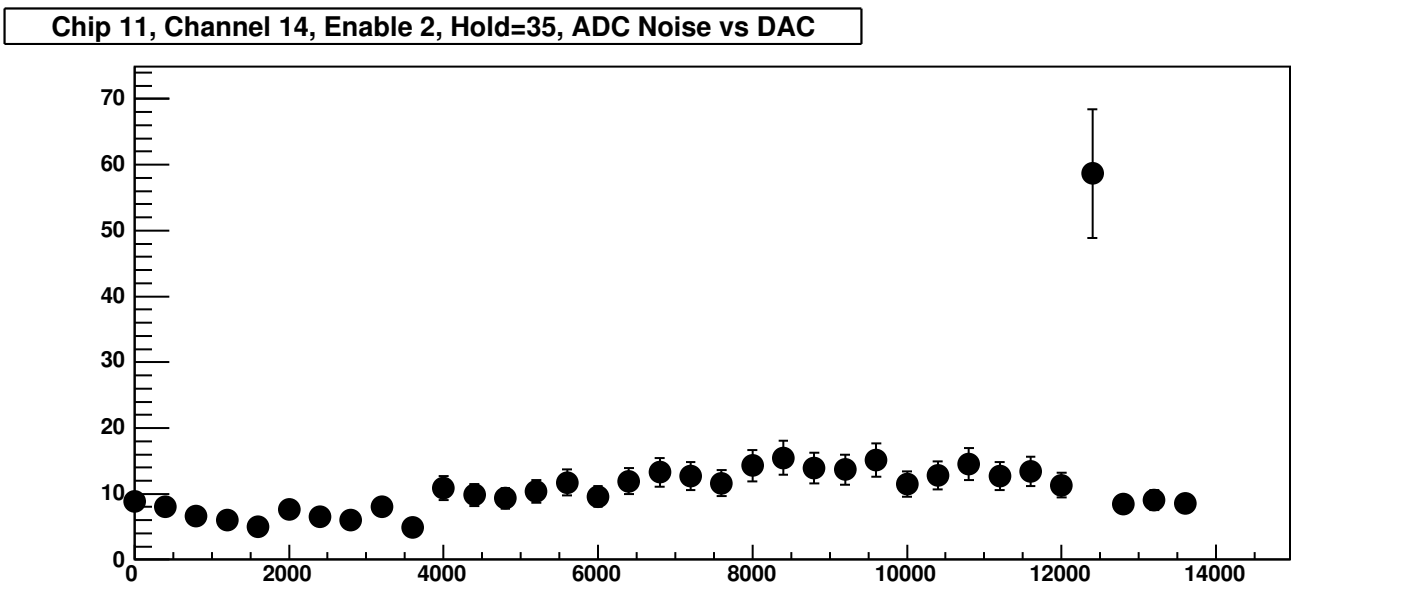

Chip 11, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

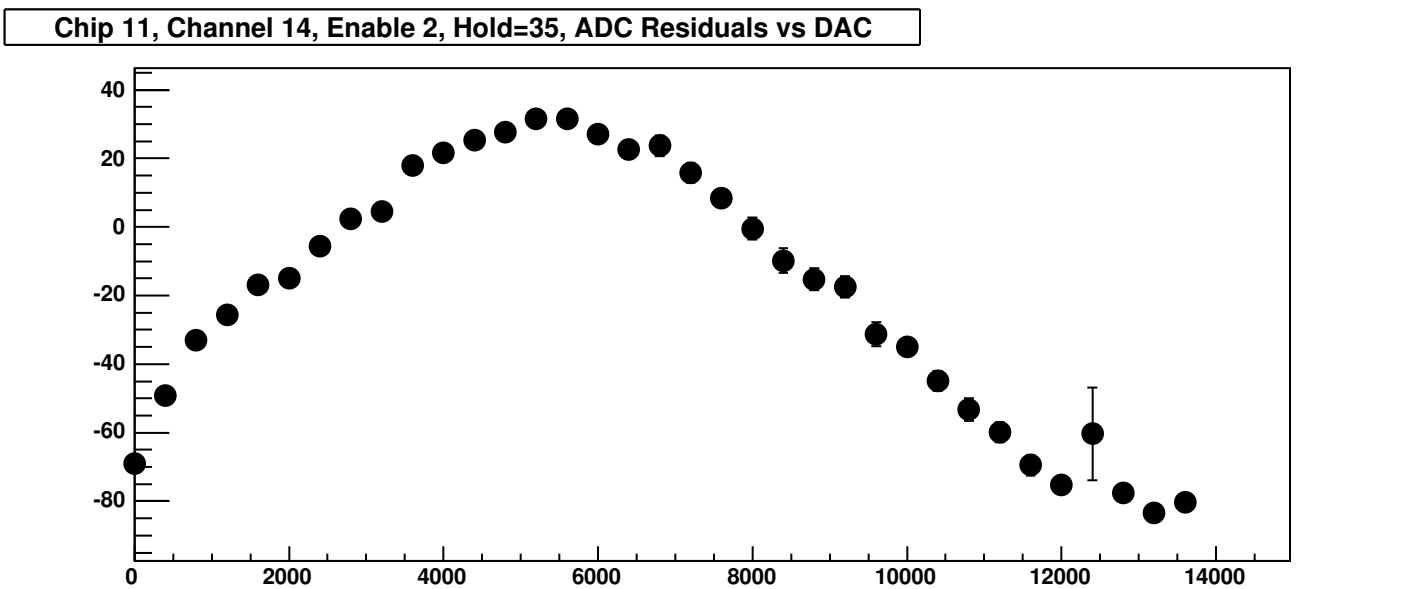

Chip 11, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.33 / 23
p0 -490.1 ± 0.7998
p1 0.01962 ± 0.0001254
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

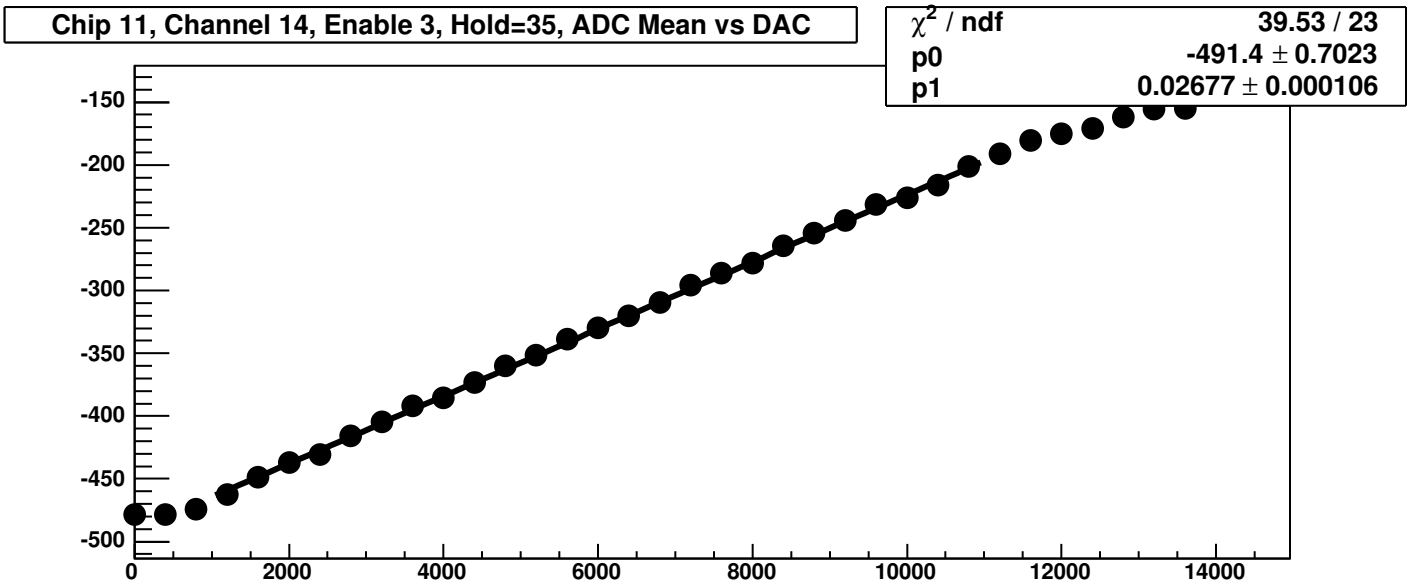

Chip 11, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

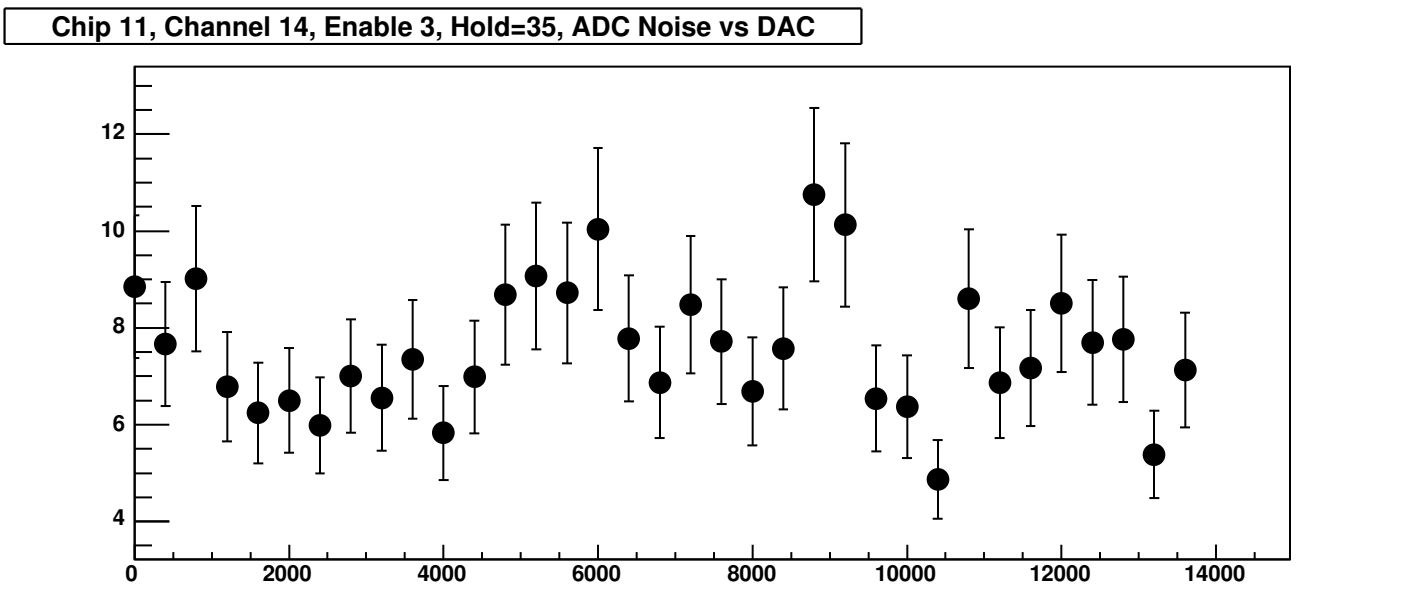

Chip 11, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

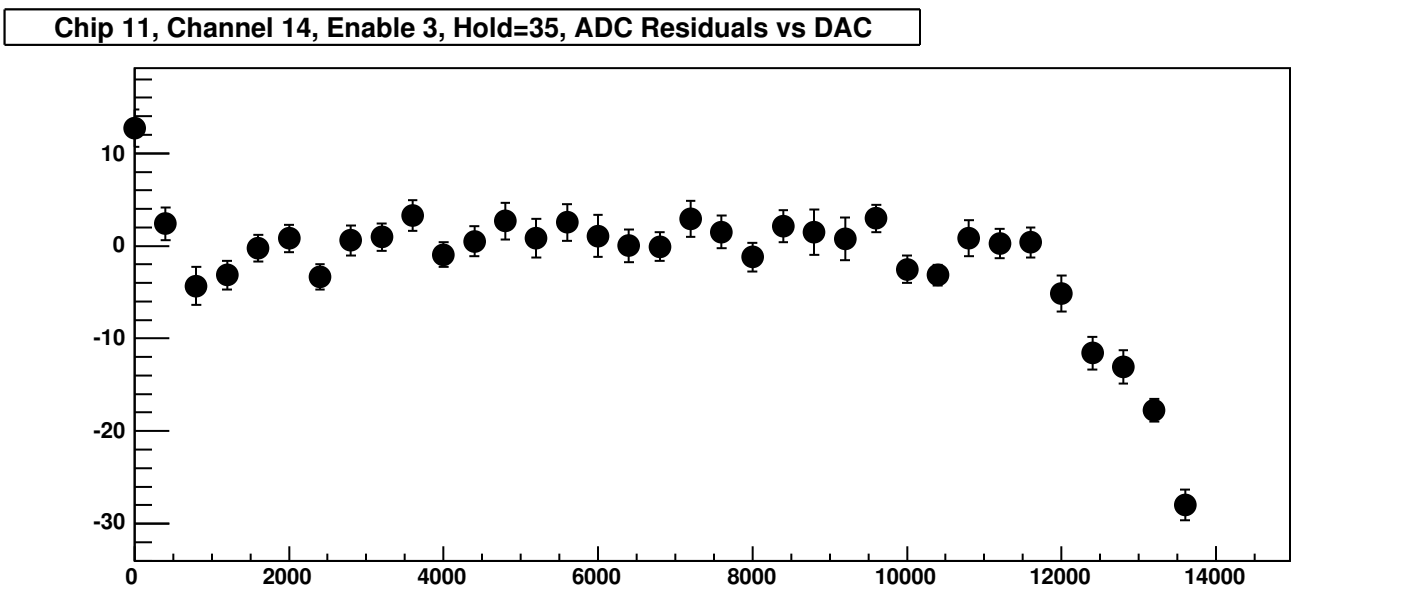

Chip 11, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2723 / 23
p0 -451.3 ± 0.7609
p1 -0.04897 ± 0.0001531
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

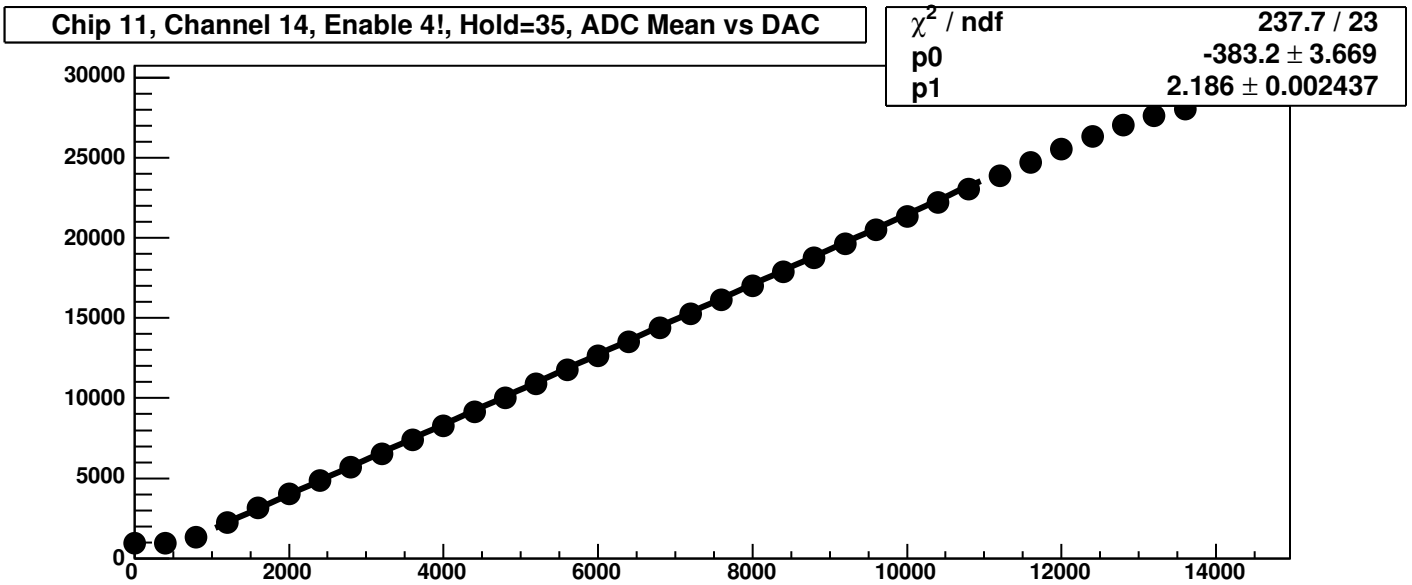

Chip 11, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

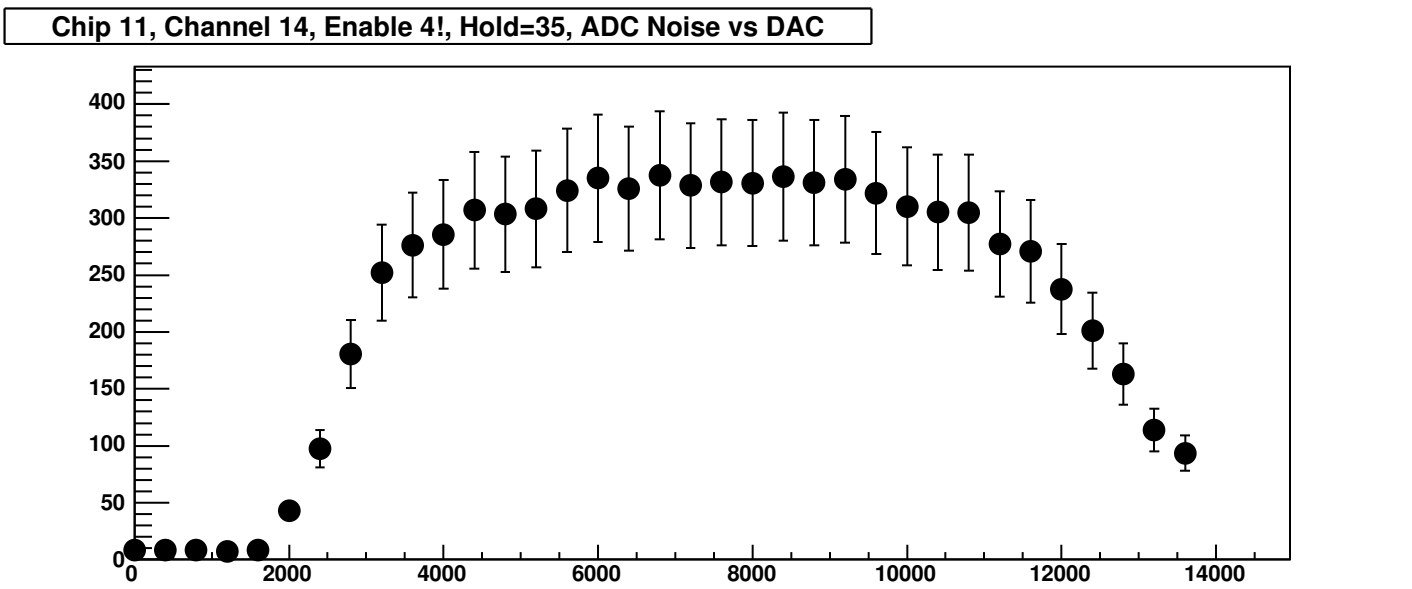

Chip 11, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

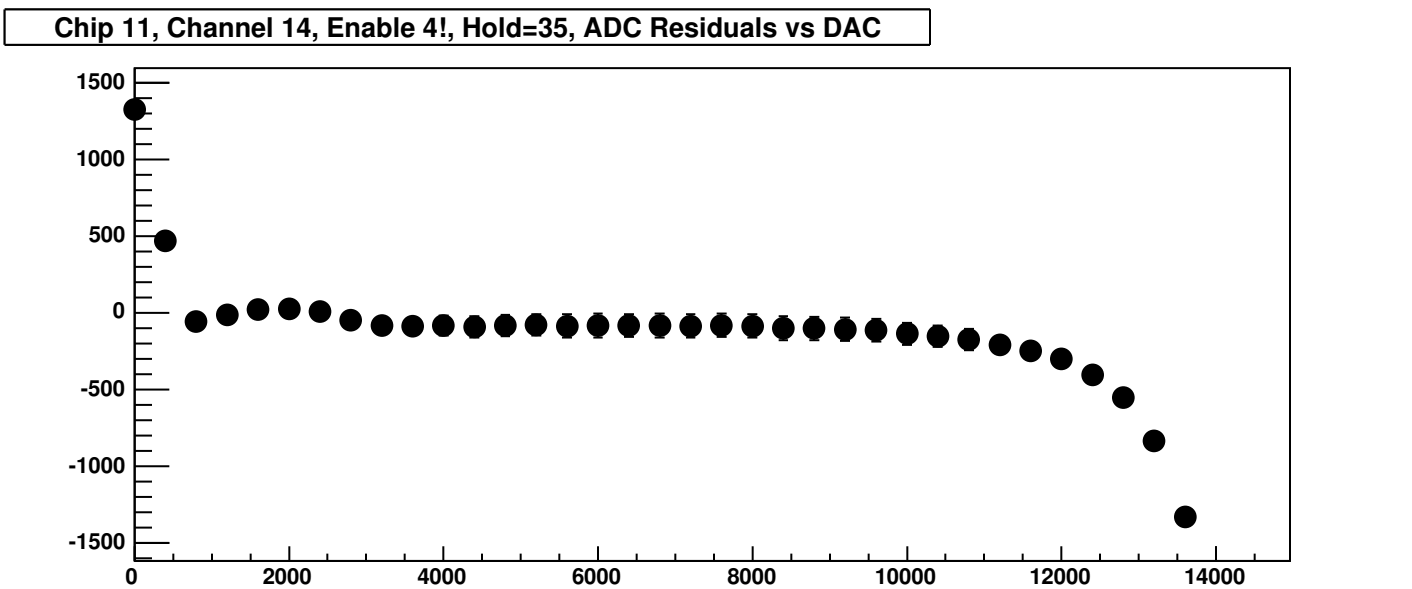

Chip 11, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.53 / 23
p0 -491.4 ± 0.7023
p1 0.02677 ± 0.000106
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

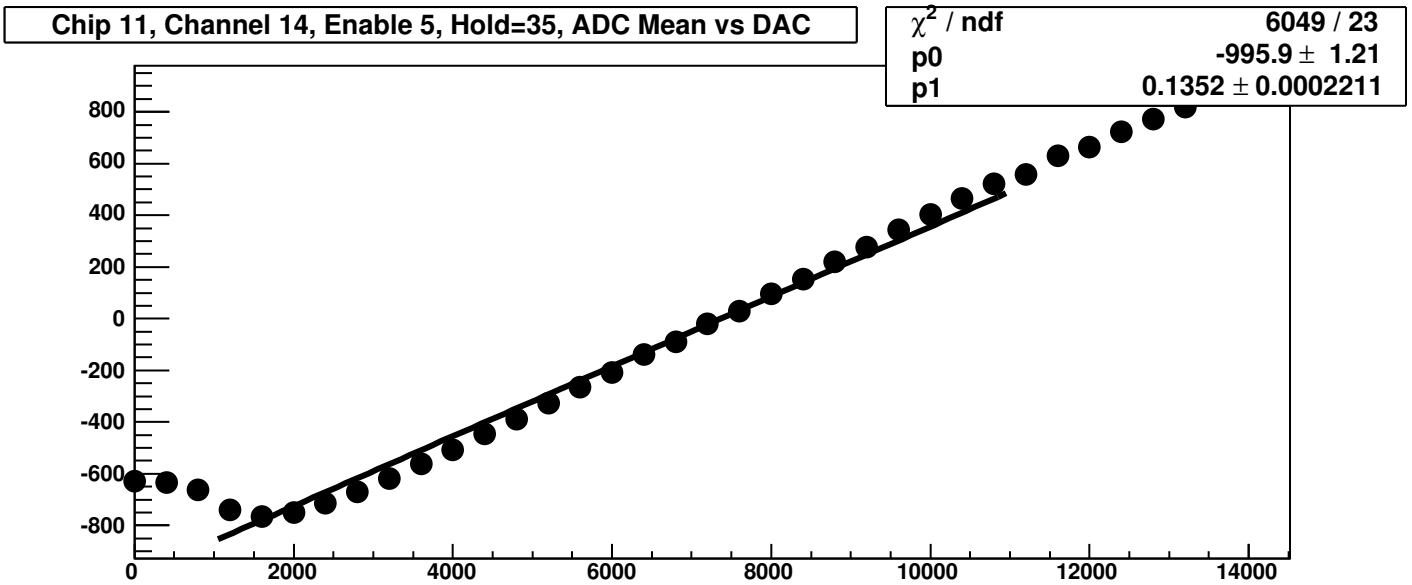

Chip 11, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

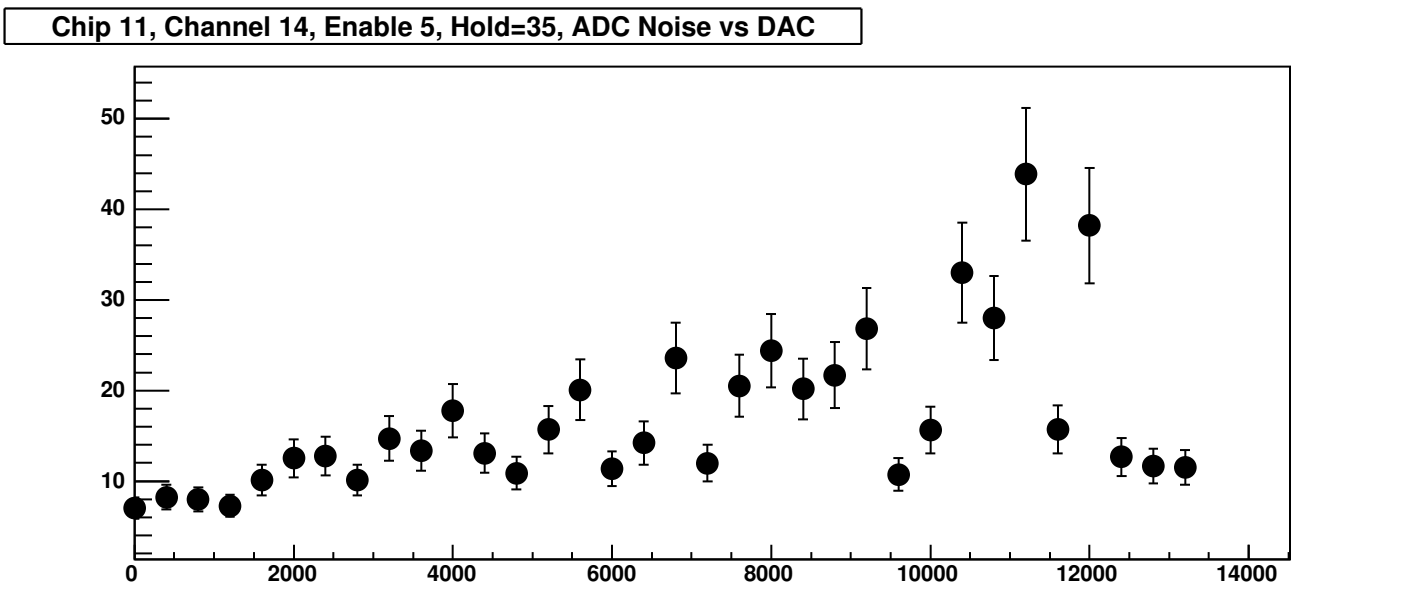

Chip 11, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

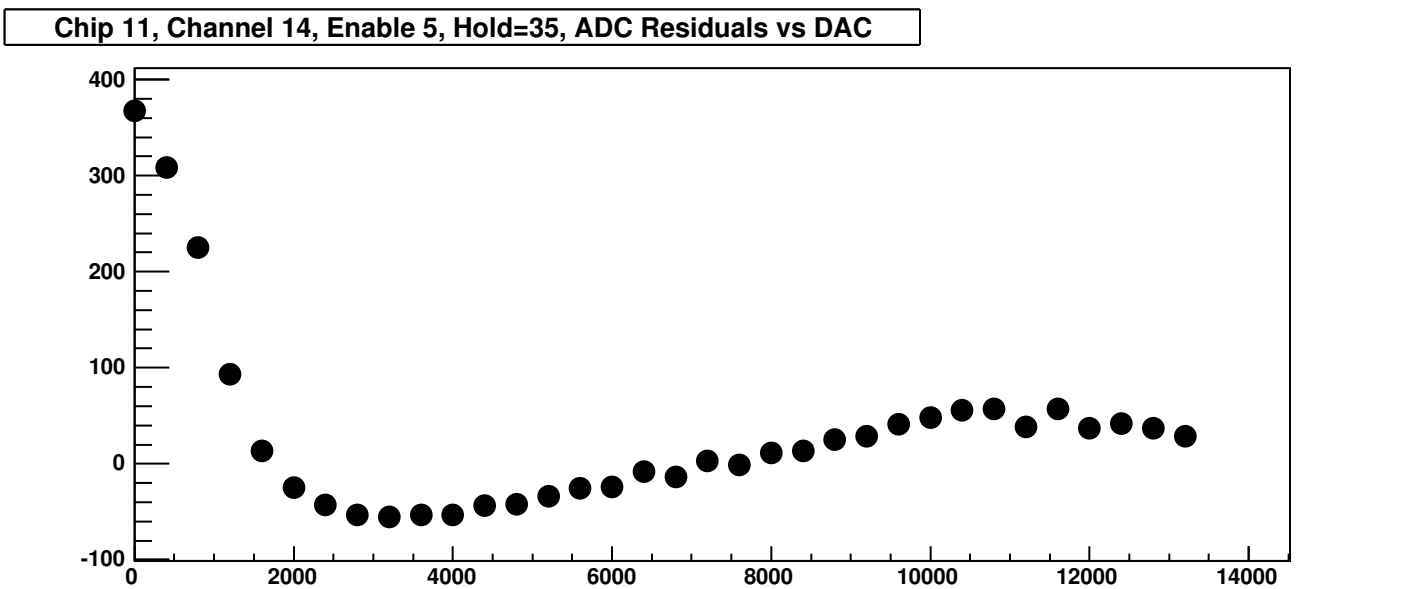

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 237.7 / 23
p0 -383.2 ± 3.669
p1 2.186 ± 0.002437
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

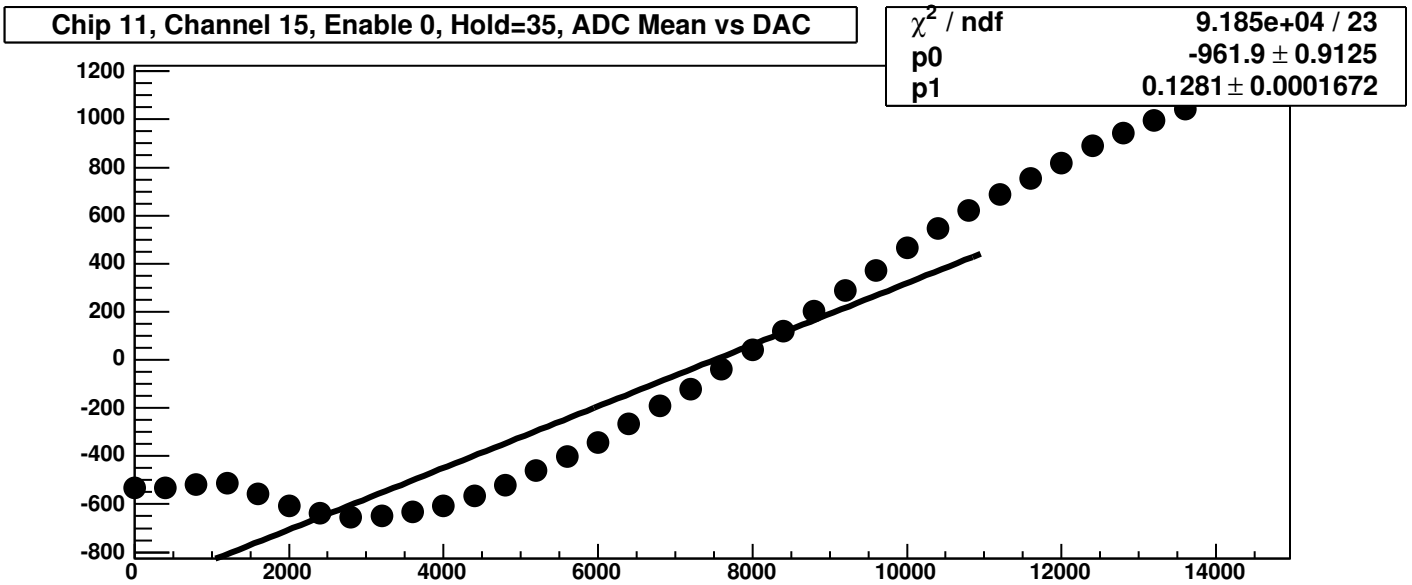

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

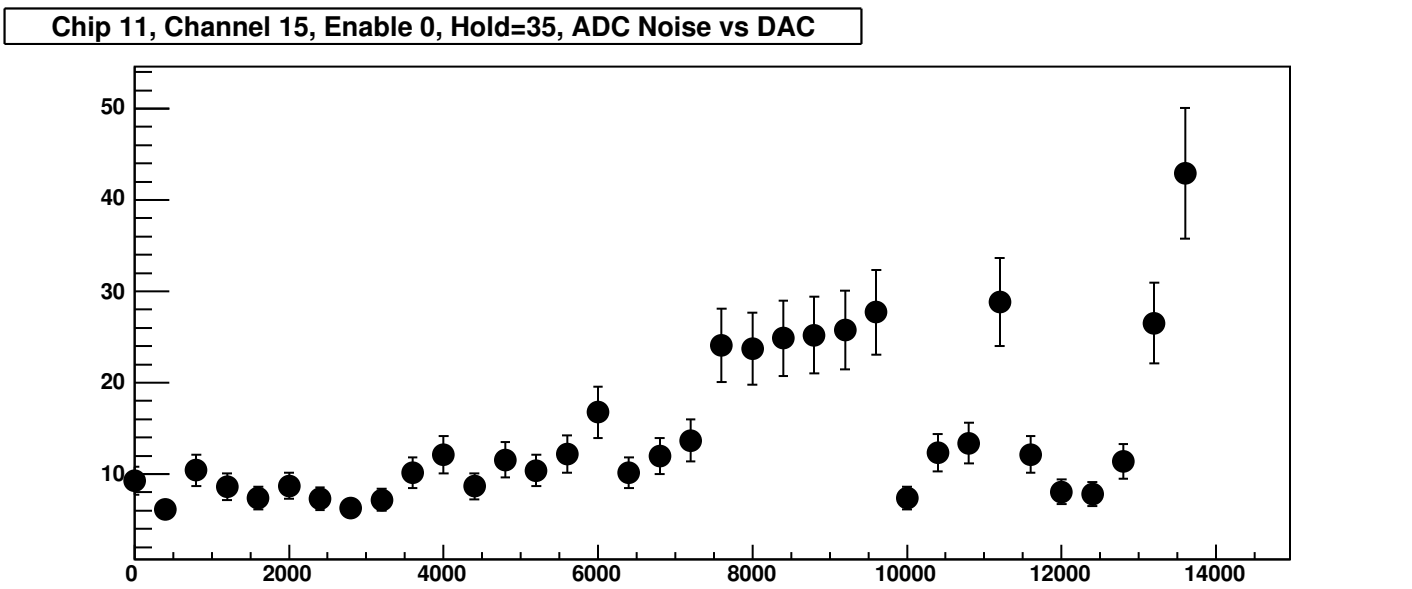

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

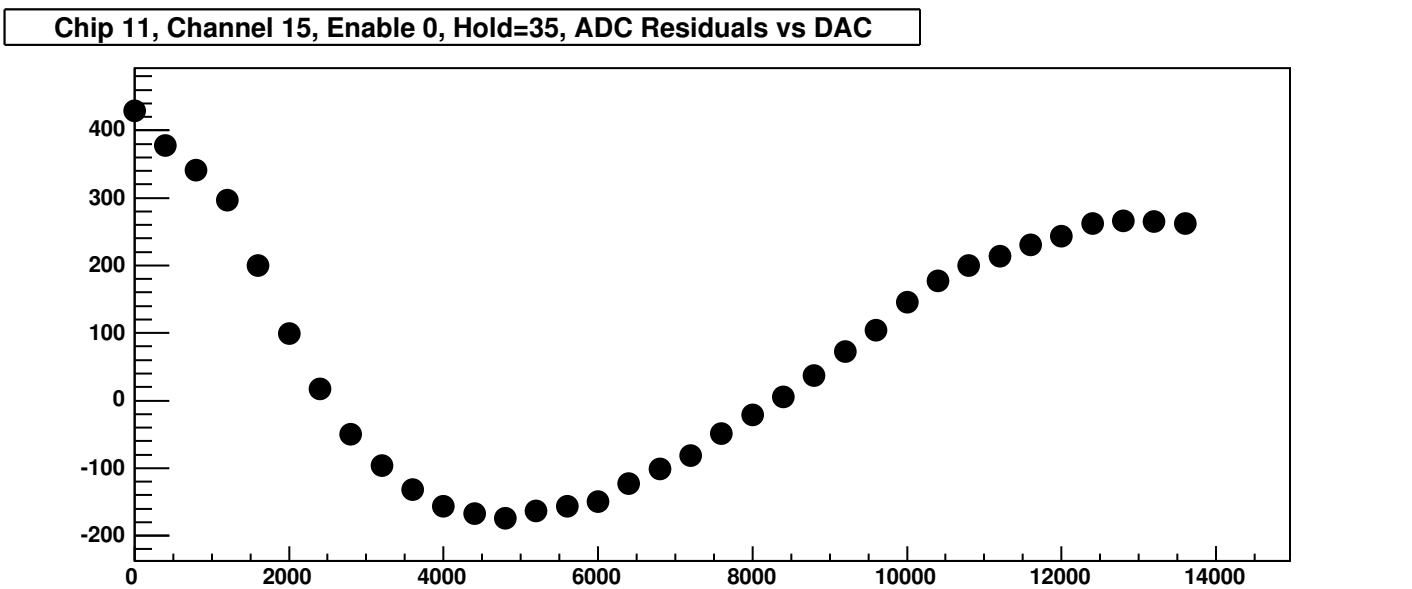

Chip 11, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6049 / 23
p0 -995.9 ± 1.21
p1 0.1352 ± 0.0002211
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

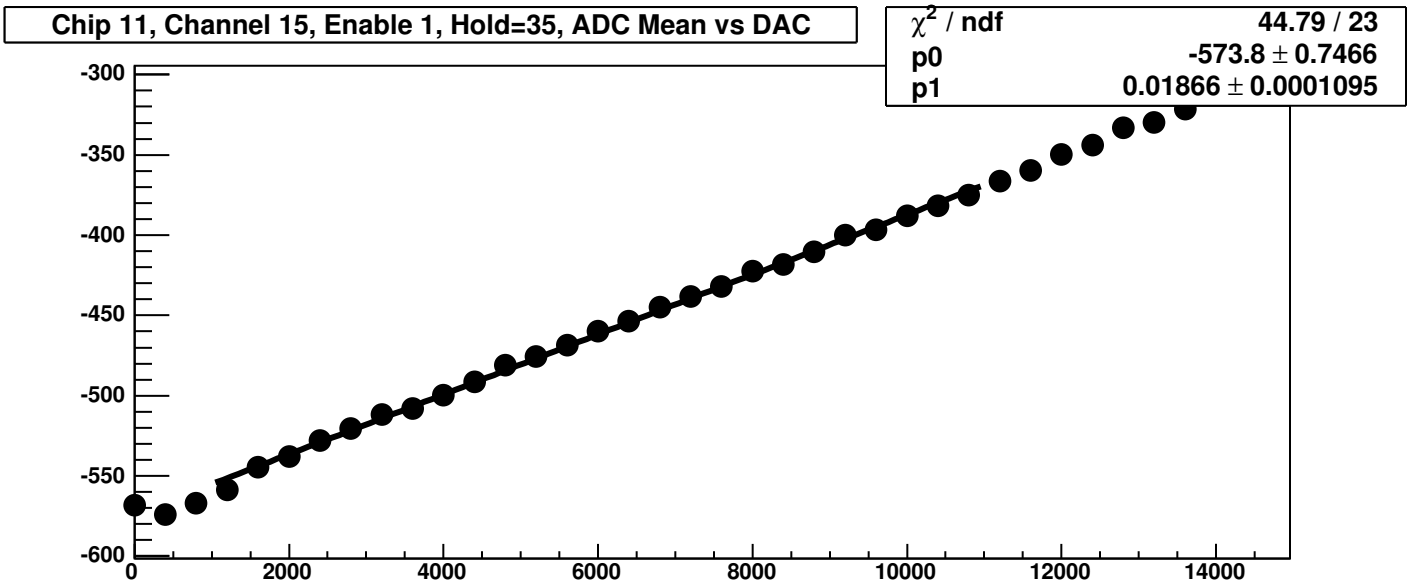

Chip 11, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

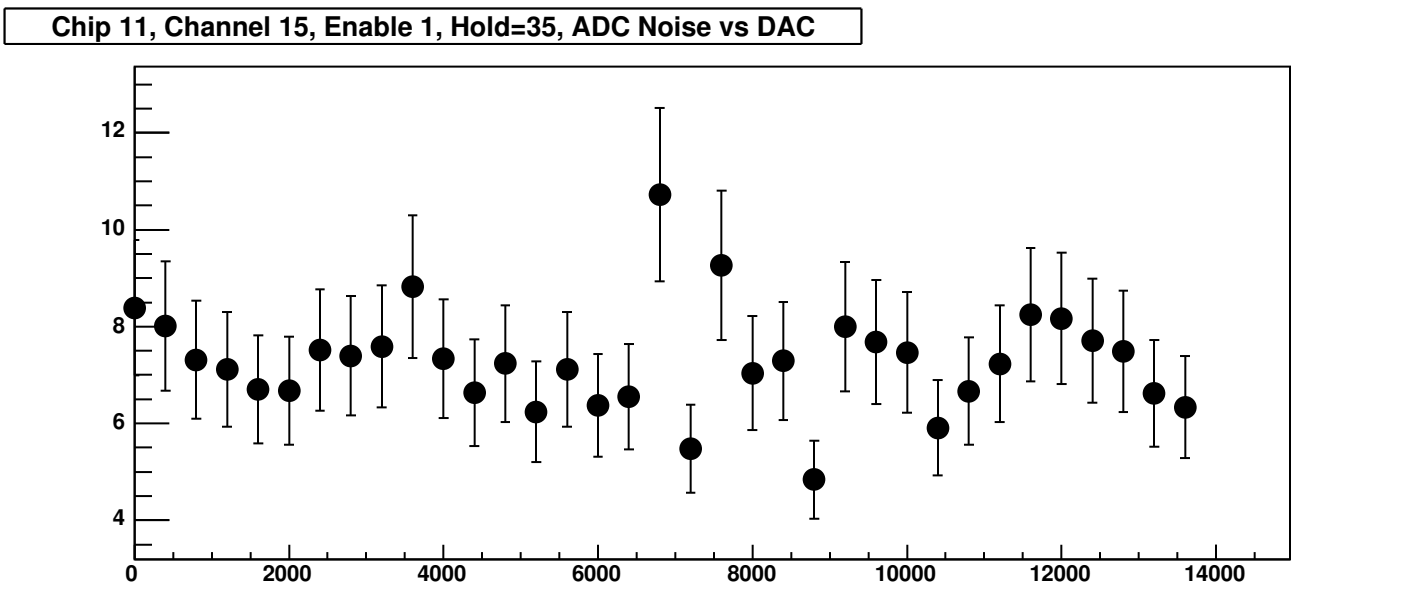

Chip 11, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

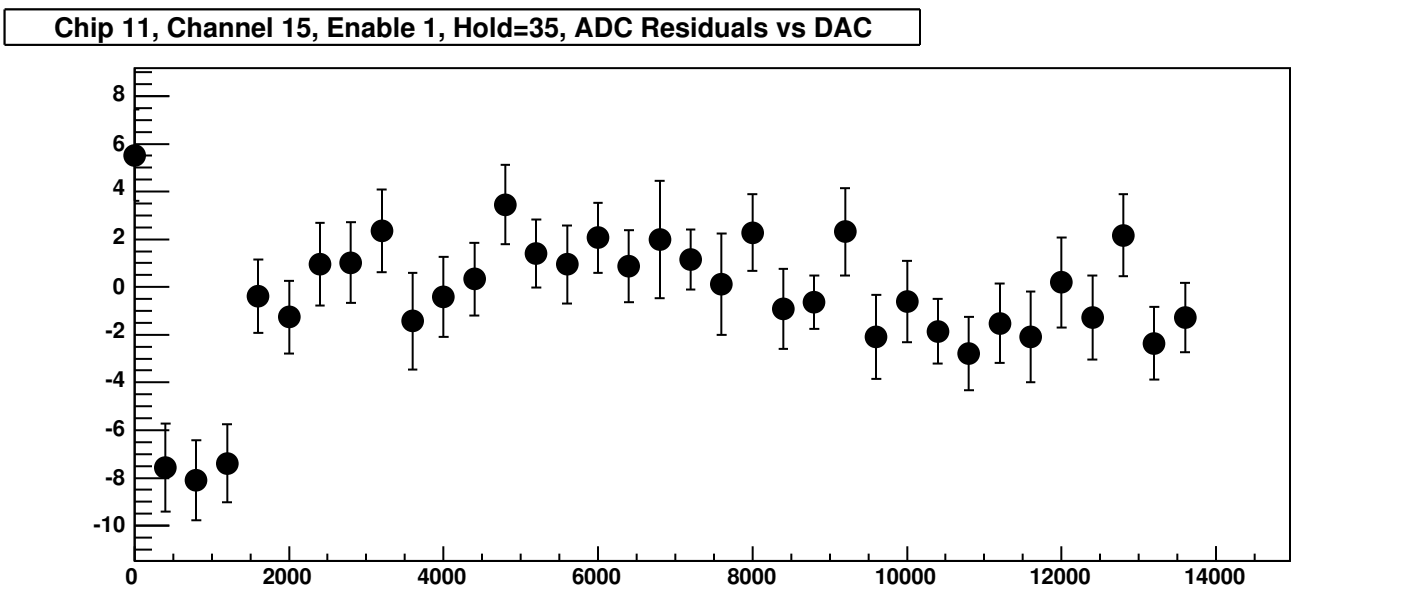

Chip 11, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9.185e+04 / 23
p0 -961.9 ± 0.9125
p1 0.1281 ± 0.0001672
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

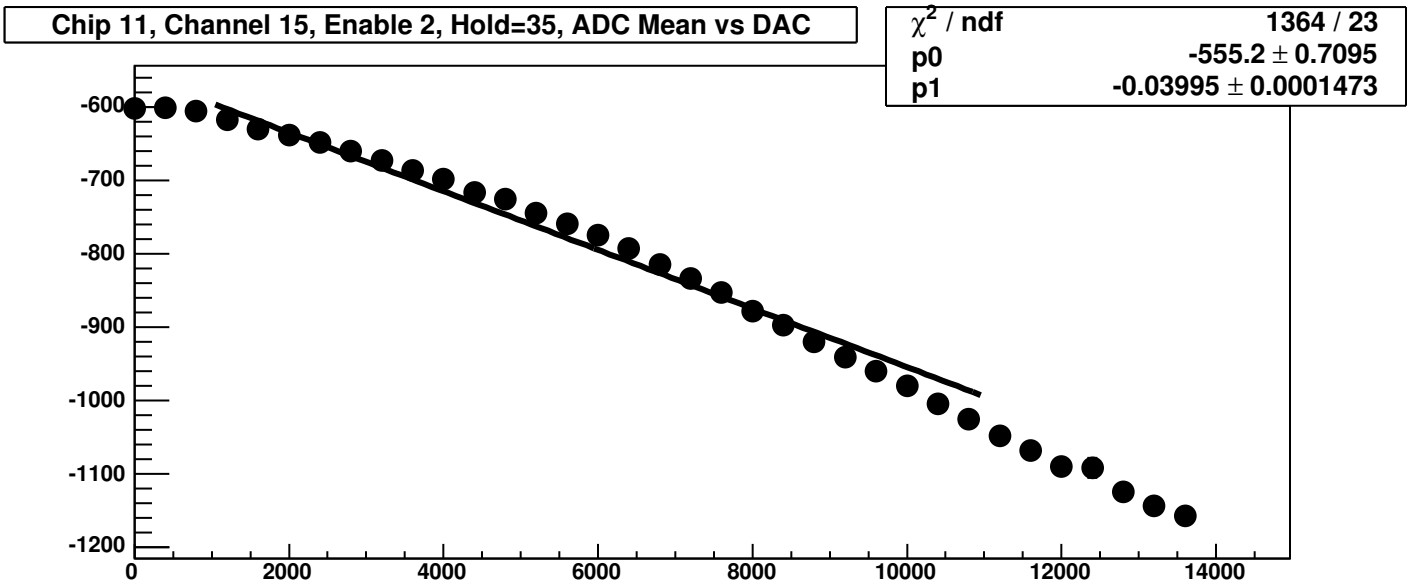

Chip 11, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

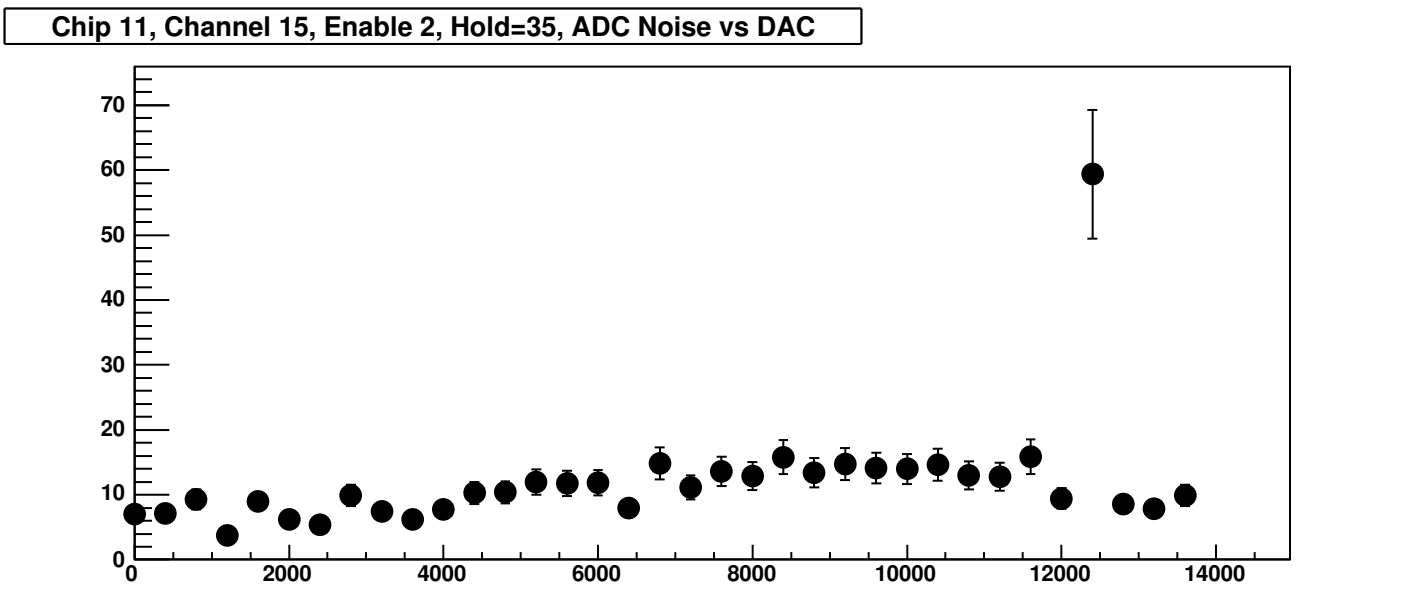

Chip 11, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

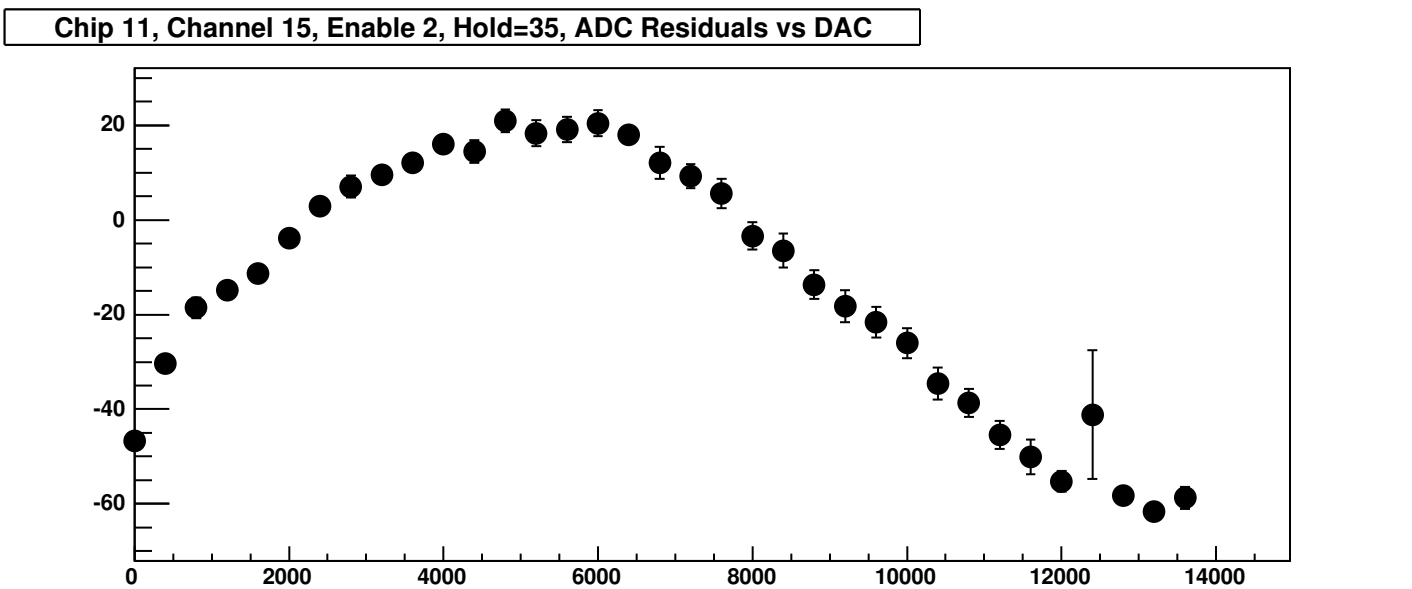

Chip 11, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.79 / 23
p0 -573.8 ± 0.7466
p1 0.01866 ± 0.0001095
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

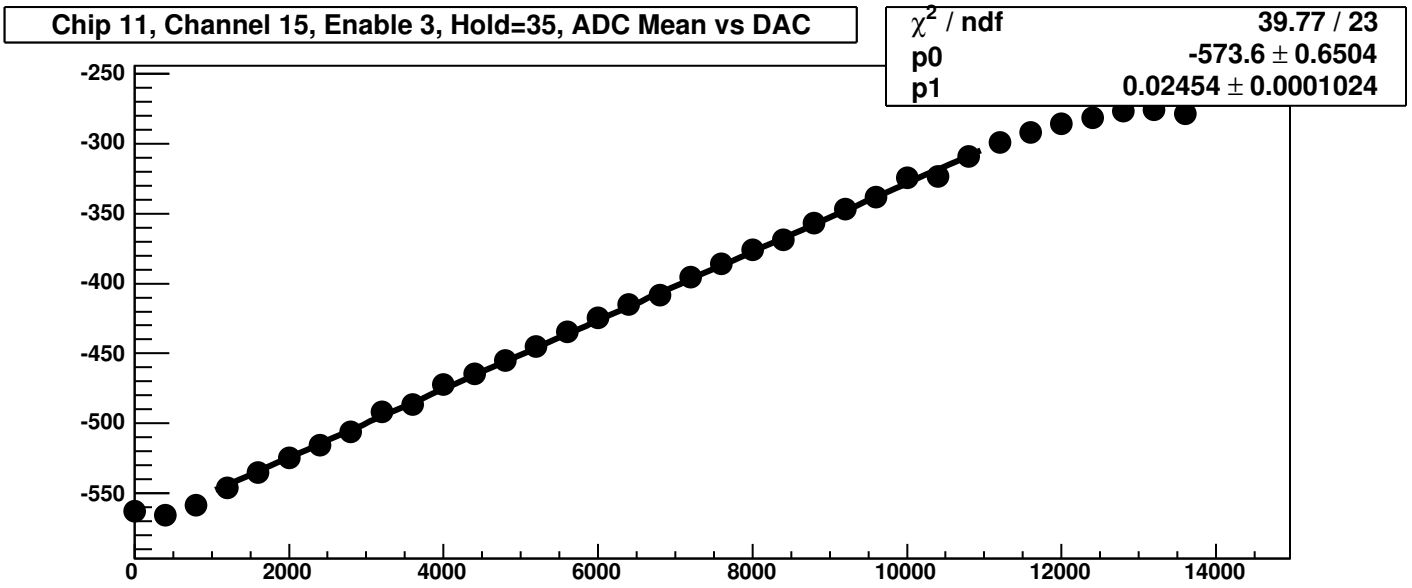

Chip 11, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

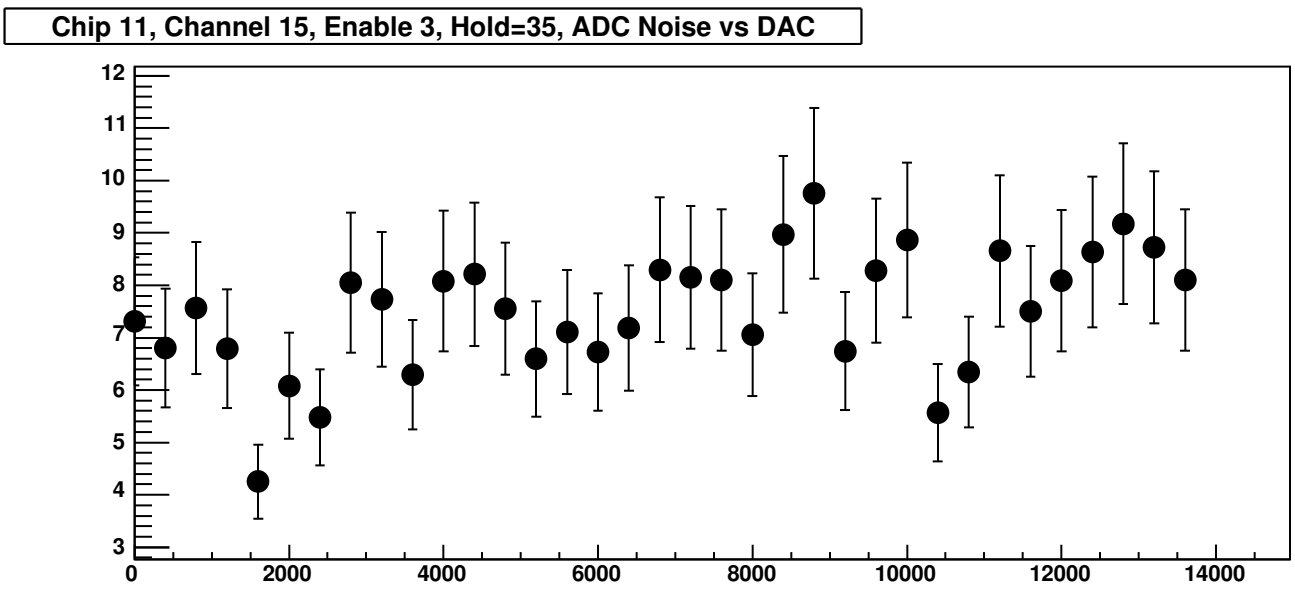

Chip 11, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

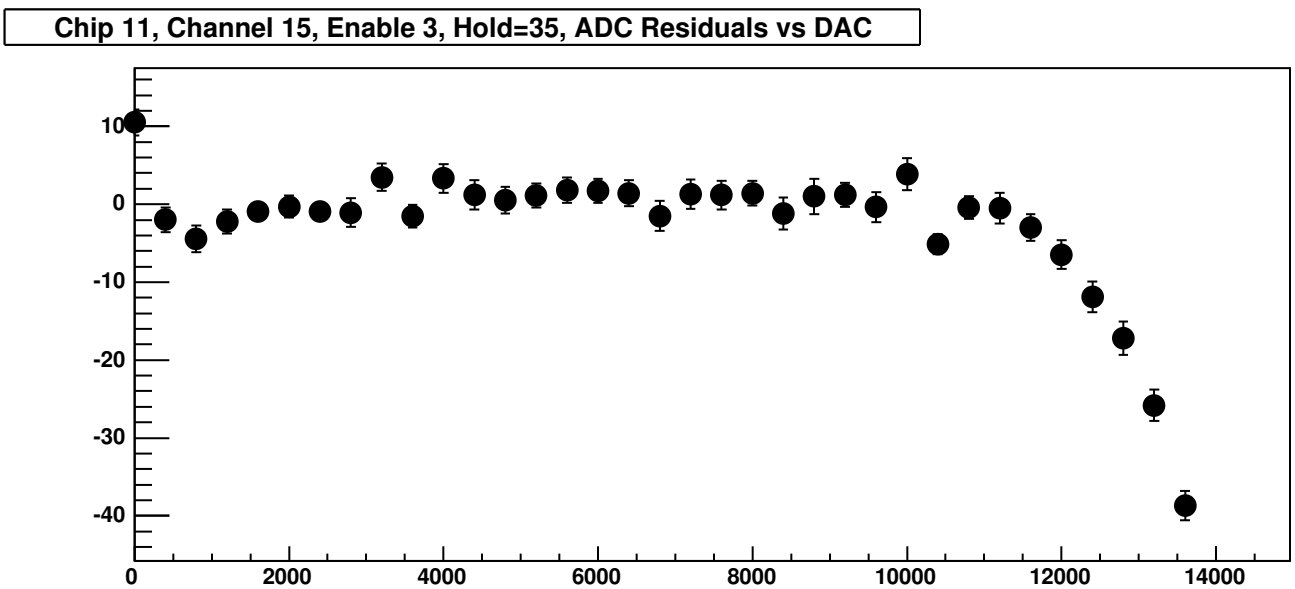

Chip 11, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1364 / 23
p0 -555.2 ± 0.7095
p1 -0.03995 ± 0.0001473
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

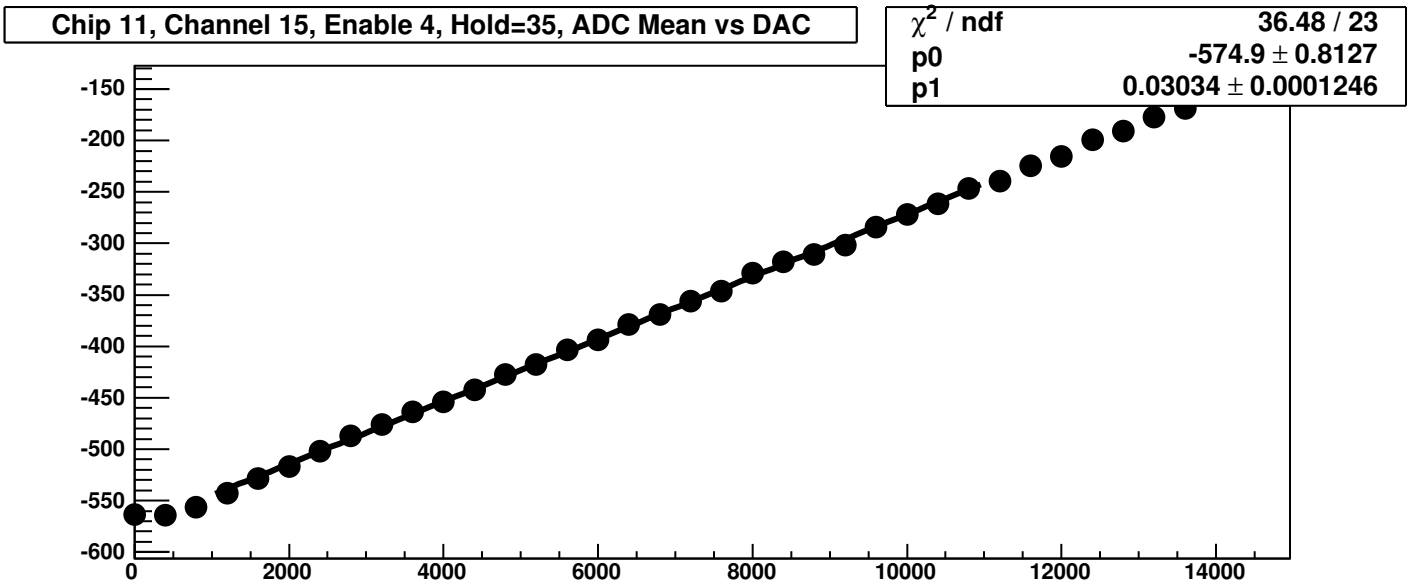

Chip 11, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

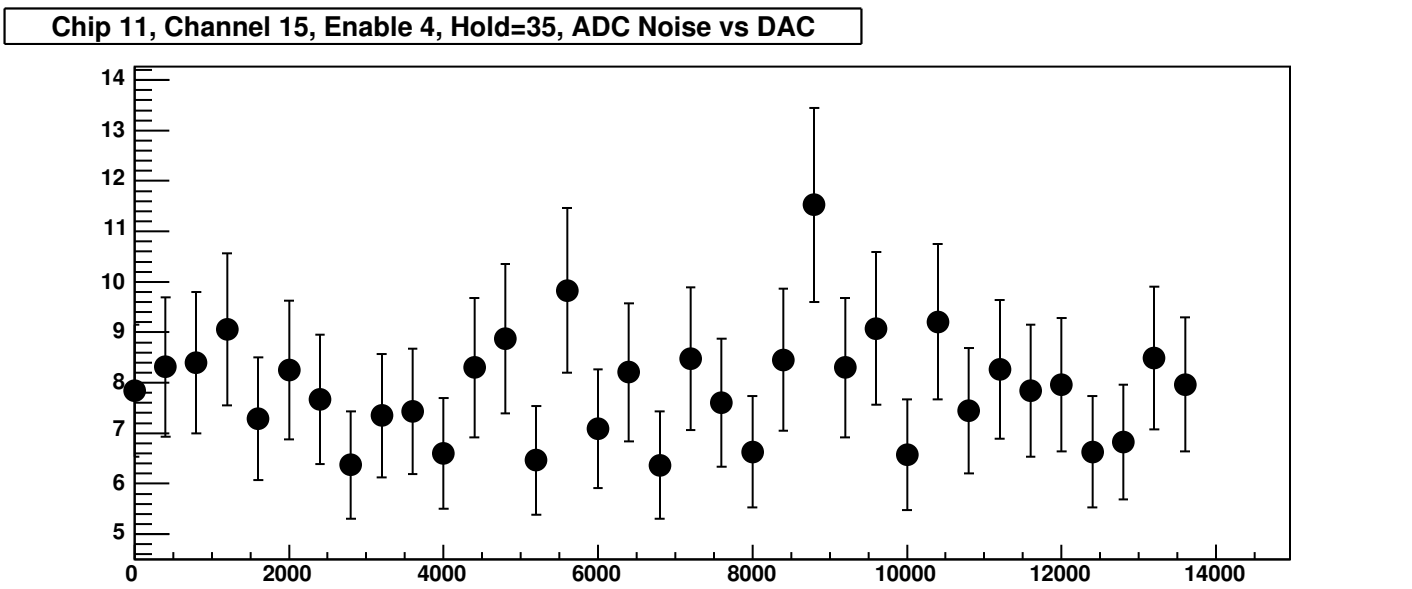

Chip 11, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

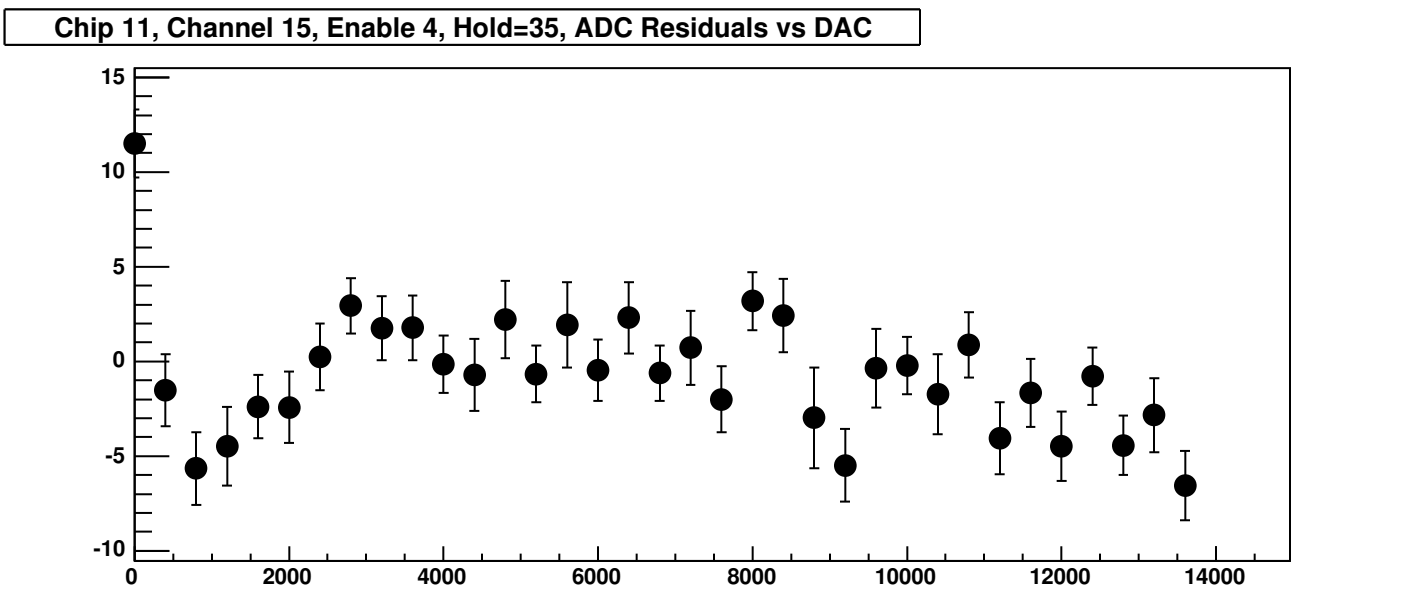

Chip 11, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.77 / 23
p0 -573.6 ± 0.6504
p1 0.02454 ± 0.0001024
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

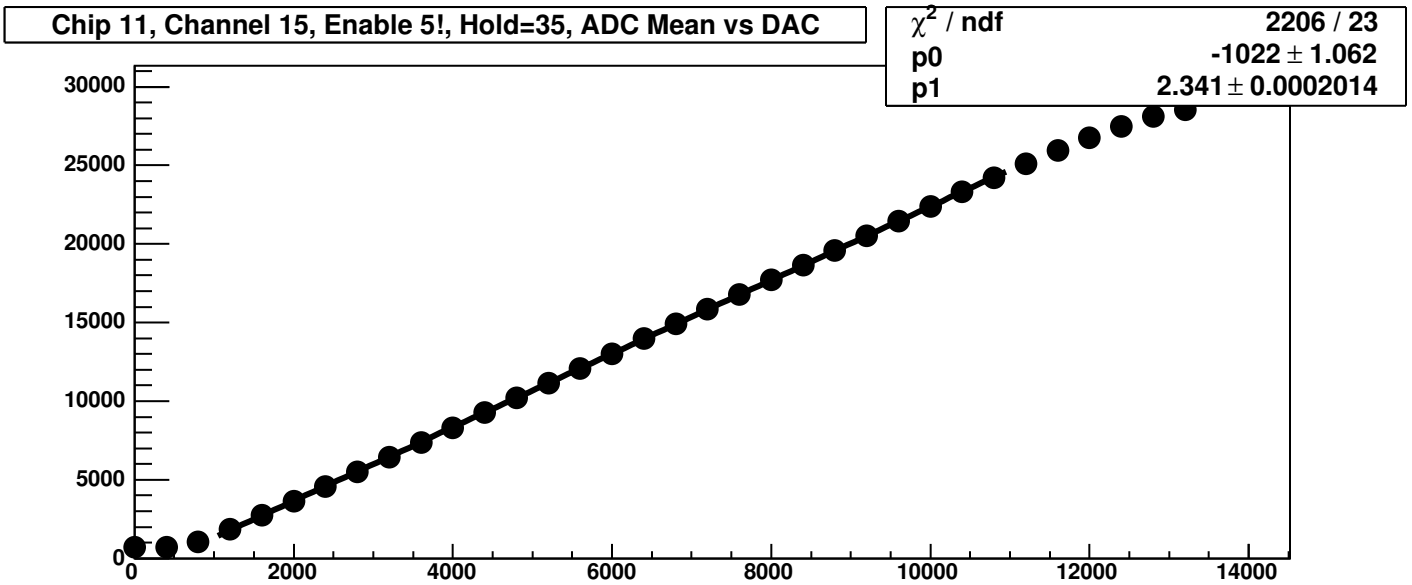

Chip 11, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

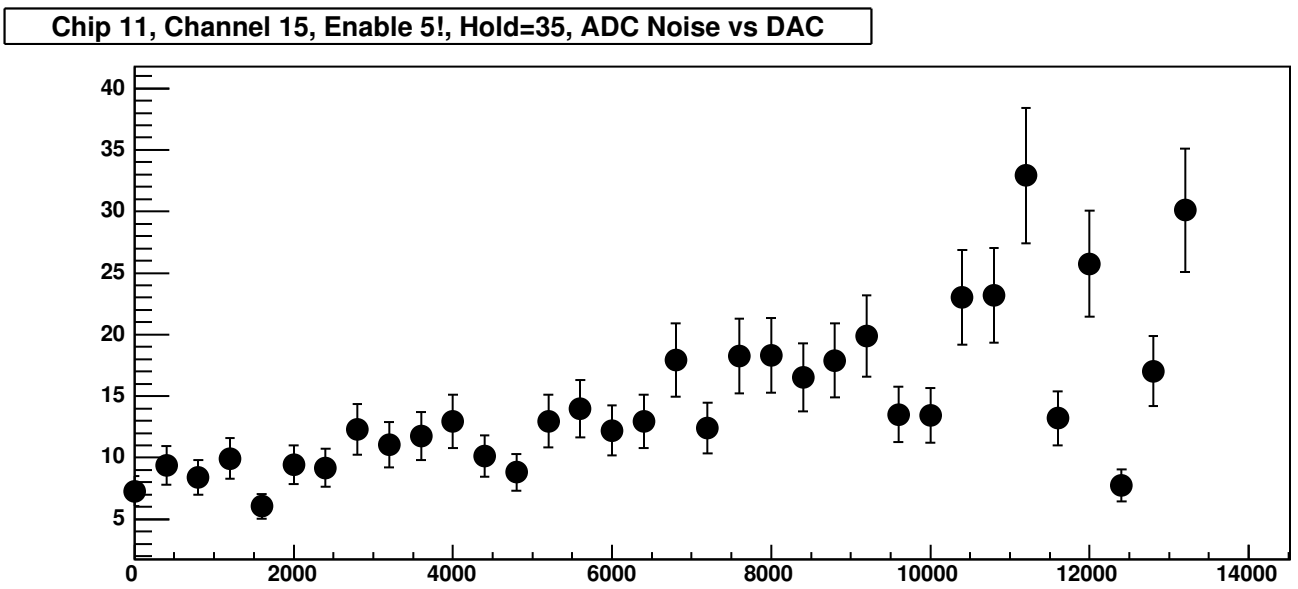

Chip 11, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

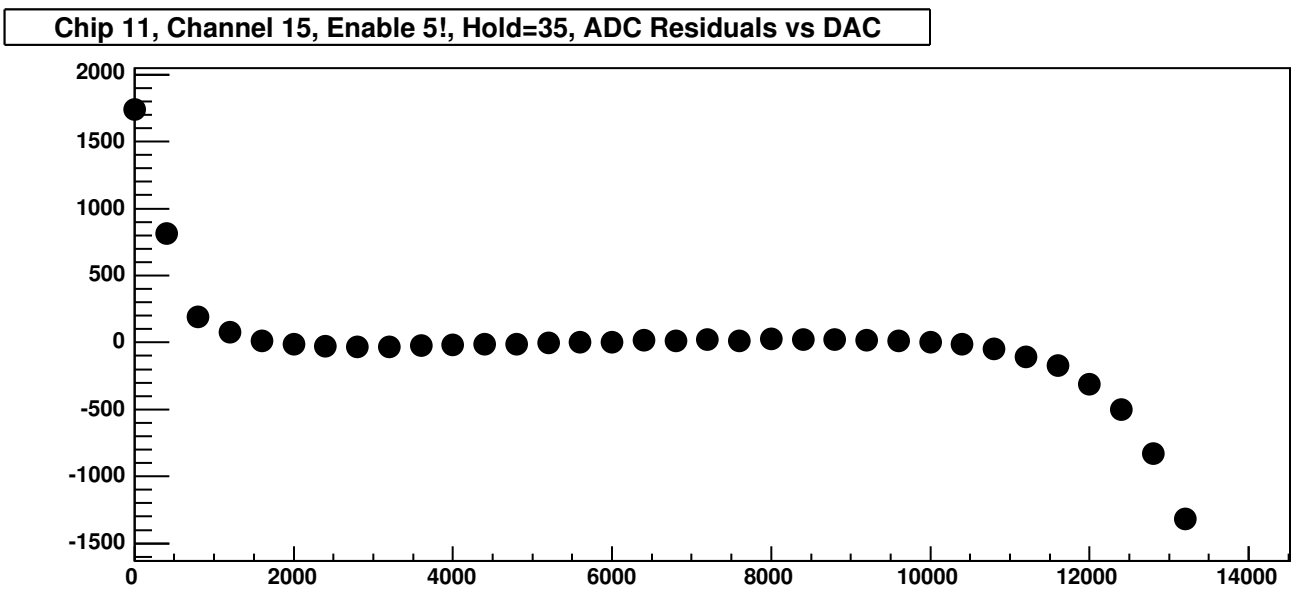

Chip 11, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.48 / 23
p0 -574.9 ± 0.8127
p1 0.03034 ± 0.0001246
-150
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

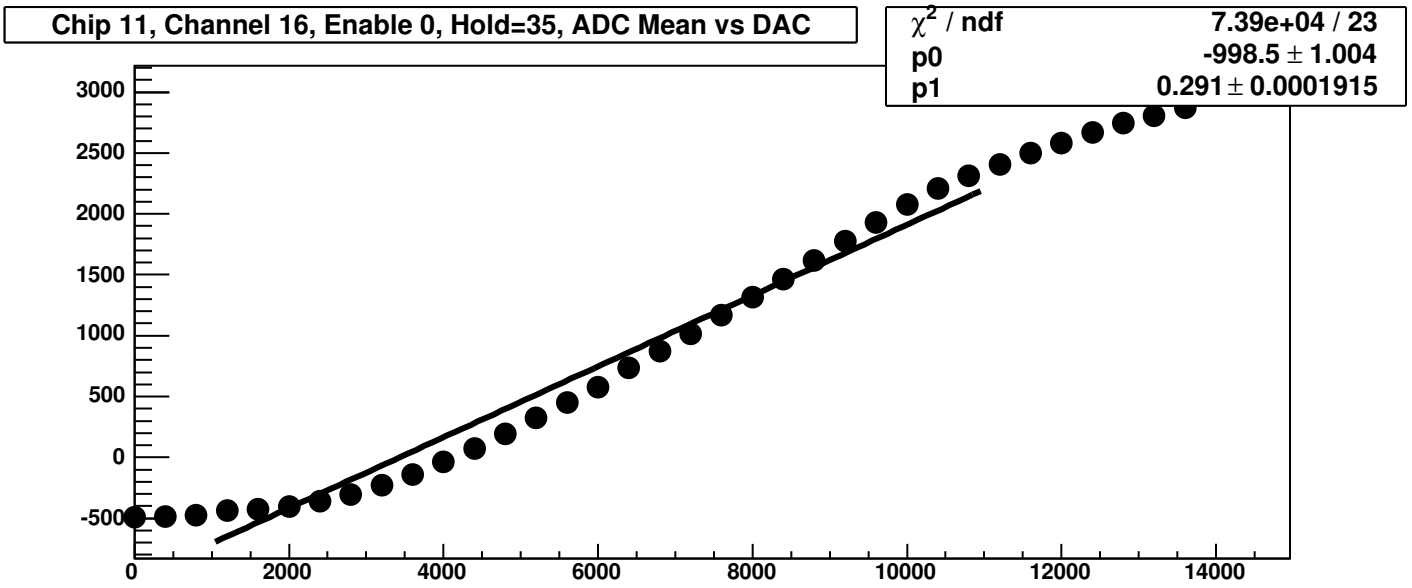

Chip 11, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

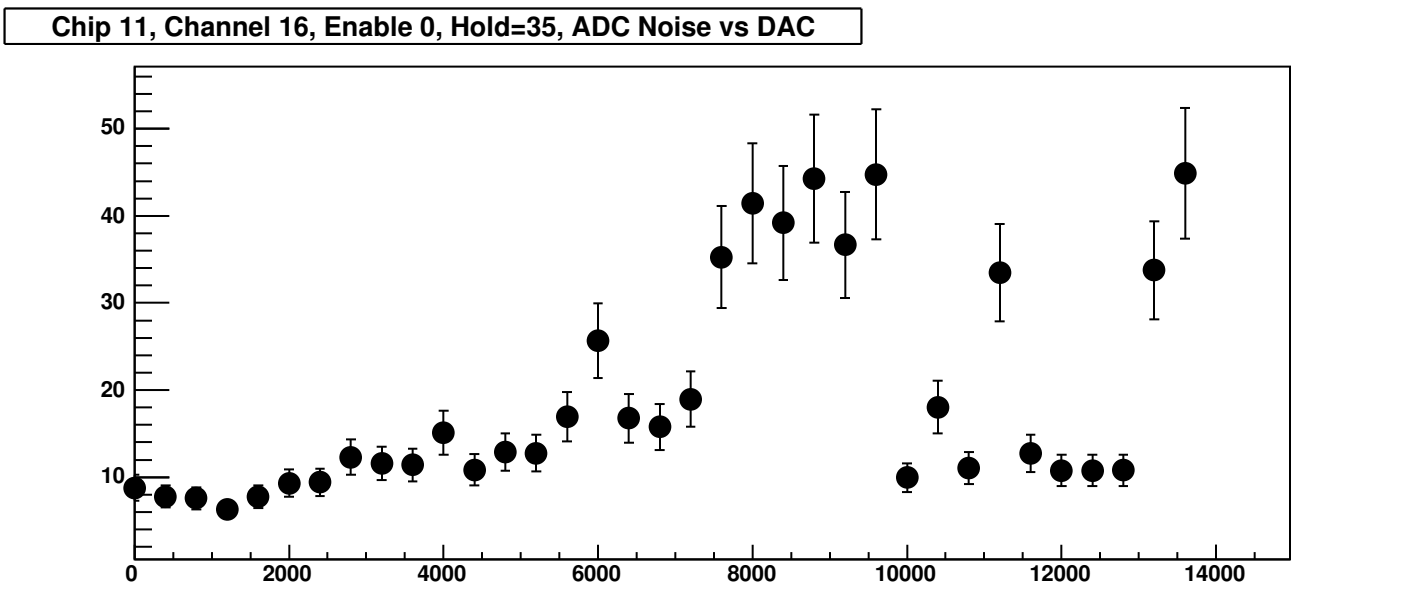

Chip 11, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

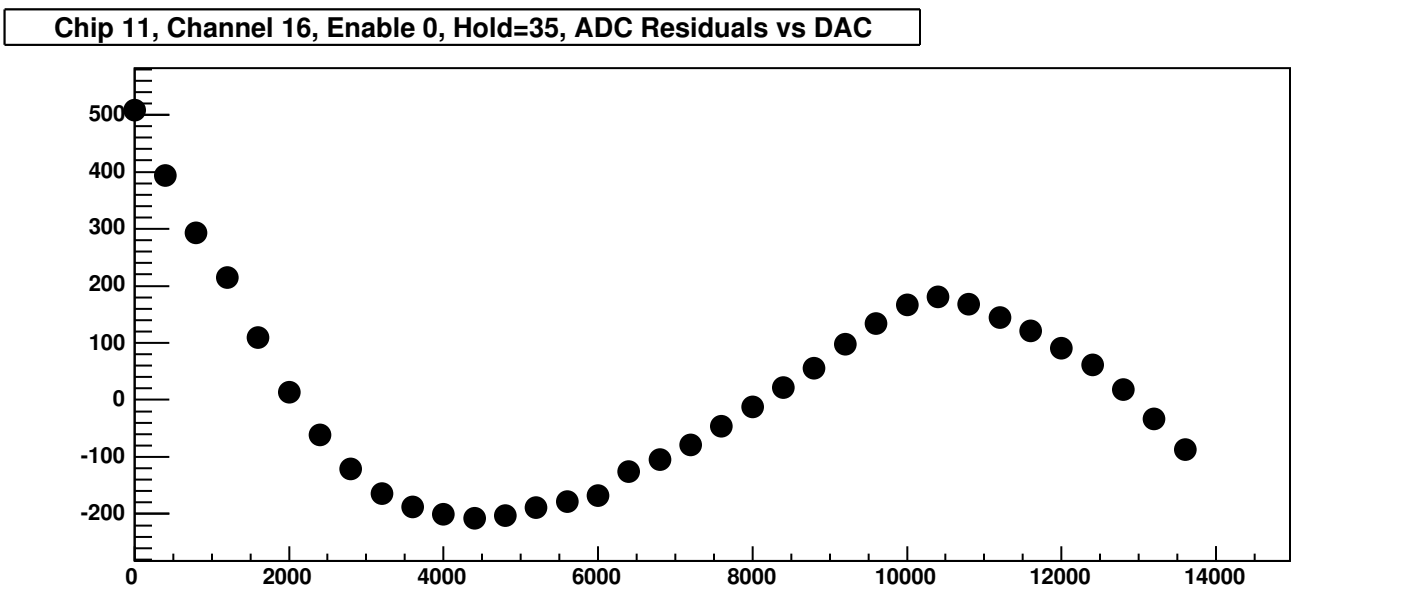

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2206 / 23
p0 -1022 ± 1.062
p1 2.341 ± 0.0002014
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

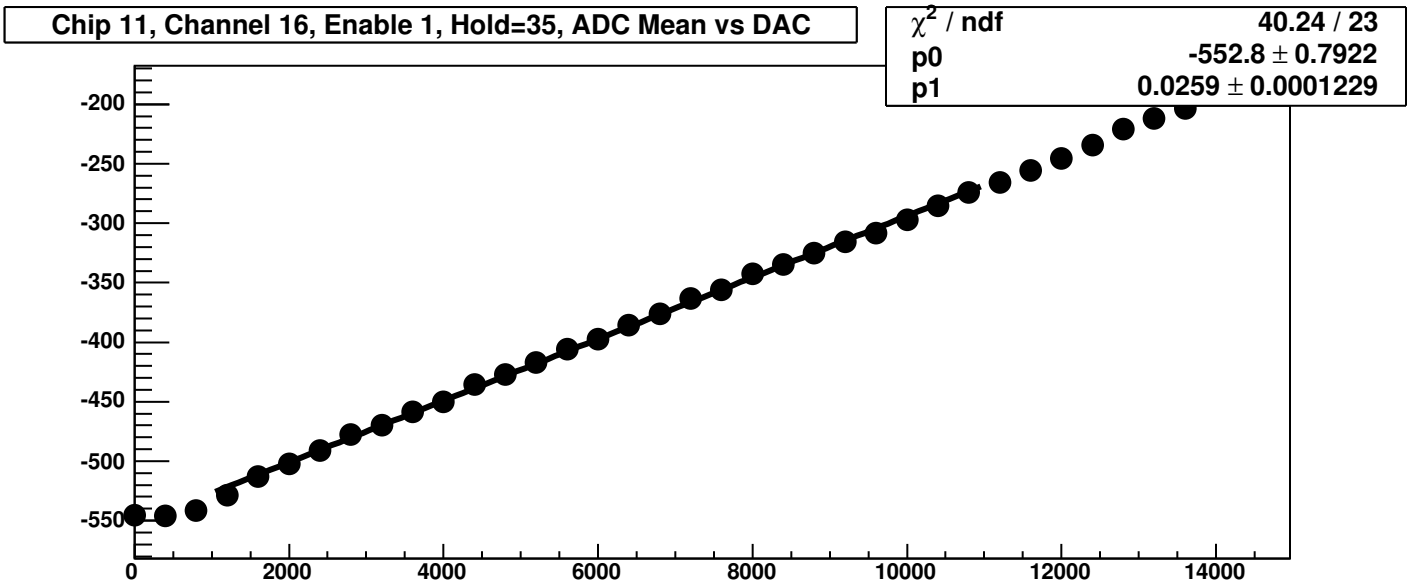

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

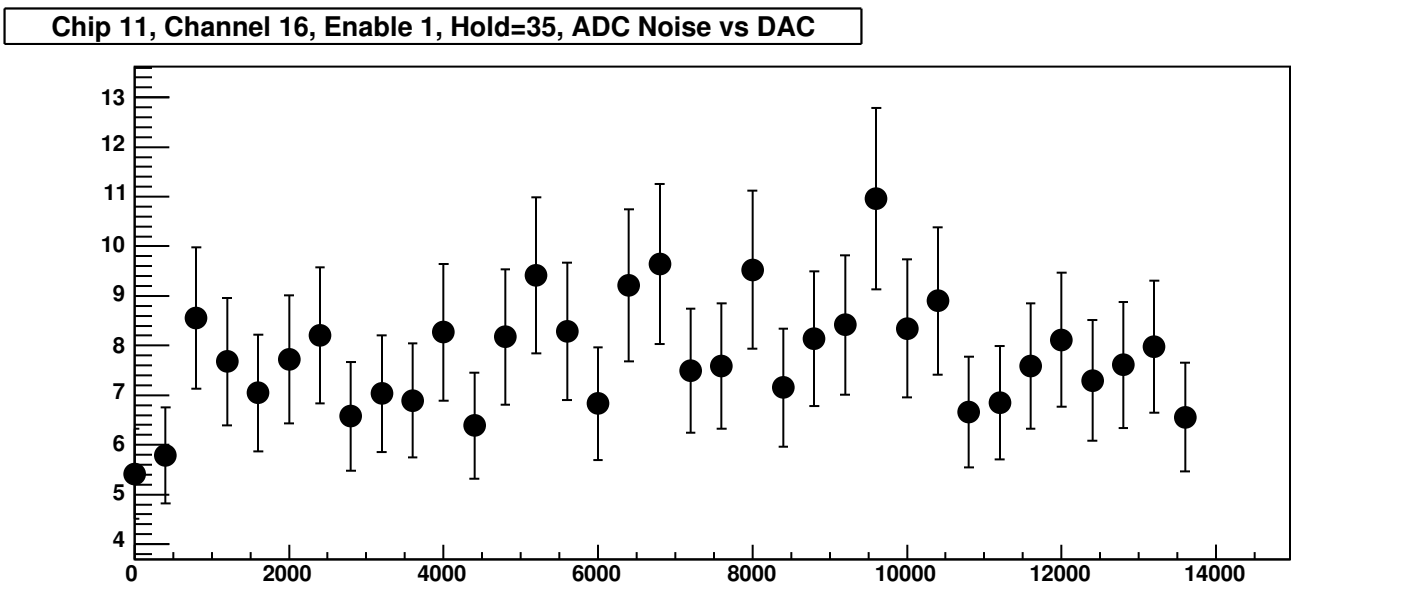

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

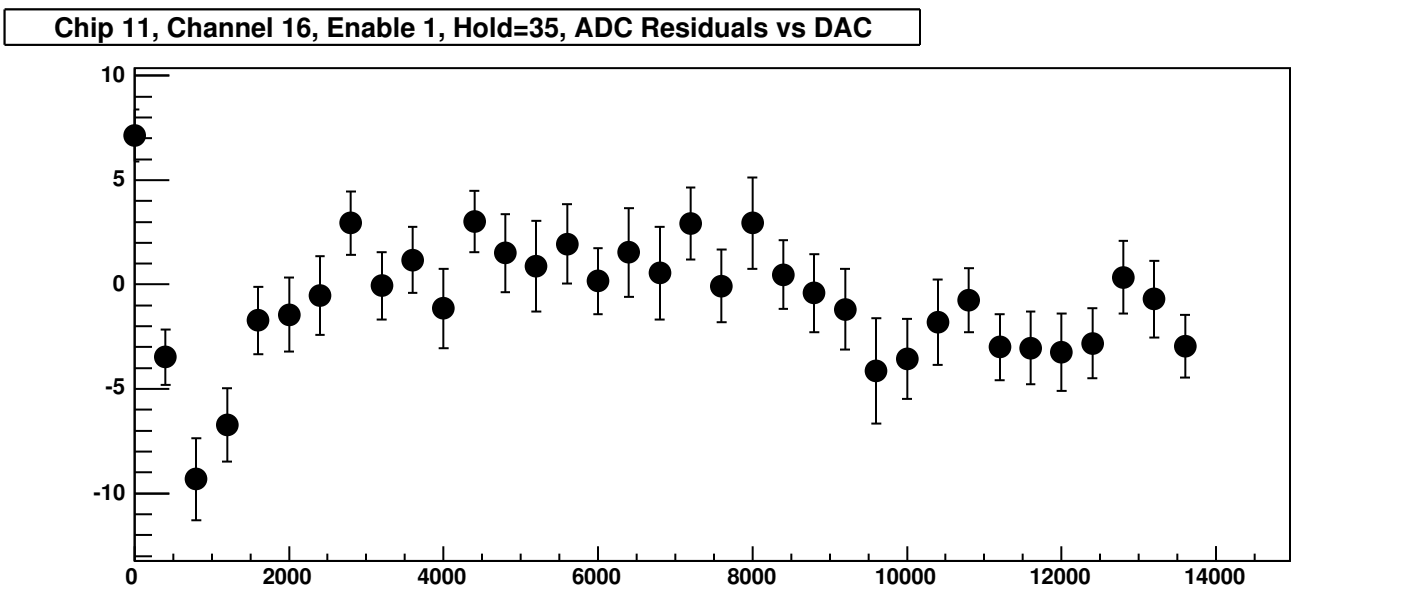

Chip 11, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.39e+04 / 23
p0 -998.5 ± 1.004
p1 0.291 ± 0.0001915
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

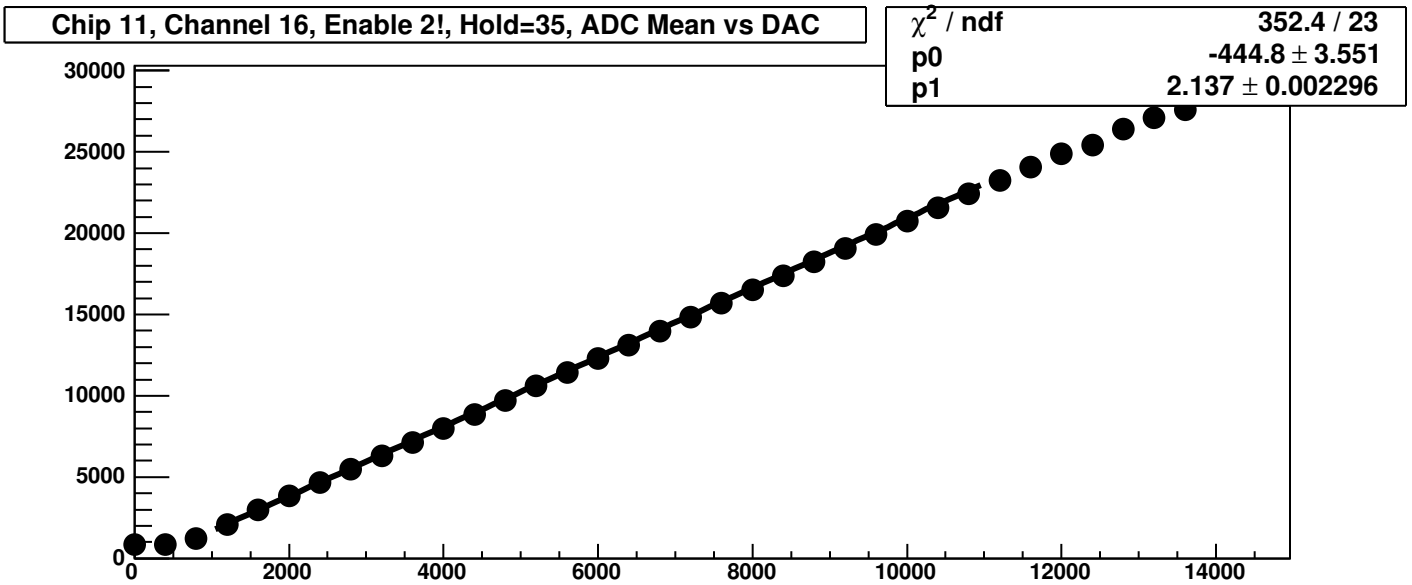

Chip 11, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

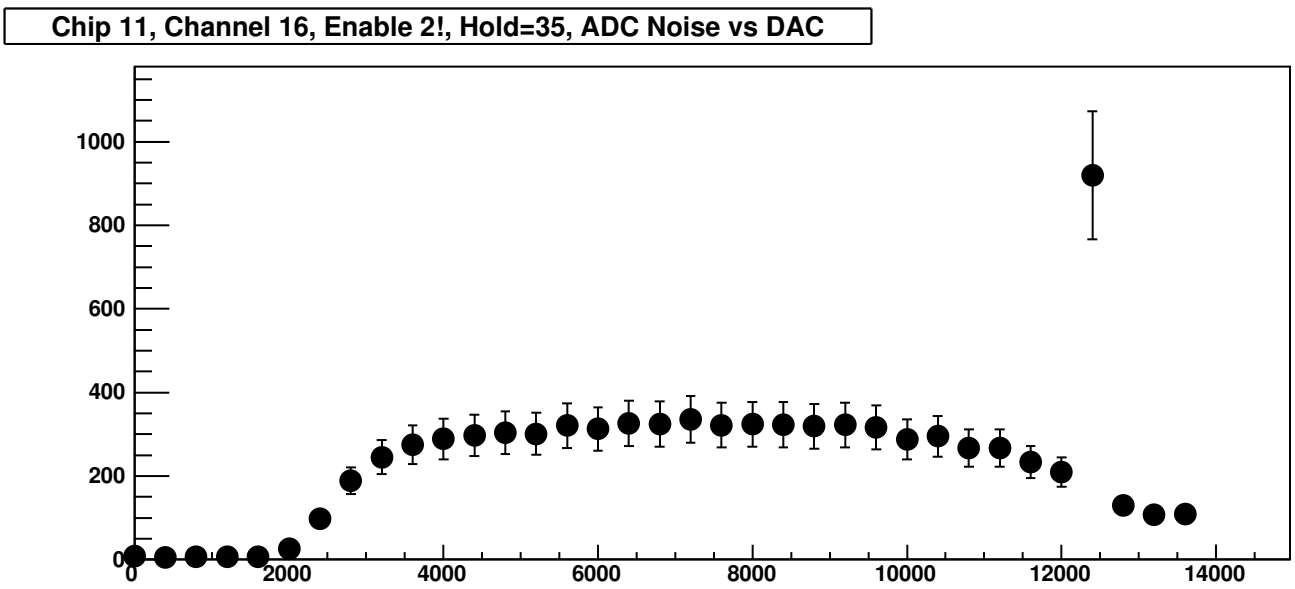

Chip 11, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

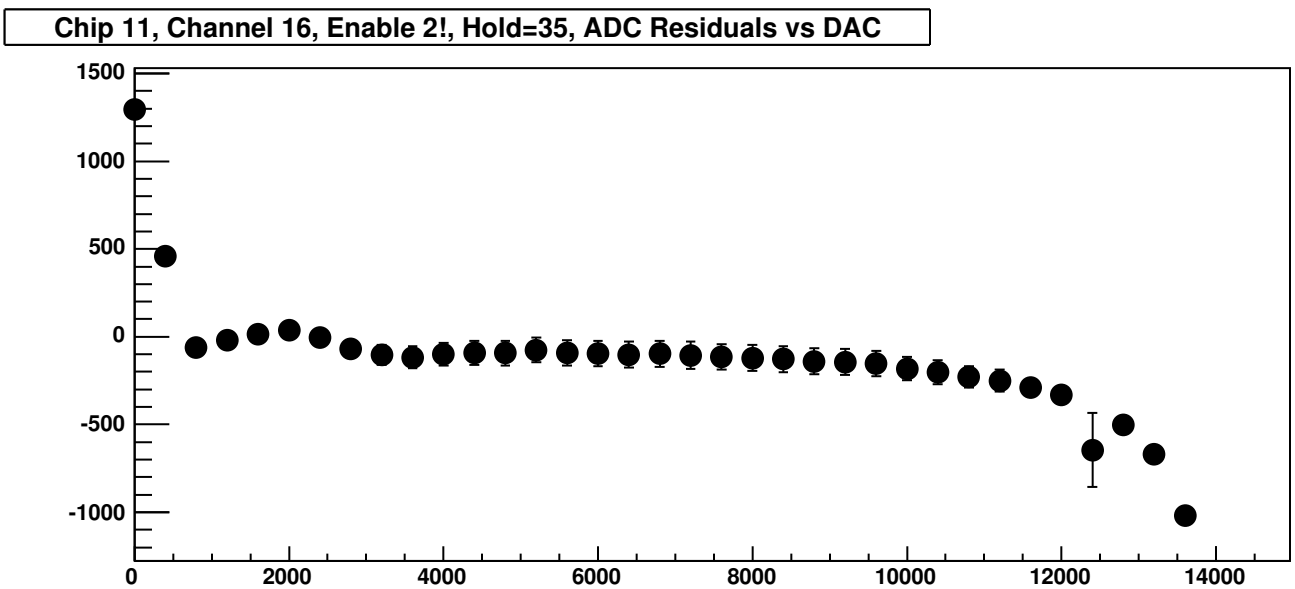

Chip 11, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.24 / 23
p0 -552.8 ± 0.7922
p1 0.0259 ± 0.0001229
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

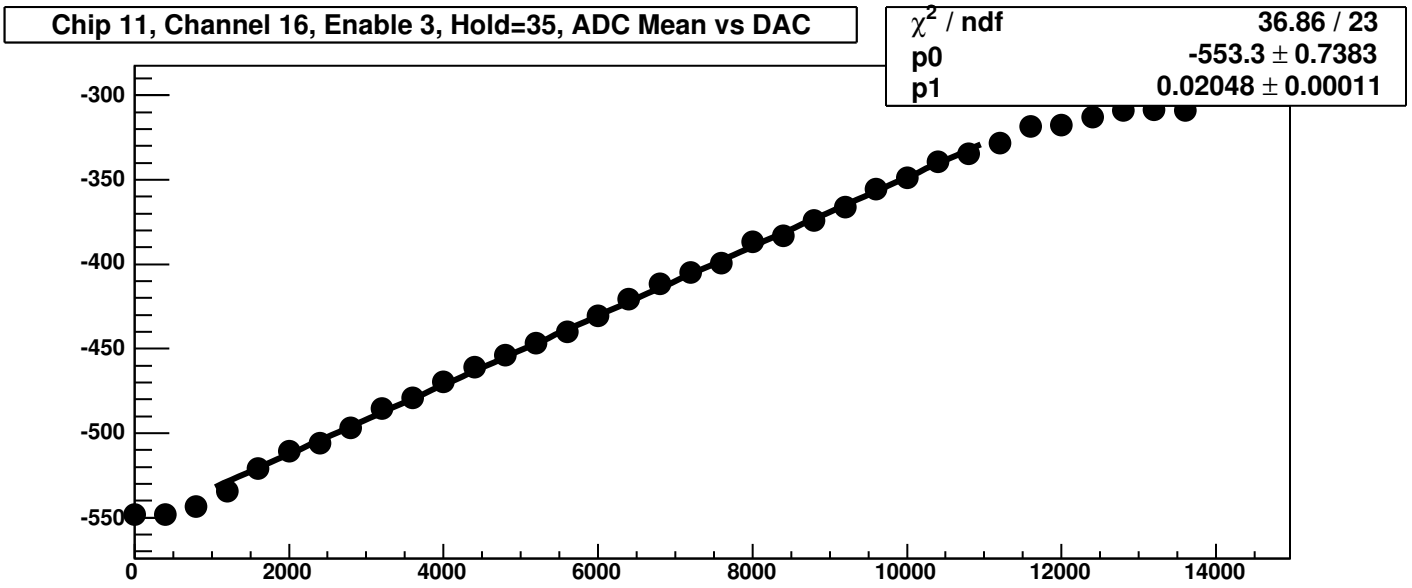

Chip 11, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

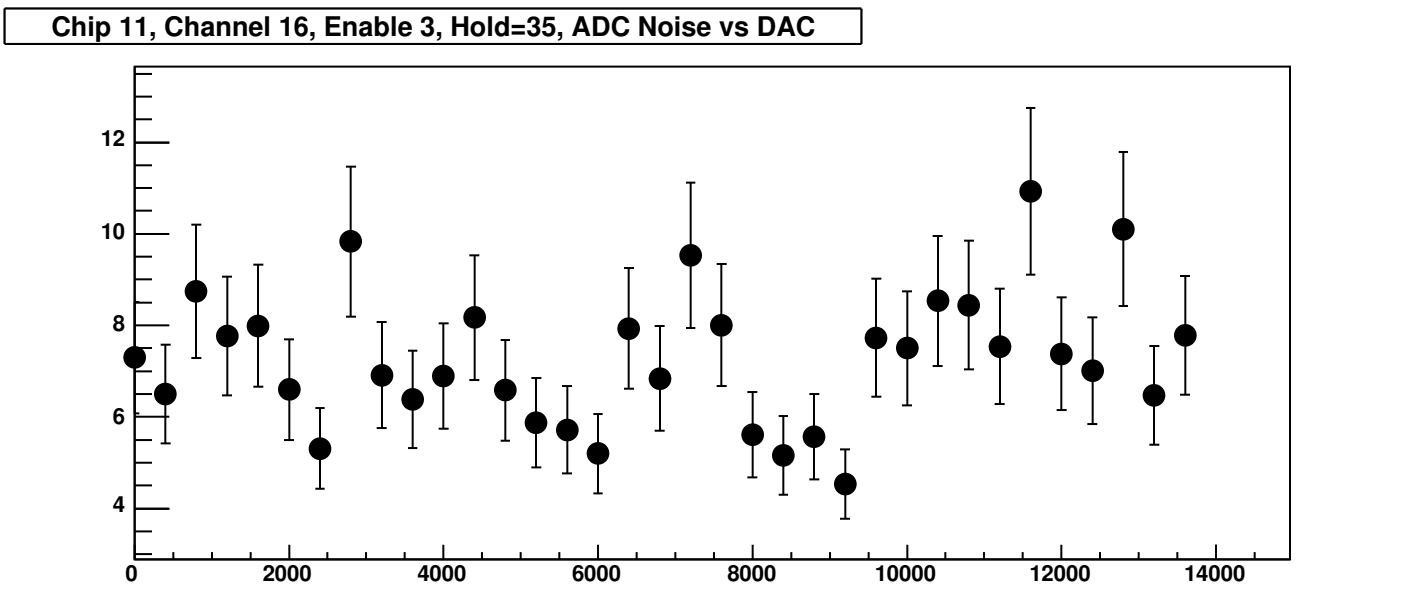

Chip 11, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

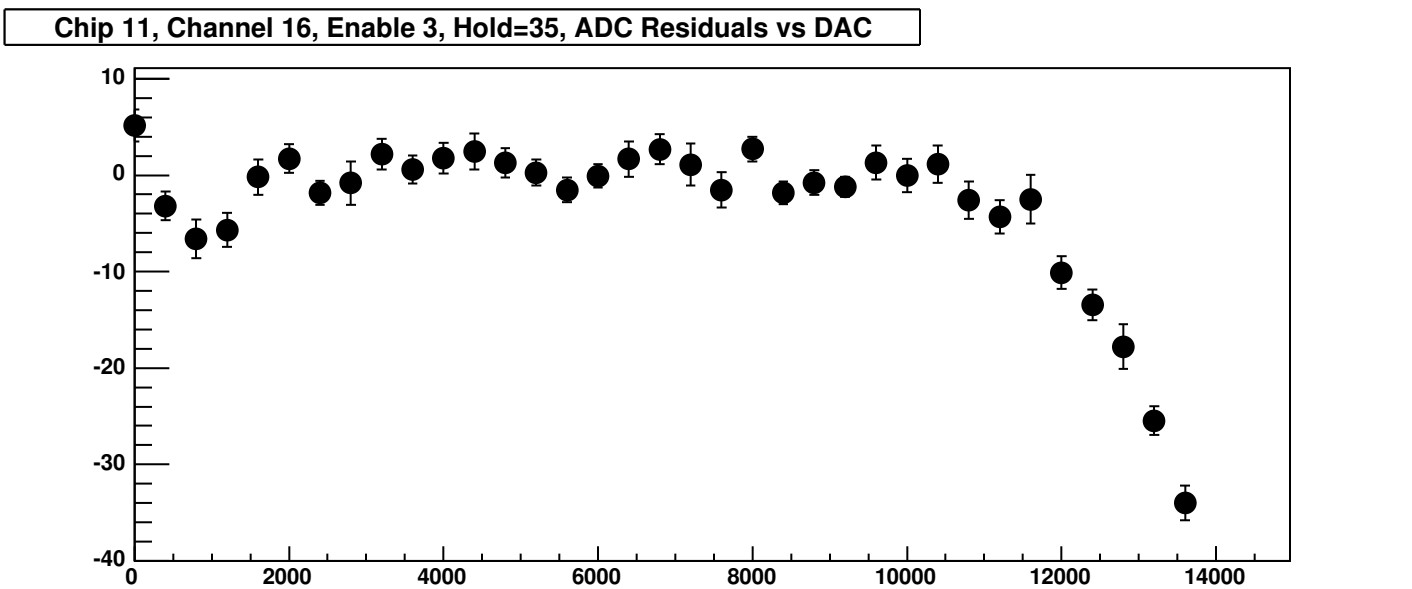

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 352.4 / 23
p0 -444.8 ± 3.551
p1 2.137 ± 0.002296
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

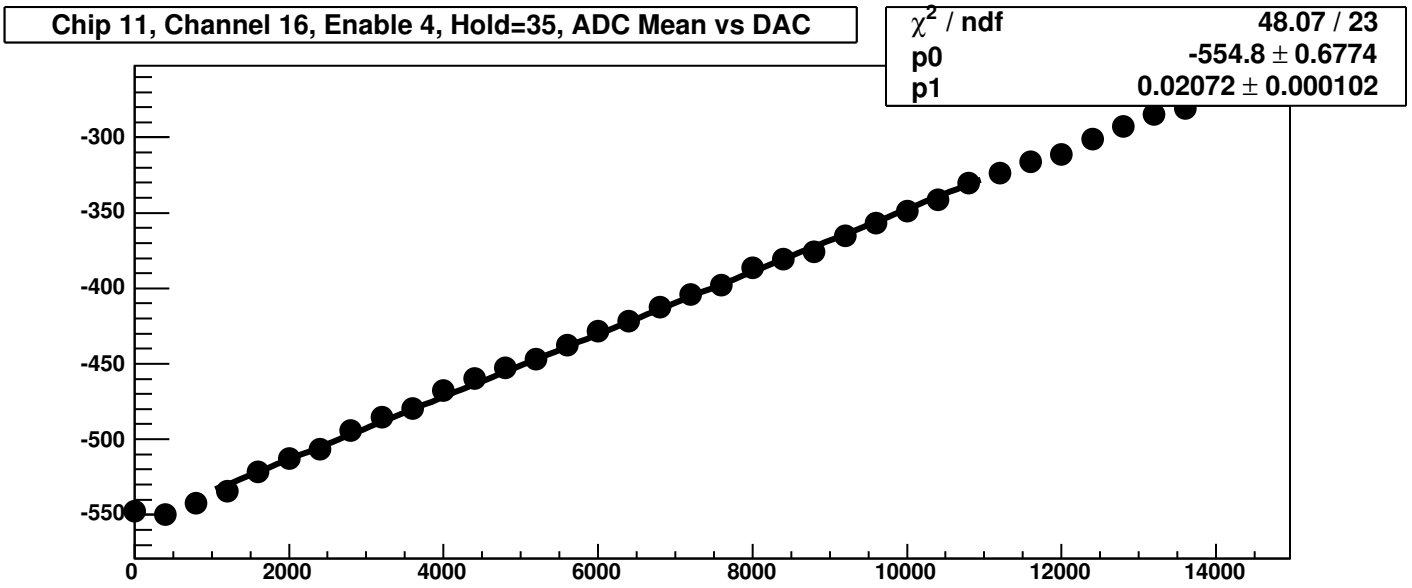

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

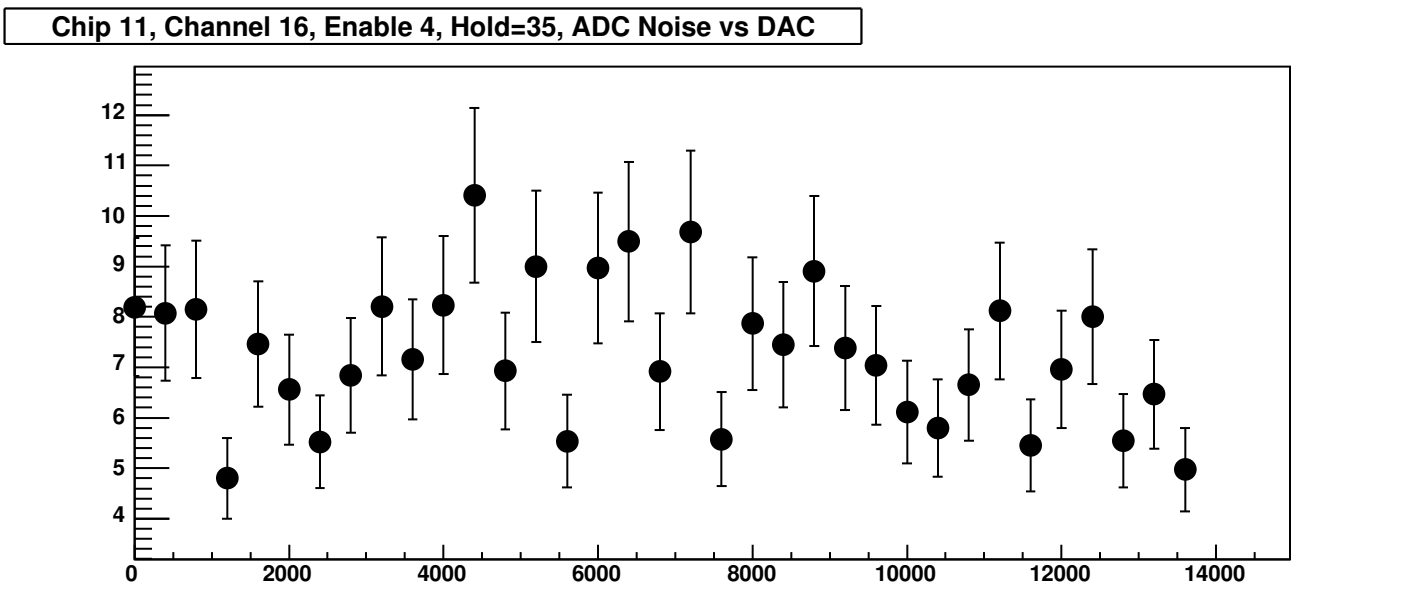

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

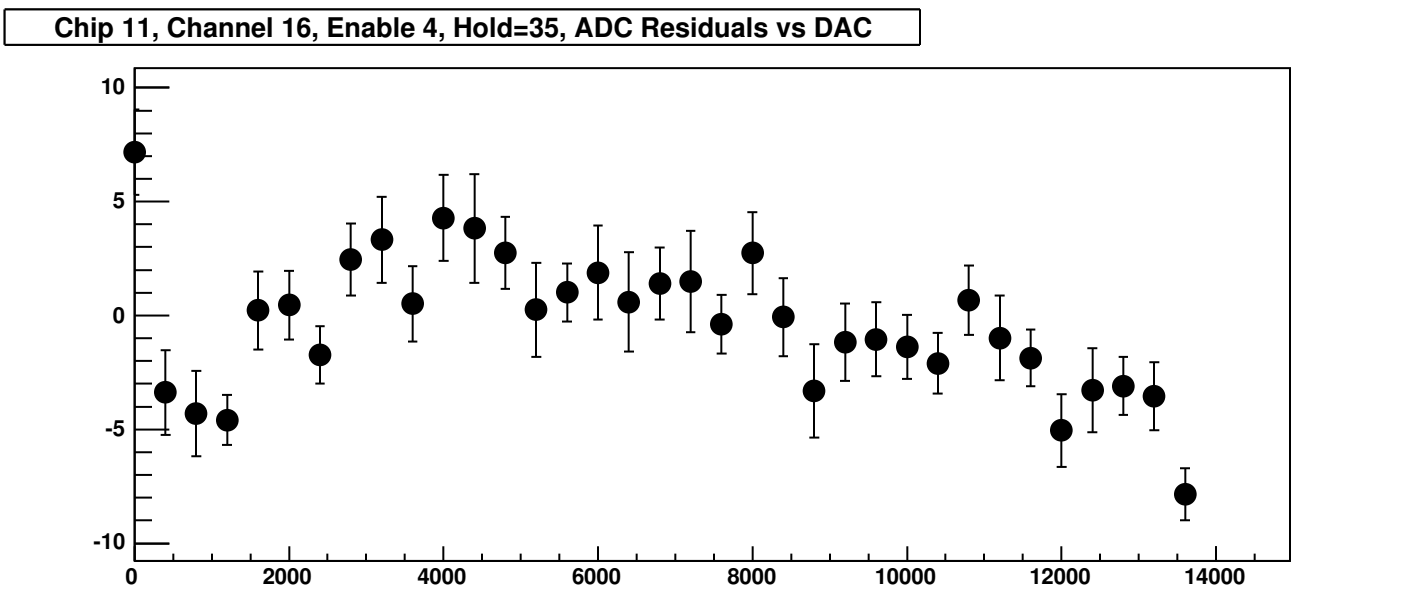

Chip 11, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.86 / 23
p0 -553.3 ± 0.7383
p1 0.02048 ± 0.00011
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

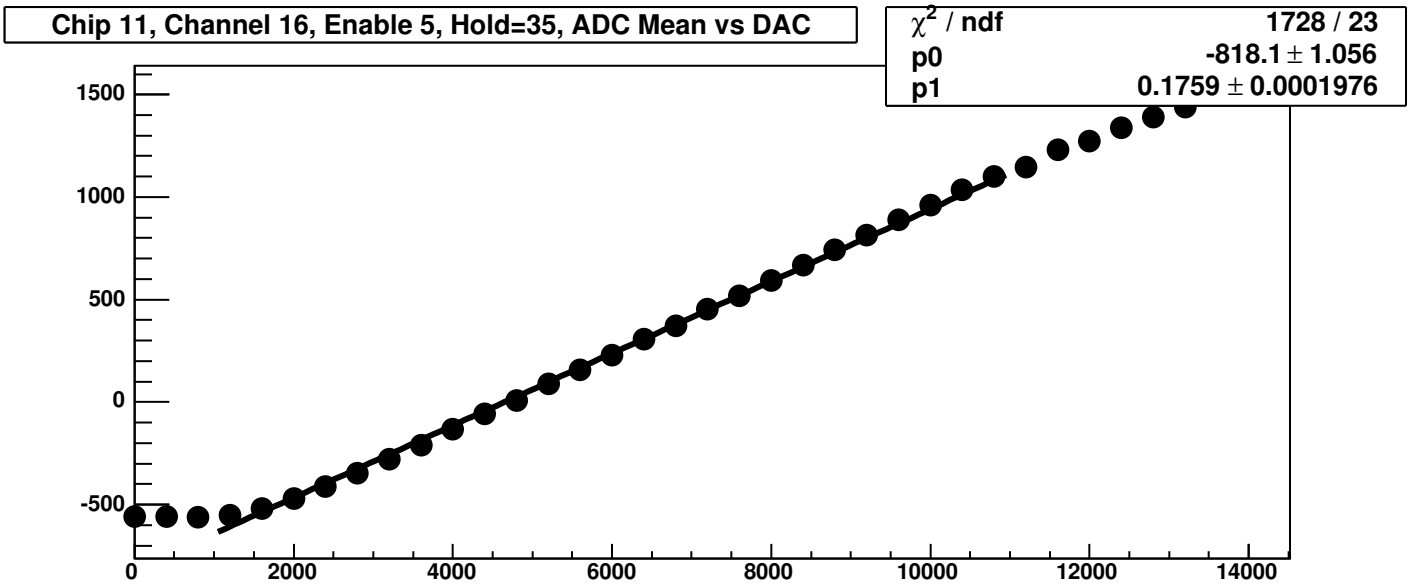

Chip 11, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

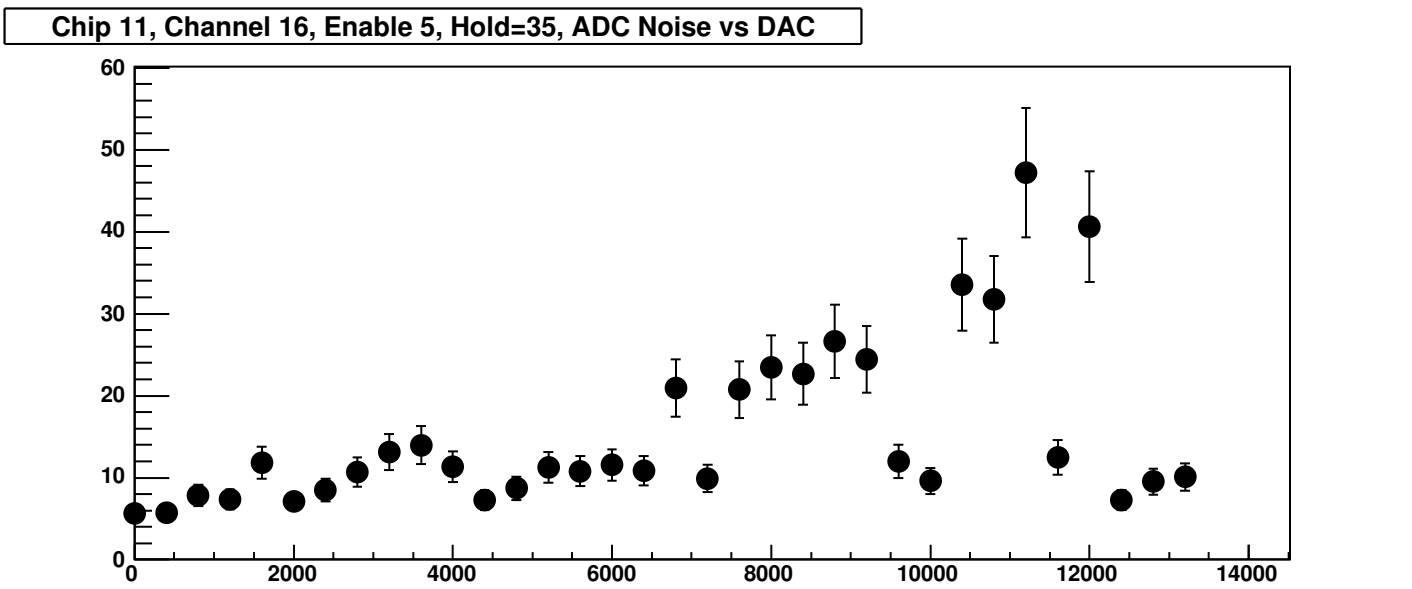

Chip 11, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

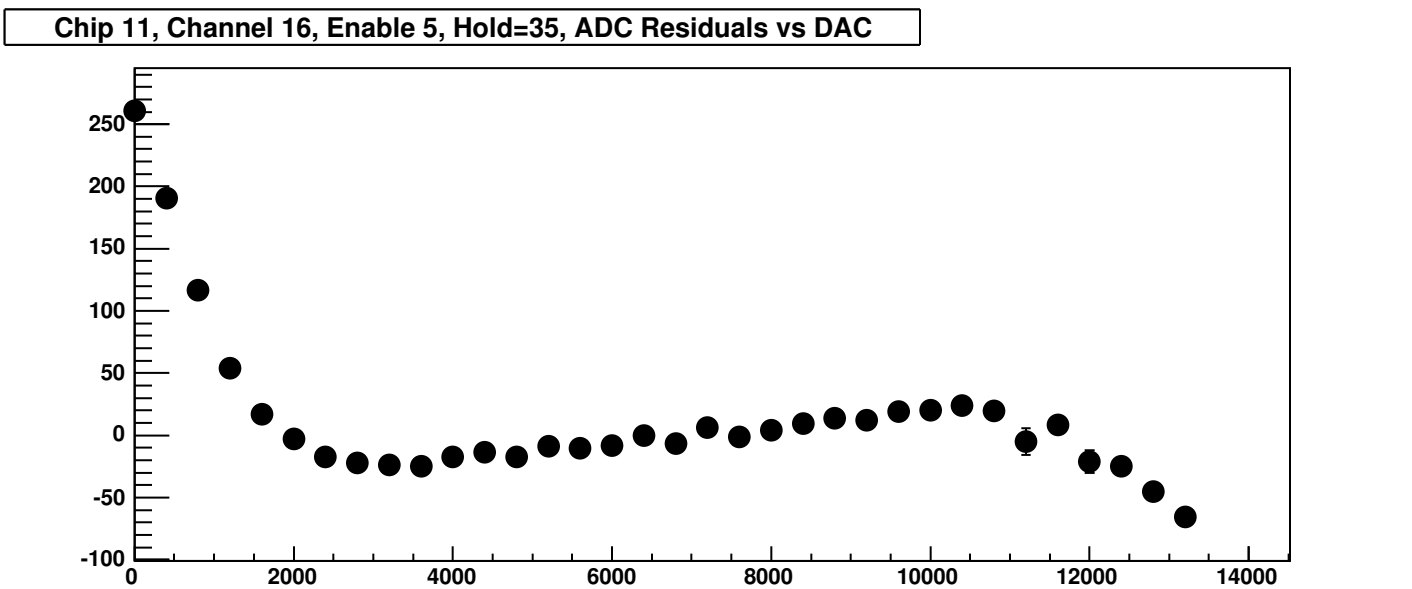

Chip 11, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.07 / 23
p0 -554.8 ± 0.6774
p1 0.02072 ± 0.000102
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

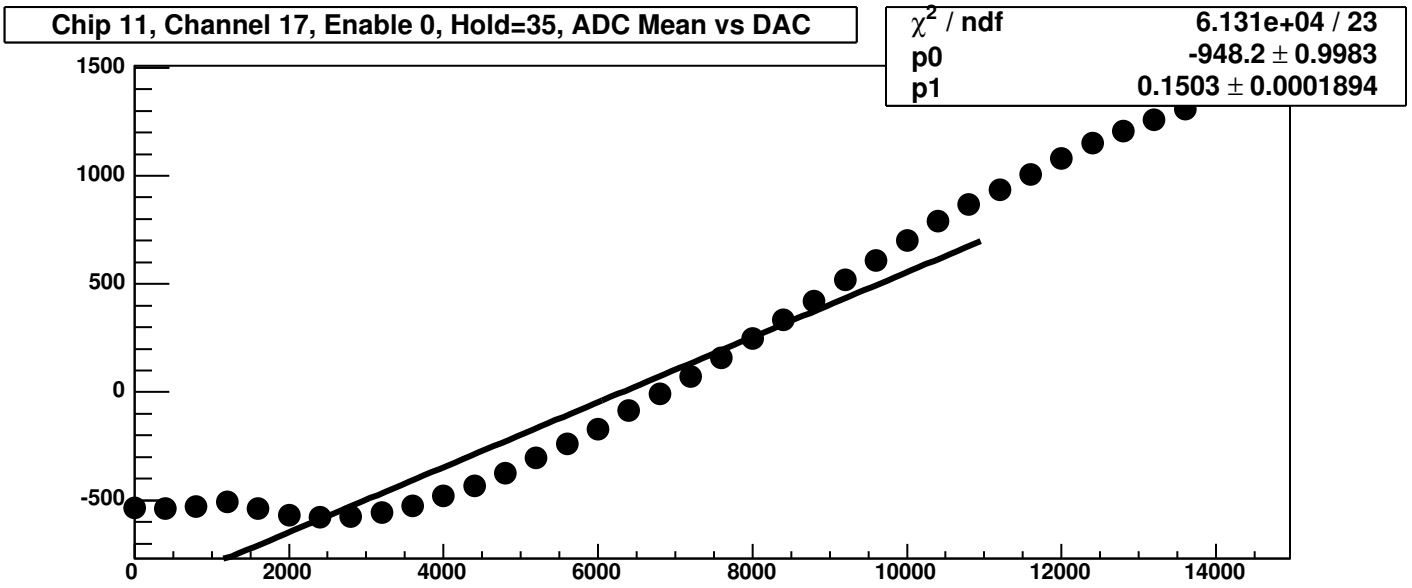

Chip 11, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

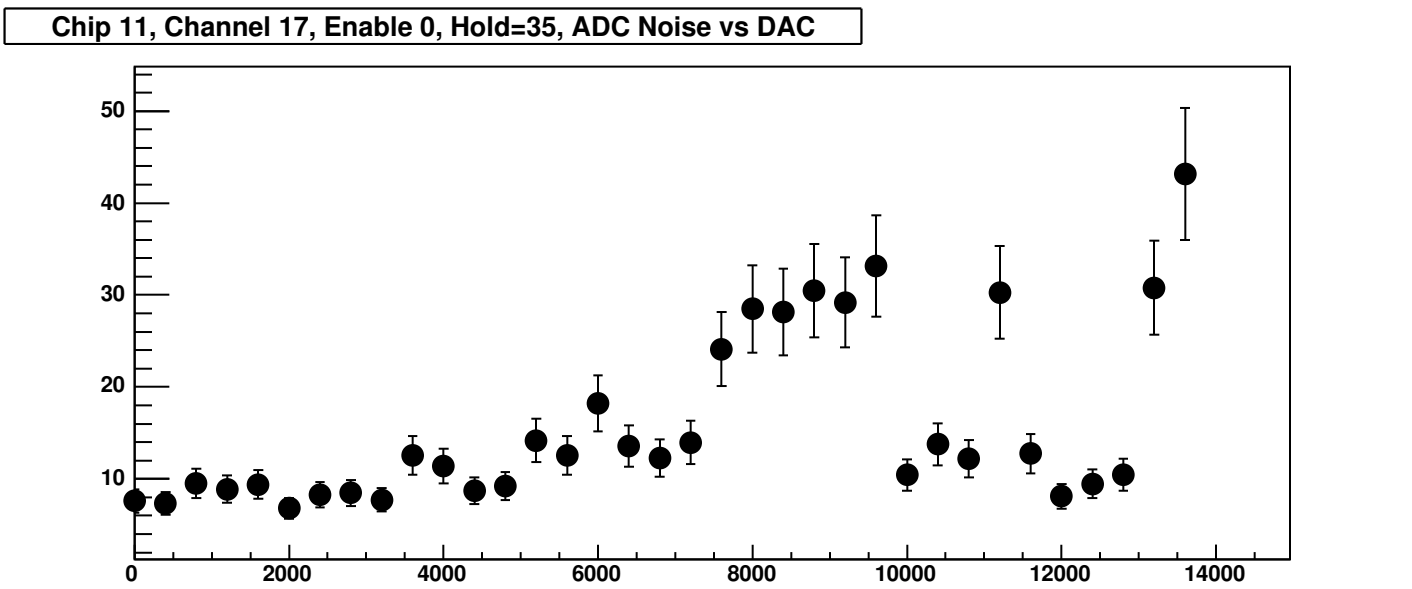

Chip 11, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

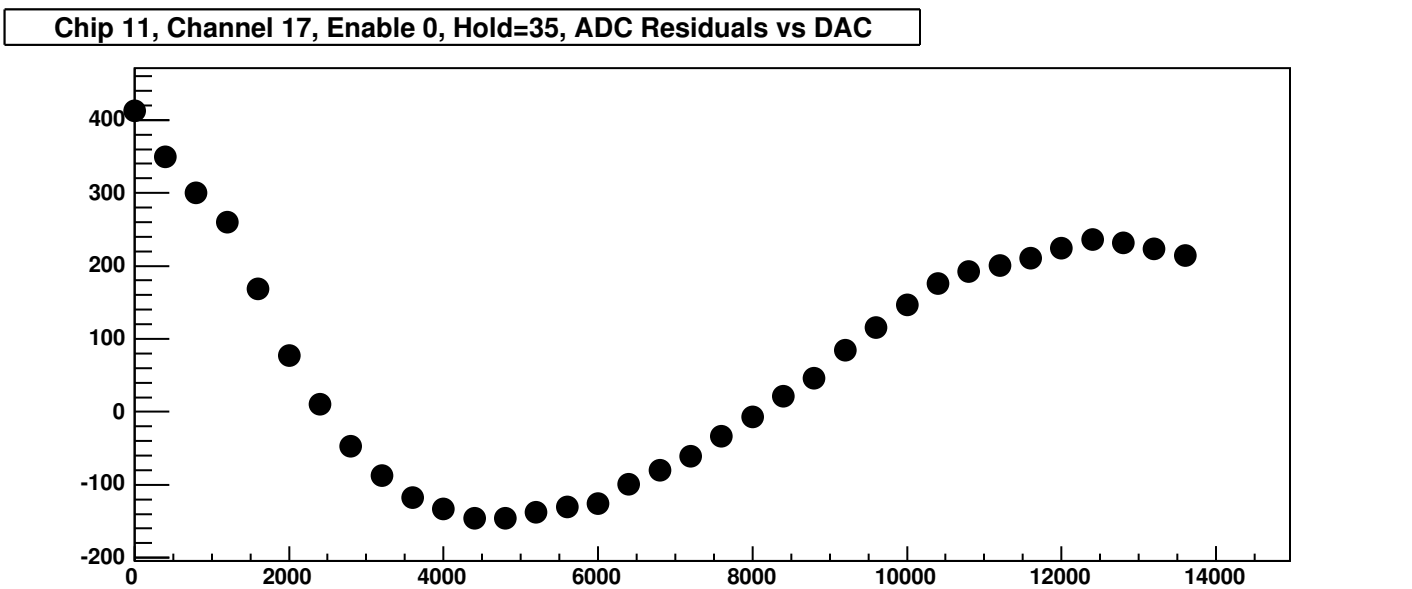

Chip 11, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1728 / 23
p0 -818.1 ± 1.056
p1 0.1759 ± 0.0001976
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

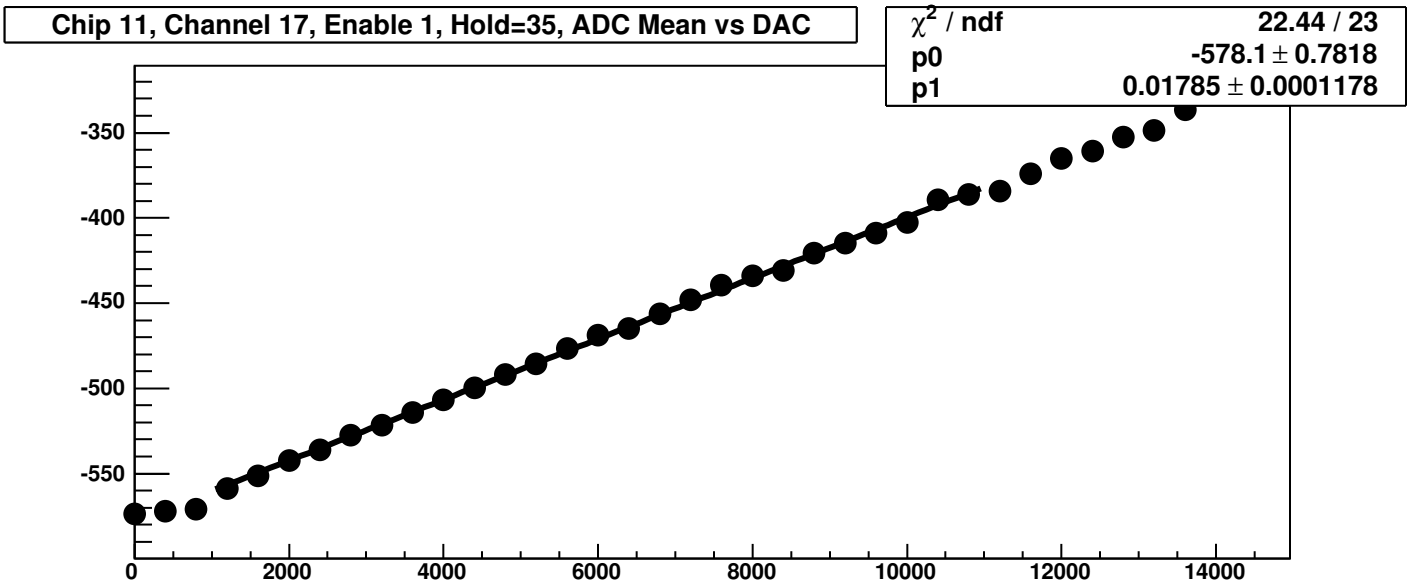

Chip 11, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

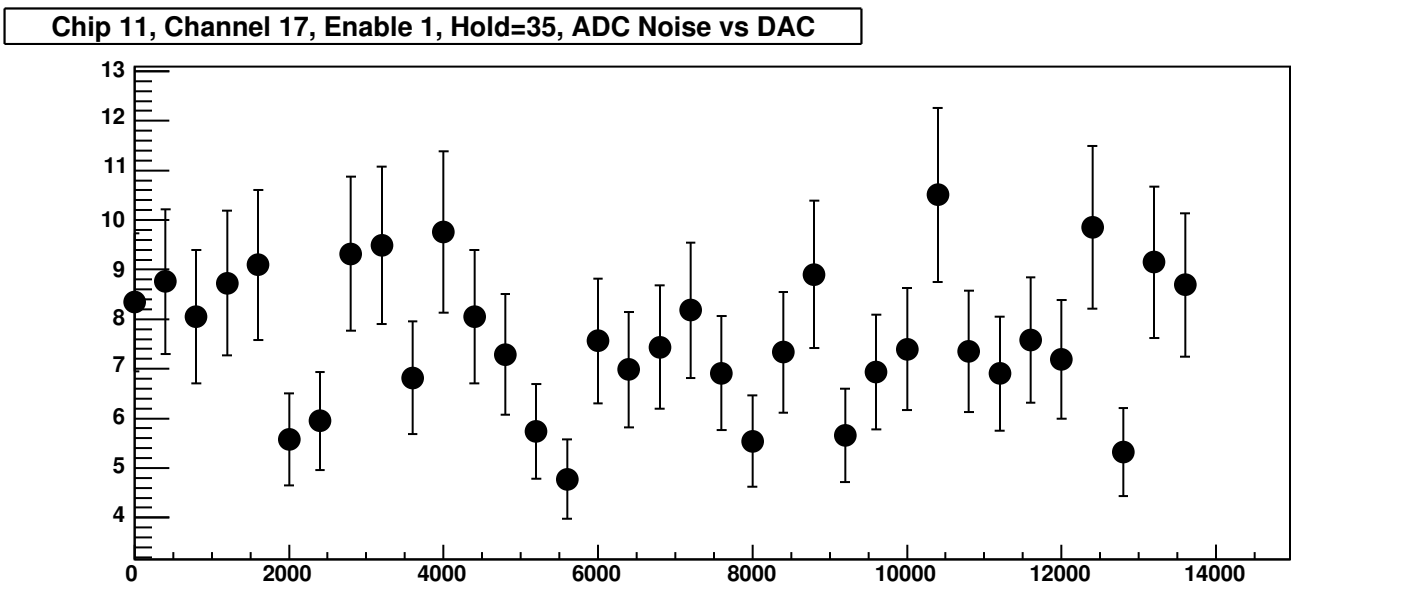

Chip 11, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

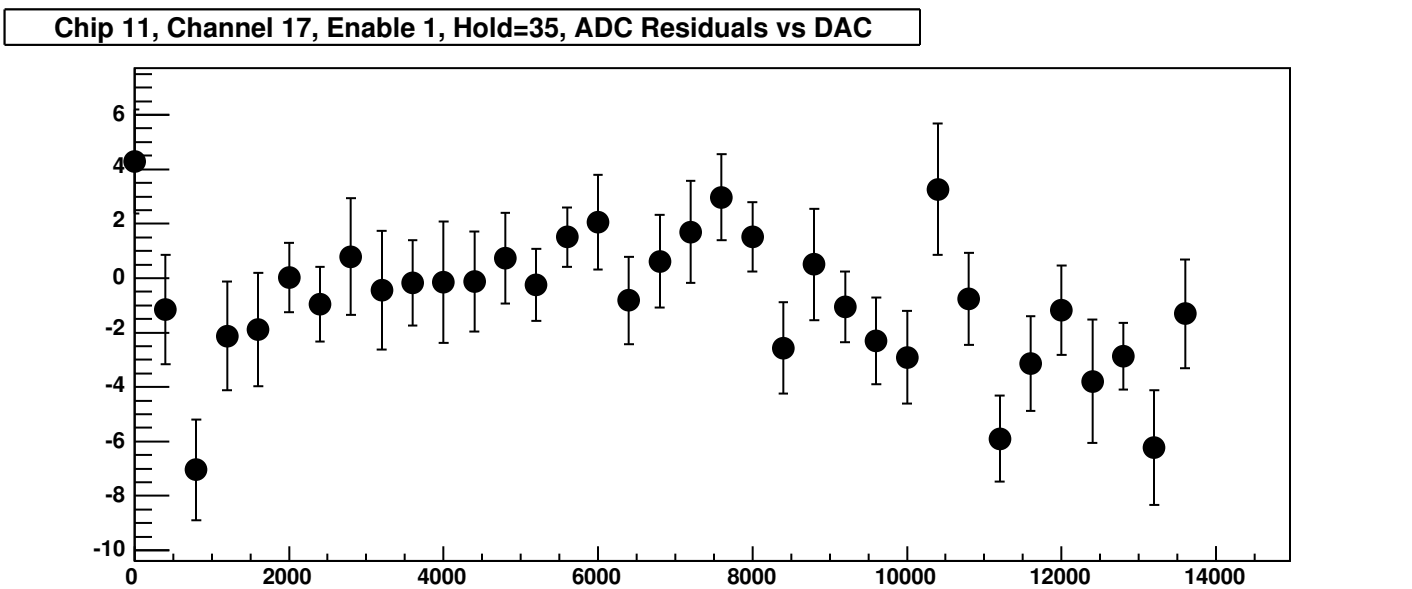

Chip 11, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.131e+04 / 23
p0 -948.2 ± 0.9983
p1 0.1503 ± 0.0001894
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

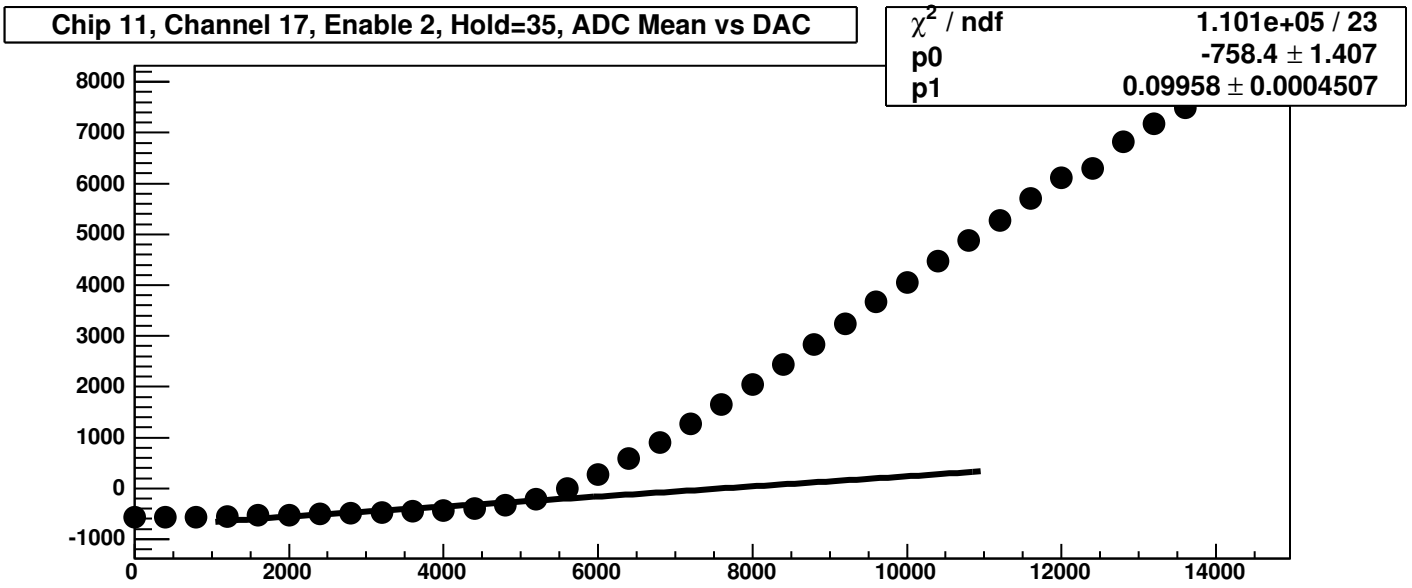

Chip 11, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

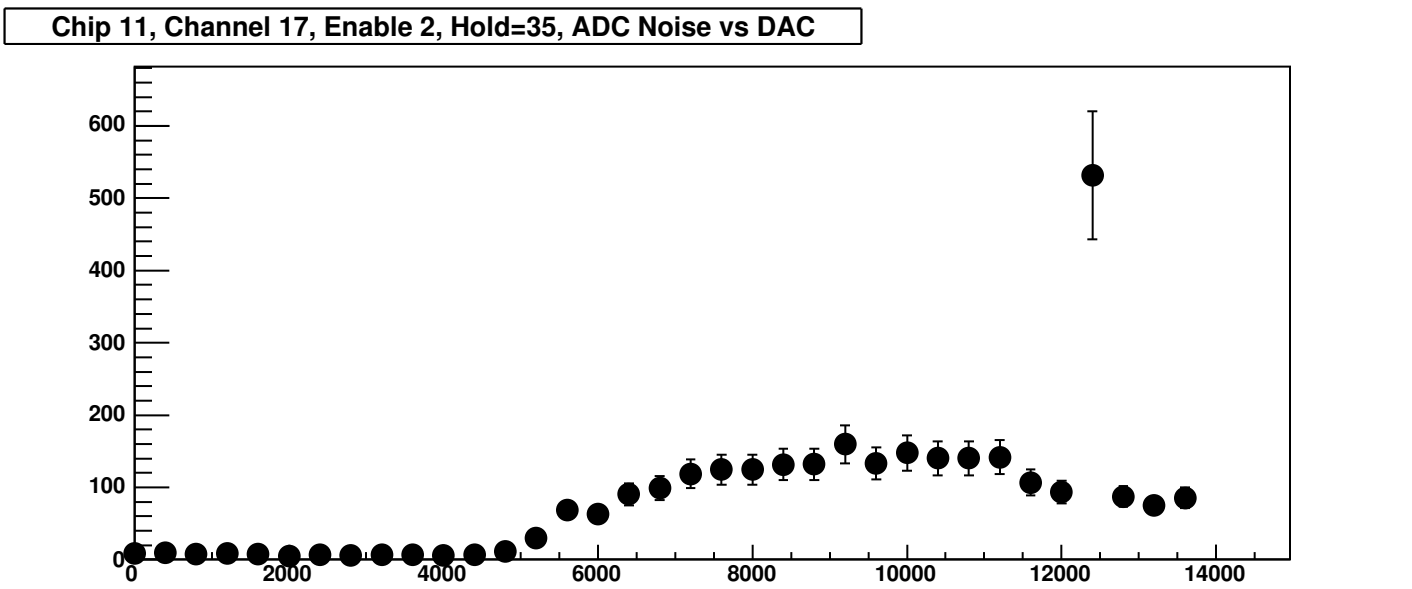

Chip 11, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

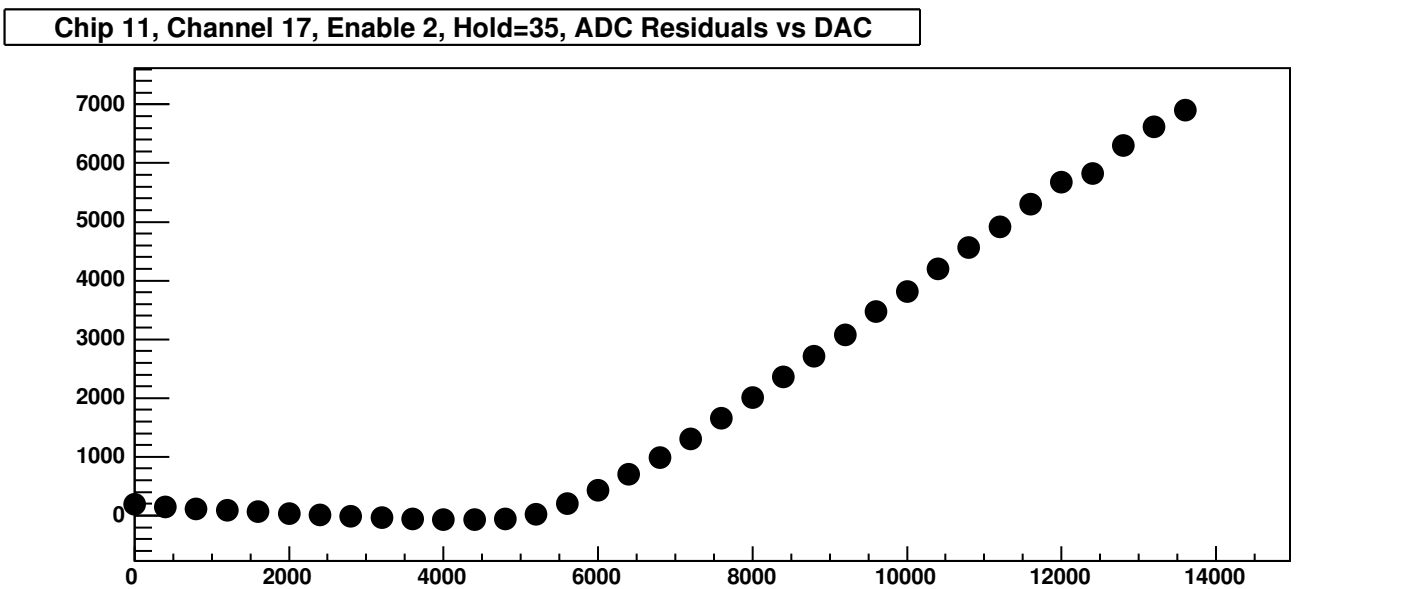

Chip 11, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.44 / 23
p0 -578.1 ± 0.7818
p1 0.01785 ± 0.0001178
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

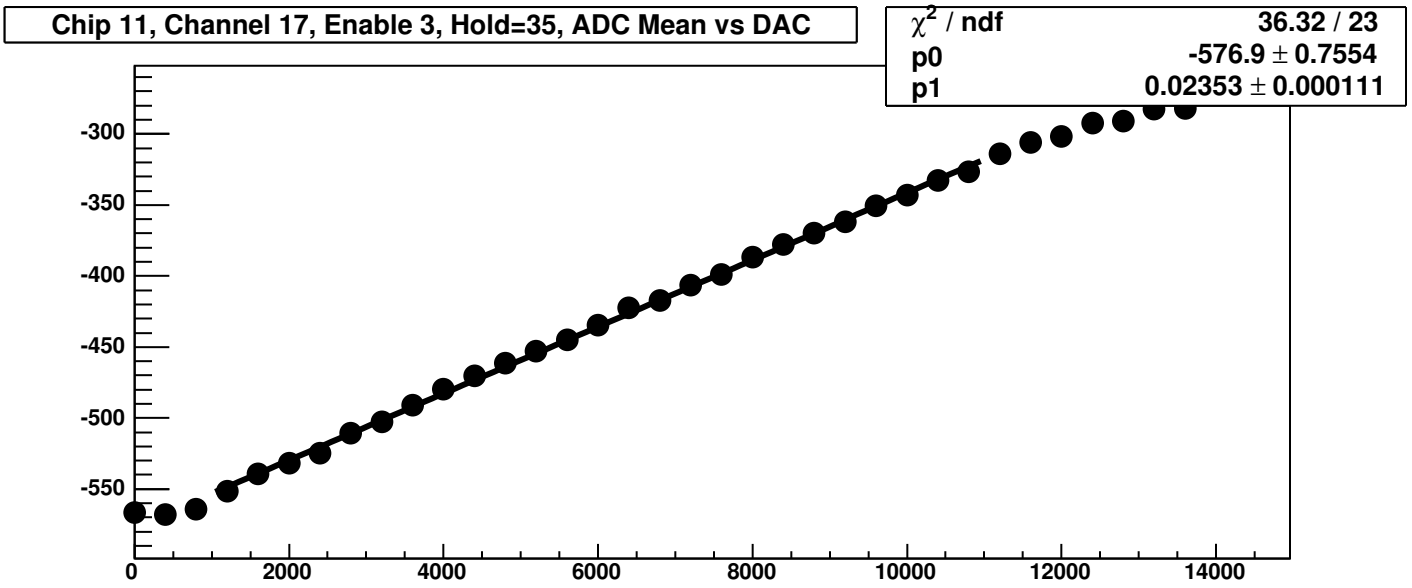

Chip 11, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

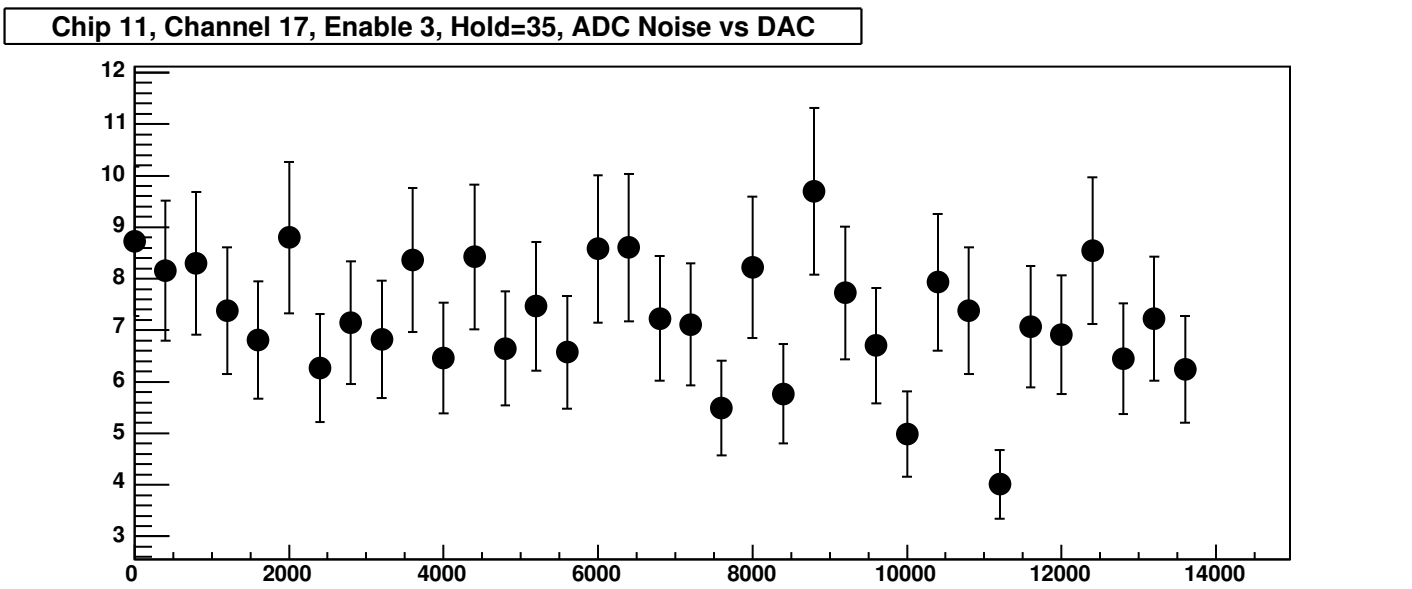

Chip 11, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

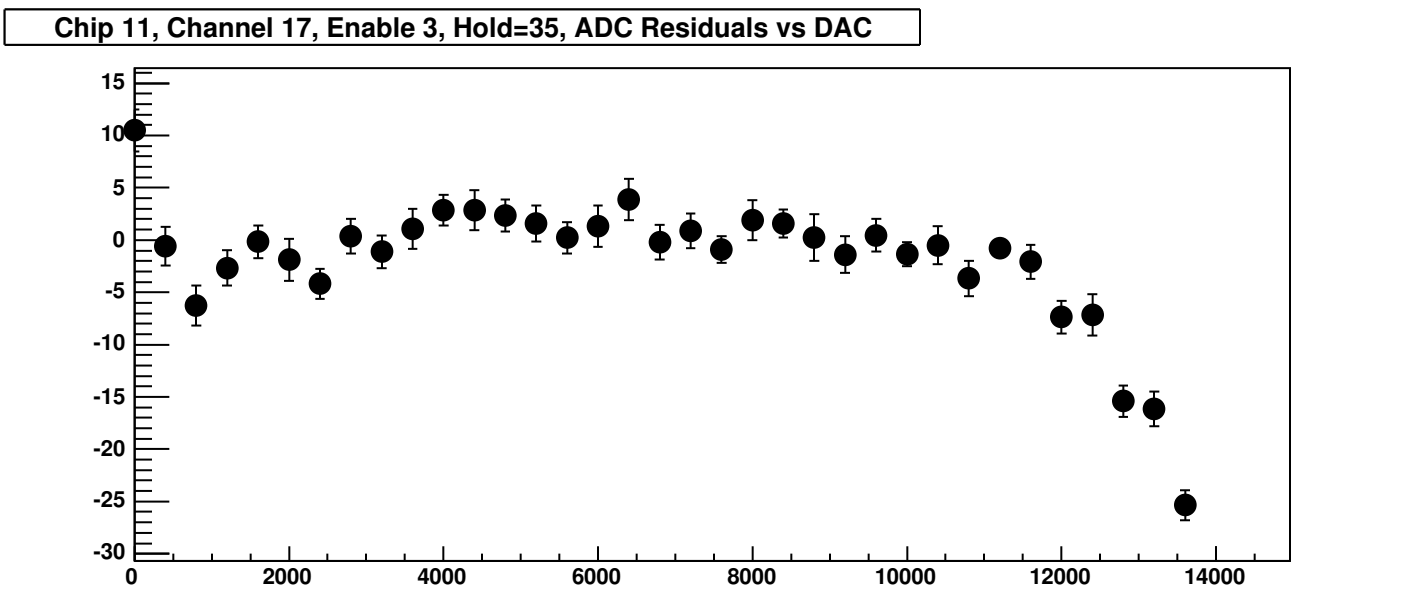

Chip 11, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.101e+05 / 23
p0 -758.4 ± 1.407
p1 0.09958 ± 0.0004507
8000
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

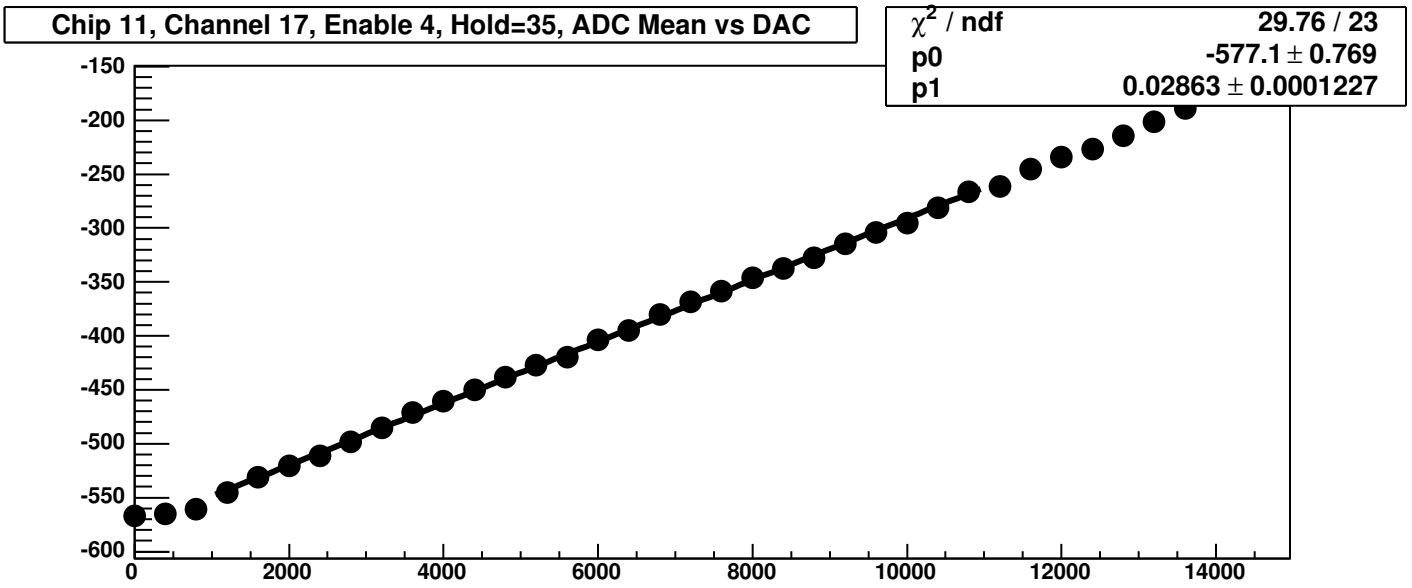

Chip 11, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

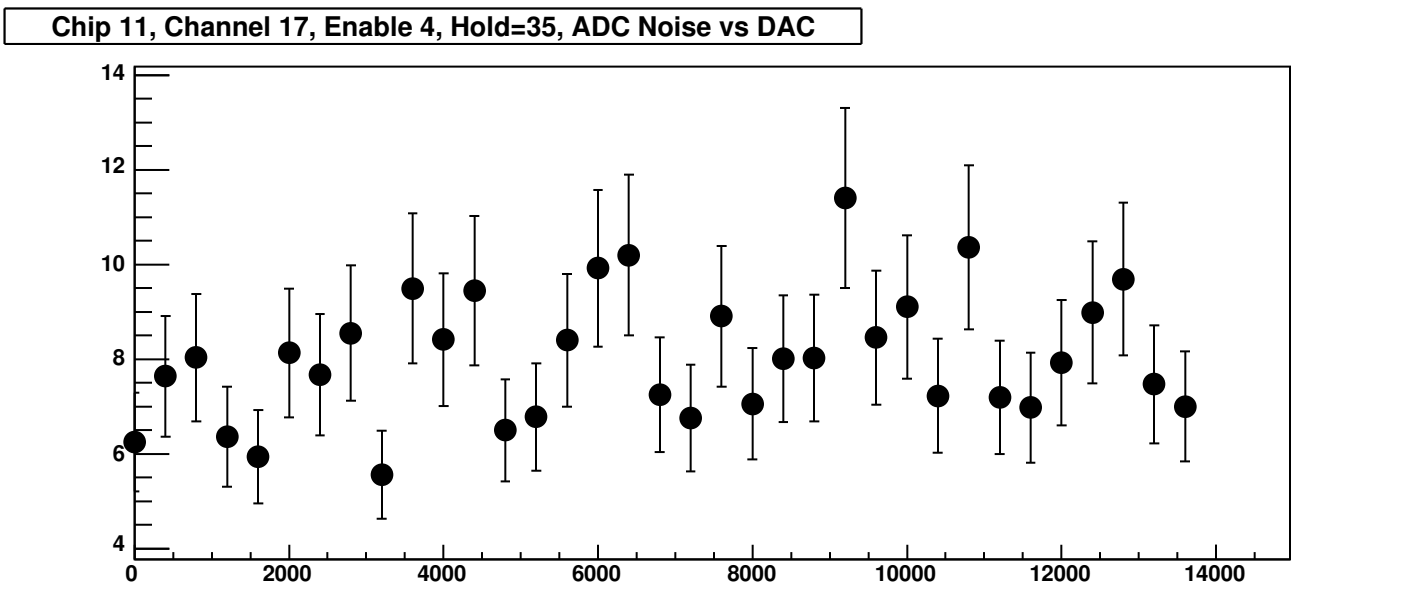

Chip 11, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

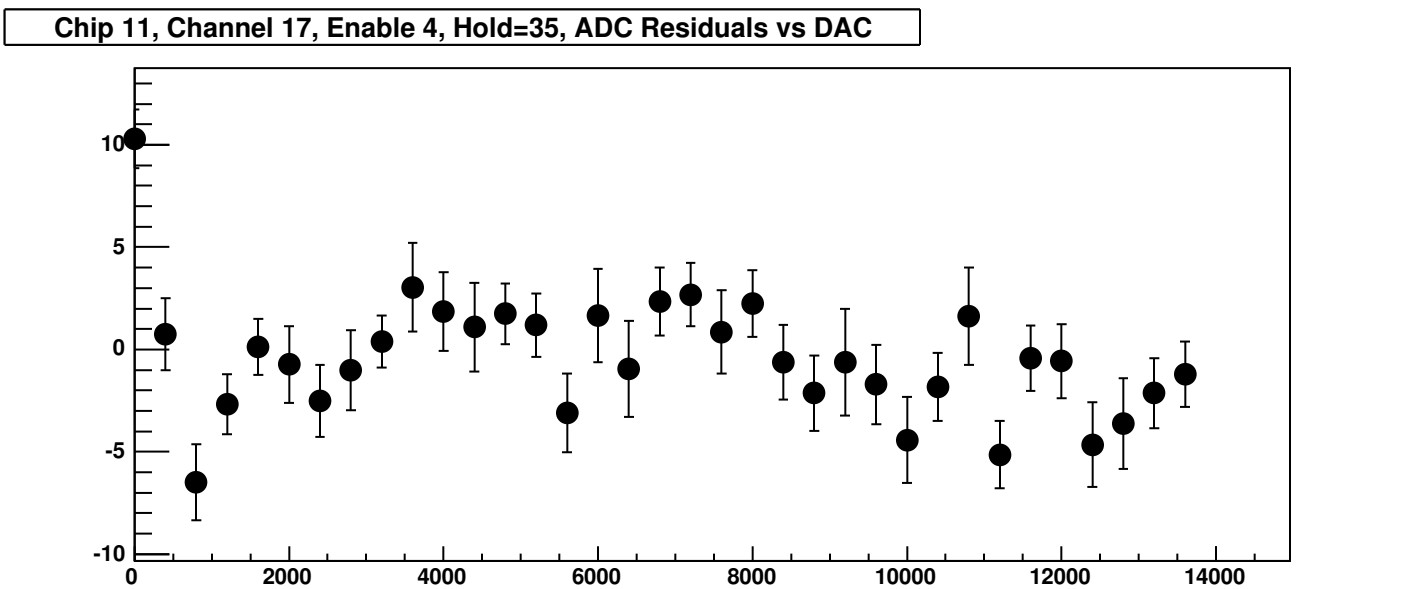

Chip 11, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.32 / 23
p0 -576.9 ± 0.7554
p1 0.02353 ± 0.000111
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.76 / 23
p0 -577.1 ± 0.769
p1 0.02863 ± 0.0001227
-150
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000