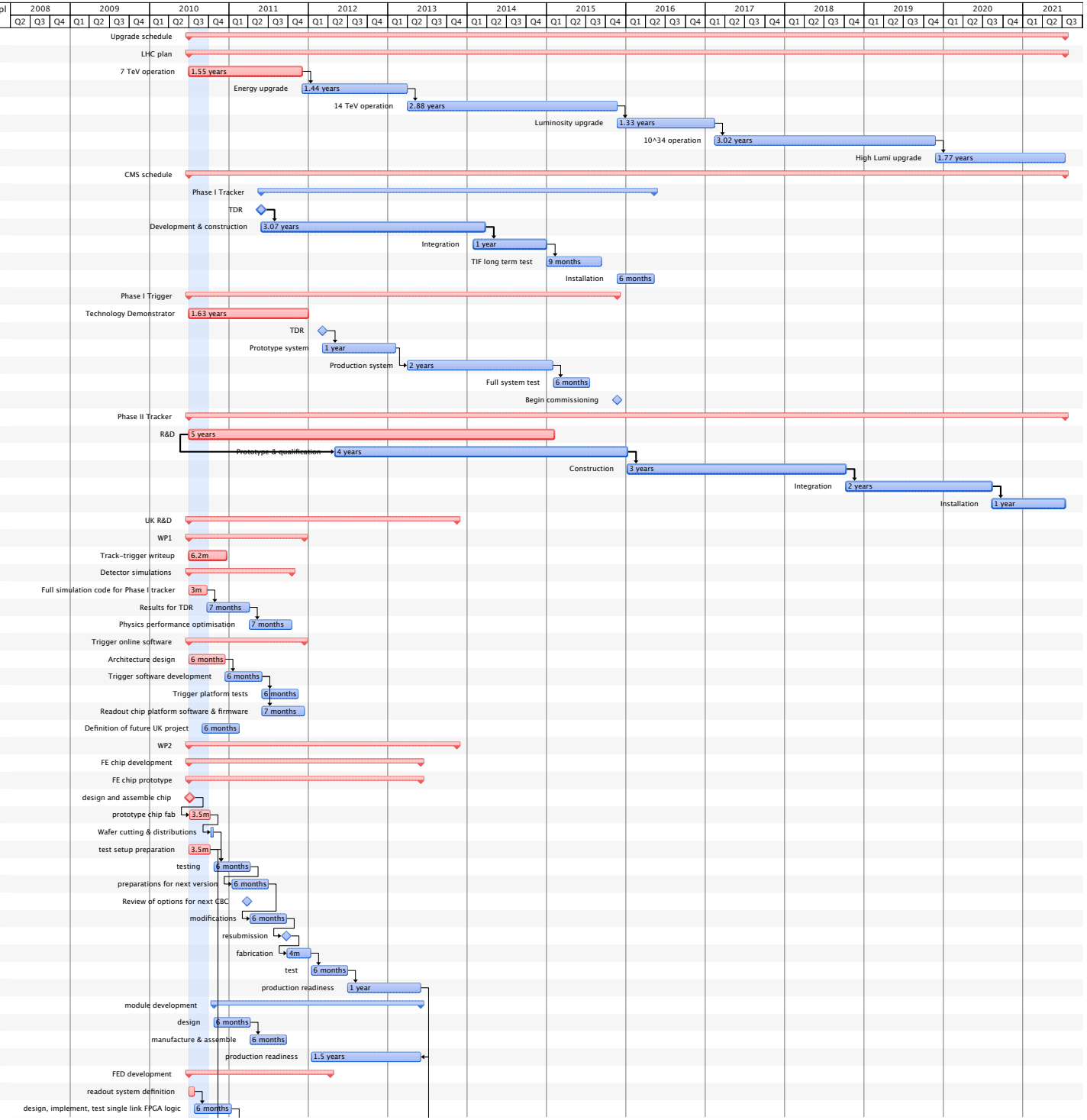
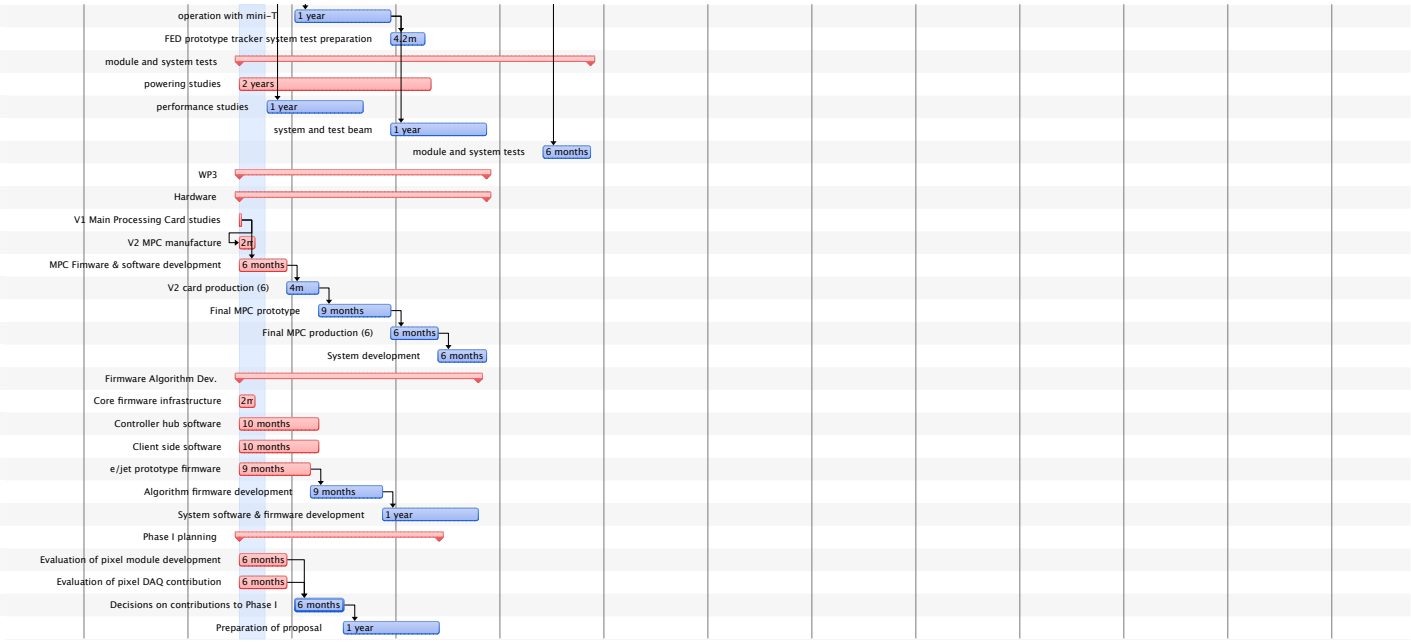
#	Info	Title	Given Planned Work	Flag	Status	# Predecessors	Expected Start	Assigned Resources	% Complete
0		**Upgrade schedule**					01/07/2010		0%
1		**LHC plan**					01/07/2010		0%
2		7 TeV operation					01/07/2010		0%
3		Energy upgrade				2	06/12/2011		0%
4		14 TeV operation				3	02/04/2013		0%
5		Luminosity upgrade				4	24/11/2015		0%
6		10^34 operation				5	14/02/2017		0%
7		High Lumi upgrade				6	26/11/2019		0%
8		**CMS schedule**					01/07/2010		0%
9		**Phase I Tracker**					01/06/2011		0%
10		TDR					01/06/2011		0%
11		Development & construction				10	01/06/2011		0%
12		Integration				11FS...	30/01/2014		0%
13		TIF long term test				12	01/01/2015		0%
14		Installation				5	24/11/2015		0%
15		**Phase I Trigger**					01/07/2010		0%
16		Technology Demonstrator					01/07/2010		0%
17		TDR					09/03/2012		0%
18		Prototype system				17	09/03/2012		0%
19		Production system				18	01/04/2013		0%
20		Full system test				19	02/02/2015		0%
21		Begin commissioning				5	24/11/2015		0%
22		**Phase II Tracker**					01/07/2010		0%
23		R&D					01/07/2010		0%
24		Prototype & qualification				23SS...	03/05/2012		0%
25		Construction				24	07/01/2016		0%
26		Integration				25	11/10/2018		0%
27		Installation				26	13/08/2020		0%
28		**UK R&D**					01/07/2010		0%
29		**WP1**					01/07/2010		0%
30		Track-trigger writeup					01/07/2010		0%
31		**Detector simulations**					01/07/2010		0%
32		Full simulation code fo...					01/07/2010		0%
33		Results for TDR				32	23/09/2010		0%
34		Physics performance o...				33	07/04/2011		0%
35		**Trigger online software**					01/07/2010		0%
36		Architecture design					01/07/2010		0%
37		Trigger software devel...				36	16/12/2010		0%
38		Trigger platform tests				37	02/06/2011		0%
39		Readout chip platform ...				37	02/06/2011		0%
40		Definition of future UK p...					02/09/2010		0%
41		**WP2**					01/07/2010		0%
42		**FE chip development**					01/07/2010		0%
43		**FE chip prototype**					01/07/2010		0%
44		design and assemble...					05/07/2010		0%
45		prototype chip fab				44	05/07/2010		0%
46		Wafer cutting & distri...				45	11/10/2010		0%
47		test setup preparation					01/07/2010		0%
48		testing				46; 47	25/10/2010		0%
49		preparations for next...				48FS...	17/01/2011		0%
50		Review of options for...					28/03/2011		0%
51		modifications				49FS...	11/04/2011		0%
52		resubmission				51	23/09/2011		0%
53		fabrication				52	26/09/2011		0%
54		test				53	16/01/2012		0%
55		production readiness				54	02/07/2012		0%
56		**module development**					25/10/2010		0%
57		design				48	25/10/2010		0%
58		manufacture & assemble				57	11/04/2011		0%
59		production readiness				55FF	16/01/2012		0%
60		**FED development**					01/07/2010		0%
61		readout system definition					01/07/2010		0%
62		design, implement, tes...				61	29/07/2010		0%



63	operation with mini-T	62	13/01/2011	0%	
64	FED prototype tracker ...	63	15/12/2011	0%	
65	**module and system tests**		**01/07/2010**	0%	
66	powering studies		01/07/2010	0%	
67	performance studies	47	07/10/2010	0%	
68	system and test beam	63	15/12/2011	0%	
69	module and system tests	59	03/06/2013	0%	
70	**WP3**		**01/07/2010**	0%	
71	**Hardware**		**01/07/2010**	0%	
72	V1 Main Processing Ca...		01/07/2010	0%	
73	V2 MPC manufacture	72	01/07/2010	0%	
74	MPC Fimware & softwa...	72	01/07/2010	0%	
75	V2 card production (6)	74	16/12/2010	0%	
76	Final MPC prototype	75	07/04/2011	0%	
77	Final MPC production (6)	76	15/12/2011	0%	
78	System development	77	31/05/2012	0%	
79	**Firmware Algorithm Dev.**		**01/07/2010**	0%	
80	Core firmware infrastru...		01/07/2010	0%	
81	Controller hub software		01/07/2010	0%	
82	Client side software		01/07/2010	0%	
83	e/jet prototype firmware		01/07/2010	0%	
84	Algorithm firmware de...	83	10/03/2011	0%	
85	System software & firm...	84	17/11/2011	0%	
86	**Phase I planning**		**01/07/2010**	0%	
87	Evaluation of pixel modu...		01/07/2010	0%	
88	Evaluation of pixel DAQ ...		01/07/2010	0%	
89	Decisions on contributio...	87; 88	14/01/2011	0%	
90	Preparation of proposal	89	01/07/2011	0%