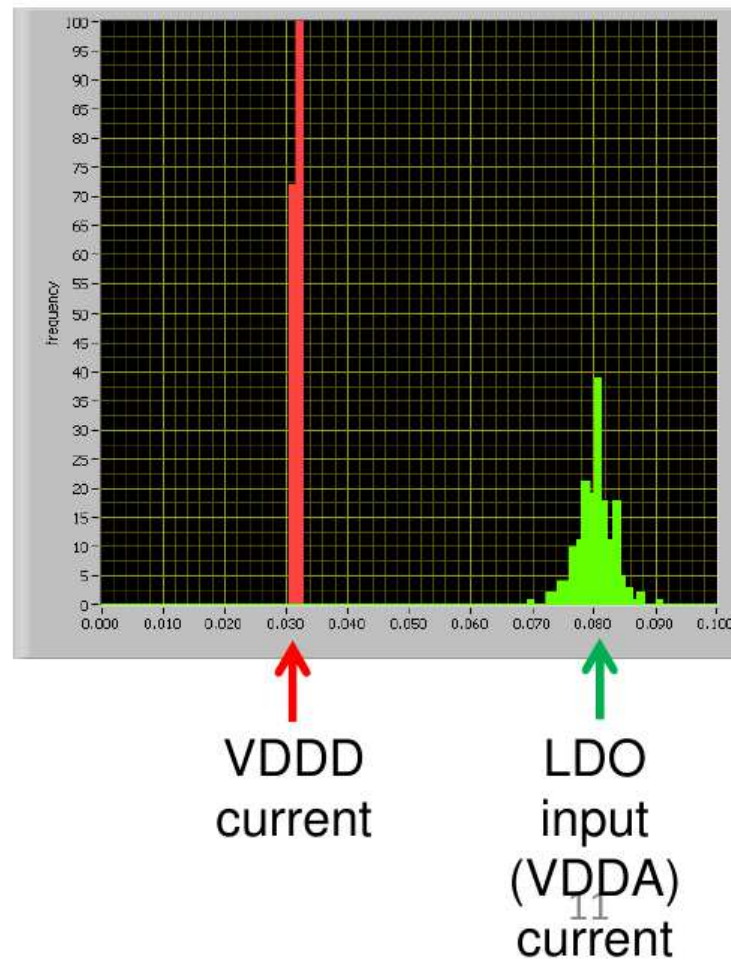# CBC3 power consumption

- At 1.2V, room temperature

- Configured state refer to
  http://www.hep.ph.ic.ac.uk/ASIC/cbc3/cbc3i2c/CBC_I2CREGS_PAGE1_EXAMPLE.txt

- Analog consumption different when chip hasn't been exposed
  to clock since power-on

State	I_{VDD} (digital)	I_{VLDOI} (Analog)
Power up without clock	13.8mA	77.7mA
Unconfigured or hard reset, clock disabled	13.8mA	100.7mA
Unconfigured or hard reset, clock enabled	30.5mA	100.7mA
Configured or hard reset, clock disabled	13.8mA	79.5mA
Configured or hard reset, clock enabled	30.5mA	79.5mA

Unconfigured I_{VLDOI} is expected to change to about 48mA for
CBC3.1.

CBC3 power consumption distribution