CBC3.1 Submission Status

Outer Tracker Electronics WG – A 31/01/2018

Design Readiness Review held 19/12/17

- External participants: K.Kloukinas (CERN), J.Borg (IC), K.Uchida (IC)
- Outcome: Some simulations remaining. Go-ahead for I²C register modification given.

Design Readiness Review held 17/01/18

- External participants: J.Borg (IC), K.Uchida (IC)
- Outcome: Some simulations still remaining.
- Simulations now complete & findings to be circulated. GOOD TO GO.
- Layout complete, DRC & LVS clean, ready for upload.
 - Some waivers related to probe pads (as for CBC3)
- Upload blocked due to some required Export documents:
 - MOSIS_Export_Review_Questionnaire_20171101
 - MOSIS_USECQ_20171207