

CBC3 power consumption

Supply voltage=1.2V, room temperature, 320MHz clock enabled.

State	I_{VDD} (Digital)	I_{VLDO1} (Analog)
CBC3 Unconfigured or hard reset	30.5mA	100.7mA
CBC3.1 Unconfigured or hard reset	30.5mA	≈ 48 mA
Configured for normal operation	30.5mA	79.5mA
Worst-case radiation damage		+3.5mA
Ipri1 register MSB set SEU (never seen yet)		+70mA
Manufacturing variations	≈ 2 mA	± 11 mA

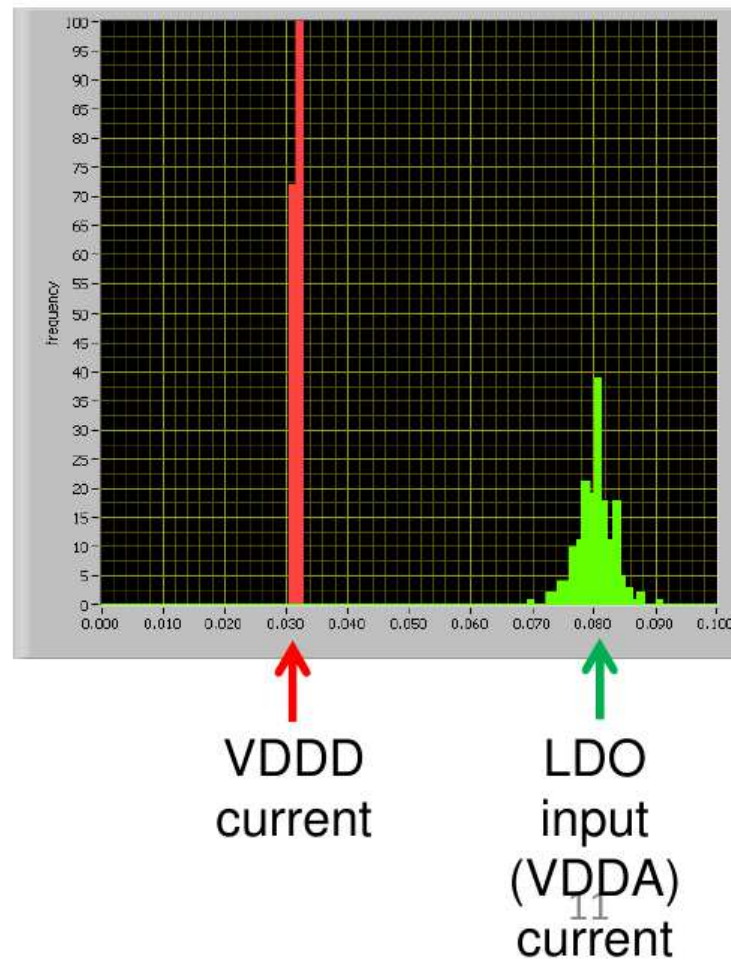
Settings for normal operation:

http://www.hep.ph.ic.ac.uk/ASIC/cbc3/cbc3i2c/CBC_I2CREGS_PAGE1_EXAMPLE.txt

For TID results see:

http://www.hep.ph.ic.ac.uk/ASIC/dmray/CBC_documentation/CBC3_IrradReport_TrackerWeek_July2017.pdf

CBC3 power consumption distribution



From

http://www.hep.ph.ic.ac.uk/ASIC/dmray/CBC_documentation/CBC3_status_May_2017.pdf