

Non-linearity measurements in the lab

hardware description method results

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## Linearity test bench



#### LabView automated

use prog. attenuator, rather than changing amp. of AWG O/P to avoid contribution from AWG O/P stage linearity

14-bit ADC -> negligible INL and DNL contribution to measurement

for MGPA test board use either RAL PCB with socket easy chip exchange

#### or

VFE card verify performance in final configuration







### Linearity measurement procedure

#### 1) remove MGPA test board and characterise system => make precision measurement of attenuator steps



2) replace MGPA and measure pulse shapes for different signal sizes



pulse shapes measured with 1ns resolution by sweeping time of charge injection using prog. delay



# **Typical non-linearity measurement (1)**



time [1 nsec. steps]



## **Typical non-linearity measurement (2)**









## **Pulse shape matching**





## Some non-linearity results (old)

#### **MGPA Version 1**



10 chips measured for each MGPA version

v. similar results V1 cf. V2

nonlinearity within (or close to)  $\pm$  0.1% specification

these measurements made on RAL card using simulated APD capacitance

#### **MGPA Version 2**





### **New results**



Why is non-linearity worse in H4 beam? (noticeable fall-off for small signals on high gain channel)

Could there be an APD effect?

No – no significant difference between 180 pF and APD biased at 350 V

Could signal risetime be shorter than expected?

No - changing charge injection 10ns -> 5ns doesn't make much difference (at least for high gain channel)



### **Final thoughts**

non-linearity measured using procedure described here does not explain test beam results results here within or very close to +/- 0.1% spec.

method used here will not include any contribution from ADC, but don't expect to be significant

could test beam linearity studies be more sensitive to pulse shape?

PSM within design spec. but does tend to show similar trend to test beam linearity plots

